

## **AN-1298 LM5007 Evaluation Board**

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### **1 Introduction**

The LM5007 evaluation board is designed to provide the design engineer with a fully functional step down switching regulator to evaluate LM5007 regulator IC, in a typical environment.

The performance of evaluation board is as follows:

- Input range: 12V to 75V
- Output voltage: 10V
- Output current: 400mA
- Measured efficiency: 90% at 300mA and  $V_{IN} = 30V$
- Board size: 31.9mm x 44.4mm

### **2 Theory of Operation**

The LM5007 Step down switching regulator features all of the functions needed to implement low cost, efficient, Buck bias regulators. This high voltage regulator contains an 80V, 0.7A N-channel Buck switch. The regulator is based on hysteretic control scheme using an on-time inversely proportional to input voltage ( $V_{IN}$ ). This feature allows the operating frequency to remain relatively constant with load and input voltage variations. The hysteretic control requires no control loop compensation, while providing fast load transient response. Additional protection features include : Thermal Shutdown,  $V_{CC}$  undervoltage lockout and maximum duty-cycle limiter. LM5007 can be used in numerous applications to efficiently regulate step down higher voltage inputs. This regulator is well suited for 48V telecom and the new 42V automotive power bus ranges.

The LM5007 operates in discontinuous conduction mode at light load currents or continuous conduction mode at heavier load currents. In discontinuous conduction mode, current through the output inductor starts at zero and ramps up to the peak value during the buck switch on time, and then back to zero during buck switch off time. In discontinuous conduction mode, the operating frequency can be relatively low and will vary with load. Therefore at light loads the conversion efficiency is maintained , since switching losses decrease with reduction in switching frequency.

The evaluation board just needs one high voltage power-supply (up to 75V and current rating of 1A). The maximum output power is 4 watts. Either an electronic load or resistor bank can be used for testing evaluation board.

### **3 Start-up Feature**

There is no soft-start feature in LM5007, and it normally comes up with full load current to supply the power demanded by the load within a short time. The LM5007 contains an intelligent current limit OFF timer intended to reduce the foldback characteristic inherent with fixed off-time over-current protection. If the current in the Buck FET exceeds 725mA the present cycle is immediately terminated (cycle by cycle current limit). Following the termination of the cycle a non-resettable current limit off timer is initiated. The duration of off time is a function of the external setting resistor ( $R_{cl}$ ) and the FB pin voltage. When FB pin voltage = 0V the current limit off time is internally preset to 15 $\mu$ s. This condition occurs in short circuit operation when a maximum amount of off time is required.

In cases of overload (not complete short circuit) the current limit off time can be reduced as a function of the output voltage (measured at the FB pin). Reducing the off time during smaller overloads reduces the amount of foldback and also reduces the initial start-up time.

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## 4 Low Ripple Configuration

For applications where lower output voltage ripple is required the output load can be connected directly to the low ESR output capacitor, as shown in Figure 1. The evaluation board is laid out in such a way that, user can use either (high ripple or low ripple) options by simply shifting the output connector. The series resistor (R) will degrade the load regulation however. Another technique for enhancing the ripple voltage at the FB pin is to place a capacitor in parallel with the feedback divider resistor R1. The addition of the capacitor reduces the attenuation of the ripple voltage from the feedback divider.

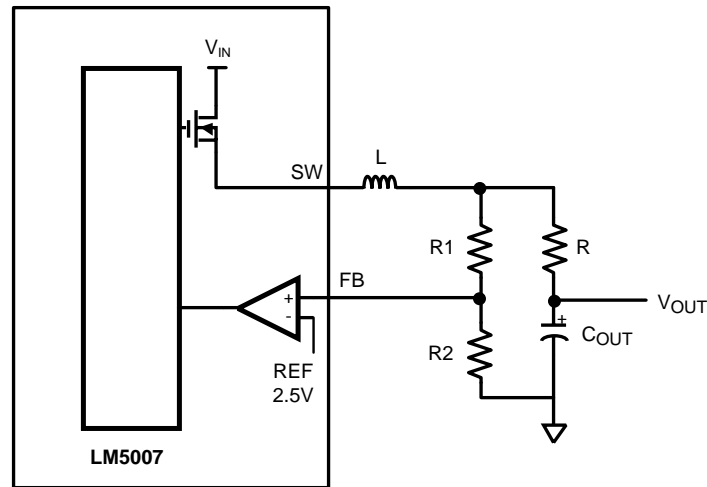


Figure 1. Low Ripple Configuration

## 5 Schematic

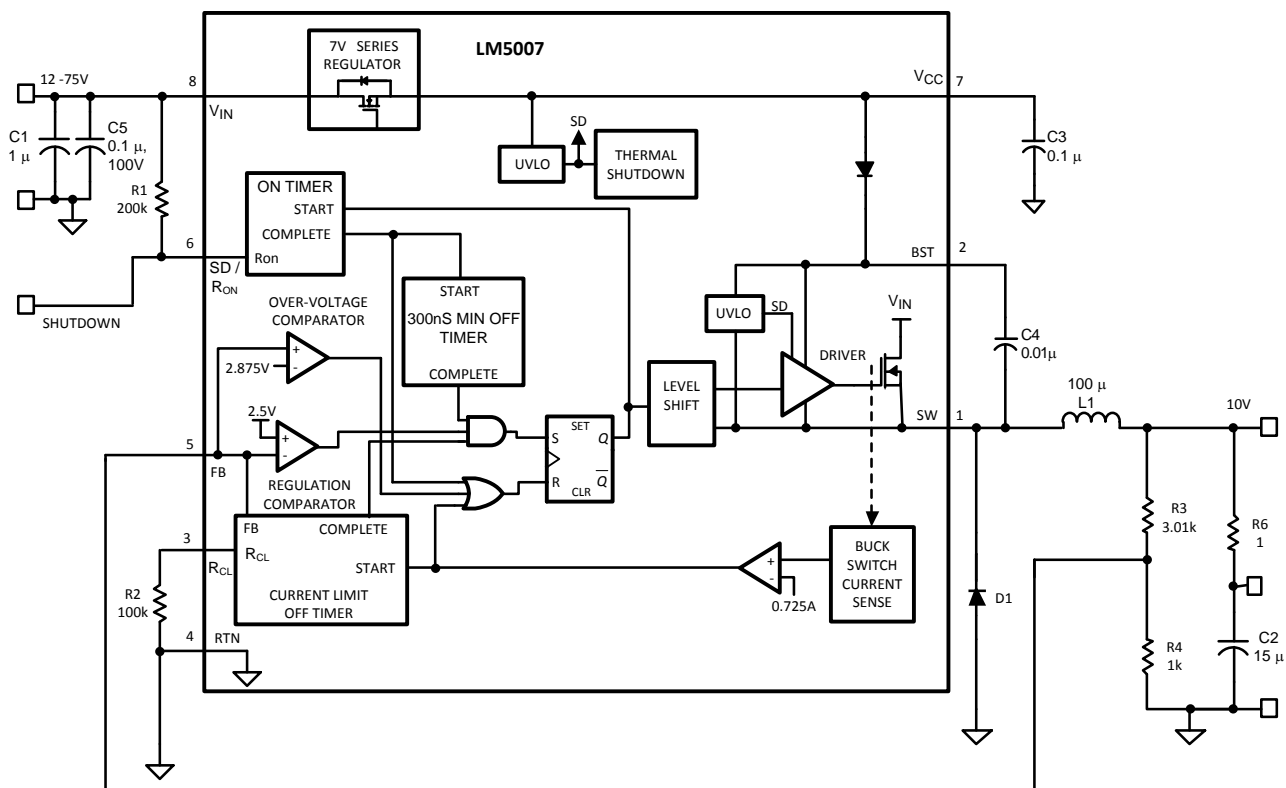


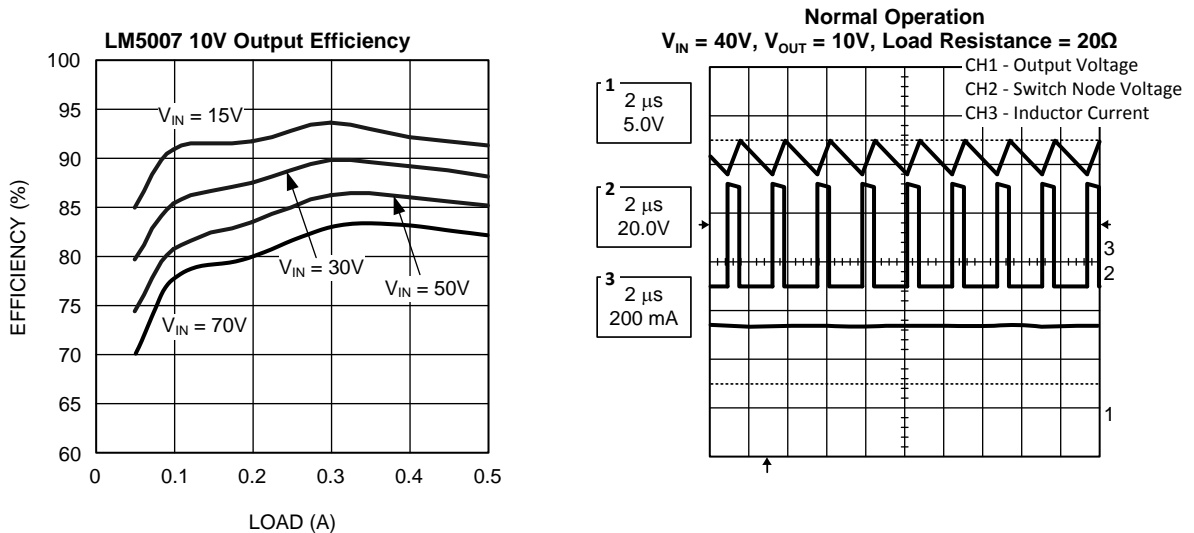
Figure 2. Evaluation Board Schematic Diagram

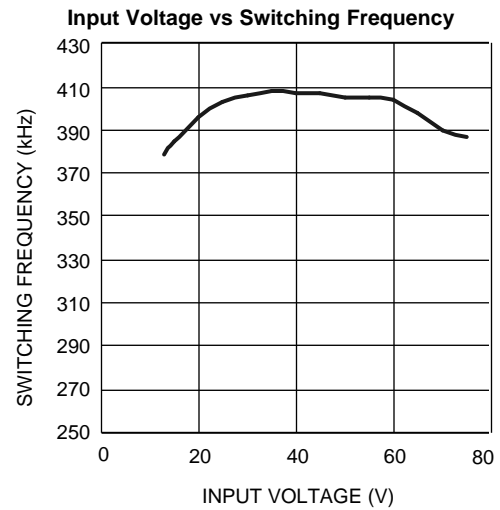
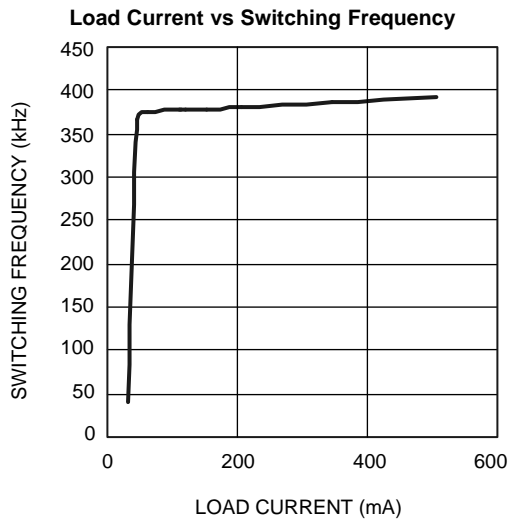
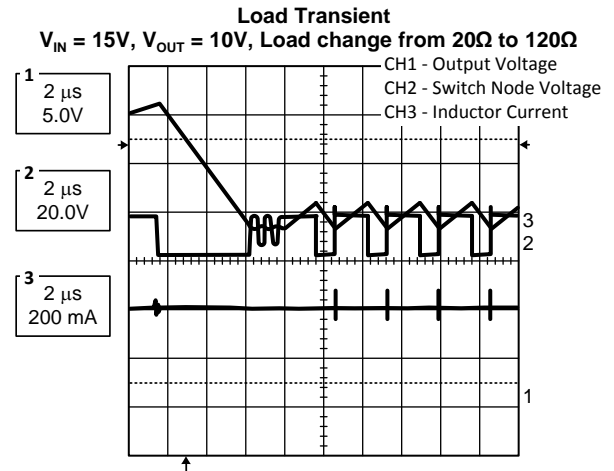
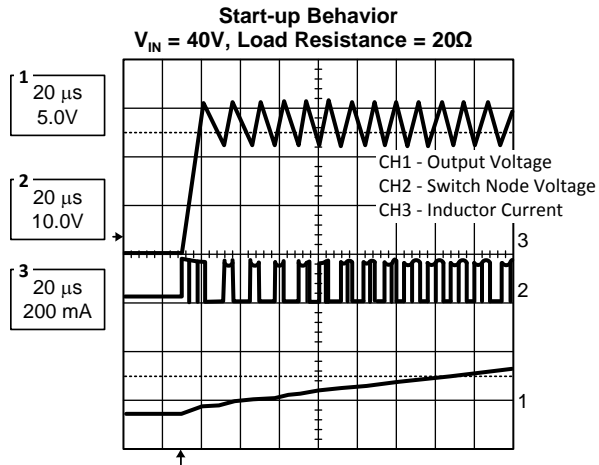
## 6 Bill of Materials

**Table 1. Bill of Materials**

| Item | Qty | Part Number        | Description                  | Value            |
|------|-----|--------------------|------------------------------|------------------|
| C    | 1   | C4532X7R2A105M     | CAPACITOR, CER, TDK          | 1 $\mu$ , 100V   |
| C    | 2   | C4532X7R1E156M     | CAPACITOR, CER, TDK          | 15 $\mu$ , 25V   |
| C    | 3   | C1206C104K5RAC     | CAPACITOR, CER, KEMET        | 0.1 $\mu$ , 50V  |
| C    | 4   | C1206C103K5RAC     | CAPACITOR, CER, KEMET        | 0.01 $\mu$ , 50V |
| C    | 5   | C3216X7R2A104KT    | CAPACITOR, CER, TDK          | 0.1 $\mu$ , 100V |
| D    | 1   | MURA110T3          | DIODE, 100V, ON SEMI         |                  |
| L    | 1   | SLF7045T-101MR60-1 | BUCK INDUCTOR, TDK           | 100 $\mu$ H      |
| R    | 1   | CRCW12062003F      | RESISTOR                     | 200K             |
| R    | 2   | CRCW12061003F      | RESISTOR                     | 100K             |
| R    | 3   | CRCW12063011F      | RESISTOR                     | 3.01K            |
| R    | 4   | CRCW12061001F      | RESISTOR                     | 1K               |
| R    | 5   | CRCW12061R00F      | RESISTOR                     | 1                |
| U    | 1   | LM5007             | REGULATOR, TEXAS INSTRUMENTS |                  |

## 7 Efficiency Measurements





8 PCB Layouts

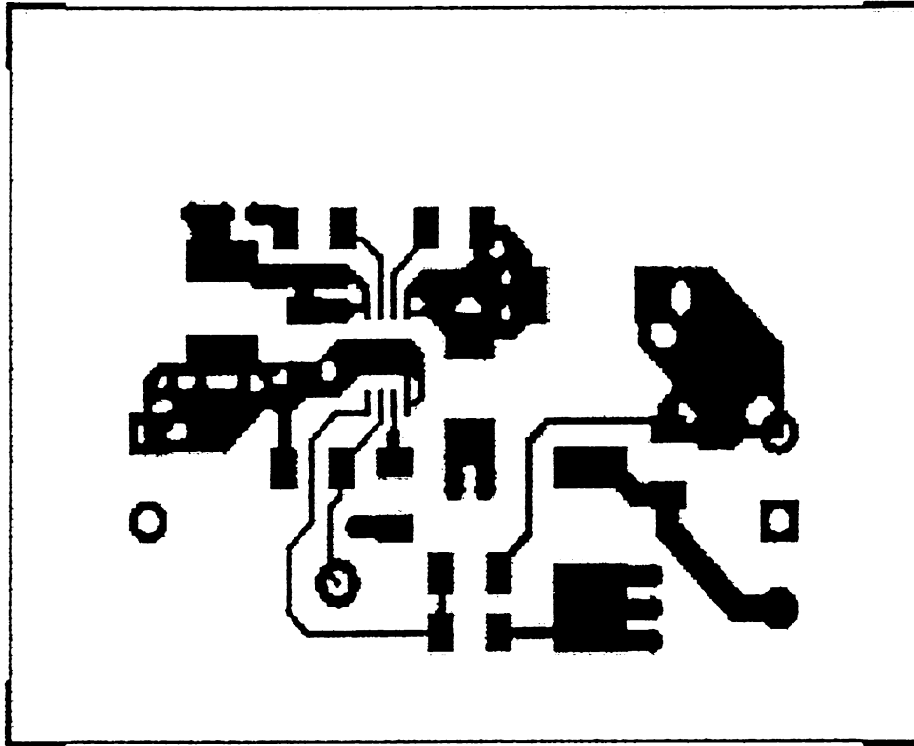


Figure 3. Top Layer

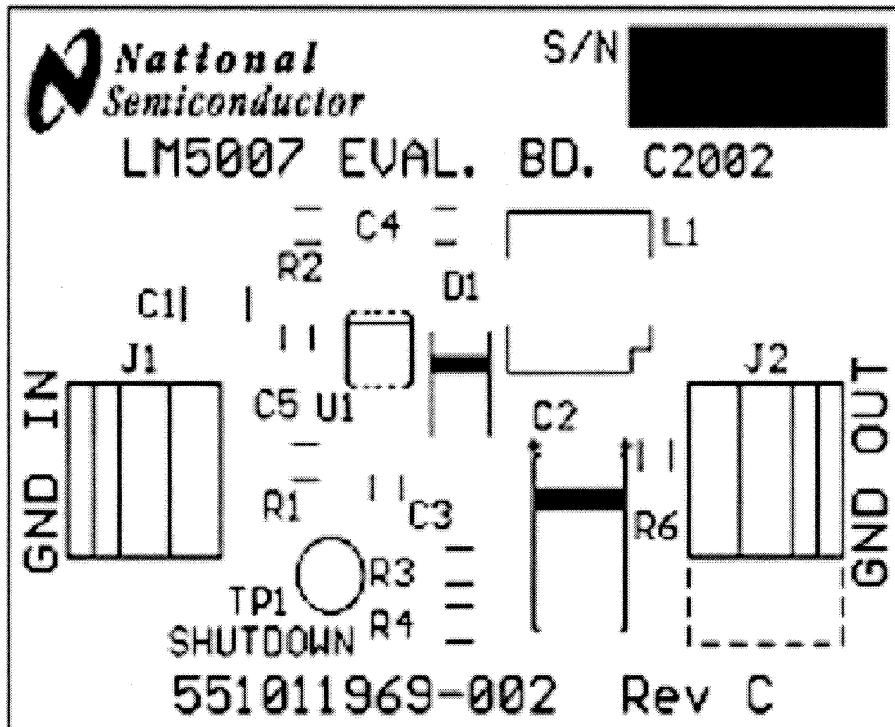


Figure 4. Silk Screen

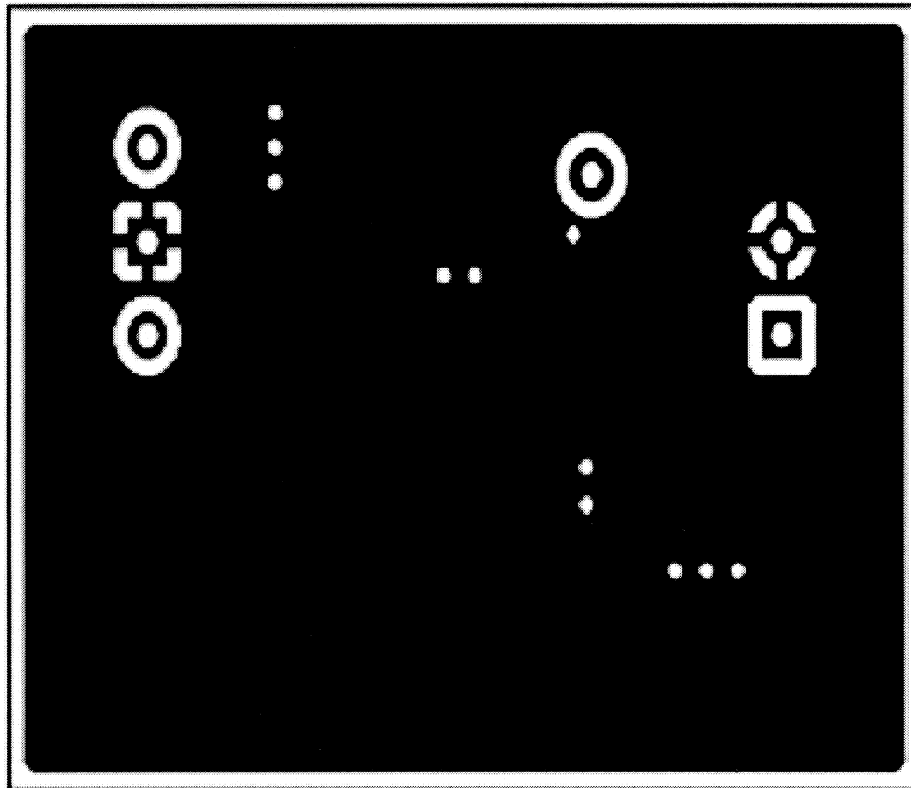


Figure 5. Bottom Layer

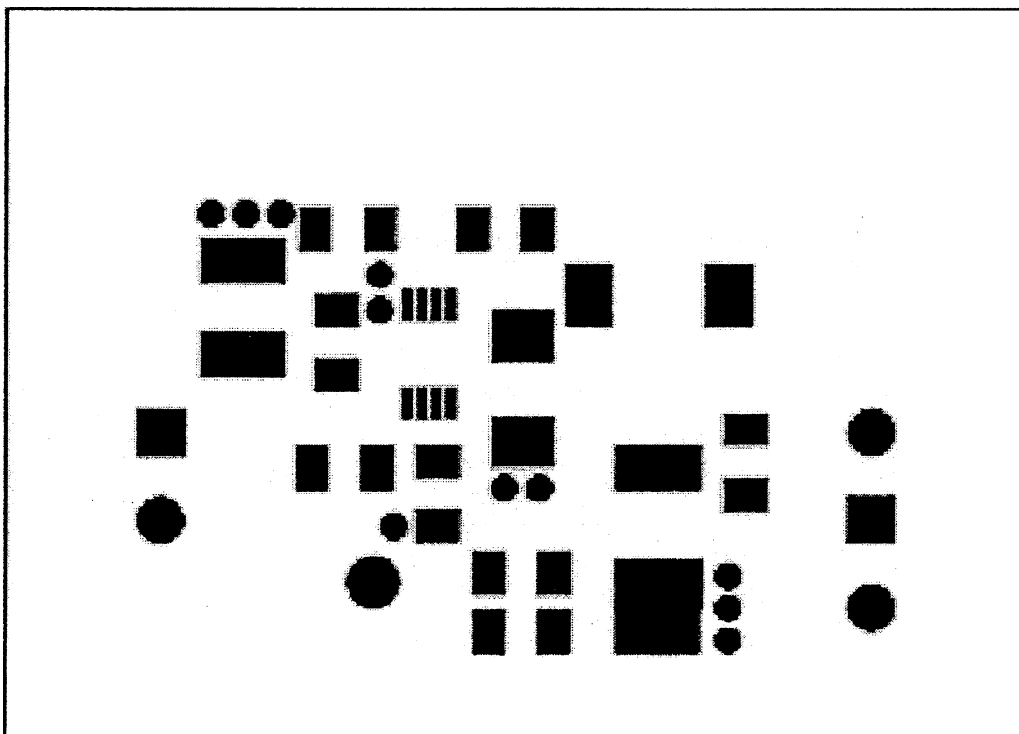


Figure 6. Top Soldermask

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