



**NOTES:**

- 1) R8 for test purposes only

**ASSEMBLY:**

- 1) built on PCB PMP2644RevB
- 2) R2, R100, C5, C6 OPEN
- 3) inductor L2 (= MSS1260 series) does NOT meet pattern
- 4) C1 bottom side assy

**Revision History**

Revision	Notes
A	*** preliminary ***
B	adjusted loop, built & tested

Designed for: <b>Public Release</b>	Mod. Date: 9/9/2016
Project Title: <b>tiny buck for low noise RF apps</b>	
Sheet Title:	
Assembly Variant: <b>[No Variations]</b>	Sheet: <b>1 of 1</b>
File: <b>PMP30122RevB.SchDoc</b>	Size: <b>A4</b>
Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

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Number: **PMP30122** | Rev: **B**  
 SVN Rev: **Version control disabled**  
 Drawn By:  
 Engineer: **B.Geck**



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