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* SN75DP130

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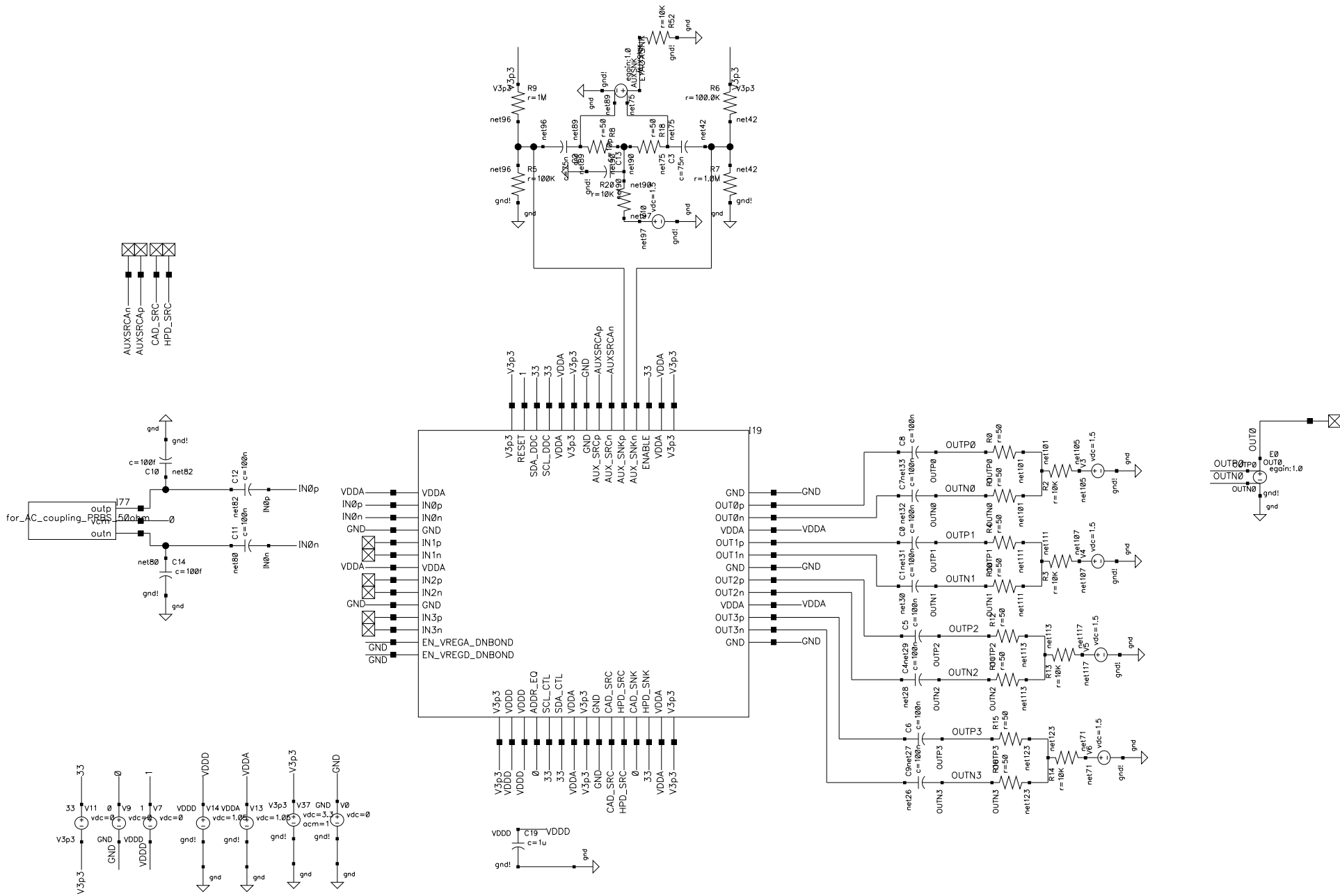
File Description

‘DP130_test_bench.sp’ -which is the test bench- invokes other 2 files:
‘nominal_process_windows.lib’ –nominal process library, and ‘DP130_subckt_windows.sp’
–subcircuits’ definition.

Instructions

- 1) Open your simulator HSPICE_A-2008.03-SP1 on Windows
- 2) On the file menu, open and run file 'DP130_test_bench.sp'.

Plots of the test-setup schematic and simulation results are shown below:



SN75DP130: HSPICE Transient Simulation

