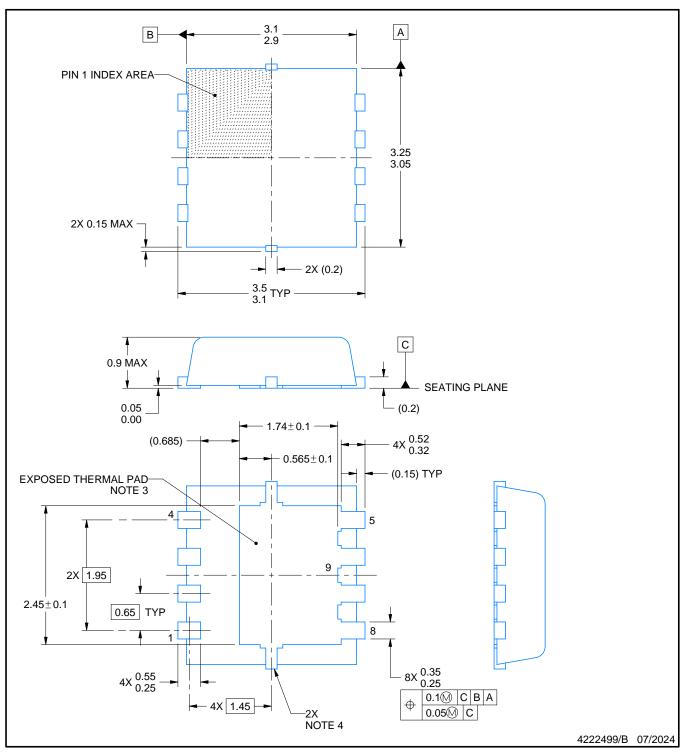
# **DNH0008A**



## **PACKAGE OUTLINE**

### VSONP - 0.9 mm max height

PLASTIC SMALL OUTLINE - NO LEAD



#### NOTES:

- 1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M. 2. This drawing is subject to change without notice.
- 3. The package thermal pad must be soldered to the printed circuit board for thermal and mechanical performance.
- 4. Metalized features are supplier options and may not be on the package.
- 5. All dimensions do not include mold flash or protrusions.

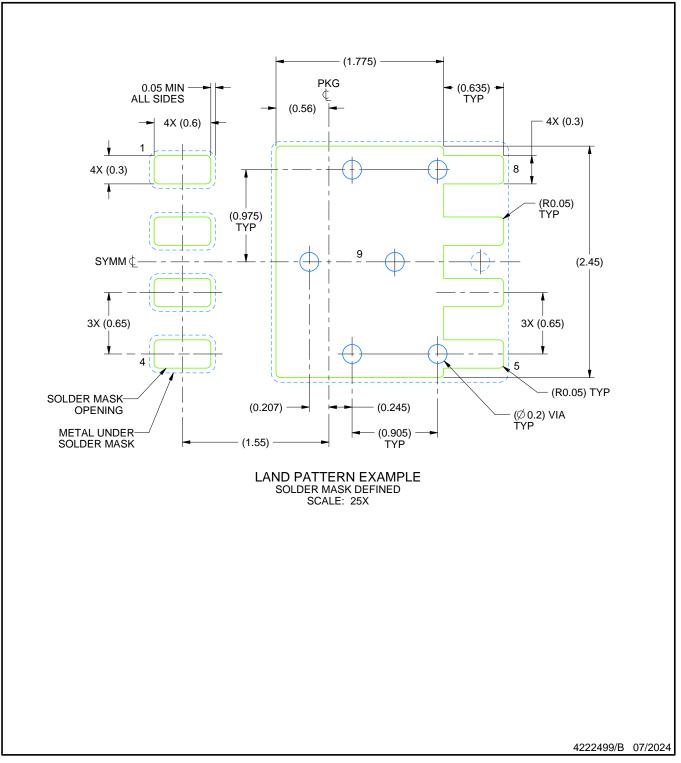


## **DNH0008A**

# **EXAMPLE BOARD LAYOUT**

#### VSONP - 0.9 mm max height

PLASTIC SMALL OUTLINE - NO LEAD



NOTES: (continued)

6. This package is designed to be soldered to a thermal pad on the board. For more information, see Texas Instruments literature number SLUA271 (www.ti.com/lit/slua271).

7. Vias are optional depending on application, refer to device data sheet. If some or all are implemented, recommended via locations are shown.

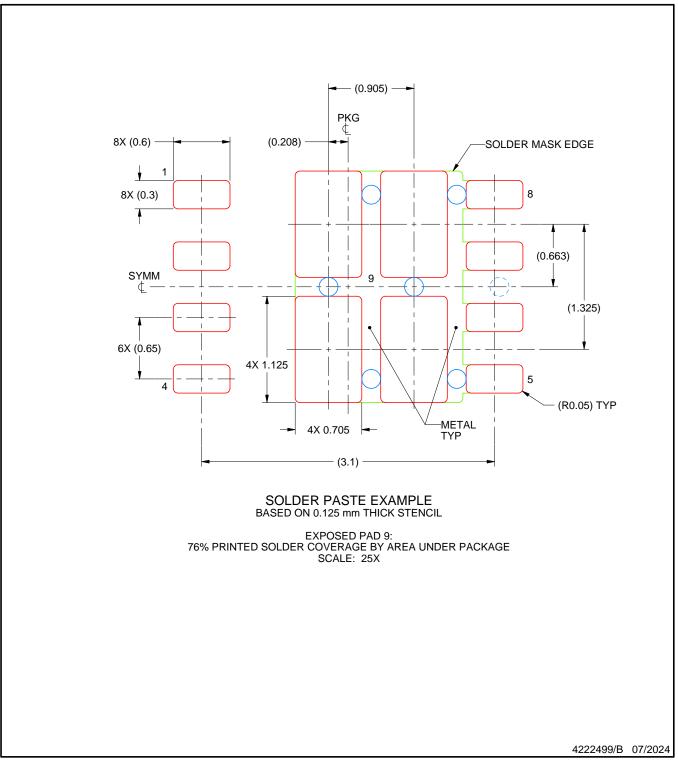


### **DNH0008A**

# **EXAMPLE STENCIL DESIGN**

### VSONP - 0.9 mm max height

PLASTIC SMALL OUTLINE - NO LEAD



NOTES: (continued)

8. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.



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