Functional safety at TI: Understanding ISO 26262 hardware element classes



Presentation summary

Session summary:

The automotive functional safety standard ISO 26262 describes the development of an ISO 26262 compliant item. The standard states that products that have not originally been developed according to the ISO 26262 standard may be evaluated for use in compliant systems. This presentation explains how TI functional safety product categories align with ISO 26262 hardware element classes.

What you'll learn:

- What makes a product functional safety compliant?
- What are the three types of ISO 26262 hardware element classes?
- How do TI functional safety product categories align with the standard?
- What documentation does TI provide for customers to evaluate hardware architectural metrics?



TI Functional Safety product categories

Functional Safety-Capable Functional Safety Quality-Managed Functional Safety-Compliant



TI functional safety product categories

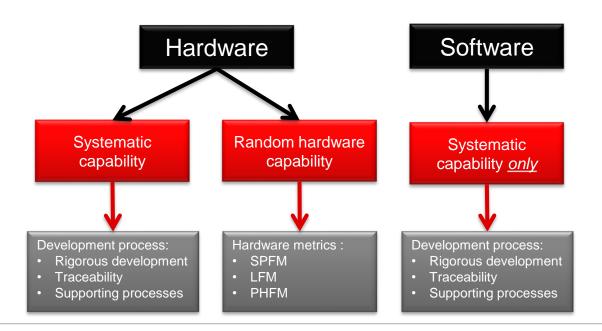
	EXAS NSTRUMENTS	Functional Safety-Capable The simplest product category of analog products that can be evaluated for use in a functionally safe system	Functional Safety Quality-Managed Moderately complex products such as an MCU	Functional Safety-Compliant The most complex products such as MCUs, microprocessors and complex analog signal- chain and power products	<image/> <image/> <image/> <image/> <text><text><text><text></text></text></text></text>
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Analysis report	Failure mode distribution (FMD) and/or pin FMA*	1	Included in FMEDA	Included in FMEDA	CERTIFICATE Undersonal Control
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* May only be available for analog power and signal chain products. ** Available for select products.

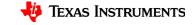


Functional safety

- Functional safety is the absence of *unreasonable risk* due to hazards caused by *malfunctioning behavior* of E/E systems.
- For a component to be compliant, the hardware must have both random hardware and systematic capability.

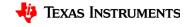






TI's role in satisfying systematic and random hardware capability

Functional Sa Functional Safety		Functional Sat	fety-Compliant
Systematic capability	Random hardware capability	Systematic capability	Random hardware capability
TI quality managed development process + Customer evaluation of hardware elements	TI provides documentation for customers to conduct FMEDA analysis	TI functional safety development process	TI provides documentation for customers to conduct FMEDA analysis



Hardware element classification

ISO 26262-8:2018

Class I: No or few states, no internal safety mechanisms

Example hardware elements:

- Resistors, Capacitors
- Diodes, Transistors
- 3-pin LDO, level shifter
- Simple logic gates
- PTC temperature sensor

Evaluation of HW element:

 Evaluation by itself is not needed Class II: Few states, no internal safety mechanisms

Example hardware elements:

- OP AMPS
- ADC
- DAC
- DC/DC converters
- CAN/LIN transceiver

Evaluation of HW element:

- Evaluation plan and argument are needed to prove functional performance
- Supported by evaluation analysis and testing

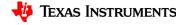
Class III: Many states, includes safety mechanisms

Example hardware elements:

- Microprocessors
- SOCs (system on a chip)
- Multichannel PMICs
- Motor drivers
- Higher function SBCs

Evaluation of HW element:

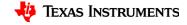
 Additional measures to argue that the risk of a safety requirement violation due to systematic faults is sufficiently low



TI functional safety product categories and ISO 26262 hardware element classes

Safety Mechanism (SM)	Class I	Class II	Class III	Compliant
No	Functional Safety-Capable	Functional Safety-Capable	N/A	Functional Safety-Compliant
Yes	N/A	If SM is not used by customer in safety concept or if SM is used by customer in safety concept and the customer assumes a certain diagnostic coverage as defined in the standard for the SM: Functional Safety-Capable	If SM is used by customer in safety concept: Functional Safety Quality-managed	Functional Safety-Compliant

* Mapping of TI functional safety product categories to ISO26262 hardware elements classes are approximations for illustration purposes. Customers are responsible for determining their own hardware element classifications.



FMEDA hardware metrics

Data needed for calculations



System FMEDA inputs for Functional Safety-Capable

			1			SINGLE POINT FAULT						LAT	ENT FAULT			SAFE FAULT TOTAL FAULT				
Component name	Component ID	rate	the Compon ent	calculations	Failure mode	mode distrib ution	potential to violate the safety goal in absence of safety mechanisms	mechanism allowing to prevent the failure mode from violating safety goal	SM ID	Failure mode coverage wrt violation of safety goal	fault failure rate	Residual failure rate	safety goal in combination with an independent failure of	Safety mechanism allowing to prevent the failure mode from being latent?	SM ID	with respect to latent failures	Latent multiple- point fault failure rate	Detected multiple- point fault failure rate	failure rate	Detected multiple- point fault failure rate
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		9			PH high side FE T	5%	V	SM	SM3	99%	0.00		NV				0.00			
		9			PWRGD false trip or	5%	NV	SM	SM4, SM7	99%	0.00	0.00	V	NSM		0%	0.45			
		9			Short circuit any two	5%	V	SM	SM4, SM5,	50%		0.23	NV				0.00			0.45
		9			BOOT open		V	SM	SM4, SM7	0%		0.00	NV				0.00			
		9			VIN open		V	SM	SM4, SM7	0%		0.00	NV				0.00	0.00	0.00	
		9			EN open		NV	NSM		0%		0.00	NV				0.00			
		9			SS/TR open		NV	NSM		0%			NV				0.00			
		9			RT/CLK open		V	SM	SM1, SM3	0%		J <u>0.00</u>	NV				0.00	0.00	0.00	
		9			PWRGD open		NV	NSM		0%				NSM		0%	0.00			
		9		SR	VSENSE open		V	SM	SM4	0%	0.00	, 0.00	NV				0.00	0.00	0.00	0.00



1. Product FIT

2. Product FMD and/or Product Pin FMA



System FMEDA inputs for Functional Safety Quality-Managed and Complaint

								SI	IGLE POIN	IT FAULT					LAT	ENT FAULT			SAFE FAULT	TOTAL FAULT
Component name INPUT	Component ID	Failure rate	Failure rate for the Compon ent	calculations	Failure mode	Failure mode distrib ution	potential to violate the safety goal in absence of safety mechanisms	mechanism allowing to prevent the failure mode from violating safety goal	SM ID	violation of safety goal	fault failure rate	Residual failure rate	safety goal in combination with an independent failure of	Safety mechanism allowing to prevent the failure mode from being latent?		to latent failures	Latent multiple- point fault failure rate CALCULATE	Detected multiple- point fault failure rate	multiple- point fault failure rate	Detected multiple- point fault failure rate CALCULATE
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			9	SR	PH high side FET	5%	V	SM	SM3	99%	0.00	0.0	NV				0.00		0.45	
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			9	SR	Short circuit any two	5%		SM	SM4, SM5,	50%	0.00	0.2	NV				0.00		0.23	
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3. Product FMEDA contains safety mechanisms and diagnostic coverage



TI Functional Safety product categories

Examples of data provided by TI for FMEDA



Functional Safety-Capable

TPS7B84-Q1 🖉 active		TEXAS Application Report
Top Product details Technical documentation Design & development	Ordering & q	INSTRUMENTS
★ = Top documentation for this product selected by TI Type Title		1 Overview
Functional safety info V Filter title by keyword	Q	This document contains information for TPS7B84-Q1 (DFN-6 and SOT223-4 package) to aid in a functional safety system design. Information provided are:
Functional safety information TPS7B84-Q1 Functional Safety FIT Rate and FMD		 Functional Safety Failure In Time (FIT) rates of the semiconductor component estimated by the application of industry reliability standards Component failure modes and their distribution (FMD) based on the primary function of the device

- 1. Product FIT
- 2. Product FMD and/or Product Pin FMA

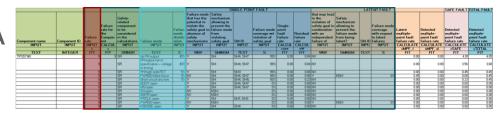
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		9		SR	PH high side FET	55	V	SM	SM3	995	0.00		1NV				0.0		0.4	0.4
		- 5		SR	PWFIGD false trip or	53	NV	SM	SM4, SM7	995	0.00	0.00	SV V	NSM		0%	0.4	5 0.00	0.00	0.4
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	-		-	58	SSTRapen		NV	NSM			0.00	0.00	1N/V		-		0.0	0.00	0.00	0.0
	-			58	STELK open		V	54	SMLSM3	00	0.00	0.00	INV				0.0	0.00	0.00	0.0
		9		SR	PWPED open		NV	NSM		05	0.00		V	NEM		05	0.0	0.00	0.00	0.0
		9		58	VSENSE open		V	59.4	53.44	15	0.00		NV				0.0	0.00	0.00	0.00



Functional Safety Quality-Managed

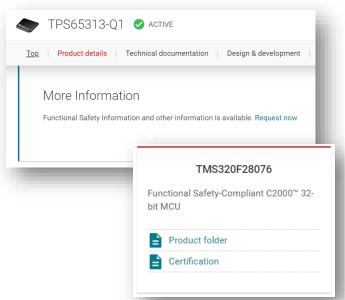
	ABSTRACT Table A-2 summarizes the functional safety mechanisms present in in software or at the system level as described in Section 7. Table 4 gives examples of what content could appear in each cell.	hardware or recommend for implementation -1 describes each column in Table A-2 and		Certification Mark:
		Texas Instruments	100500	Factory(les): Texas Instruments Inc. 12500 Ti Boulevant, Dallas TX 75243-4136, USA
Functional safety information TCAN114x-Q1 Functional Safety Manual	Functional Safety Manual for TCAN1144-Q1 and TCAN1146-Q1		005.001	Holder of Certificate: Texas Instruments Inc. 12500 TI Bouleward Dates TX 75245-4130 USA
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- 1. Product FIT
- 2. Product FMD and/or Product Pin FMA
- 3. Product FMEDA
- 4. Functional Safety Manual



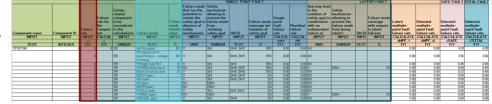


Functional Safety-Compliant





- 1. Product FIT
- 2. Product FMD and/or Product Pin FMA
- 3. Product FMEDA
- 4. Functional Safety Manual





Functional Safety-Compliant FMEDA

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 | LUE-U LUE-U 5.31E-04 L
 |
 | | | | | | | | |
 | 5.312-13 3 | | |
| ROM2
 | 15. EEPROM
 | 15. EEPROH | Dila are flipped after dealar paurerap | 51.00X
 | 5.512-11 1.112-11
 | 5.302-01 L.002-01 L.002-01 L.
 | .112-11 1.112-11 1
 | 1.112-11 1.112-11 1.112-11 | | | | | | | | 5.582-84
 | 1.112-11 | .112-11 1.112 | E-11 1.11E-1 |
|
 |
 | | | 1.01
 | 1.00 0.54
 | 1.54 1.00 1.54
 |
 | 1.11 1.53 1.32 | | | | | | | |
 | | | |
| etter I
 | 1. PIAS_TOP
 | 1.1061 | DC1 will ser in her kink. | 1.4647X
 |
 |
 |
 | | | | | | | | |
 | 6.02.05 | | |
| AMAZ
 | 1. PIAS_TOP
 | 1,1961 | PG1 millionr in two law. | 1.4547X
 |
 | LUCU LUCU LUCU LUCU L
 |
 | | | | | | | | |
 | | | |
| ANAS
 | 1. PIAS_TOP
 | 1.1001 | Safe DG1 failure unde | LIIIIX
 | 1.112.11 1.112.11
 | LUE-U LUE-U LUE-U L
 |
 | | | | | | | | | 6.02-0
 | 6.002-05 6 | 75E-85 E.84E | |
| ANA4
 | 1. PIAS_TOP
1. PIAS_TOP
 | | AVDD1 aslas1 is les high. | 8.5347X
 |
 |
 | 112-11 1.112-11 1
 | | | | | | | | | 6.00E-03
 | 1.02.0 | .112-11 1.112 | E-11 1.11E-1 |
| AMAS
 |
 | 1.2 AVDD1 | |
 | 2.712-03 2.712-03
 | LUC-U LUC-U 2.712-03 L
 | .012-01 1.002-01 1
.012-01 2.702-05 1
 | LIE-II 2.75E-IS 2.79E-IS | | | | | | | | 6.002-03
1.002-03
2.702-03
 | 1.002-00 0
2.702-05 2 | 11E-11 1.11E
21E-11 2.25E | C-11 1.112-1
C-15 1.112-1 |
| ABA7
 |
 | 1.2 AVDD1 | AVDD1 salpatia las las. | 8.5947X
 | 2.712-13 2.712-13
 | LIE-0 LIE-0 2,712-0 L.
 | .02-0 1.02-0 1
.02-0 2.702-0 1
.02-0 2.702-0 1
 | LINE-IN 2.75E-IN 2.79E-IN
LINE-IN 2.75E-IN 2.79E-IN | | | | | | | | 6.002-03
1.002-03
2.702-03
2.702-03
 | 1.012-01 1
2.702-05 2
2.702-05 2 | .01E-01 1.01E
29E-03 2.75E
29E-03 2.75E | E-11 1.11E-1
E-15 1.11E-1
E-15 1.11E-1 |
| AMAI
 | 1. PIAS_TOP
1. PIAS_TOP
 | 1.2 AVDD1
1.2 AVDD1
1.2 AVDD1 | AVDD4 salpalia las las.
AVDD4 salpalia saulata.
Sufa AVDD4 fullas saulata. | 1.5947X
1.5947X
1.000X
 | 2.716-03 2.716-03
 | LUC-U LUC-U 2.712-03 L
 | .02-0 1.02-0 1
.02-0 2.702-0 1
.02-0 2.702-0 1
.02-0 2.702-0 1
 | LUE-U 2,752-05 2,752-05
LUE-U 2,752-05 2,752-05
LUE-U 2,752-05 LUE-U | | | | | | | | 6.002-03
1.002-03
2.702-03
2.702-03
2.702-03
1.002-03
 | 1.002-00
2.702-05
2.702-05
2.702-05
1.002-00 | 11E-11 1.11E
275E-13 2.25E
275E-13 2.25E
11E-11 2.25E
11E-11 1.11E | C-01 D.00E-0
C-05 D.00E-0
C-05 D.00E-0
C-05 D.00E-0
C-05 D.00E-0
C-01 D.00E-0 |
| EANAS
 | 1. PIAS_TOP
1. PIAS_TOP
 | 1.2 AVDD1
1.2 AVDD1
1.2 AVDD1
1.2 AVDD1
1.2 VDD15T | AVDD1 salyal is las las.
AVDD1 salyal is unal Alto.
Sufe AVDD1 failare made
VDD15T fails Is unar ay uith aith any ly proved at AVIH pic. | 8.5947X
8.5947X
8.8088X
8.8688X
 | 2.71E-03 2.71E-03
2.71E-03 2.71E-03
0.01E-01 1.01E-01
1.01E-03 1.01E-01
 | LIDE-01 LIDE-01 LIDE-01 2.71E-03 I. LIDE-01 LIDE-01 2.71E-03 I. I. LIDE-01 LIDE-01 2.71E-03 I. I. LIDE-01 LIDE-01 LIDE-01 I. I. LIDE-01 LIDE-01 LIDE-01 I. I. LIDE-01 LIDE-01 LIDE-01 I.
 | .02:0 1.02:0 1
.02:0 2.712:45 1
.02:0 2.712:45 1
.02:0 2.712:45 1
.02:0 2.712:45 1
.02:0 1.02:40 1
.02:0 1.02:40 1
 | LITE-01 2.73E-83 2.73E-83
LITE-01 2.73E-83 2.73E-83
LITE-01 2.73E-83 LITE-01
LITE-01 LITE-01 LITE-01
LITE-01 LITE-01 LITE-01 | | | | | | | | 5.02-09
1.02-09
2.702-09
2.702-09
2.702-09
1.002-09
1.702-09
 | 1.012-01
2.712-05
2.712-05
2.712-05
2.712-05
1.012-01 | 11E-11 1.11E
27E-13 2.25E
27E-13 2.25E
11E-11 2.25E
11E-11 2.25E
11E-11 1.11E | C III I.102-0
C-15 I.102-0
C-15 I.102-0
C-15 I.102-0
C-15 I.102-0
C-10 I.102-0
C-10 I.102-0 |
|
 | 1. DIAS_TOP
1. DIAS_TOP
1. DIAS_TOP
 | 1.2 64004
1.2 64004
1.2 64004
1.3 64004
1.3 900457
1.3 900457 | AYDD4 salpatis ta ta tav.
AYDD4 salpatis asalaha.
Safa AYDD4 falar sada
VDD451 fala ta tava ng sila salit angkgarana al AYIN yin.
YDD451 ff fala ta tabata | 6.5947X
6.5947X
6.6001X
6.5640X
 | 2,712-03 2,712-03
2,712-03 2,712-03
1,112-01 1,112-01
1,712-03 1,112-01
1,712-03 1,112-01
 | LHE-H LHE-H 2-21E-H 1. LHE-H LHE-H LHE-H 1. CHE-H LHE-H LHE-H 1. CHE-H LHE-H LHE-H 1.
 | INE-II LINE-III I INE-II 2.71E-II 1 INE-II LINE-III 1 INE-II LINE-III 1 INE-II LINE-III 1
 | LUC41 2,752-03 2,732-03
LUC41 2,752-03 2,732-03
LUC41 2,752-03 LUC41
LUC41 2,752-03 LUC41
LUC41 LUC41 LUC41
LUC41 LUC41 LUC41 | | | | | | | | 1.02.03
1.02.03
2.702.03
2.702.03
2.702.03
1.02.03
1.702.03
 | L.HE-H
2.7HE-H
2.7HE-H
2.7HE-H
2.7HE-H
1.HE-H
1.HE-H | 02-0 L02
792-0 L732
792-0 L732
02-0 L732
02-0 L02
02-0 L02
02-0 L02 | CII LIUE-I
CIS LIUE-I
CIS LIUE-I
CIS LIUE-I
CIS LIUE-I
CII LIUE-I
CII LIUE-I
CII LIUE-I |
| ANA1
ANA11
 | 1. PIAS_TOP
1. PIAS_TOP
 | 1.2 AVDD1
1.2 AVDD1
1.2 AVDD1
1.2 AVDD1
1.2 VDD15T | AVDD1 salyal is las las.
AVDD1 salyal is unal Alto.
Sufe AVDD1 failare made
VDD15T fails Is unar ay uith aith any ly proved at AVIH pic. | 8.5947X
8.5947X
8.8088X
8.8688X
 | 2,716-03 2,716-03
2,716-03 2,716-03
1,016-01 1,016-01
4,716-03 1,016-01
4,716-03 1,016-01
1,016-01 1,016-01
 | LHE-HI LHE-HI 2718-HI 1. LHE-HI LHE-HI 2716-HI 1. LHE-HI LHE-HI 2716-HI 1. LHE-HI LHE-HI 2716-HI 1. LHE-HI LHE-HI LHE-HI 1. LHE-HI LHE-HI LHE-HI 1. 4716-HI LHE-HI LHE-HI LHE-HI LHE-HI LHE-HI LHE-HI LHE-HI
 | AIE-II L.UE-II 1 AIE-II 2.71E-II 1 AIE-II 1.01E-III 1 AIE-II 1.01E-III 1 AIE-II 1.01E-III 1
 | LUE-01 2.252-03 2.232-03
LUE-01 2.252-03 2.232-05
LUE-01 2.252-03 2.232-05
LUE-01 2.252-03 LUE-01
LUE-01 LUE-01 LUE-01
LUE-01 LUE-01 LUE-01
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LUE-01 LUE-01 LUE-01
LUE-01 LUE-01 LUE-01 | | | | | | | | 5.002-03
1.002-03
2.702-03
2.702-03
2.702-03
1.002-01
4.702-03
1.702-03
1.702-03
 | 1.012-01
2.712-05
2.712-05
2.712-05
2.712-05
1.012-01 | IIE-II I.IIE 73E-II 2.73E 73E-II 2.73E 73E-II 2.73E 71E-II 2.73E 71E-II 2.73E 71E-II 2.73E 71E-II 1.73E 71E-II 1.71E 71E-II 1.71E 71E-II 1.71E | -11 1.11E-1 -15 1.11E-1 -15 1.11E-1 -15 1.11E-1 -15 1.11E-1 -11 1.11E-1 |
| ANA11
ANA12
 | 1. DIAL TOP
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 | 1.2 97051
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4000 tupitis cutatio.
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 | 2,716-05 2,716-05
2,716-05 2,716-05
1,716-03 2,716-05
1,716-03 1,716-01
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5,756-04 1,716-01
 | LHE-H LHE-H 272(3) L LHE-H LHE-H 271(3) L LHE-H LHE-H 271(3) L LHE-H LHE-H 271(3) L LHE-H LHE-H LHE-H L S7244 LHE-H LHE-H LHE-H
 | IIE-II LIBE-II LIBE-II LIBE-II LIBE-II LIBE-II LIBE-II LIBE-II I IIE-II 2.71E-15 I
 | LHE-H 2.73E-H 2.74E-H LHE-H 2.73E-H 2.74E-H LHE-H 2.73E-H 2.74E-H LHE-H 2.73E-H 2.74E-H LHE-H 2.73E-H 1.14E-H LHE-H LHE-H LHE-H | | | | | | | | 6.002-03
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5.752-04
 | 1.11E-11 1
2.71E-15 2
2.71E-15 2
2.71E-15 1
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1.11E-11 1 | INE-IN 1.10E 73E-IN 2.75E 73E-IN 2.75E 10E-IN 2.75E 10E-IN 2.75E 10E-IN 2.75E 10E-IN 2.75E 10E-IN 1.10E 10E-IN 1.10E 10E-IN 1.10E 10E-IN 1.10E 10E-IN 1.10E 10E-IN 1.10E | E-11 L.112-1 C-15 L.112-1 C-15 L.112-1 C-15 L.112-1 C-15 L.112-1 C-11 L.112-1 |
| ANA11
ANA12
ANA13
 | 1.0445_TOP
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1.0445_TOP
1.0445_TOP
 | 1.3 AV001
1.3 AV001
1.3 AV001
1.3 V001
1.3 V001T
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1.4 V001T
1.4 V001T
1.4 V001T
1.4 V001T | 4000 tarjaší ta ta tao.
4000 tarjaší ta ta tao.
7 do 4000 tarjaší na kaldo.
9000 tří ča tao na veli koli kongelga novad al 4000 pisa.
9000 tří ča ta tao na veli kongelga novad al 4000 pisa.
9000 tří ča tao na veli kongelga na veli kongelga
9000 tarjaší na tao na veli kongelga na veli kongelga na veli kongelga
9000 tarjaší na kongelga na kongelga. | 6.5547X
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6.4224X
 | 2.716-03 2.716-03
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1.116-01 1.116-01
4.716-03 1.116-01
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 | LHE-11 LHE-11 LHE-11 LHE-11 VIE-13 LHE-11 LHE-11 LHE-11 VIE-13 LHE-11 LHE-11 LHE-11 LHE-11 LHE-11 LHE-11 LHE-11 LHE-11 LHE-11 LHE-11 LHE-11 S702-64 LHE-11 LHE-11 LHE-11 S702-64 LHE-11 LHE-11 LHE-11
 | IIE-III LIE-III T IIE-III 2.71E-IS 1 IIE-III 2.71E-IS 1 IIE-III 2.71E-IS 1 IIE-III 2.71E-IS 1 IIE-III LIE-III 1
 | LUC-U 2,255.0 2,215.0
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 | 1.012-01
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2.702-05
1.002-01
1.002-01
1.002-01
1.002-01
1.002-01
1.002-01
1.002-01 | 11E-11 1.11E 23E-13 2.75E 27E-13 2.75E 27E-13 2.75E 10E-11 2.75E 10E-11 1.11E | E-11 L.112-1 C-15 L.112-1 C-15 L.112-1 C-15 L.112-1 C-15 L.112-1 C-15 L.112-1 C-11 L.112-1 |
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 | 1. DIAL TOP
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NYPPT study in a class.
NYPPT study in a class.
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 | 2.718-03 2.718-03
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 | LHE-H LHE-H 272-33 L LHE-H LHE-H 272-33 L LHE-H LHE-H 2712-33 L LHE-H LHE-H 2712-33 L LHE-H LHE-H LHE-H 2712-33 LHE-H LHE-H LHE-H LHE-H V712-33 LHE-H LHE-H LHE-H LHE-H LHE-H LHE-H LHE-H LHE-H LHE-H LHE-H LHE-H S722-44 LHE-H LHE-H LHE-H LHE-H LHE-H LHE-H LHE-H S722-44 LHE-H LHE-H LHE-H S722-44 LHE-H LHE-H LHE-H S722-44 LHE-H LHE-H LHE-H
 | ITE-II LITE-II I IDE-II 2.71E-15 I IDE-II LIDE-II I IDE-II LIDE-III I IDE-II LIDE-II I IDE-II LIDE-II I
 | LHEAN 2,726,31 2,726,31
LHEAN 2,726,31 2,726,31
LHEAN 2,756,31 2,726,31
LHEAN 2,756,31 2,416,31
LHEAN 1,416,31 LHEAN
LHEAN 1,416,31 LHEAN 1,416,31 LHEAN
LHEAN 1,416,31 LHEAN 1,416,31 LHEAN
LHEAN 1,416,31 LHEAN 1,416,31 LHEAN
LHEAN 1,416,31 LHEAN 1,416,31 LHE | | | | | | | | 6.002-09
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1.002- | 1.11E-11
2.71E-15
2.71E-15
2.71E-15
1.11E-11
1.11E-11
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 | HE-H 1.11E 27E-H3 2.73E 27E-H3 2.73E 10E-H1 2.73E HE-H1 1.11E | E-10 L.012-0 E-15 L.012-0 E-15 L.012-0 E-15 L.012-0 E-10 L.012-0 |
| ANA11
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 | (
 | 1.1 ar0001
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Tab: Details-ISO26262



Functional Safety-Compliant FMEDA

TI FMEDA for TPS65313-Q1- Version: 2.2 - Date: 9-20-2018

TI Confidential - NDA Restrictions

	Die		Package	Overall
	Permanent	Transient	Permanent	Sum
Total FIT (Raw FIT)	1.87	0.88	23.51	26.26
Safety related FIT	1.20	0.88	16.46	18.54
Probabilistic Metrics for random Hardware Failures - PMHF (in FIT)	0.05	0.19	0.08	0.32
Single Point Fault Metric - SPFM	96.01%	78.37%	99.52%	98.29%
Latent Fault Metric - LFM	75.54%	NA	99. 52 %	97.94%

Metric	ASIL A	ASIL B	ASIL C	ASIL D
PMHF	-	< 100 FIT	< 100 FIT	< 10 FIT
SPFM	-	>= 90%	>= 97%	>= 99%
LFM	-	>= 60%	>= 80%	>= 90%

ISO 26262 categorization as in ISO 26262:2011-10, 8.1.8

		Die		Package	Overall
		Permanent	Transient	Permanent	Sum
Total faults	λ	1.87	0.88	23.51	26.26
Total Safety Related faults	λ_{SR}	1.20	0.88	16.46	18.54
Total Not Safety Related faults	λ_{nSR}	0.67	0.00	7.05	7.73
Total Safe faults	λ _s	0.06	0.19	8.23	8.48
Total not Safe faults	λ _{nS}	1.14	0.69	8.23	10.06
Total faults with prob. of violate the SG	λ_{PVSG}	0.93	0.19	8.23	9.35
Total single point faults	λ _{SPF}	0.03	0.19	0.00	0.22
Total residual faults	λ_{RF}	0.01	0.00	0.08	0.09
Total Multi Point ^(ad) [non-PVSG]	$\lambda_{MPF}^{(ad)}$	0.21	0.50	0.00	0.71
Total Multi Point ^(t) [PVSG]	$\lambda_{MPF}^{(t)}$	0.88	0.00	8.15	9.03
Total Multi Point detected faults	$\lambda_{MPF_{det}}$	0.81	0.43	8.07	9.31
Total Multi Point latent faults	$\lambda_{\text{MPF,I}}$	0.28	NA	0.08	0.36

PMHF=	$LSPF + \lambda RF + \lambda MPF(t) \times \lambda MPF(sm_latent) \times Total Hr$
	10^9
SPFM =	$\lambda S + \lambda MPF (ad) + \lambda MPF (t)$

λS + λnS

LFM = ΔS + λMPF (det)

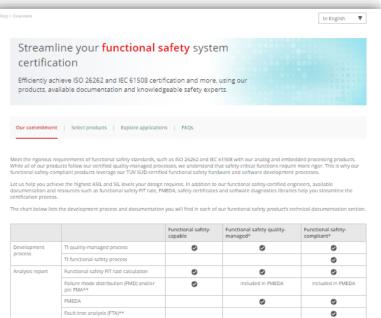
.S + λnS - λRF - λSPF

Tab: Totals-ISO26262



To learn more about functional safety at TI ...

Go to <u>www.ti.com/functionalsafety</u>



* Functional safety-compliant products include SafeTI-26262 or SafeTI-61508 products, and functional safety quality-managed products include SafeTI-60730 or SafeTI-QM products.

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** May only be available for analog power and signal chain products.

Functional safety manual

Functional safety product certificate***

*** Available for select products.

Diagnostics

description



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