EVM User's Guide: TPSM861252, TPSM861253, TPSM861257

TPSM86125xEVM Step-Down Converter Evaluation Module

Description

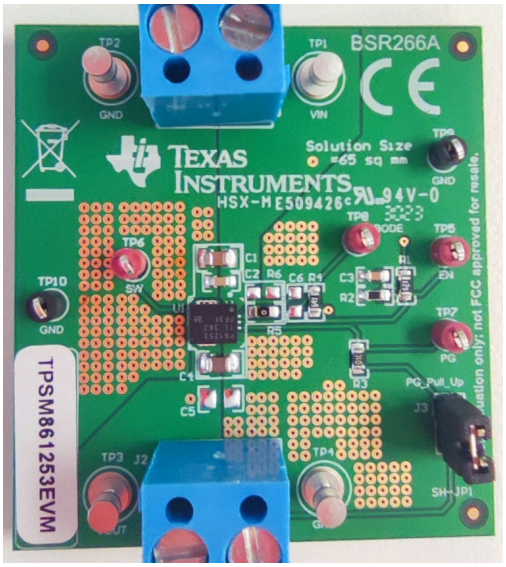
The TPSM86125x is a single, D-CAP3™ control mode, synchronous buck converter module with input voltage ranging from 3 V to 17 V and supports up to 1 A continuous current. The TPSM861252 operates in Eco-mode and the TPSM861253 and TPSM861257 operates in FCCM mode. The TPSM861253 has a fixed 3.3V Vout, and the TPSM861252/7 has an adjustable output voltage. The TPSM86125xEVM is a fully assembled and tested circuit for evaluating the TPSM86125x converter module.

Features

- 3-V to 17-V input voltage range
- Adjustable/Fixed 3.3-V output voltage
- 1-A continuous output current
- Eco/FCCM mode at light load
- Fast transient D-CAP3™ control mode

Applications

- Merchant network and server PSU
- AC/DC adapters/PSU
- Factory automation and control
- Test and measurement



TPSM861253EVM Board (Top View)

1 Evaluation Module Overview

1.1 Introduction

The TPSM86125x is a single, adaptive on-time, D-CAP3 control mode, synchronous buck converter module that requires a very low external component count. The D-CAP3 control circuit is optimized for low-ESR output capacitors such as POSCAP, SP-CAP, or ceramic types and features fast transient response with no external compensation. The switching frequency is internally set at a nominal 1.4MHz. The high-side and low-side switching MOSFETs are incorporated inside the TPSM86125x package along with the gate-drive circuitry. The low drain-to-source on resistance of the MOSFETs and fast switching slew rate allow the TPSM86125x to achieve high efficiency and help keep the junction temperature low at high output currents. Power sequencing is possible by correctly configuring the enable and power-good indicator. Rated input voltage and output current ranges for the evaluation module are given in Table 1-1.

Table 1-1. Input Voltage, output voltage and output Current Summary

EVM	Input Voltage (V_{IN}) Range	Output Voltage (V_{out}) Range	Output Current (I_{OUT}) Range
TPSM861252EVM	3V to 17V	0.6V to 10V	0A to 1A
TPSM861253EVM	3.8V to 17V	Fixed 3.3V	0A to 1A
TPSM861257EVM	3V to 17V	0.6V to 5.5V	0A to 1A

This user's guide mainly introduces the TPSM861253 features as well as support documentation for the TPSM861253EVM. This document includes the following information:

- Performance specifications
- Board layout
- Schematic
- Bill of materials

1.2 Kit Contents

- One TPSM86125xEVM board
- EVM disclaimer Read Me

1.3 Specification

A summary of the TPSM861253EVM performance specifications is provided in Table 1-2. Specifications are given for an input voltage of V_{IN} = 12 V and an output voltage of 3.3 V and the ambient temperature is 25°C for all measurement, unless otherwise noted.

Table 1-2. Performance Specifications Summary

Specifications	Test Conditions	MIN	TYP	MAX	Unit
Input voltage range		3.8	12	17	V
Output voltage set point			3.3		V
Operating frequency	V_{IN} = 12 V, I_O = 1 A		1.4		MHz
Output current range		0		1	A
Over current limit	V_{IN} = 12 V		2.1		A
Output ripple voltage	V_{IN} = 12 V, I_O = 1 A		13		mV_{PP}

1.4 Device Information

Table 1-3. Input Voltage and Output Current Summary

EVM	Input Voltage (V_{IN}) Range	Output Current (I_{OUT}) Range
TPSM861253EVM	V_{IN} = 3.8 V to 17 V	0 A to 1 A

2 Hardware

2.1 Input and Output Connections

The TPSM861253EVM is provided with input and output connectors and test points as shown in Table 2-1. Figure 2-1 shows connectors and jumpers placement on the TPSM861253EVM board.

A power supply capable of supplying 1 A must be connected to J1 through a pair of 20-AWG wires. The load must be connected to J2 through a pair of 20-AWG wires. The maximum load current capability is 1 A. Wire lengths are minimized to reduce losses in the wires. The TP1 provides a place to monitor the V_{IN} input voltages with TP2 providing a convenient ground reference. TP3 is used to monitor the output voltage with TP4 as the ground reference.

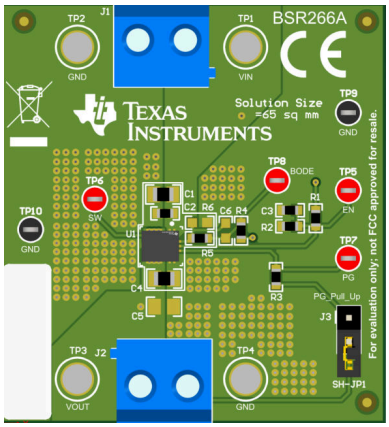


Figure 2-1. TPSM861253EVM Connectors and Jumpers Placement

Table 2-1. Connection and Test Points

Reference Designator	Function
J1	V_{IN} (see Table 1-1 for V_{IN} range)
J2	V_{OUT}, 3.3 V at 1 A maximum
J3	Source selection for PGOOD, Short pin 1 and pin 2, PG is pull high to Vout
TP1	V_{IN} positive power point
TP2, TP4	GND power point
TP3	V_{OUT} positive power point
TP5	EN test point
TP6	Switch node test point
TP7	PGOOD test point
TP8	Test point for loop response measurements
TP9, TP10	GND monitor point

2.2 Output Voltage Setpoint

The output voltage of the TPSM861252/7EVM can be selected by changing the value of resistor R_5 (R_{FBT}) and R_6 (R_{FBB}). TI recommends using 1% tolerance or better divider resistors. Start with a 10kΩ or 30kΩ for R_6 (R_{FBB}) and use Equation 1 to calculate R_5 (R_{FBT}). To improve efficiency at light loads, consider using larger value resistors. If the values are too high, the regulator is more susceptible to noise and voltage errors from the FB input current are noticeable. The TPSM861253EVM is a fixed 3.3V output and does not need to select the divider resistors.

$$R_5 = \frac{R_6 \times (V_{out} - 0.6V)}{0.6V} \quad (1)$$

2.3 Start-Up Procedure

1. Apply appropriate input voltage to VIN (J1-1) and GND (J1-2).
2. Apply the loading to VOUT (J2-1) and GND (J2-2).

3 Implementation Results

3.1 Test Setup and Results

This section describes how to properly connect, set up, and use the TPSM861253EVM. The section also includes test results typical for the evaluation modules and the following:

- Efficiency
- Output load regulation
- Output line regulation
- Load transient response
- Start-up
- Shutdown
- Output voltage ripple

3.1.1 Efficiency

Figure 3-1 shows the efficiency for the TPSM861253EVM at an ambient temperature of 25°C.

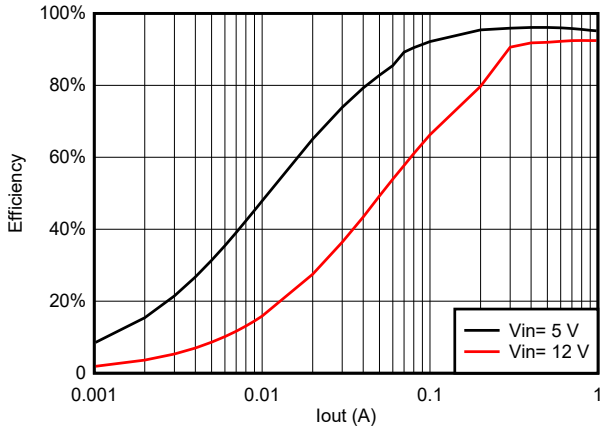


Figure 3-1. TPSM861253EVM Efficiency

3.1.2 Load Regulation

Figure 3-2 shows the load regulation for the TPSM861253EVM.

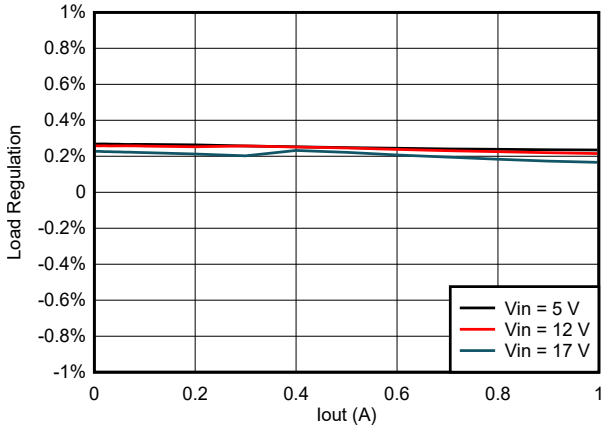


Figure 3-2. TPSM861253EVM Load Regulation

3.1.3 Line Regulation

Figure 3-3 shows the line regulation for the TPSM861253EVM.

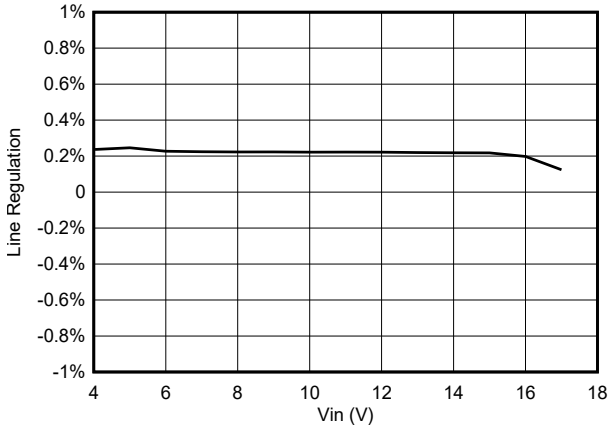


Figure 3-3. TPSM861253EVM Line Regulation

3.1.4 Load Transient Response

Figure 3-4 shows the TPSM861253EVM response to load transient. The current steps slew rate is set as 0.8 A/µs.

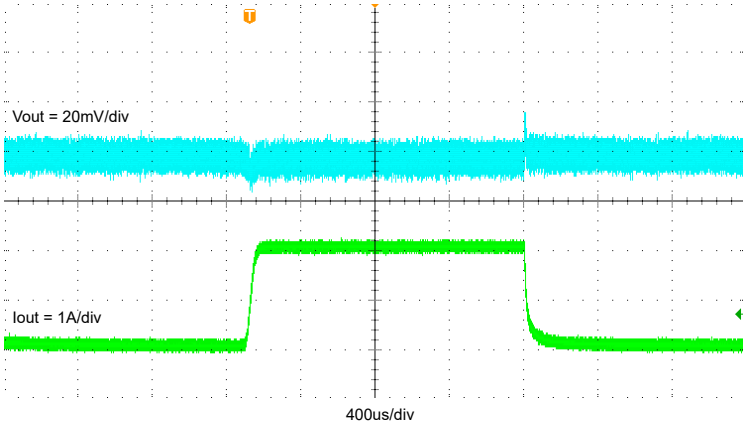


Figure 3-4. TPSM861253EVM Load Transient Response, 10% to 90% (0.3-A to 2.7-A) Load Step

3.1.5 Start-Up

Figure 3-5 shows the TPSM861253EVM start-up waveform relative to V_{IN}.

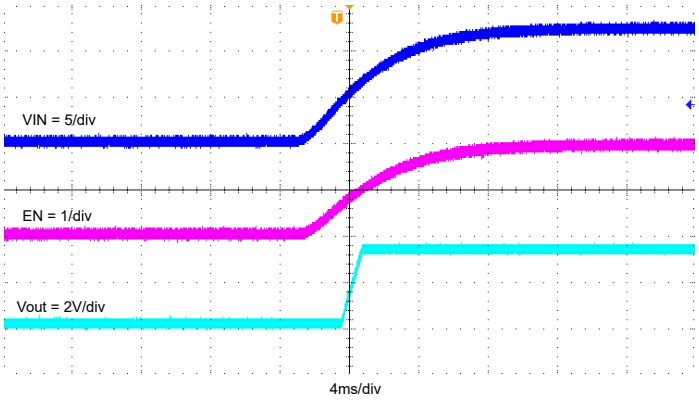


Figure 3-5. TPSM861253EVM Start-Up Relative to V_{IN}, I_{OUT} = 1 A

Figure 3-6 shows the TPSM861253EVM start-up waveform relative to enable (EN).

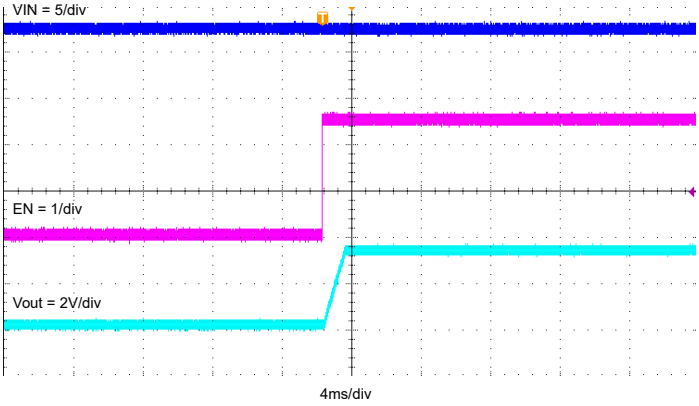


Figure 3-6. TPSM861253EVM Start-Up Relative to EN, I_{OUT} = 1 A

3.1.6 Shutdown

Figure 3-7 shows the TPSM861253EVM shutdown waveform relative to V_{IN}.

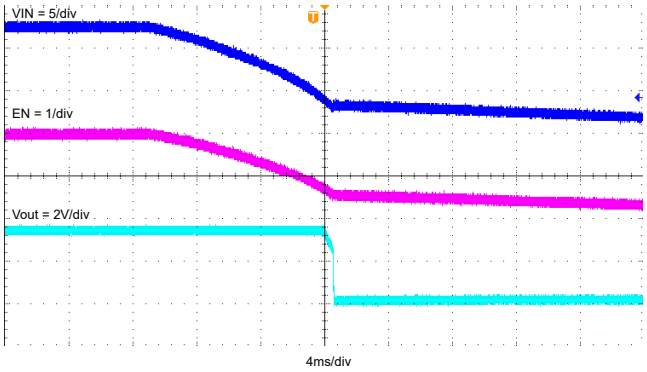


Figure 3-7. TPSM861253EVM Shutdown Relative to V_{IN}, I_{OUT} = 1 A

Figure 3-8 shows the TPSM861253EVM shutdown waveform relative to EN.

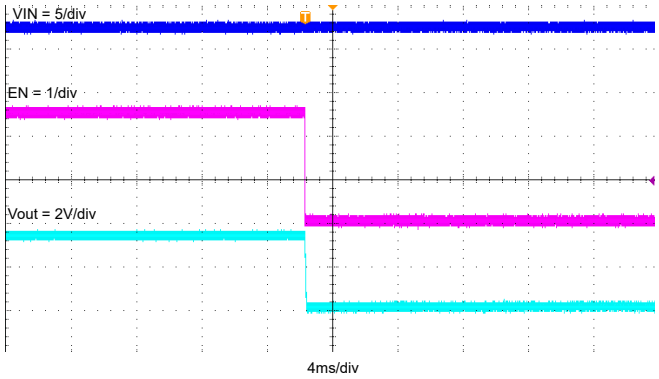


Figure 3-8. TPSM861253EVM Shutdown Relative to EN, I_{OUT} = 1 A

3.1.7 Output Voltage Ripple

Figure 3-9 and Figure 3-10 show the TPSM861253EVM output voltage ripple.

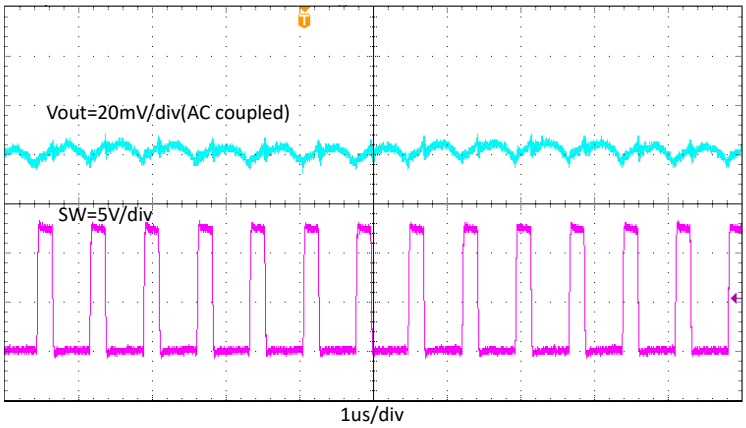


Figure 3-9. TPSM861253EVM Output Voltage Ripple, I_{OUT} = 1 A

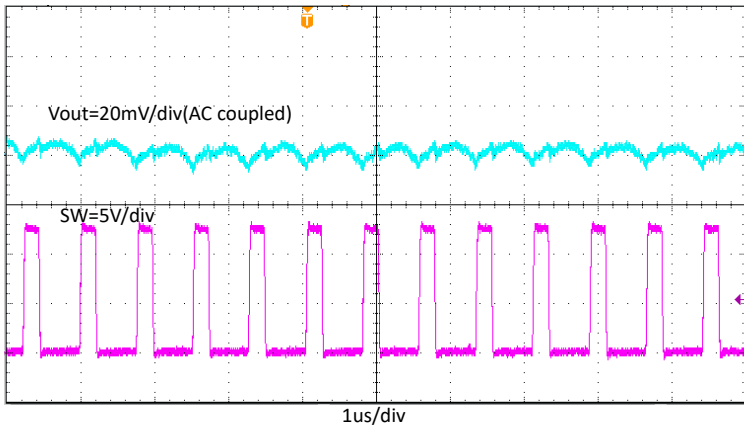


Figure 3-10. TPSM861253EVM Output Voltage Ripple, I_{OUT} = 0.01 A

4 Hardware Design Files

4.1 Schematic

Figure 4-1 is the schematic for the TPSM861253EVM.

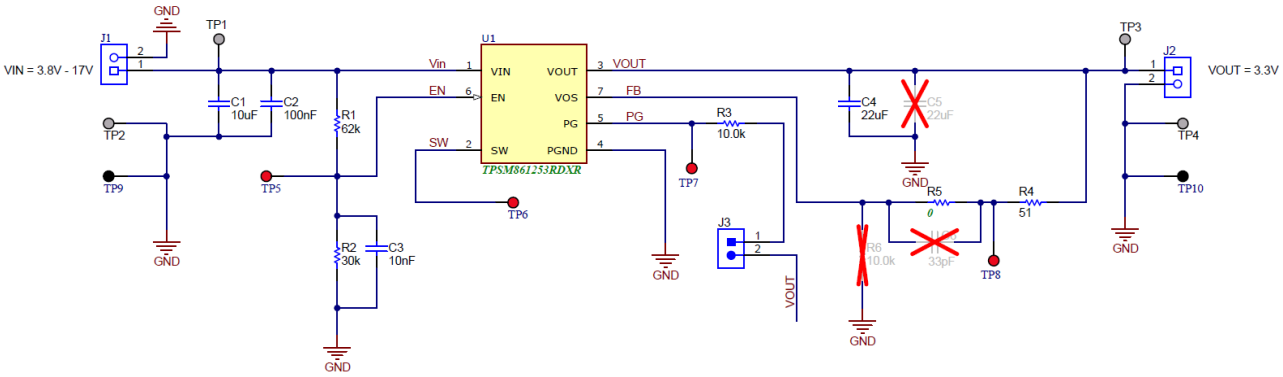


Figure 4-1. TPSM861253EVM Schematic Diagram

4.2 PCB Layout

This section provides a description of the TPSM86125xEVM, board layout, and layer illustrations.

Figure 4-2, Figure 4-3, Figure 4-4, Figure 4-5, and Figure 4-6 show the board layout for the TPSM861253EVM. The top layer contains the main power traces for VIN, VOUT, and ground. Connections for the pins of the TPSM861253 and a large area filled with ground are also on the top layer. Most of the signal traces are also located on the top side. The input decoupling capacitors C1 and C2 are located as close to the IC as possible. The input and output connectors, test points, and all of the components are located on the top side. The bottom layer is a ground plane along with the signal ground copper fill and the feedback trace from the point of regulation to the top of the resistor divider network. Two internal layers are both set to ground plane. Both the top layer and bottom layer use 2-oz copper thickness and two internal layers use 1-oz copper thickness.

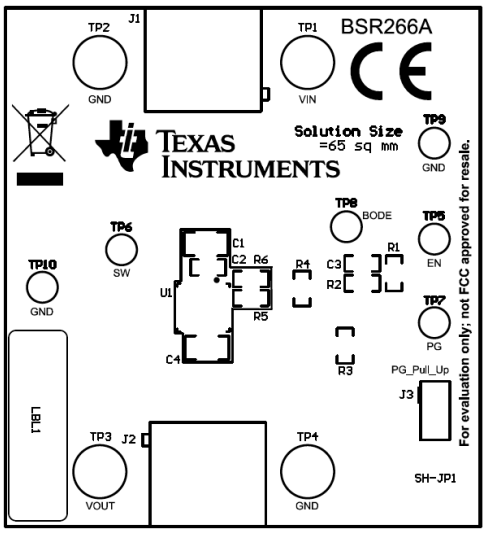


Figure 4-2. TPSM861253EVM Top Assembly

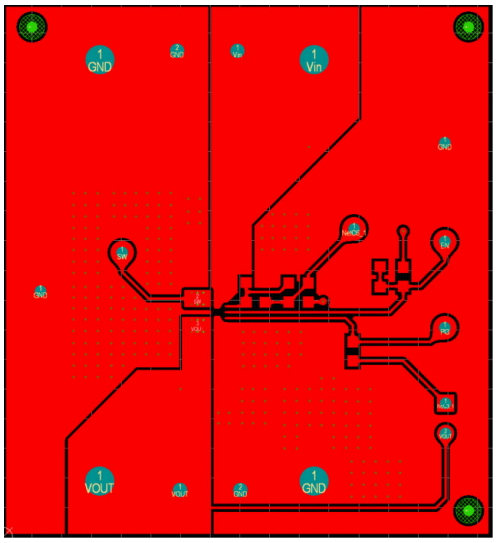


Figure 4-3. TPSM861253EVM Top Layer

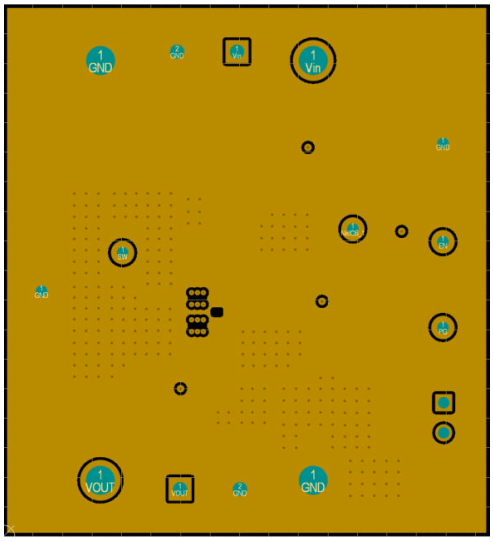


Figure 4-4. TPSM861253EVM Inner1 Layer

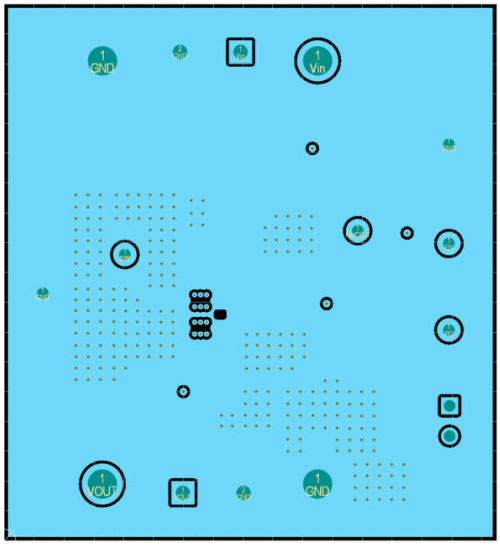


Figure 4-5. TPSM861253EVM Inner2 Layer

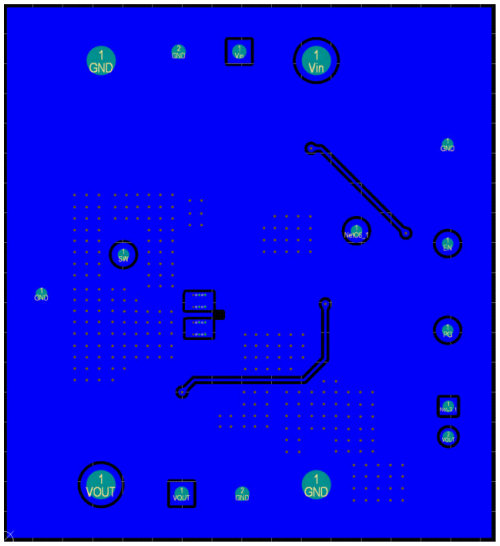


Figure 4-6. TPSM861253EVM Bottom Layer

4.3 Bill of Materials

Table 4-1. Bill of Materials

Designator	Qty	Description	Part Number	Manufacturer
C1	1	Capacitor, ceramic, 10 µF, 25 V, ±20%, X5R, 0805	GRM21BR61E106MA73L	MuRata
C2	1	Capacitor, ceramic, 0.1 µF, 50 V, ±10%, X7R, 0603	C0603C104K5RACTU	Kemet
C3	1	Capacitor, ceramic, 0.01 µF, 50 V, ±10%, X7R, 0603	C0603X103K5RACTU	Kemet
C4	1	Capacitor, ceramic, 22 µF, 16 V, ±20%, X6S, 0805	GRM21BC81C226ME44L	MuRata
J1, J2	2	Terminal block, 5.08 mm, 2 × 1, Brass, TH	ED120/2DS	On-Shore Technology
J3	1	Header, 100 mil, 2 × 1, Tin, TH	PEC02SAAN	Sullins Connector Solutions
LBL1	1	Thermal transfer printable labels, 0.650" W × 0.200" H – 10,000 per roll	THT-14-423-10	Brady
R1	1	Resistor, 62 kΩ, 5%, 0.1 W, AEC-Q200 Grade 0, 0603	CRCW060362K0JNEA	Vishay-Dale
R2	1	Resistor, 30 kΩ, 5%, 0.1 W, AEC-Q200 Grade 0, 0603	CRCW060330K0JNEA	Vishay-Dale
R3	2	Resistor, 10.0 kΩ, 1%, 0.1 W, AEC-Q200 Grade 0, 0603	CRCW060310K0FKEA	Vishay-Dale
R4	1	Resistor, 51 Ω, 5%, 0.1 W, AEC-Q200 Grade 0, 0603	CRCW060351R0JNEA	Vishay-Dale
R5	1	Resistor, 0, 5%, 0.1 W, 0603	RC0603JR-070RL	Yageo
SH-JP1	1	Shunt, 100 mil, gold plated, black	SNT-100-BK-G	Samtec
TP1, TP2, TP3, TP4	4	Terminal, turret, TH, double	1502-2	Keystone
TP5, TP6, TP7, TP8	4	Test point, miniature, red, TH	5000	Keystone
TP9, TP10	2	Test point, miniature, black, TH	5001	Keystone
U1	1	3.8-V to 17-V Input, 1-A Synchronous Buck Module in QFN Package	TPSM861253RDXR	Texas Instruments

5 Additional Information

Trademarks

D-CAP3™ is a trademark of Texas Instruments.
All trademarks are the property of their respective owners.

6 Related Documentation

1. Texas Instruments, *TPSM86125x 3V to 17V Input, 1A Synchronous Buck Module in QFN Package* data sheet

7 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Revision * (September 2023) to Revision A (February 2024)	Page
• Added application descriptions for TPSM861252 and TPSM861257	2
• Added *Output Voltage Setpoint* section	4

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   1.2 EVMs are not intended for consumer or household use. EVMs may not be sold, sublicensed, leased, rented, loaned, assigned, or otherwise distributed for commercial purposes by Users, in whole or in part, or used in any finished product or production system.

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   2.1 These terms do not apply to Software. The warranty, if any, for Software is covered in the applicable Software License Agreement.

   2.2 TI warrants that the TI EVM will conform to TI's published specifications for ninety (90) days after the date TI delivers such EVM to User. Notwithstanding the foregoing, TI shall not be liable for a nonconforming EVM if (a) the nonconformity was caused by neglect, misuse or mistreatment by an entity other than TI, including improper installation or testing, or for any EVMs that have been altered or modified in any way by an entity other than TI, (b) the nonconformity resulted from User's design, specifications or instructions for such EVMs or improper system design, or (c) User has not paid on time. Testing and other quality control techniques are used to the extent TI deems necessary. TI does not test all parameters of each EVM. User's claims against TI under this Section 2 are void if User fails to notify TI of any apparent defects in the EVMs within ten (10) business days after delivery, or of any hidden defects with ten (10) business days after the defect has been detected.

   2.3 TI's sole liability shall be at its option to repair or replace EVMs that fail to conform to the warranty set forth above, or credit User's account for such EVM. TI's liability under this warranty shall be limited to EVMs that are returned during the warranty period to the address designated by TI and that are determined by TI not to conform to such warranty. If TI elects to repair or replace such EVM, TI shall have a reasonable time to repair such EVM or provide replacements. Repaired EVMs shall be warranted for the remainder of the original warranty period. Replaced EVMs shall be warranted for a new full ninety (90) day warranty period.

WARNING

Evaluation Kits are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems.

User shall operate the Evaluation Kit within TI’s recommended guidelines and any applicable legal or environmental requirements as well as reasonable and customary safeguards. Failure to set up and/or operate the Evaluation Kit within TI’s recommended guidelines may result in personal injury or death or property damage. Proper set up entails following TI’s instructions for electrical ratings of interface circuits such as input, output and electrical loads.

NOTE:

EXPOSURE TO ELECTROSTATIC DISCHARGE (ESD) MAY CAUSE DEGREDATION OR FAILURE OF THE EVALUATION KIT; TI RECOMMENDS STORAGE OF THE EVALUATION KIT IN A PROTECTIVE ESD BAG.

3 *Regulatory Notices:*

3.1 *United States*

3.1.1 *Notice applicable to EVMs not FCC-Approved:*

FCC NOTICE: This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

3.1.2 *For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:*

CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- *Reorient or relocate the receiving antenna.*
- *Increase the separation between the equipment and receiver.*
- *Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.*
- *Consult the dealer or an experienced radio/TV technician for help.*

3.2 *Canada*

3.2.1 *For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247*

Concerning EVMs Including Radio Transmitters:

This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:

(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concerning EVMs Including Detachable Antennas:

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur

3.3 *Japan*

3.3.1 *Notice for EVMs delivered in Japan:* Please see http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_01.page 日本国内に輸入される評価用キット、ボードについては、次のところをご覧ください。

https://www.ti.com/ja-jp/legal/notice-for-evaluation-kits-delivered-in-japan.html

3.3.2 *Notice for Users of EVMs Considered "Radio Frequency Products" in Japan:* EVMs entering Japan may not be certified by TI as conforming to Technical Regulations of Radio Law of Japan.

If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required to follow the instructions set forth by Radio Law of Japan, which includes, but is not limited to, the instructions below with respect to EVMs (which for the avoidance of doubt are stated strictly for convenience and should be verified by User):

1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

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1. 電波法施行規則第6条第1項第1号に基づく平成18年3月28日総務省告示第173号で定められた電波暗室等の試験設備でご使用いただく。
2. 実験局の免許を取得後ご使用いただく。
3. 技術基準適合証明を取得後ご使用いただく。

なお、本製品は、上記の「ご使用にあたっての注意」を譲渡先、移転先に通知しない限り、譲渡、移転できないものとします。

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東京都新宿区西新宿 6 丁目 2 4 番 1 号

西新宿三井ビル

3.3.3 *Notice for EVMs for Power Line Communication:* Please see http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_02.page

電力線搬送波通信についての開発キットをお使いになる際の注意事項については、次のところをご覧ください。https://www.ti.com/ja-jp/legal/notice-for-evaluation-kits-for-power-line-communication.html

3.4 *European Union*

3.4.1 *For EVMs subject to EU Directive 2014/30/EU (Electromagnetic Compatibility Directive)*:

This is a class A product intended for use in environments other than domestic environments that are connected to a low-voltage power-supply network that supplies buildings used for domestic purposes. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

4 *EVM Use Restrictions and Warnings:*

4.1 EVMS ARE NOT FOR USE IN FUNCTIONAL SAFETY AND/OR SAFETY CRITICAL EVALUATIONS, INCLUDING BUT NOT LIMITED TO EVALUATIONS OF LIFE SUPPORT APPLICATIONS.

4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.

4.3 *Safety-Related Warnings and Restrictions:*

4.3.1 User shall operate the EVM within TI's recommended specifications and environmental considerations stated in the user guide, other available documentation provided by TI, and any other applicable requirements and employ reasonable and customary safeguards. Exceeding the specified performance ratings and specifications (including but not limited to input and output voltage, current, power, and environmental ranges) for the EVM may cause personal injury or death, or property damage. If there are questions concerning performance ratings and specifications, User should contact a TI field representative prior to connecting interface electronics including input power and intended loads. Any loads applied outside of the specified output range may also result in unintended and/or inaccurate operation and/or possible permanent damage to the EVM and/or interface electronics. Please consult the EVM user guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative. During normal operation, even with the inputs and outputs kept within the specified allowable ranges, some circuit components may have elevated case temperatures. These components include but are not limited to linear regulators, switching transistors, pass transistors, current sense resistors, and heat sinks, which can be identified using the information in the associated documentation. When working with the EVM, please be aware that the EVM may become very warm.

4.3.2 EVMs are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems. User assumes all responsibility and liability for proper and safe handling and use of the EVM by User or its employees, affiliates, contractors or designees. User assumes all responsibility and liability to ensure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard. User assumes all responsibility and liability for any improper or unsafe handling or use of the EVM by User or its employees, affiliates, contractors or designees.

4.4 User assumes all responsibility and liability to determine whether the EVM is subject to any applicable international, federal, state, or local laws and regulations related to User's handling and use of the EVM and, if applicable, User assumes all responsibility and liability for compliance in all respects with such laws and regulations. User assumes all responsibility and liability for proper disposal and recycling of the EVM consistent with all applicable international, federal, state, and local requirements.

5. *Accuracy of Information:* To the extent TI provides information on the availability and function of EVMs, TI attempts to be as accurate as possible. However, TI does not warrant the accuracy of EVM descriptions, EVM availability or other information on its websites as accurate, complete, reliable, current, or error-free.

6. *Disclaimers:*

6.1 EXCEPT AS SET FORTH ABOVE, EVMS AND ANY MATERIALS PROVIDED WITH THE EVM (INCLUDING, BUT NOT LIMITED TO, REFERENCE DESIGNS AND THE DESIGN OF THE EVM ITSELF) ARE PROVIDED "AS IS" AND "WITH ALL FAULTS." TI DISCLAIMS ALL OTHER WARRANTIES, EXPRESS OR IMPLIED, REGARDING SUCH ITEMS, INCLUDING BUT NOT LIMITED TO ANY EPIDEMIC FAILURE WARRANTY OR IMPLIED WARRANTIES OF MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF ANY THIRD PARTY PATENTS, COPYRIGHTS, TRADE SECRETS OR OTHER INTELLECTUAL PROPERTY RIGHTS.

6.2 EXCEPT FOR THE LIMITED RIGHT TO USE THE EVM SET FORTH HEREIN, NOTHING IN THESE TERMS SHALL BE CONSTRUED AS GRANTING OR CONFERRING ANY RIGHTS BY LICENSE, PATENT, OR ANY OTHER INDUSTRIAL OR INTELLECTUAL PROPERTY RIGHT OF TI, ITS SUPPLIERS/LICENSORS OR ANY OTHER THIRD PARTY, TO USE THE EVM IN ANY FINISHED END-USER OR READY-TO-USE FINAL PRODUCT, OR FOR ANY INVENTION, DISCOVERY OR IMPROVEMENT, REGARDLESS OF WHEN MADE, CONCEIVED OR ACQUIRED.

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