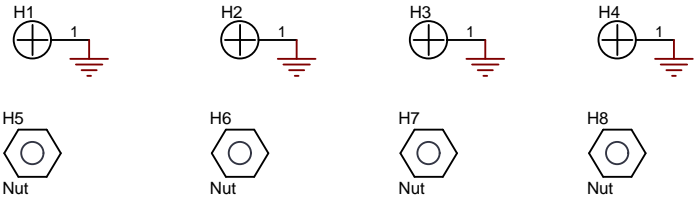


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Orderable: WIRED-OR-MUXEVM	Designed for: Public Release	Mod. Date: 7/20/2018
TID #: TIDA-01565	Project Title: Op-Amp Wired OR Mux using Disable Pin	
Number: TIDA-01565	Rev: E2	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 1 of 2
Drawn By:	File: TIDA-01565E2.SchDoc	Size: B
Engineer: Sean Cashin/Robert Clifton	Contact: http://www.ti.com/support	© Texas Instruments 2018





Shunt for J2



SH-J1

Shunt for J3



SH-J2

Shunt for J4



SH-J3

Shunt for J7



SH-J4

Shunt for J11



SH-J5

Shunt for J10



SH-J8

Shunt for J10



SH-J9

Shunt for J10



SH-J10

Shunt for J10



SH-J11

Shunt for J15



SH-J6

Shunt for J16



SH-J7



PCB Number: TIDA-01565

PCB Rev: E2

PCB

LOGO

Texas Instruments



PCB

LOGO

FCC disclaimer

PCB

LOGO

WEEE logo

LBL1

PCB Label

Size: 0.65" x 0.20 "

ZZ1

Label Assembly Note

This Assembly Note is for PCB labels only

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Variant/Label Table	
Variant	Label Text
001	WIRED-OR-MUXEVM