# TMUX2889YBH Evaluation Module

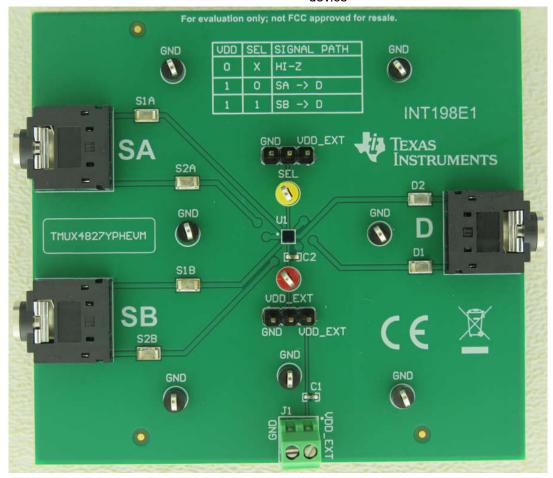


## **Description**

The TMUX2889YBHEVM is used to evaluate the performance of the TMUX2889. The evaluation module (EVM) comes with a TMUX2889YBH device soldered on. The EVM allows for an easy way for engineers to evaluate the TMUX2889 with audio signals by using on board audio jacks. Additionally, test points on board are provided to allow for the capability to test for signals that aren't audio signals as well.

### **Features**

- · TMUX2889YBH pre-soldered on board
- 2 power supply decoupling capacitor from VDD to Ground (1μF 0402; 0.1μF 0402)
- 6 test points on I/O's supporting TMUX2889 full current capabilities
- 7 additional GND test points for easy of probing
- 1 3-pin header for connecting/disconnecting device from external power
- 1 3-pin header to change signal path state of device



TMUX2889YBHEVM (Top View)

### 1 Evaluation Module Overview

### 1.1 Introduction

This user's guide describes the TMUX2889YBHEVM evaluation module (EVM) and the intended use. This board allows for the quick prototyping and characterization of Tl's TMUX2889 multiplexers in an 9-Pin YBH package. This EVM allows for evaluation of the signal path along with the power sequencing flexibility of the TMUX2889.

### 1.2 Kit Contents

The EVM kit includes the following:

TMUX2889YBHEVM board

## 1.3 Specification

The TMUX2889YBHEVM is used for quick prototyping of the TMUX2889 in the YBH package. The EVM has two 3-pin headers. One header for toggling the SEL pin to switch control the signal path routing of the device. The other 3-pin header allows for the VDD supply to be connected to an external source, to the board ground or left floating.

The EVM has test points on each I/O for a total of six total test points that are rated up to 2A to support testing the TMUX2889 at the fullest current carrying capabilities. Seven extra ground test points are provided to allow for easy testing of the board.

## 1.4 Device Information

The TMUX2889 is a complementary metal-oxide semiconductor (CMOS) multiplexer with two independently selectable 2:1, single-pole, doublethrow (SPDT) switch channels. This device works with a single supply (1.8V to 5.5V), but can pass bidirectional analog and digital signals beyond the supply from -5.5V to 5.5V.

The TMUX2889 also features powered off protection up to  $\pm 5.5$ V, which isolates the Dx from a voltage on the Sx even when there is no supply voltage present (VDD = 0V). Without this protection feature, any voltage on the switch can back-power the supply rail through an internal ESD diode and cause potential damage to the rest of the system.

With 0.001% THD+N and  $1m\Omega$  RON-flatness, the TMUX2889 is an excellent choice for passing precision analog and audio signals without adding distortion.

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### 2 Hardware

### 2.1 Power Requirements

TMUX2889YPHEVM requires a 1.8V to 5.5V supply provided either through the J1 terminal, or directly hooked to the red VDD\_EXT test point to provide a passive signal pathway between the Sx and Dx pins in according to the logic selected.

## 2.2 Header and Jumper Information

The TMUX2889YBHEVM has two 3-pin headers to control the power supply connection, location C2 of the TMUX2889, and the control inputs, location A2.

### 1. Supply Header J2

Header J2 connects the VDD pin to either the external power or to ground via a jumper. If Header J2 is not connected, then the devices supply is left floating. Figure 2-1 shows header J2.

- a. To connect to the external supply, short the J2-2 location on the header to J2-3. The VDD\_EXT terminal is now supplying the device power.
- b. To connect to ground, short the J2-2 location on the header to J2-1. The device supply pin is now grounded.
- c. To leave the device supply pin floating, leave J2-2 unconnected and floating.

### 2. Control Header J7

Header J7 connects the SEL pin to either the external power or ground via a jumper. If Header J7 is not connected, then the devices SEL pin are left floating.

- a. To connect to the external supply, short the J7-2 location on the header to J7-1. The VDD\_EXT terminal is now connected to the devices SEL pin.
- b. To connect to ground, GND, short the J7-2 location on the header to J7-3. The device SEL pin is now grounded.
- c. Leaving J7-2 unconnected leaves the SEL pin floating. Doing this is not recommended as the device is in an unknown state.



Figure 2-1. Header J2: J2-1(GND), J2-2(Connection to device VDD), J2-3 (VDD\_EXT)

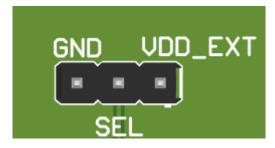


Figure 2-2. Header J7: J7-1(VDD EXT), J7-2(Connection to device SEL), J7-3 (GND)

The logic of headers J2(VDD) and J7(SEL) are shown at the top side of the TMUX2889YPHEVM.



Hardware W

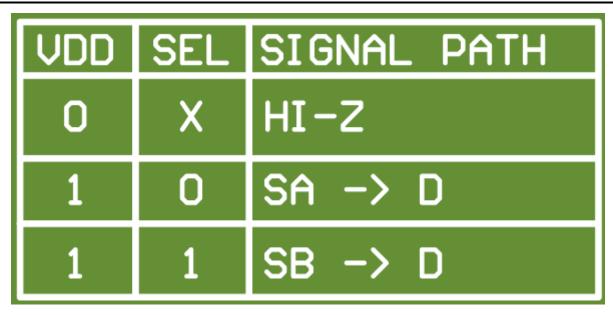


Figure 2-3. TMUX2889YBHEVM Truth Table

## 2.3 Test Points

The board has a total of 15 test points. 7 GND, 1 SEL, 1 VDD, and 6 I/O.

Test Point ID	Description	Signal
S1A	Surface mount	S1A
S1B	Surface mount	S1B
S2A	Surface mount	S2A
S2B	Surface mount	S2B
D1	Surface mount	D1
D2	Surface mount	D2
VDD_EXT	Red	VDD
SEL	Yellow	SEL
GND	Black	GND

www.ti.com Hardware Design Files

# 3 Hardware Design Files

The following section includes hardware design files for TMUX2889YBHEVM. This section includes the board level schematic, PCB layout and Bill of materials (BOM).

## 3.1 Schematics

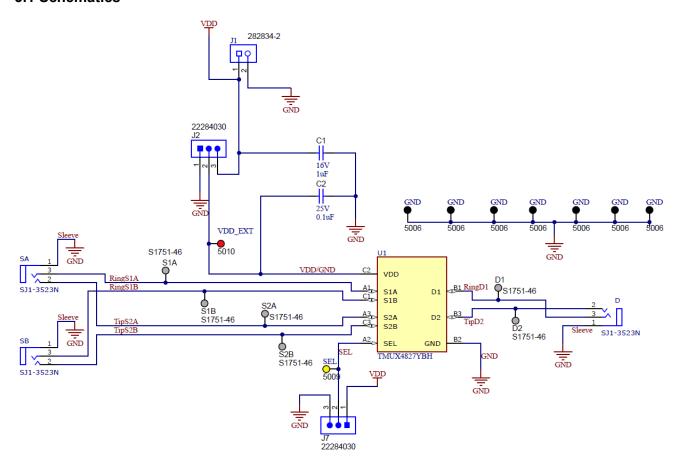


Figure 3-1. TMUX2889YBHEVM Schematic



# 3.2 PCB Layouts

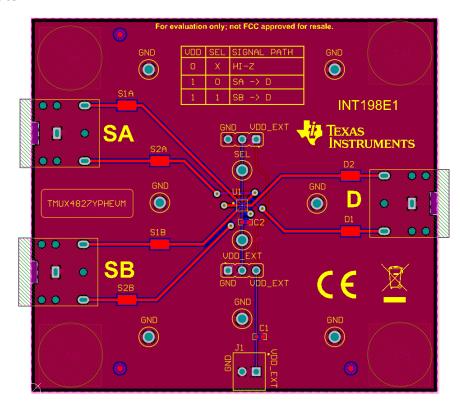


Figure 3-2. TMUX2889YBHEVM Top Layer Layout

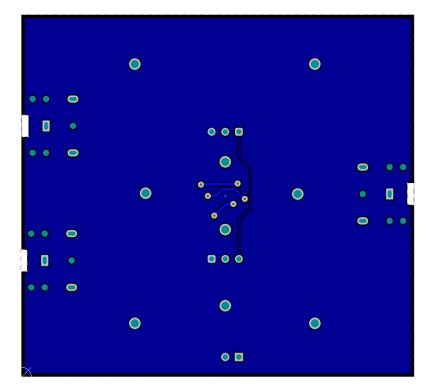


Figure 3-3. TMUX2889YBHEVM Bottom Layer Layout



# 3.3 Bill of Materials (BOM)

# Table 3-1. Bill of Materials

Designator	Qty	Value	Description	Manufacturer	Part Number
		1uF			
C1	1		CAP, CERM, 1uF, 16V, +/- 10%, X5R, 0402	Taiyo Yuden	EMK105BJ105KVHF
C2	1	0.1uF	CAP, CERM, 0.1uF, 25V, +/- 10%, X5R, 0402	MuRata	GRM155R61E104KA87D
D	1		3.5mm, Stereo, Right Angle, Through Hole, 3 Conductors, 0 Internal Switches, Audio Jack Connector	CUI Devices	SJ1-3523N
D1	1		Test Lead clips and hooks, SMT	Harwin	S1751-46
D2	1		Test Lead clips and hooks, SMT	Harwin	S1751-46
GND	1		Test Point, Compact, Black, TH	Keystone	5006
GND	1		Test Point, Compact, Black, TH	Keystone	5006
GND	1		Test Point, Compact, Black, TH	Keystone	5006
GND	1		Test Point, Compact, Black, TH	Keystone	5006
GND	1		Test Point, Compact, Black, TH	Keystone	5006
GND	1		Test Point, Compact, Black, TH	Keystone	5006
GND	1		Test Point, Compact, Black, TH	Keystone	5006
H9	1		Bumpon, Hemisphere, 0.44 X 0.20, Clear	3M	SJ-5303 (CLEAR)
H10	1		Bumpon, Hemisphere, 0.44 X 0.20, Clear	3M	SJ-5303 (CLEAR)
H11	1		Bumpon, Hemisphere, 0.44 X 0.20, Clear	3М	SJ-5303 (CLEAR)
H12	1		Bumpon, Hemisphere, 0.44 X 0.20, Clear	3M	SJ-5303 (CLEAR)
J1	1		Terminal Block, 2x1, 2.54mm, TH	TE Connectivity	282834-2
J2	1		Header, 2.54mm, 3x1, Tin, TH	Molex	22284030
J7	1		Header, 2.54mm, 3x1, Tin, TH	Molex	22284030
S1A	1		Test Lead clips and hooks, SMT	Harwin	S1751-46
S1B	1		Test Lead clips and hooks, SMT	Harwin	S1751-46
S2A	1		Test Lead clips and hooks, SMT	Harwin	S1751-46
S2B	1		Test Lead clips and hooks, SMT	Harwin	S1751-46
SA	1		3.5mm, Stereo, Right Angle, Through Hole, 3 Conductors, 0 Internal Switches, Audio Jack Connector	CUI Devices	SJ1-3523N
SB	1		3.5mm, Stereo, Right Angle, Through Hole, 3 Conductors, 0 Internal Switches, Audio Jack Connector	CUI Devices	SJ1-3523N
SEL	1		Test Point, Compact, Yellow, TH	Keystone Electronics	5009
TMUX2889YPHEVM	1		Thermal Transfer Printable Labels, 0.650" W x 0.200" H - 10,000 per roll	Brady	THT-14-423-10
U1	1		±5.5V Beyond the Supply, 2:1 (SPDT) 2-channel, Power-Off Protection Switch, with 0.2Ω Ron and 1.8V Compatible Logic	Texas Instruments	TMUX2889YBH
VDD_EXT	1		Test Point, Multipurpose, Red, TH	Keystone Electronics	5010



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### FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

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- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- · Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

## 3.2 Canada

3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247

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## Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

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Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types lated in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

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