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Texas Instruments Enhanced Plastic Products Reliability Report

(Subject To Attached Disclaimers)

Device Type/Device Family: SN74LVTH16245AIDLREP
 Package Type: 48/DLR
 Wafer Fabrication Facility: Freising
 Assembly/Test Facility: MLA
 Compiled: 05/12

Biased Life Test

Test Method: JESD22-A108
 Test Condition: 125°C / 1000 hours or equivalent
 Sample Size: 5706
 Rejects: 0
 Activation Energy (eV): 0.7
 Equivalent Device Hours: 6.50E+08
 Failure Rate (FIT)*: 1.41

*Derated to +55°C with a 60% Confidence Level

Package Related Tests

<u>Description</u>	<u>Condition</u>	<u>Referenced Method</u>	<u>Sample Size</u>	<u>Rejects</u>	
Biased Humidity or HAST	85°C / 85% / 1000 hours or 130°C / 85% / 96 hours	JESD22-A101 JESD22-A110	231	0	*
Autoclave	121°C @ 2 atmospheres absolute for 96 hours	JESD22-A102	231	0	*
Temperature Cycle	-65°C to +150°C non-biased for 500 cycles or equivalent	JESD22-A104	231	0	*
High Temp Storage	150°C / 1,000 hours	JESD22-A103-A	231	0	*

* Preconditioning per JEDEC Std. 22, Method A112/A113

Initial Product Qualification

The subject Enhanced Plastic device, device family, and/or package family have passed Texas Instruments product qualification as follows:

<u>Description</u>	<u>Condition</u>	<u>Referenced Method</u>	<u>Sample Size</u>	
Electrical Characterization	TI Data Sheet	N/A	3 lot(s)/90 Units	
Electrostatic Discharge Sensitivity	HBM	EIA/JESD22-A114	3 Units/voltage	
	MM	EIA/JESD22-A115	N/A	
	CDM	JESD22-C101	N/A	
Latch-up	Per Technology	EIA/JESD78	0/0	
Physical Dimensions	TI Data Sheet	EIA/JESD22- B100	0/0	
Thermal Impedance	Theta-JA on board	EIA/JESD51	Per Pin-Package	
Bias Life Test	125°C / 1000 hours or equivalent	JESD22-A108	0/0	
Biased Humidity or HAST	85°C / 85% / 1000 hours or	JESD22-A101	156/0	*
	130°C / 85% / 96 hours	JESD22-A110		
Autoclave	121°C @ 2 atmospheres absolute for 96 hours	JESD22-A102	276/0	*
Temperature Cycle	-65°C to +150°C non-biased for 500 cycles or equivalent	JESD22-A104	231/0	*
High Temp Storage	150°C / 1,000 hours	JESD22-A103-A	231/0	*
Solder Heat	260°C for 10 seconds	JESD22-B106	0/0	
Solderability	Condition A (steam age for 8 hours)	ANSI/J-STD-002-92	0/0	
Bond Strength	-	ASTM F-459	15/1	
Moisture Sensitivity	Surface Mount Only	J-STD-020-A	4/0	

* Preconditioning per JEDEC Std. 22, Method A112/A113

Supplemental Device Characteristics

Master Die: RLVTH16245BIM	Assembly Site: MLA
Wafer Fab: Freising	Pin/Package Type: 48/DL
Fab Process: ASL2BSE	Lead Composition: Cu
Fab Technology: BiCMOS	Lead Finish: NiPdAu
Die Revision: B	Mount Compound: Hitachi En-4088Z
Passivation: 7KACN	Bond: Au/24.3Um
Metal 1: TiW/AI-Cu2	Mold Compound: Gr825-73B
Metal 2: TiW/AI-Cu2	Die Thickness: 10.5 mils

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