

TLIN1024-Q1 Duty Cycle Over V_{SUP}

The TLIN1024-Q1 device has been released to production with associated LIN conformance documentation. TI has been made aware of, and validated that a 50-mV window exists, where the device does not meet the duty cycle requirements specified in the data sheet. The window and duty cycle variation for the device are noted in the *Summary*. If the following changes affect your system, TI recommends that you notify your OEM and request a variance.

Summary

TLIN1024-Q1:

- When V_{SUP} is between 7.4 V and 9.4 V, there is a window of 50 mV where the D1 minimum value exceeds the ISO 17987 and SAE J2602 requirements by approximately 2%.
- When V_{SUP} is between 7.4 V and 9.4 V, there is a window of 50 mV where the D2 maximum value exceeds the ISO 17987 and SAE J2602 requirements by approximately 7%.
- When V_{SUP} is between 7.4 V and 9.4 V, there is a window of 50 mV where the D4 maximum value exceeds the ISO 17987 and SAE J2602 requirements by approximately 1%.

For more information, please contact your TI sales representative.

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