TUSB521-Q1 Evaluation Module



Description

The TUSB521Q1-EVM is a linear redriving switch supporting data rates up to 5Gbps. This device uses 5th generation USB redriver technology. The TUSB521Q1-EVM can be used with a host/hub upstream with two devices and hubs downstream to demonstrate the mixture of the multiplexer capabilities with the redriving capabilities that the TUSB521-Q1 can provide.

Features

- USB Type-C® 2:1 or 1:2 redriver MUX
- Supports USB 3.2 up to 5Gbps
- · Ultra-low-power architecture
- Linear redriver with up to 12dB equalization at 2.5GHz

- Sixteen equalization settings
- Configuration through GPIO or I2C
- Supports either 1.8V or 3.3V I2C signaling levels
- · Hot-plug capable
- · No host or device side requirement
- · Operates from a single 3.3V supply
- Automotive grade 2 temperature range: –40°C to 105°C
- 5mm × 7mm, 0.5mm pitch VQFN package

Applications

- · PC and notebooks
- TV
- Gaming
- · Home theater and entertainment
- Smart phones
- Tablets



TUSB521Q1-EVM



1 Evaluation Module Overview

1.1 Introduction

The TUSB521Q1-EVM comes with two USB3.0 Type-A receptacles for connecting to downstream devices/hubs, and a USB3.0 Micro-B receptacle for connecting to any upstream host/hub.

This user's guide describes how to bring up the EVM, and includes schematics that can be used as a reference design for implementations of the TUSB521-Q1 in a system

1.2 Kit Contents

The TUSB521Q1-EVM includes:

- The TUSB521-Q1 device
- Two USB3.0 Type-A receptacles that connect to a downstream device
- A USB3.0 Micro-B connector for connecting to an upstream device/source.
- Standard 5V barrel jack power source receptacle
- Headers for configuring various TUSB521-Q1 features

1.3 Specification

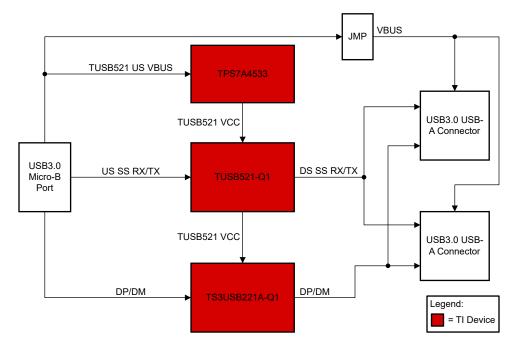


Figure 1-1. TUSB521Q1-EVM Block Diagram

1.4 Device Information

The TUSB521-Q1 is a linear redriver with 1:2 DeMUX or 2:1 MUX function for USB Type-C applications. The TUSB521-Q1 is intended to reside between a Host and a USB-C receptacle or between a USB device and a USB-C receptacle. The TUSB521-Q1 supports USB 3.2 data rates up to 5Gbps and supports USB 3.2 low power states (disconnect, U1, U2, and U3)

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2 Hardware

2.1 Setup

Table 2-1 lists the default configurations on each configurable pin when the EVM is provided. These configurations settings need to be further customized by the user to properly account for the amount of loss that can be present in a system. The EVM is configured to having GPIO mode enabled by default. If I2C is desired, then JMP1 can be changed to have pins 1 and 2 connected, and the shunt connecting pin 5 and 6 removed. The EVM comes with I2C VCC set to 3.3V. For 1.8V, an external voltage source needs to be used to connect to pin 2 of JMP5.

Table 2-1. TUSB521-Q1 EVM Default Pin Configuration

Designator	Function	Default Configuration	
JMP1	I2C_EN	Shunt on pins 5 and 6 (GPIO mode)	
JMP2	FLIP/SCL Shunt on pins 3 and 5 (normal)		
JMP3	CTL0/SDA Shunt on pins 1 and 3 (USB enabled)		
JMP5	I2C VCC Shunt on pins 1 and 2 (I2C 3.3V)		
JMP6	EQ0	Shunt on pins 5 and 6 (0)	
JMP7	EQ1	Shunt on pins 5 and 6 (0)	
JMP8	SSEQ0/A0	Shunt on pins 5 and 6 (0)	
JMP9	SSEQ1 Shunt on pins 5 and 6 (0)		
JMP10	EN	Shunt on pins 1 and 2 (device enabled)	
JMP11	A1	Shunt on pins 5 and 6 (0)	
JMP19	Power source select	Shunt on pins 2 and 3 (VBUS powered)	
JMP21	VBUS Select Shunt on pins 1 and 2 (VBUS to port 1)		
SW2	SW2 TS3USB221 S Pulled to position 1 and 2 (USB2 Port 1)		

For selecting which port users want to use, JMP2, JMP21, and SW2 need to be set up accordingly.

For Port 1, JMP2 (Flip/SCL) needs to have a shunt connecting pins 3 and 5 of the jumper to pull the pin down, JMP21 (VBUS select) needs to have a shunt connecting pins 1 and 2 to send VBUS to port 1, and SW2 (TS3USB221 S) needs to be pulled down connecting pins 1 and 2, sending the USB2 signals to port 1.

For Port 2, JMP2 (Flip/SCL) needs to have a shunt connecting pins 1 and 3 of the jumper to pull the pin up, JMP21 (VBUS select) needs to have a shunt connecting pins 2 and 3 to send VBUS to port 2, and SW2 (TS3USB221 S) needs to be pulled up connecting pins 2 and 3, sending the USB2 signals to port 2.

2.2 Equalization Control

Equalization (EQ) can be controlled using I2C or pin-strapping. Each of the TUSB521-Q1 receiver lanes has individual controls for receiver equalization (see Configuration Pin-Level Definitions). Table 2-3 list the gain values of each available combination of downstream and upstream configurations.

Table 2-2. Configuration Pin-Level Definitions

Level	Settings		
0	Option 1: Tie 1kΩ 5% to GND		
	Option 2: Tie directly to GND		
R	Tie 20kΩ 5% to GND		
F	Float (leave pin open)		
1	Option 1: Tie 1kΩ 5% to VCC		
	Option 2: Tie directly to VCC		

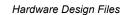
The EQ0/1 Pins affect the receiver EQ setting of the upstream-facing RX1 and RX2 Ports, while the SSEQ0/1 Pins affect the receiver EQ setting of the downstream-facing SSTX Port.



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Table 2-3. USB 3.1 Equalization Settings

	USB 3.2 Downstream Facing Ports			USB 3.2 Upstream Facing Ports		
EQ Setting #	EQ1	EQ0	EQ Gain at 2.5GHz (dB)	SSEQ1	SSEQ0	EQ Gain at 2.5GHz (dB)
0	0	0	-0.9	0	0	-2.4
1	0	R	0.2	0	R	-1.3
2	0	F	1.2	0	F	-0.4
3	0	1	2.2	0	1	0.7
4	R	0	3.1	R	0	1.5
5	R	R	4.0	R	R	2.5
6	R	F	4.8	R	F	3.2
7	R	1	5.6	R	1	4.0
8	F	0	6.3	F	0	4.8
9	F	R	7.0	F	R	5.5
10	F	F	7.5	F	F	6.0
11	F	1	8.1	F	1	6.6
12	1	0	8.5	1	0	7.1
13	1	R	9.1	1	R	7.6
14	1	F	9.5	1	F	8.0
15	1	1	9.9	1	1	8.5





3 Hardware Design Files

3.1 Schematics

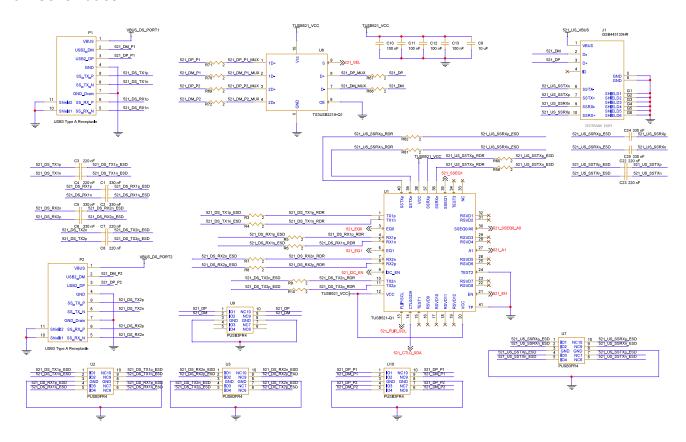


Figure 3-1. Schematic Page 1



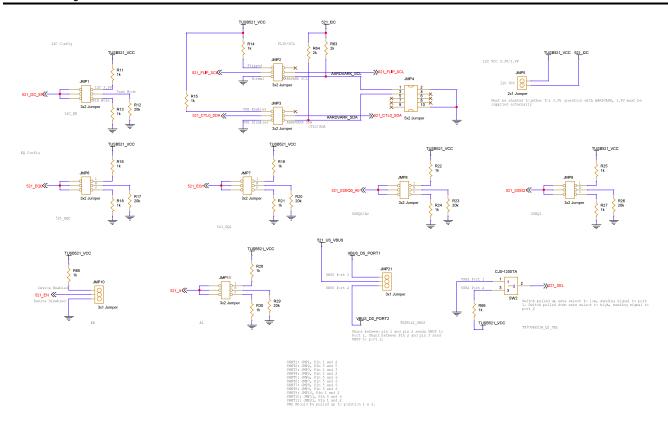


Figure 3-2. Schematic Page 2

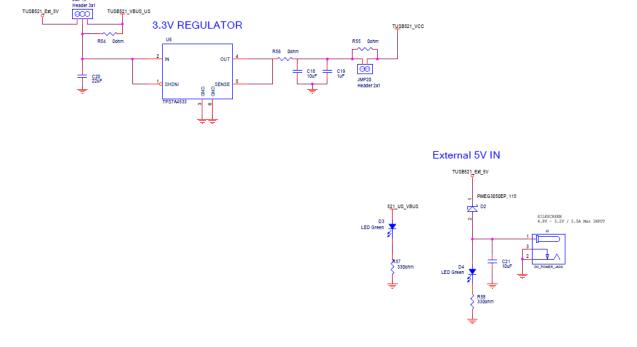


Figure 3-3. Schematic Page 3

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3.2 PCB Layouts

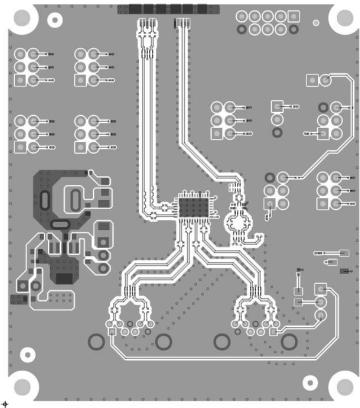


Figure 3-4. TUSB521Q1-EVM PCB Layer 1 (Top)

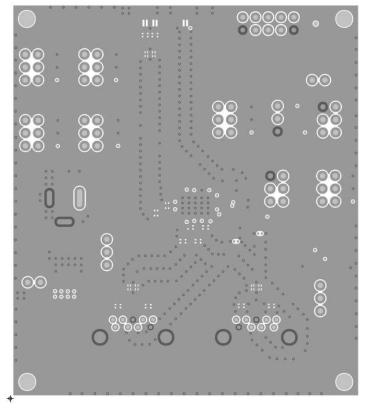


Figure 3-5. TUSB521Q1-EVM PCB Layer 2 (Ground)



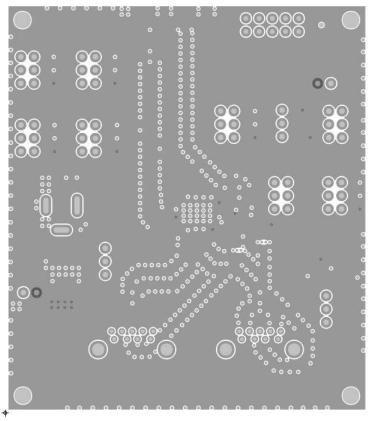


Figure 3-6. TUSB521Q1-EVM PCB Layer 3 (Power)

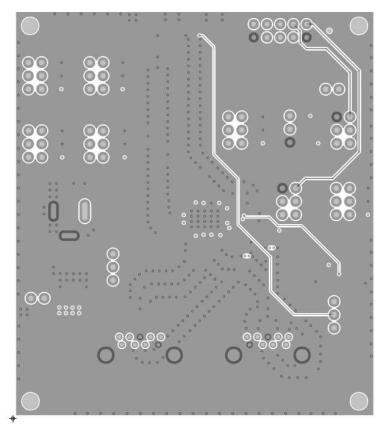


Figure 3-7. TUSB521Q1-EVM PCB Layer 4 (Signal)

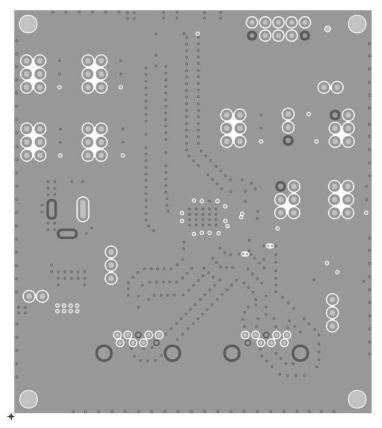


Figure 3-8. TUSB521Q1-EVM PCB Layer 5 (Ground)

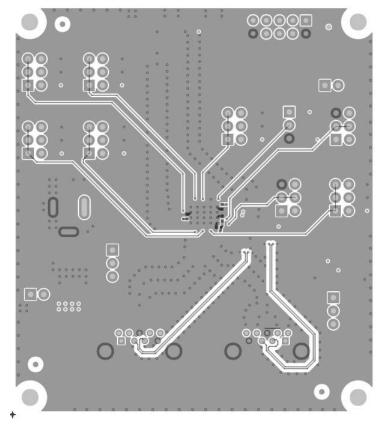


Figure 3-9. TUSB521Q1-EVM PCB Layer 6 (Bottom)

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3.3 Bill of Materials (BOM)

Table 3-1 lists the bill of materials for TUSB521Q1-EVM.

Table 3-1. Bill of Materials

Item	Quantity	Reference	Description	Part	PCB Footprint	Manufacturer	Part Number
1	6	C1,C2,C5,C6,C24,C25	CAP CER 0.33UF 6.3V X5R 0201	330nF	CC0201	Murata Electronics	GRM033R60J334ME90D
2	6	C3,C4,C7,C8,C22,C23	CAP CER 0.22UF 6.3V X6S 0201	220nF	CC0201	Murata Electronics	GRM033C80J224KE90D
3	1	C9	CAP CER 10UF 16V X6S 0603	10 uF	CC0603	Murata Electronics	GRM188C81C106MA73J
4	4	C10,C11,C12,C13	CAP CER 0.1UF 25V X7R 0402	100nF	CC0402	Murata Electronics	GRM155R71E104KE14D
5	2	C18,C21	CAP CER 10UF 16V X6S 0603	10uF	CC0603	Murata Electronics	GRM188C81C106MA73J
6	1	C19	CAP CER 1UF 10V X6S 0402	1uF	CC0402	Murata Electronics	GRM155C81A105KA12D
7	1	C20	CAP CER 22UF 6.3V X6S 0603	22uF	CC0603	Murata Electronics	GRM188C80J226ME15D
8	1	D2	DIODE SCHOTTKY 30V 5A SOD128	SCHOTTKY	DIO_SOD128	NXP	PMEG3050EP,115
9	2	D3,D4	LED, GREEN CLEAR, 0805 SMD	LED Green	LED0805	LITE-ON TECHNOLOGY CORPORATION	LTST-C170KGKT
10	8	JMP1,JMP2,JMP3,JMP 6,JMP7,JMP8,JMP9,JM P11	CONN HEADER VERT 6POS 2.54MM	3x2 Jumper	3M_961206-6404-AR	3M	961206-6404-AR
11	1	JMP4	Connector Header Through Hole 10 position 0.100" (2.54mm)	5x2 Jumper	con_thvt_shrd_2x5_100 _m	3M	N2510-6002-RB
12	1	JMP5	Connector Header Through Hole 2 position 0.100" (2.54mm)	2x1 Jumper	FCI_68001-402HLF	Amphenol ICC (FCI)	68001-402HLF
13	1	JMP10	Connector Header Through Hole 3 position 0.100" (2.54mm)	3x1 Jumper	3M_961103-6404-AR	3M	961103-6404-AR
14	1	JMP19	Connector Header Through Hole 3 position 0.100" (2.54mm)	Header 3x1	3M_961103-6404-AR	3M	961103-6404-AR
15	1	JMP20	Connector Header Through Hole 2 position 0.100" (2.54mm)	Header 2x1	FCI_68001-402HLF	Amphenol ICC (FCI)	68001-402HLF
16	1	J1	CONN RCPT USB3.1 MICRO B SMD R/A	GSB443133HR	AMPHENOL_GSB4431 33HR	Amphenol ICC (FCI)	GSB443133HR
17	1	J2	CONN PWR JACK 2X5.5MM KINKED PIN	DC_PWR_JACK	CUI_PJ-202AH	CUI Inc.	PJ-202AH
18	1	LB1	LABEL 0.2"H X 0.65"W WH 1=10KPCS	THD-47-478-10	rectangle	Brady	THT-14-423-10
19	1	PCB1	РСВ	HSDC173	n/a	Any	HSDCxxx
21	2	P1, P2	CONN RCPT USB3.1 TYPEA 9POS R/A	USB3 Type A Receptacle	USB3_TYPEA	Amphenol ICC (FCI)	GSB4111312HR
22	12	R3,R4,R5,R6,R7,R8,R9 ,R10,R59,R60,R61,R62	RES SMD 2 OHM 5% 1/20W 0201	2	RC0201	Panasonic Electronic Components	ERJ-1GNJ2R0C



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Table 3-1. Bill of Materials (continued)

Item	Quantity	Reference	Description	Part	PCB Footprint	Manufacturer	Part Number
23	16	R11,R13,R14,R15,R16, R18,R19,R21,R22,R24, R25,R27,R28,R30,R65, R66	RES SMD 1K OHM 5% 1/20W 0201	1k	RC0201	Panasonic Electronic Components	ERJ-1GNJ102C
24	6	R12,R17,R20,R23,R26, R29	RES SMD 20K OHM 5% 1/20W 0201	20k	RC0201	Panasonic Electronic Components	ERJ-1GNJ203C
25	3	R54,R55,R56	RES 0 OHM JUMPER 0603 SMD	0	RC0603	Panasonic Electronic Components	ERJ-U030R00V
26	2	R57,R58	RES SMD 330 OHM 5% 1/10W 0402	330	RC0402	Panasonic Electronic Components	ERJ-2GEJ331X
27	2	R63,R64	RES SMD 2K OHM 5% 1/20W 0201	2k	RC0201	Panasonic Electronic Components	ERJ-1GNJ202C
28	4	SCRW1,SCRW2,SCRW 3,SCRW4	MACHINE SCREW PAN PHILLIPS 4-40	NY PMS 440 005 PH	screw	B & F Fastener	NY PMS 440 0050 PH
29	15	SHNT1,SHNT2,SHNT3, SHNT4,SHNT5,SHNT6, SHNT7,SHNT8,SHNT9, SHNT10,SHNT11,SHN T12,SHNT13,SHNT14, SHNT15	CONN JUMPER SHORTING .100" GOLD	QPC02SXGN-RC	0.1	Sullins Connector Solutions	QPC02SXGN-RC
30	4	STDOFF1,STDOFF2,S TDOFF3,STDOFF4	HEX STANDOFF #4-40 NYLON 1"	1902E	Standoff	Keystone	1902E
31	1	SW1	SWITCH SLIDE SPDT 500MA 125V	G-107-0513	ON-ON SPDT	CW Industries	G-107-0513
32	1	SW2	SWITCH SLIDE SPDT 100MA 6V	CJS-1200TA	ON-ON SPDT	Nidec Components Corporation	CJS-1200TA
33	1	U1	TUSB521-Q1	TUSB521-Q1	RGF0040F	Texas Instruments	TUSB521RGFRQ1
34	3	U2,U3,U7	TVS DIODE 3.3VWM 3VC DFN2510A-10	PUSB3FR4	SOT1176-1	Nexperia USA Inc.	PUSB3FR4Z
35	1	U6	IC REG LINEAR 3.3V 1.5A SOT223-6	TPS7A4533	SOT223-6	Texas Instruments	TPS7A4533DCQ
36	1	U8	TS3USB221E	TS3USB221E	RSE0010A	Texas Instruments	TS3USB221ERSER



4 Additional Information

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NOTE:

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3 Regulatory Notices:

3.1 United States

3.1.1 Notice applicable to EVMs not FCC-Approved:

FCC NOTICE: This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

3.1.2 For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:

CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- · Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

3.2 Canada

3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247

Concerning EVMs Including Radio Transmitters:

This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:

(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concerning EVMs Including Detachable Antennas:

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types lated in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur

3.3 Japan

- 3.3.1 Notice for EVMs delivered in Japan: Please see http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_01.page 日本国内に輸入される評価用キット、ボードについては、次のところをご覧ください。
 - https://www.ti.com/ja-jp/legal/notice-for-evaluation-kits-delivered-in-japan.html
- 3.3.2 Notice for Users of EVMs Considered "Radio Frequency Products" in Japan: EVMs entering Japan may not be certified by TI as conforming to Technical Regulations of Radio Law of Japan.

If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required to follow the instructions set forth by Radio Law of Japan, which includes, but is not limited to, the instructions below with respect to EVMs (which for the avoidance of doubt are stated strictly for convenience and should be verified by User):

- 1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
- 2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
- 3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above. User will be subject to penalties of Radio Law of Japan.

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西新宿三井ビル

- 3.3.3 Notice for EVMs for Power Line Communication: Please see http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_02.page 電力線搬送波通信についての開発キットをお使いになる際の注意事項については、次のところをご覧ください。https://www.ti.com/ja-jp/legal/notice-for-evaluation-kits-for-power-line-communication.html
- 3.4 European Union
 - 3.4.1 For EVMs subject to EU Directive 2014/30/EU (Electromagnetic Compatibility Directive):

This is a class A product intended for use in environments other than domestic environments that are connected to a low-voltage power-supply network that supplies buildings used for domestic purposes. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

- 4 EVM Use Restrictions and Warnings:
 - 4.1 EVMS ARE NOT FOR USE IN FUNCTIONAL SAFETY AND/OR SAFETY CRITICAL EVALUATIONS, INCLUDING BUT NOT LIMITED TO EVALUATIONS OF LIFE SUPPORT APPLICATIONS.
 - 4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.
 - 4.3 Safety-Related Warnings and Restrictions:
 - 4.3.1 User shall operate the EVM within TI's recommended specifications and environmental considerations stated in the user guide, other available documentation provided by TI, and any other applicable requirements and employ reasonable and customary safeguards. Exceeding the specified performance ratings and specifications (including but not limited to input and output voltage, current, power, and environmental ranges) for the EVM may cause personal injury or death, or property damage. If there are questions concerning performance ratings and specifications, User should contact a TI field representative prior to connecting interface electronics including input power and intended loads. Any loads applied outside of the specified output range may also result in unintended and/or inaccurate operation and/or possible permanent damage to the EVM and/or interface electronics. Please consult the EVM user guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative. During normal operation, even with the inputs and outputs kept within the specified allowable ranges, some circuit components may have elevated case temperatures. These components include but are not limited to linear regulators, switching transistors, pass transistors, current sense resistors, and heat sinks, which can be identified using the information in the associated documentation. When working with the EVM, please be aware that the EVM may become very warm.
 - 4.3.2 EVMs are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems. User assumes all responsibility and liability for proper and safe handling and use of the EVM by User or its employees, affiliates, contractors or designees. User assumes all responsibility and liability to ensure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard. User assumes all responsibility and liability for any improper or unsafe handling or use of the EVM by User or its employees, affiliates, contractors or designees.
 - 4.4 User assumes all responsibility and liability to determine whether the EVM is subject to any applicable international, federal, state, or local laws and regulations related to User's handling and use of the EVM and, if applicable, User assumes all responsibility and liability for compliance in all respects with such laws and regulations. User assumes all responsibility and liability for proper disposal and recycling of the EVM consistent with all applicable international, federal, state, and local requirements.
- 5. Accuracy of Information: To the extent TI provides information on the availability and function of EVMs, TI attempts to be as accurate as possible. However, TI does not warrant the accuracy of EVM descriptions, EVM availability or other information on its websites as accurate, complete, reliable, current, or error-free.

6. Disclaimers:

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