



ABSTRACT

This report discusses the results of the total ionizing dose (TID) testing for TPS7H5001-SP, Texas Instruments 4-V to 14-V radiation-hardened, current mode, dual output PWM controller optimized for Si and GaN DC-DC converters in space applications. The study was done to determine TID effects under high dose rate (HDR) and low dose rate (LDR) up to 100 krad(Si). The results show that all samples passed within the specified limits up to 100 krad(Si).

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1 Device Information

1.1 Product Description

The TPS7H5001-SP is a radiation-hardness-assured, current mode, dual output PWM controller optimized for Si and GaN DC-DC converters in space applications. The high switching frequency capability of the TPS7H5001-SP, small footprint, and low-current consumption makes the device ideal for fully exploiting the area reduction and high-efficiency benefits of GaN based DC-DC converters. The TPS7H5001-SP features integrated synchronous rectifier control outputs and dead-time programmability in order to target high-efficiency and high-performance topologies. In addition, the TPS7H5001-SP supports single-ended converter topologies by providing the user the flexibility to control the maximum duty cycle. The 0.613-V $\pm 1\%$ accurate internal reference allows design of high-current buck converters for FPGA core voltages.

The TPS7H5001-SP can be driven using an external clock through the SYNC pin or run using its internal oscillator at a frequency programmed by the user. Other programmable features include the UVLO threshold, soft start, and slope compensation. The TPS7H5001-SP is packaged in a very small 22-pin ceramic dual flat package.

1.2 Device Details

Table 1-1 lists the device information used in the initial TID HDR and LDR characterization.

Table 1-1. Device and Exposure Details

TID Exposure Details	
TI Device	TPS7H5001-SP
TI Part Name	5962R1822201VXC
Package	22-pin CFP (HFT)
Technology	LBC7
Quantity Tested	HDR - 67, LDR - 77
Lot Accept/Reject	All levels tested and passed up to 100 krad(Si) for HDR and LDR
HDR Radiation Facility	Texas Instruments, Santa Clara, CA
HDR Dose Level	3 krad(Si), 10 krad(Si), 30 krad(Si), 50 krad(Si), 100 krad(Si)
HDR Dose Rate	54.72 rads(Si)/s [50 to 60.19 rad(Si)/s]
HDR Radiation Source	Gammacell 220 Excel (GC-220E) Co-60
LDR Radiation Facility	Radiation Test Solutions (RTS) - Colorado Springs, CO
LDR Dose Level	3 krad(Si), 10 krad(Si), 30 krad(Si), 50 krad(Si), 100 krad(Si)
LDR Dose Rate	0.01 rad(Si)/s
LDR Radiation Source	Gammacell (JLS-81-22) Co-60
Irradiation Temperature	Ambient room temperature



Figure 1-1. TPS7H5001-SP Device Picture

2 Total Dose Test Setup

2.1 Test Overview

The TPS7H5001-SP was tested according to MIL-STD-883, Test Method 1019.9. For this testing, Conditions A and D were used. The product was irradiated up to the target radiation level, and then put through full electrical parametric testing on the production automated test equipment (ATE). All devices remained functional passing all parametric test limits.

2.2 Test Description and Facilities

The TPS7H5001-SP HDR exposure was performed on biased and unbiased devices in a Co-60 gamma cell at TI facility in Santa Clara, California. The un-attenuated dose rate of this cell is 54.72 rads(Si)/s [50 to 60.19 rad(Si)/s]. After exposure, the devices were packed in dry ice (per MIL-STD-883 Method 1019.9 section 3.10) and returned to TI Dallas for a full post-radiation electrical evaluation using Texas Instruments ATE. ATE test limits are set per SMD electrical limits based on qualification and characterization data. Post-radiation measurements were taken within 30 minutes of removing the devices from the dry ice container. The devices were allowed to reach room temperature prior to electrical post-radiation measurements.

The TPS7H5001-SP LDR exposure was performed on biased and unbiased devices in a Co-60 gammacell under a 10-mrad(Si)/s exposure rate. The dose rate of the irradiator used in the exposure ranges from < 10 mrad(Si)/s to a maximum of approximately 84 rad(Si)/s, determined by the distance from the source. For the LDR (10 mrad(Si)/s) exposure, the test box was positioned approximately 2 m from the source. The exposure boards are housed in a lead-aluminum box (as specified in MIL-STD-883 TM 1019.9) to harden the gamma spectrum and minimize dose enhancement effects. The irradiator calibration is maintained by Logmire Laboratories using Thermoluminescence Dosimeters (TLDs) traceable to the National Institute of Standards and Technology (NIST) and the dosimetry was verified using TLDs prior to the radiation exposures. After exposure, devices were electrically tested at Texas Instruments. ATE test limits are set per SMD electrical limits based on qualification and characterization data.

2.3 Test Setup Details

The devices under HDR and LDR exposure were tested in two conditions, biased and unbiased, as described in the following.

2.3.1 Unbiased

For the unbiased HDR and LDR conditions, the exposure was performed with all pins grounded.

2.3.2 Biased

[Figure 2-1](#) shows the diagram for HDR and LDR exposure with biased condition.

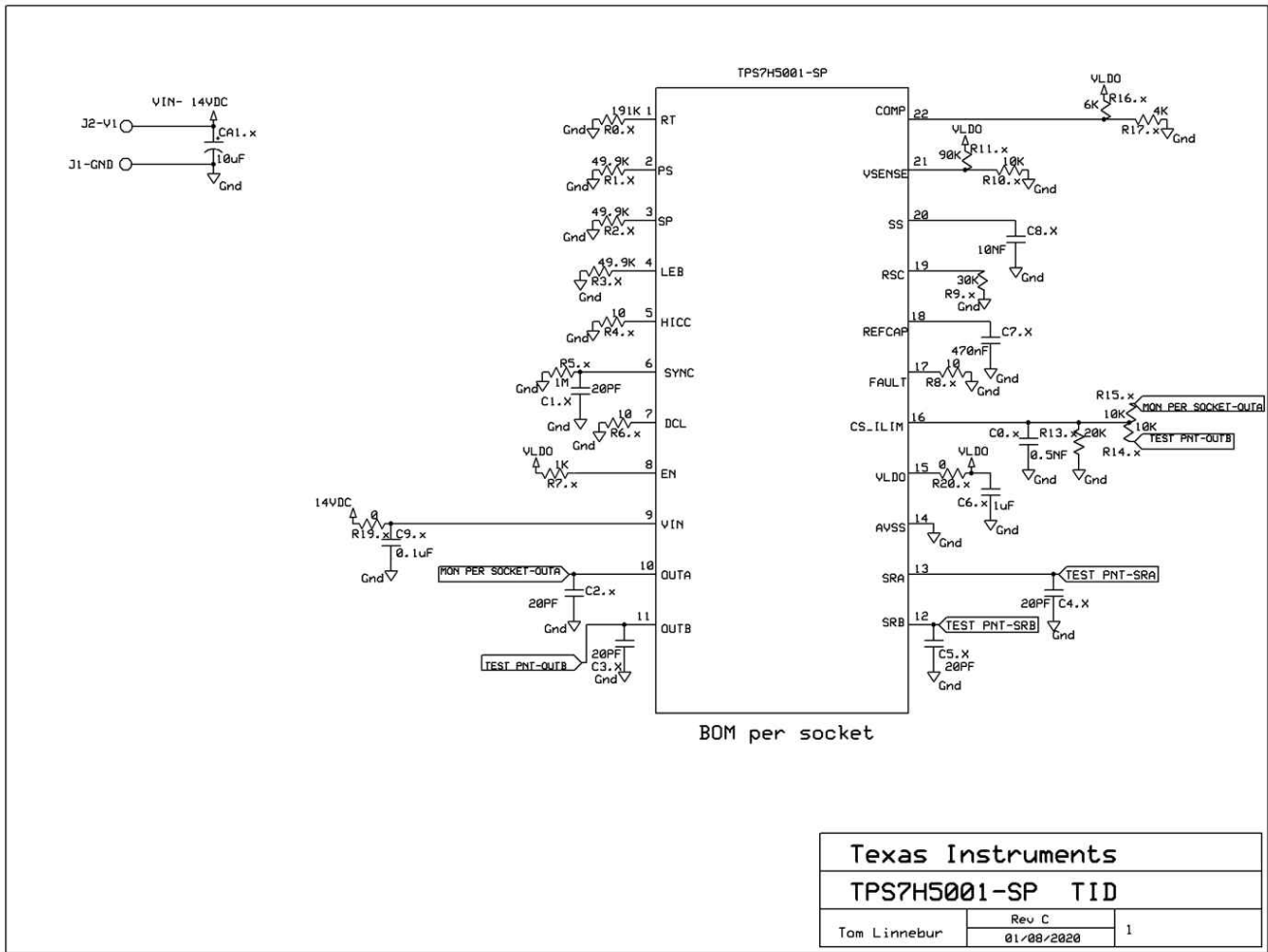


Figure 2-1. Bias Diagram Used in TID Exposure

2.4 Test Configuration and Condition

HDR devices were stressed at 3 krad(Si), 10 krad(Si), 30 krad(Si), 50 krad(Si), and 100 krad(Si) for biased and unbiased conditions.

LDR devices were stressed at 3 krad(Si), 10 krad(Si), 30 krad(Si), 50 krad(Si), and 100 krad(Si) for biased and unbiased conditions.

Table 2-1. HDR: 3-krad(Si) to 100-krad(Si)/s Biased Device Information

Total Samples: 42				
Exposure Levels				
3 krad(Si)	10 krad(Si)	30 krad(Si)	50 krad(Si)	100 krad(Si)
301, 306, 307, 308, 309	317, 318, 319, 321, 322	334, 335, 337, 339, 341	349, 350, 351, 353, 354	1, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 24

Table 2-2. HDR: 3-krad(Si) to 100-krad(Si)/s Unbiased Device Information

Total Samples: 25				
Exposure Levels				
3 krad(Si)	10 krad(Si)	30 krad(Si)	50 krad(Si)	100 krad(Si)
310, 311, 312, 314, 316	323, 325, 326, 328, 333	342, 345, 346, 347, 348	355, 357, 359, 362, 363	25, 26, 27, 28, 29

Table 2-3. LDR: 3-krad(Si) to 100-krad(Si)/s Biased Device Information

Total Samples: 48				
Exposure Levels				
3 krad(Si)	10 krad(Si)	30 krad(Si)	50 krad(Si)	100 krad(Si)
61, 62, 63, 64, 65, 66	73, 74, 75, 76, 77, 78	86, 89, 91, 92, 94, 95	106, 107, 108, 109, 110, 111	129, 131, 132, 133, 134, 135, 138, 139, 140, 141, 142, 143, 144, 220, 221, 224, 225, 226, 228, 232, 233, 236, 238, 243

Table 2-4. LDR: 3-krad(Si) to 100-krad(Si)/s Unbiased Device Information

Total Samples: 29				
Exposure Levels				
3 krad(Si)	10 krad(Si)	30 krad(Si)	50 krad(Si)	100 krad(Si)
67, 68, 69, 70, 71, 72	79, 80, 81, 82, 83, 84	100, 101, 102, 103, 104, 105	113, 114, 115, 116, 126, 128	244, 246, 248, 374, 376

3 TI Characterization Test Results

3.1 TID Characterization Summary Results

The parametric data for the TPS7H5001-SP passes up to 100-krad(Si) HDR and 100-krad(Si) LDR TID irradiation. The drifts of SMD electrical parameters through HDR and LDR were within the SMD limits.

Overall, the TPS7H5001-SP showed a strong degree of hardness to HDR and LDR TID irradiation up to 100 krad(Si) for both biased and unbiased exposure conditions. The measurements taken post-irradiation for each sample set showed a marginal shift for most parameters at each dose level for both biased and unbiased. The parameters that did show a greater degree of change between pre- and post-irradiation were still within the Electrical Performance Characteristics specified in Standard Microcircuit Drawing (Data Sheet Electrical Parameters). See [Table 3-1](#) for the Data Sheet Electrical Parameters and Associated Tests.

See [Appendix A](#) for Specifications Requiring Clarification.

See [Appendix B](#) for HDR report up to 100 krad(Si).

See [Section 8](#) for LDR report up to 100 krad(Si).

3.2 Specification Compliance Matrix

Table 3-1. TPS7H5001-SP Electrical Parameters Table

PARAMETER	TEST CONDITION	TPS7H5001-SP DATA SHEET				TEST #
		MIN	TYP	MAX	UNIT	
SUPPLY VOLTAGES AND CURRENTS						
IDD Operating supply current	f _{SW} = 500 kHz, No load for OUTA, OUTB, SRA, and SRB		6.25	8	mA	5.24 __IDD_ACT_500K_NOLOAD_4V, 5.25 __IDD_ACT_500K_NOLOAD_5V, 5.26 __IDD_ACT_500K_NOLOAD_12V, 5.27 __IDD_ACT_500K_NOLOAD_14V
	f _{SW} = 1 MHz, No load for OUTA, OUTB, SRA, and SRB		6.75	9.5	mA	5.28 __IDD_ACT_1M_NOLOAD_4V, 5.29 __IDD_ACT_1M_NOLOAD_5V, 5.30 __IDD_ACT_1M_NOLOAD_12V, 5.31 __IDD_ACT_1M_NOLOAD_14V
	f _{SW} = 2 MHz, No load for OUTA, OUTB, SRA, and SRB		8.5	13.5	mA	5.36 __IDD_ACT_2M_NOLOAD_4V, 5.37 __IDD_ACT_2M_NOLOAD_5V, 5.38 __IDD_ACT_2M_NOLOAD_12V, 5.39 __IDD_ACT_2M_NOLOAD_14V
	f _{SW} = 500 kHz, C _{LOAD} = 100pF for OUTA, OUTB, SRA, and SRB		7.5	9.5	mA	5.48 __IDD_ACT_500K_4V, 5.49 __IDD_ACT_500K_5V, 5.50 __IDD_ACT_500K_12V, 5.51 __IDD_ACT_500K_14V
	f _{SW} = 1 MHz, C _{LOAD} = 100pF for OUTA, OUTB, SRA, and SRB		9	12	mA	5.52 __IDD_ACT_1M_4V, 5.53 __IDD_ACT_1M_5V, 5.54 __IDD_ACT_1M_12V, 5.55 __IDD_ACT_1M_14V
	f _{SW} = 2 MHz, C _{LOAD} = 100pF for OUTA, OUTB, SRA, and SRB		14	19.5	mA	5.60 __IDD_ACT_2M_4V, 5.61 __IDD_ACT_2M_5V, 5.62 __IDD_ACT_2M_12V, 5.63 __IDD_ACT_2M_14V
I _{DD(dis)} Standby current	EN = 0 V			3	mA	5.1 __IDD_DIS_4V, 5.2 __IDD_DIS_5V, 5.3 __IDD_DIS_12V, 5.4 __IDD_DIS_14V
VLDO Internal linear regulator output voltage	5 V ≤ VIN ≤ 14 V, f _{sw} ≤ 1 MHz	4.75	5	5.2	V	5.65 __V_LDO_100K_5V, 5.66 __V_LDO_100K_12V, 5.67 __V_LDO_100K_14V, 5.69 __V_LDO_200K_5V, 5.70 __V_LDO_200K_12V, 5.71 __V_LDO_200K_14V, 5.73 __V_LDO_500K_5V, 5.74 __V_LDO_500K_12V, 5.75 __V_LDO_500K_14V, 5.77 __V_LDO_1M_5V, 5.78 __V_LDO_1M_12V, 5.79 __V_LDO_1M_14V
	5 V ≤ VIN ≤ 14 V, f _{sw} = 2 MHz	4.65	5	5.2	V	5.85 __V_LDO_2M_5V, 5.86 __V_LDO_2M_12V, 5.87 __V_LDO_2M_14V
ENABLE AND UNDERVOLTAGE LOCKOUT						
V _{ENR} EN threshold rising		0.57	0.6	0.65	V	6.5 __V_EN_RISE_4V, 6.8 __V_EN_RISE_5V, 6.11 __V_EN_RISE_12V, 6.14 __V_EN_RISE_14V
V _{ENF} EN threshold falling		0.47	0.5	0.55	V	6.6 __V_EN_FALL_4V, 6.9 __V_EN_FALL_5V, 6.12 __V_EN_FALL_12V, 6.15 __V_EN_FALL_14V
V _{ENH} EN hysteresis voltage		85	95	105	mV	6.7 __V_EN_HYS_4V, 6.10 __V_EN_HYS_5V, 6.13 __V_EN_HYS_12V, 6.16 __V_EN_HYS_14V
I _{EN} EN pin input leakage current	VIN = 14 V, EN = 5 V			50	nA	6.1 __I_EN_LEAK_4V, 6.2 __I_EN_LEAK_5V, 6.3 __I_EN_LEAK_12V, 6.4 __I_EN_LEAK_14V
VLDO _{UVLOR} VLDO UVLO rising		3.44	3.55	3.66	V	6.34 __UVLO_VLDO_RISE_1MHz, 6.37 __UVLO_VLDO_RISE_100kHz, 6.40 __UVLO_VLDO_RISE_200kHz, 6.43 __UVLO_VLDO_RISE_500kHz, 6.46 __UVLO_VLDO_RISE_2MHz
VLDO _{UVLOF} VLDO UVLO falling		3.29	3.4	3.51	V	6.35 __UVLO_VLDO_FALL_1MHz, 6.38 __UVLO_VLDO_FALL_100kHz, 6.41 __UVLO_VLDO_FALL_200kHz, 6.44 __UVLO_VLDO_FALL_500kHz, 6.47 __UVLO_VLDO_FALL_2MHz
VLDO _{UVLOH} VLDO UVLO hysteresis		115	135	160	mV	6.36 __UVLO_VLDO_HYS_1MHz, 6.39 __UVLO_VLDO_HYS_100kHz, 6.42 __UVLO_VLDO_HYS_200kHz, 6.45 __UVLO_VLDO_HYS_500kHz, 6.48 __UVLO_VLDO_HYS_2MHz
SOFT START						
I _{SS} Soft-start current	SS = 0.3 V	1.98	2.7	3.32	μA	7.1 __I_SS_4V, 7.3 __I_SS_5V, 7.5 __I_SS_12V, 7.7 __I_SS_14V
ERROR AMPLIFIER						
E _{A_{gm}} Transconductance	-2 μA < I _{COMP} < 2 μA, V _(COMP) = 1 V	1150	1800	2500	μA/V	8.9 __EA_GM_4V, 8.10 __EA_GM_5V, 8.11 __EA_GM_12V, 8.12 __EA_GM_14V

Table 3-1. TPS7H5001-SP Electrical Parameters Table (continued)

PARAMETER	TEST CONDITION	TPS7H5001-SP DATA SHEET				TEST #
		MIN	TYP	MAX	UNIT	
EA _{ISRC} Error amplifier source current	V _(COMP) = 1 V, 100-mV input overdrive	100		190	μA	8.13 __EA_I_SOURCE_4V, 8.14 __EA_I_SOURCE_5V, 8.15 __EA_I_SOURCE_12V, 8.16 __EA_I_SOURCE_14V
EA _{ISNK} Error amplifier sink current	V _(COMP) = 1 V, 100-mV input overdrive	100		190	μA	8.17 __EA_I_SINK_4V, 8.18 __EA_I_SINK_5V, 8.19 __EA_I_SINK_12V, 8.20 __EA_I_SINK_14V
EA _{OS} Error amplifier offset voltage		-2		2	mV	8.5 __EA_OS_4V, 8.6 __EA_OS_5V, 8.7 __EA_OS_12V, 8.8 __EA_OS_14V
OSCILLATOR						
SYNC _{RT} SYNC out low-to-high rise time (10%/90%)	C _{LOAD} = 25 pF		6	15	ns	9.1 __T_SYNC_RISE_100kHz_4V, 9.5 __T_SYNC_RISE_200kHz_4V, 9.9 __T_SYNC_RISE_500kHz_4V, 9.13 __T_SYNC_RISE_1MHz_4V, 9.17 __T_SYNC_RISE_1p5MHz_4V, 9.21 __T_SYNC_RISE_2MHz_4V, 9.25 __T_SYNC_RISE_100kHz_5V, 9.29 __T_SYNC_RISE_200kHz_5V, 9.33 __T_SYNC_RISE_500kHz_5V, 9.37 __T_SYNC_RISE_1MHz_5V, 9.41 __T_SYNC_RISE_1p5MHz_5V, 9.45 __T_SYNC_RISE_2MHz_5V, 9.49 __T_SYNC_RISE_100kHz_12V, 9.53 __T_SYNC_RISE_200kHz_12V, 9.57 __T_SYNC_RISE_500kHz_12V, 9.61 __T_SYNC_RISE_1MHz_12V, 9.65 __T_SYNC_RISE_1p5MHz_12V, 9.69 __T_SYNC_RISE_2MHz_12V, 9.73 __T_SYNC_RISE_100kHz_14V, 9.77 __T_SYNC_RISE_200kHz_14V, 9.81 __T_SYNC_RISE_500kHz_14V, 9.85 __T_SYNC_RISE_1MHz_14V, 9.89 __T_SYNC_RISE_1p5MHz_14V, 9.93 __T_SYNC_RISE_2MHz_14V
SYNC _{FT} SYNC out high-to-low fall time (10%/90%)	C _{LOAD} = 25 pF		6	17	ns	9.2 __T_SYNC_FALL_100kHz_4V, 9.14 __T_SYNC_FALL_1MHz_4V, 9.22 __T_SYNC_FALL_2MHz_4V, 9.26 __T_SYNC_FALL_100kHz_5V, 9.38 __T_SYNC_FALL_1MHz_5V, 9.46 __T_SYNC_FALL_2MHz_5V, 9.74 __T_SYNC_FALL_100kHz_14V, 9.86 __T_SYNC_FALL_1MHz_14V, 9.94 __T_SYNC_FALL_2MHz_14V
SYNC _{OL} SYNC out low level	I _{OL} = 10 mA			500	mV	9.186 __SYNC_VOL_4V, 9.187 __SYNC_VOL_5V, 9.188 __SYNC_VOL_12V, 9.189 __SYNC_VOL_14V
EXT _{DT} Externally set frequency detection time	RT = Open, f = 200 kHz			20	μs	9.185 __T_SYNC_DETECT
FSW _{EXT} Externally set frequency	RT = 1.07 MΩ	95	105	115	kHz	9.4 __FSW_EXT_RT_100kHz_4V, 9.28 __FSW_EXT_RT_100kHz_5V, 9.52 __FSW_EXT_RT_100kHz_12V, 9.76 __FSW_EXT_RT_100kHz_14V
	RT = 511 kΩ	190	210	230	kHz	9.8 __FSW_EXT_RT_200kHz_4V, 9.32 __FSW_EXT_RT_200kHz_5V, 9.56 __FSW_EXT_RT_200kHz_12V, 9.80 __FSW_EXT_RT_200kHz_14V
	RT = 90.9 kΩ	900	1000	1100	kHz	9.16 __FSW_EXT_RT_1MHz_4V, 9.40 __FSW_EXT_RT_1MHz_5V, 9.64 __FSW_EXT_RT_1MHz_12V, 9.88 __FSW_EXT_RT_1MHz_14V
	RT = 34.8 kΩ	1700	2000	2300	kHz	9.24 __FSW_EXT_RT_2MHz_4V, 9.48 __FSW_EXT_RT_2MHz_5V, 9.72 __FSW_EXT_RT_2MHz_12V, 9.96 __FSW_EXT_RT_2MHz_14V
VOLTAGE REFERENCE						
VREF Internal voltage reference initial tolerance	Measured at COMP, 25°C	0.609	0.613	0.615	V	8.1 __VREF_4V, 8.2 __VREF_5V, 8.3 __VREF_12V, 8.4 __VREF_14V

Table 3-1. TPS7H5001-SP Electrical Parameters Table (continued)

PARAMETER	TEST CONDITION	TPS7H5001-SP DATA SHEET				TEST #
		MIN	TYP	MAX	UNIT	
REFCAP REFCAP voltage	REFCAP = 470 nF	1.213	1.225	1.237	V	5.88 __V_REFCAP_100K_4V, 5.89 __V_REFCAP_100K_5V, 5.90 __V_REFCAP_100K_12V, 5.91 __V_REFCAP_100K_14V, 5.92 __V_REFCAP_200K_4V, 5.93 __V_REFCAP_200K_5V, 5.94 __V_REFCAP_200K_12V, 5.95 __V_REFCAP_200K_14V, 5.96 __V_REFCAP_500K_4V, 5.97 __V_REFCAP_500K_5V, 5.98 __V_REFCAP_500K_12V, 5.99 __V_REFCAP_500K_14V, 5.100 __V_REFCAP_1M_4V, 5.101 __V_REFCAP_1M_5V, 5.102 __V_REFCAP_1M_12V, 5.103 __V_REFCAP_1M_14V, 5.104 __V_REFCAP_1P5M_4V, 5.105 __V_REFCAP_1P5M_5V, 5.106 __V_REFCAP_1P5M_12V, 5.107 __V_REFCAP_1P5M_14V, 5.108 __V_REFCAP_2M_4V, 5.109 __V_REFCAP_2M_5V, 5.110 __V_REFCAP_2M_12V, 5.111 __V_REFCAP_2M_14V
CURRENT SENSE, CURRENT LIMIT, AND HICCUF						
CCSR	COMP to CS_ILIM ratio	2.00	2.06	2.12		10.49 __CCSR_Ratio
V _{CS_ILIM} Current limit (over-current) threshold			1.05	1.09	V	10.1 __V_CS_ILIM_OC_Rise_4V, 10.3 __V_CS_ILIM_OC_Rise_5V, 10.5 __V_CS_ILIM_OC_Rise_12V, 10.7 __V_CS_ILIM_OC_Rise_14V
FAULT						
V _{FLTR} FLT threshold rising		0.57	0.6	0.65	V	12.1 __V_FAULT_RISE_4V, 12.4 __V_FAULT_RISE_5V, 12.7 __V_FAULT_RISE_12V, 12.10 __V_FAULT_RISE_14V
V _{FLTF} FLT threshold falling		0.47	0.5	0.55	V	12.2 __V_FAULT_FALL_4V, 12.5 __V_FAULT_FALL_5V, 12.8 __V_FAULT_FALL_12V, 12.11 __V_FAULT_FALL_14V
V _{FLTH} FLT hysteresis voltage		90	100	110	mV	12.3 __V_FAULT_HYS_4V, 12.6 __V_FAULT_HYS_5V, 12.9 __V_FAULT_HYS_12V, 12.12 __V_FAULT_HYS_14V
T _{FLT} FLT minimum pulse width	V _{FLT} = 1 V	0.4		1.4	µs	12.14 __T_FAULT_MIN
t _{DFLT} FLT delay duration	f _{sw} = 100 kHz	140	152	169	µs	12.15 __T_FAULT_DELAY_100kHz
	f _{sw} = 200 kHz	66	78	86	µs	12.17 __T_FAULT_DELAY_200kHz
	f _{sw} = 1 MHz	14	17	21	µs	12.21 __T_FAULT_DELAY_1MHz
	f _{sw} = 2 MHz	7	11	14	µs	12.23 __T_FAULT_DELAY_2MHz
PRIMARY AND SYNCHRONOUS RECTIFIER OUTPUTS						
Rise/fall time	R _{LOAD} = 50 kΩ, C _{LOAD} = 100 pF, 10% to 90%		10	17	ns	13.1 __OUTA_RISE_1MHz_4V, 13.9 __OUTA_RISE_1MHz_5V, 13.17 __OUTA_RISE_1MHz_12V, 13.25 __OUTA_RISE_1MHz_14V, 13.3 __OUTA_FALL_1MHz_4V, 13.11 __OUTA_FALL_1MHz_5V, 13.19 __OUTA_FALL_1MHz_12V, 13.27 __OUTA_FALL_1MHz_14V, 13.2 __OUTB_RISE_1MHz_4V, 13.10 __OUTB_RISE_1MHz_5V, 13.18 __OUTB_RISE_1MHz_12V, 13.26 __OUTB_RISE_1MHz_14V, 13.4 __OUTB_FALL_1MHz_4V, 13.12 __OUTB_FALL_1MHz_5V, 13.20 __OUTB_FALL_1MHz_12V, 13.28 __OUTB_FALL_1MHz_14V
t _{MIN} Minimum on-time LEB = 10 kΩ	LEB = 10 kΩ			85	ns	13.241 __OUT_T_ON_MIN

Table 3-1. TPS7H5001-SP Electrical Parameters Table (continued)

PARAMETER	TEST CONDITION	TPS7H5001-SP DATA SHEET				TEST #
		MIN	TYP	MAX	UNIT	
TD _{PS} Primary off to secondary on dead time	PS = floating, 5 V ≤ VIN ≤ 14 V, 90% of OUTx falling edge to 10% of SRx rising edge with OUTx and SRx floating	5	8	11	ns	13.37 __PSA_DT_0ns_1M_5V, 13.38 __PSB_DT_0ns_1M_5V, 13.41 __PSA_DT_0ns_1M_12V, 13.42 __PSB_DT_0ns_1M_12V, 13.45 __PSA_DT_0ns_1M_14V, 13.46 __PSB_DT_0ns_1M_14V
	PS = 49.9 kΩ, 5 V ≤ VIN ≤ 14 V, 90% of OUTx falling edge to 10% of SRx rising edge with OUTx and SRx floating	43	50	55	ns	13.53 __PSA_DT_50ns_1M_5V, 13.54 __PSB_DT_50ns_1M_5V, 13.57 __PSA_DT_50ns_1M_12V, 13.58 __PSB_DT_50ns_1M_12V, 13.61 __PSA_DT_50ns_1M_14V, 13.62 __PSB_DT_50ns_1M_14V
	PS = 107 kΩ, 5 V ≤ VIN ≤ 14 V, 90% of OUTx falling edge to 10% of SRx rising edge with OUTx and SRx floating	85	100	110	ns	13.69 __PSA_DT_100ns_1M_5V, 13.70 __PSB_DT_100ns_1M_5V, 13.73 __PSA_DT_100ns_1M_12V, 13.74 __PSB_DT_100ns_1M_12V, 13.77 __PSA_DT_100ns_1M_14V, 13.78 __PSB_DT_100ns_1M_14V
TD _{SP} Secondary off to primary on dead time	SP = floating, 5 V ≤ VIN ≤ 14 V, 90% of SRx falling edge to 10% of OUTx rising edge with OUTx and SRx floating	5	8	11	ns	13.39 __SPA_DT_0ns_1M_5V, 13.40 __SPB_DT_0ns_1M_5V, 13.43 __SPA_DT_0ns_1M_12V, 13.44 __SPB_DT_0ns_1M_12V, 13.47 __SPA_DT_0ns_1M_14V, 13.48 __SPB_DT_0ns_1M_14V
	SP = 49.9 kΩ, 5 V ≤ VIN ≤ 14 V, 90% of SRx falling edge to 10% of OUTx rising edge with OUTx and SRx floating	43	50	55	ns	13.55 __SPA_DT_50ns_1M_5V, 13.56 __SPB_DT_50ns_1M_5V, 13.59 __SPA_DT_50ns_1M_12V, 13.60 __SPB_DT_50ns_1M_12V, 13.63 __SPA_DT_50ns_1M_14V, 13.64 __SPB_DT_50ns_1M_14V
	SP = 100 kΩ, 5 V ≤ VIN ≤ 14 V, 90% of SRx falling edge to 10% of OUTx rising edge with OUTx and SRx floating	85	100	110	ns	13.71 __SPA_DT_100ns_1M_5V, 13.72 __SPB_DT_100ns_1M_5V, 13.75 __SPA_DT_100ns_1M_12V, 13.76 __SPB_DT_100ns_1M_12V, 13.79 __SPA_DT_100ns_1M_14V, 13.80 __SPB_DT_100ns_1M_14V
DUTY CYCLE						
D _{MAX} Maximum duty cycle	DCL = AVSS	45	48	50	%	13.256 __MAX_DC_DCL_AVSS
	DCL = floating	70	75	80	%	13.259 __MAX_DC_DCL_OPEN
	DCL = VLDO			100	%	13.262 __MAX_DC_DCL_VLDO

4 Applicable and Reference Documents

4.1 Applicable Documents

- Texas Instruments, [TPS7H500x-SP Radiation-Hardness-Assured 2-MHz Current-Mode PWM Controllers data sheet](#)
- Texas Instruments, [TPS7H5001EVM-CVAL Evaluation Module user's guide](#)
- Texas Instruments, [TPS7H5001-SP Single-Event Effects \(SEE\) radiation report](#)
- Texas Instruments, [TPS7H5001-SP Neutron Displacement Damage \(NDD\) Characterization radiation report](#)

4.2 Reference Documents

Texas Instruments total ionizing dose radiation (total dose) test procedure follows the standards put forth in MIL-STD-883 TM 1019. The document can be found at the DLA website.

5 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Revision * (July 2021) to Revision A (February 2022)	Page
• Added LDR data throughout the document.....	1

A Appendix: Specifications Requiring Clarification

Table A-1. Specifications Requiring Clarification - Set #1

Data sheet specifications for which there is no parametric data and, therefore, not in the TID report. Functionality is assured by ATE testing.

PARAMETER	TEST CONDITION	TPS7H5001-SP DATA SHEET			
		MIN	TYP	MAX	UNIT
V _{IN} Operating voltage		4		14	V
F _{SYNC} SYNC in frequency range		200		4000	kHz
D _{SYNC} SYNC in duty cycle	Duty cycle of external clock	40		60	%

Table A-2. Specifications Requiring Clarification - Set #2

Data sheet specifications for which no data is available in TID report as specification was verified through bench testing. TID testing is by ATE.

PARAMETER	TEST CONDITION	TPS7H5001-SP DATA SHEET			
		MIN	TYP	MAX	UNIT
E _{A_{DC}} DC gain	V _{SENSE} = 0.6 V		10000		V/V
E _{A_{r_o}} Error amplifier output resistance			7		MΩ
E _{A_{I_B}} Error amplifier bias current				35	nA
E _{A_{B_W}} Bandwidth			10		MHz
S _{Y_NC_{I_L}} SYNC in low-level	V _{IN} < 5 V			0.8	V
	V _{IN} ≥ 5 V			0.8	V
S _{Y_NC_{I_H}} SYNC in high-level	V _{IN} < 5 V		3.5		V
	V _{IN} ≥ 5 V		3.5		V
(V _{LDO} - S _{Y_NC_{O_H}}) SYNC out high level	I _{O_H} = 10 mA			0.5	V

Table A-3. Specifications Requiring Clarification - Set #3

Data sheet specifications for which there is no 'MIN'/'MAX' value, only 'TYP', therefore, not in the TID report. Functionality is assured by ATE testing.

PARAMETER	TEST CONDITION	TPS7H5001-SP DATA SHEET			
		MIN	TYP	MAX	UNIT
I _{HICC_DEL} Hiccup delay current	C _{S_LIM} = 1.3 V, C _{OMP} = 3 V, V _{SENSE} = REF _{CAP} /2 V, C _{HICC} = 3 nF, L _{EB} = 49.9 kΩ, f _{SW} = 100 kHz		80		μA
I _{HICC_RST} Hiccup restart current			1		μA
V _{HICC_PU} Hiccup pull-up threshold			1.0		V
V _{HICC_SD} Hiccup shut-down threshold			0.6		V
V _{HICC_RS} T Hiccup restart threshold			0.3		V
Slope compensation	f _{SW} = 100 kHz, R _{SC} = 1.18 MΩ		0.033		V/μs
	f _{SW} = 200 kHz, R _{SC} = 562 kΩ		0.066		V/μs
	f _{SW} = 1000 kHz, R _{SC} = 100 kΩ		0.333		V/μs
	f _{SW} = 2000 kHz, R _{SC} = 49.9 kΩ		0.666		V/μs
PRIMARY AND SYNCHRONOUS RECTIFIER OUTPUTS Low-level threshold	I _{SINK} = 10 mA		0.5		V

Table A-3. Specifications Requiring Clarification - Set #3 (continued)

Data sheet specifications for which there is no 'MIN'/'MAX' value, only 'TYP', therefore, not in the TID report. Functionality is assured by ATE testing.

PARAMETER	TEST CONDITION	TPS7H5001-SP DATA SHEET			
		MIN	TYP	MAX	UNIT
PRIMARY AND SYNCHRONOUS RECTIFIER OUTPUTS High-level threshold	$I_{SOURCE} = 10 \text{ mA}$		4.5		V
R _{SRC_P} Output source resistance	$I_{OUT} = 20 \text{ mA}$		15		Ω
R _{SINK_P} Output sink resistance	$I_{OUT} = 20 \text{ mA}$		15		Ω

Table A-4. Specifications Requiring Clarification - Set #4

Data sheet specifications that are not covered in TID report because specification condition is at temperature other than 25°C. TID testing is at 25°C only.

PARAMETER	TEST CONDITION	TPS7H5001-SP DATA SHEET			
		MIN	TYP	MAX	UNIT
VREF Internal voltage reference	Measured at COMP, -55°C	0.607	0.609	0.612	V
	Measured at COMP, +125°C	0.611	0.614	0.617	V

B Appendix: HDR TID Report Data

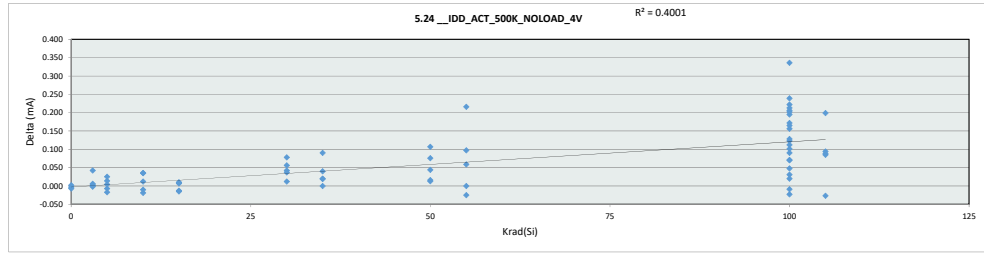
This appendix contains the HDR TID report data.

HDR TID REPORT
TPS7H5001-SP

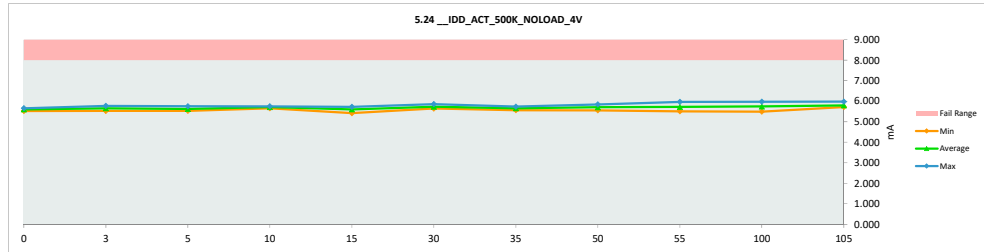
IDENTIFIER	DESCRIPTION
0	Control Unit
3	3 krad(Si) biased
5	3 krad(Si) unbiased
10	10 krad(Si) biased
15	10 krad(Si) unbiased
30	30 krad(Si) biased
35	30 krad(Si) unbiased
50	50 krad(Si) biased
55	50 krad(Si) unbiased
100	100 krad(Si) biased
105	100 krad(Si) unbiased

HDR TID Report
TPS7H5001-SP

		5.24 IDD_ACT_500K_NOLOAD_4V		
Test Site				
Tester				
Test Number				
Unit		mA	mA	
Max Limit		8	8	
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	5.531	5.524	-0.007
0	118	5.628	5.630	0.002
0	122	5.656	5.653	-0.003
3	301	5.627	5.633	0.006
3	306	5.589	5.589	0.000
3	307	5.752	5.755	0.003
3	308	5.486	5.528	0.042
3	309	5.776	5.774	-0.002
5	310	5.592	5.575	-0.017
5	311	5.528	5.532	0.004
5	312	5.666	5.680	0.014
5	314	5.727	5.752	0.025
5	316	5.573	5.566	-0.007
10	317	5.737	5.749	0.012
10	318	5.758	5.739	-0.019
10	319	5.620	5.655	0.035
10	321	5.742	5.732	-0.010
10	322	5.656	5.691	0.035
15	323	5.408	5.417	0.009
15	325	5.540	5.527	-0.013
15	326	5.629	5.636	0.007
15	328	5.680	5.690	0.010
15	333	5.740	5.725	-0.015
30	334	5.815	5.857	0.042
30	335	5.608	5.645	0.037
30	337	5.590	5.646	0.056
30	339	5.753	5.831	0.078
30	341	5.683	5.695	0.012
35	342	5.692	5.692	0.000
35	345	5.630	5.670	0.040
35	346	5.599	5.619	0.020
35	347	5.647	5.737	0.090
35	348	5.545	5.564	0.019
50	349	5.692	5.736	0.044
50	350	5.738	5.845	0.107
50	351	5.629	5.705	0.076
50	353	5.672	5.685	0.013
50	354	5.539	5.555	0.016
55	355	5.532	5.507	-0.025
55	357	5.749	5.965	0.216
55	359	5.647	5.706	0.059
55	362	5.603	5.603	0.000
55	363	5.732	5.829	0.097
100	1	5.595	5.790	0.195
100	3	5.573	5.550	-0.023
100	4	5.697	5.869	0.172
100	5	5.635	5.857	0.222
100	6	5.565	5.767	0.202
100	7	5.643	5.744	0.101
100	8	5.585	5.576	-0.009
100	9	5.620	5.690	0.070
100	10	5.690	5.896	0.206
100	11	5.791	5.947	0.156
100	12	5.461	5.492	0.031
100	13	5.699	5.822	0.123
100	14	5.609	5.774	0.165
100	15	5.734	5.973	0.239
100	16	5.664	5.735	0.071
100	17	5.404	5.516	0.112
100	18	5.539	5.629	0.090
100	19	5.602	5.650	0.048
100	20	5.679	5.892	0.213
100	21	5.569	5.589	0.020
100	22	5.557	5.893	0.336
100	24	5.596	5.724	0.128
105	25	5.788	5.761	-0.027
105	26	5.671	5.759	0.088
105	27	5.660	5.754	0.094
105	28	5.632	5.717	0.085
105	29	5.780	5.979	0.199
	Max	5.815	5.979	0.336
	Average	5.640	5.703	0.063
	Min	5.404	5.417	-0.027
	Std Dev	0.089	0.126	0.080

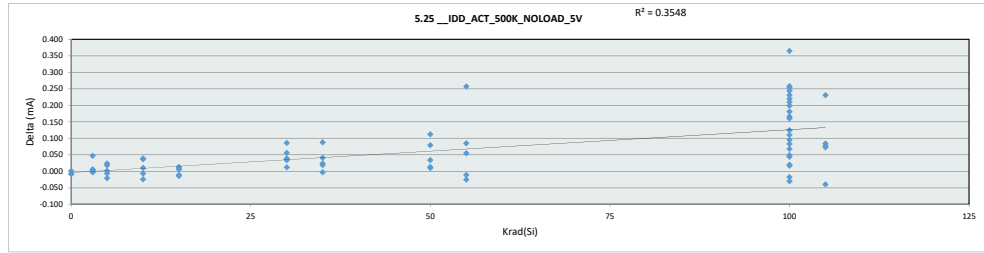


		5.24 IDD_ACT_500K_NOLO										
Test Site												
Tester												
Test Number												
Max Limit		8	mA									
Min Limit			mA									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min	5.524	5.528	5.532	5.655	5.417	5.645	5.564	5.555	5.507	5.492	5.717	5.717
Average	5.602	5.656	5.621	5.713	5.599	5.735	5.656	5.705	5.722	5.744	5.794	5.794
Max	5.653	5.774	5.752	5.749	5.725	5.857	5.737	5.845	5.965	5.973	5.979	5.979
UL	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000

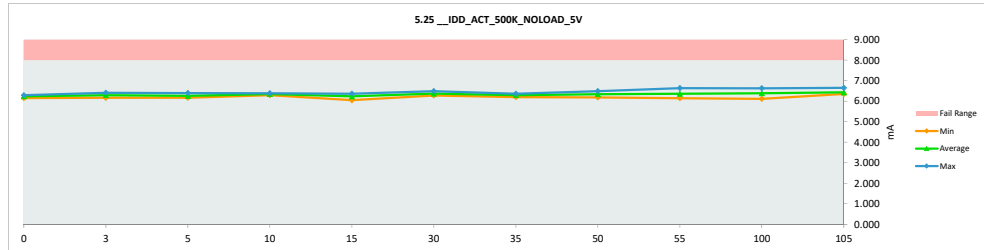


HDR TID Report
TPS7H5001-SP

		5.25 IDD_ACT_500K_NOLOAD_5V		
Test Site	Tester			
Test Number	Unit	mA	mA	
Max Limit	8	8		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	6.165	6.157	-0.008
0	118	6.263	6.264	0.001
0	122	6.300	6.292	-0.008
3	301	6.268	6.274	0.006
3	306	6.226	6.223	-0.003
3	307	6.399	6.401	0.002
3	308	6.120	6.167	0.047
3	309	6.414	6.415	0.001
5	310	6.231	6.210	-0.021
5	311	6.170	6.171	0.001
5	312	6.304	6.322	0.018
5	314	6.372	6.396	0.024
5	316	6.210	6.203	-0.007
10	317	6.377	6.387	0.010
10	318	6.404	6.380	-0.024
10	319	6.260	6.299	0.039
10	321	6.366	6.359	-0.007
10	322	6.294	6.331	0.037
15	323	6.037	6.047	0.010
15	325	6.175	6.164	-0.011
15	326	6.265	6.271	0.006
15	328	6.318	6.331	0.013
15	333	6.377	6.363	-0.014
30	334	6.452	6.491	0.039
30	335	6.244	6.278	0.034
30	337	6.231	6.287	0.056
30	339	6.386	6.472	0.086
30	341	6.322	6.334	0.012
35	342	6.333	6.330	-0.003
35	345	6.267	6.308	0.041
35	346	6.241	6.265	0.024
35	347	6.281	6.369	0.088
35	348	6.186	6.205	0.019
50	349	6.331	6.365	0.034
50	350	6.379	6.491	0.112
50	351	6.265	6.344	0.079
50	353	6.313	6.323	0.010
50	354	6.173	6.186	0.013
55	355	6.170	6.145	-0.025
55	357	6.384	6.641	0.257
55	359	6.286	6.341	0.055
55	362	6.249	6.238	-0.011
55	363	6.374	6.459	0.085
100	1	6.235	6.455	0.220
100	3	6.213	6.183	-0.030
100	4	6.331	6.530	0.199
100	5	6.268	6.478	0.210
100	6	6.195	6.426	0.231
100	7	6.278	6.373	0.095
100	8	6.230	6.212	-0.018
100	9	6.259	6.327	0.068
100	10	6.326	6.579	0.253
100	11	6.432	6.598	0.166
100	12	6.095	6.115	0.020
100	13	6.321	6.481	0.160
100	14	6.247	6.428	0.181
100	15	6.373	6.631	0.258
100	16	6.307	6.356	0.049
100	17	6.035	6.145	0.110
100	18	6.177	6.260	0.083
100	19	6.235	6.279	0.044
100	20	6.322	6.566	0.244
100	21	6.210	6.227	0.017
100	22	6.189	6.554	0.365
100	24	6.239	6.363	0.124
105	25	6.430	6.390	-0.040
105	26	6.310	6.387	0.077
105	27	6.296	6.369	0.073
105	28	6.269	6.353	0.084
105	29	6.419	6.650	0.231
	Max	6.452	6.650	0.365
	Average	6.277	6.343	0.066
	Min	6.035	6.047	-0.040
	Std Dev	0.090	0.134	0.089

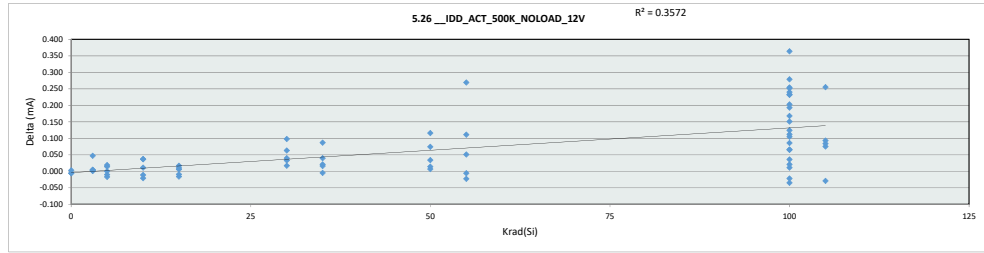


		5.25 IDD_ACT_500K_NOLO									
Test Site	Tester										
Test Number	Unit	8	mA								
Max Limit	8	mA									
Min Limit											
Krad(Si)	LL	3	5	10	15	30	35	50	55	100	105
Min	6.157	6.167	6.171	6.299	6.047	6.278	6.205	6.186	6.145	6.115	6.353
Average	6.238	6.296	6.260	6.351	6.235	6.372	6.295	6.342	6.365	6.389	6.430
Max	6.292	6.415	6.396	6.387	6.363	6.491	6.369	6.491	6.641	6.631	6.650
UL	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000

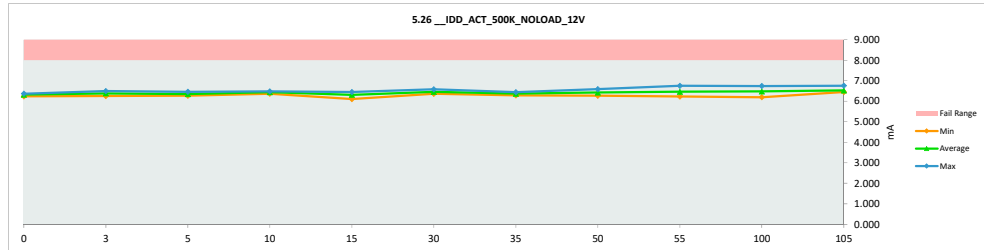


HDR TID Report
TPS7H5001-SP

		5.26 IDD_ACT_500K_NOLOAD_12V		
Test Site				
Tester				
Test Number				
Unit		mA	mA	
Max Limit		8	8	
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	6.237	6.231	-0.006
0	118	6.357	6.360	0.003
0	122	6.371	6.365	-0.006
3	301	6.364	6.368	0.004
3	306	6.304	6.305	0.001
3	307	6.471	6.476	0.005
3	308	6.205	6.252	0.047
3	309	6.492	6.494	0.002
5	310	6.304	6.287	-0.017
5	311	6.267	6.267	0.000
5	312	6.407	6.421	0.014
5	314	6.437	6.456	0.019
5	316	6.285	6.275	-0.010
10	317	6.460	6.471	0.011
10	318	6.480	6.459	-0.021
10	319	6.326	6.363	0.037
10	321	6.486	6.475	-0.011
10	322	6.381	6.418	0.037
15	323	6.098	6.104	0.006
15	325	6.244	6.235	-0.009
15	326	6.341	6.351	0.010
15	328	6.400	6.417	0.017
15	333	6.460	6.444	-0.016
30	334	6.532	6.572	0.040
30	335	6.328	6.361	0.033
30	337	6.295	6.358	0.063
30	339	6.478	6.576	0.098
30	341	6.391	6.408	0.017
35	342	6.406	6.401	-0.005
35	345	6.352	6.392	0.040
35	346	6.314	6.335	0.021
35	347	6.352	6.439	0.087
35	348	6.262	6.278	0.016
50	349	6.418	6.452	0.034
50	350	6.468	6.584	0.116
50	351	6.329	6.403	0.074
50	353	6.391	6.405	0.014
50	354	6.256	6.263	0.007
55	355	6.252	6.229	-0.023
55	357	6.479	6.748	0.269
55	359	6.372	6.423	0.051
55	362	6.334	6.328	-0.006
55	363	6.460	6.571	0.111
100	1	6.318	6.558	0.240
100	3	6.290	6.255	-0.035
100	4	6.406	6.609	0.203
100	5	6.357	6.589	0.232
100	6	6.282	6.515	0.233
100	7	6.376	6.481	0.105
100	8	6.298	6.276	-0.022
100	9	6.326	6.392	0.066
100	10	6.400	6.654	0.254
100	11	6.534	6.702	0.168
100	12	6.170	6.191	0.021
100	13	6.422	6.573	0.151
100	14	6.327	6.520	0.193
100	15	6.458	6.737	0.279
100	16	6.370	6.435	0.065
100	17	6.121	6.233	0.112
100	18	6.264	6.350	0.086
100	19	6.325	6.361	0.036
100	20	6.405	6.657	0.252
100	21	6.282	6.293	0.011
100	22	6.262	6.626	0.364
100	24	6.336	6.460	0.124
105	25	6.521	6.492	-0.029
105	26	6.397	6.472	0.075
105	27	6.363	6.447	0.084
105	28	6.352	6.445	0.093
105	29	6.500	6.755	0.255
	Max	6.534	6.755	0.364
	Average	6.359	6.427	0.068
	Min	6.098	6.104	-0.035
	Std Dev	0.093	0.141	0.093

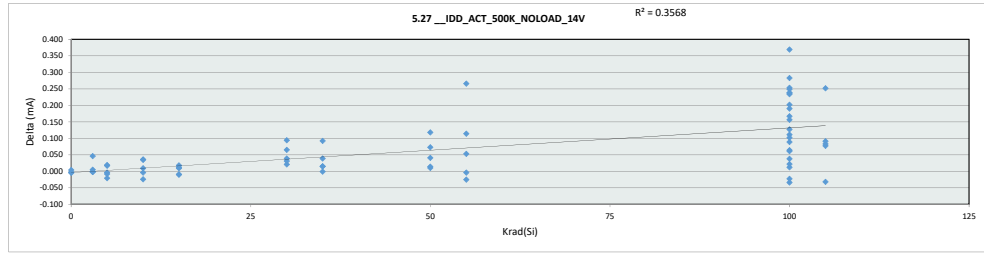


		5.26 __IDD_ACT_500K_NOLO									
Test Site											
Tester											
Test Number											
Max Limit		8	mA								
Min Limit			mA								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.231	6.252	6.267	6.363	6.104	6.358	6.278	6.263	6.229	6.191	6.445
Average	6.319	6.379	6.341	6.437	6.310	6.455	6.369	6.421	6.460	6.476	6.522
Max	6.365	6.494	6.456	6.475	6.444	6.576	6.439	6.584	6.748	6.737	6.755
UL	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000

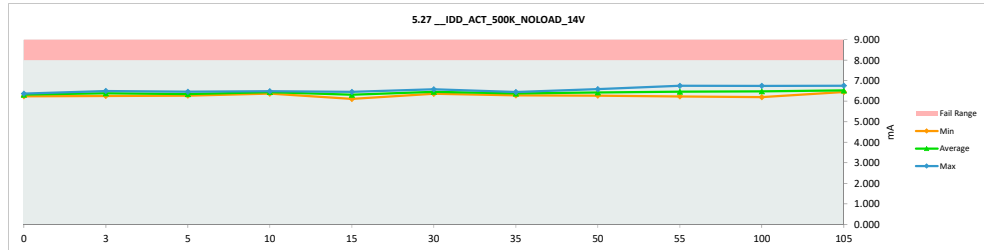


HDR TID Report
TPS7H5001-SP

		5.27 IDD ACT 500K NOLOAD 14V		
Test Site				
Tester				
Test Number				
Unit		mA	mA	
Max Limit		8	8	
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	6.241	6.238	-0.003
0	118	6.358	6.362	0.004
0	122	6.375	6.370	-0.005
3	301	6.369	6.374	0.005
3	306	6.311	6.309	-0.002
3	307	6.478	6.477	-0.001
3	308	6.208	6.254	0.046
3	309	6.497	6.495	-0.002
5	310	6.310	6.289	-0.021
5	311	6.274	6.270	-0.004
5	312	6.411	6.428	0.017
5	314	6.443	6.462	0.019
5	316	6.287	6.279	-0.008
10	317	6.467	6.476	0.009
10	318	6.487	6.463	-0.024
10	319	6.333	6.368	0.035
10	321	6.487	6.483	-0.004
10	322	6.388	6.424	0.036
15	323	6.099	6.111	0.012
15	325	6.248	6.239	-0.009
15	326	6.345	6.354	0.009
15	328	6.403	6.421	0.018
15	333	6.462	6.452	-0.010
30	334	6.537	6.576	0.039
30	335	6.331	6.362	0.031
30	337	6.298	6.363	0.065
30	339	6.485	6.579	0.094
30	341	6.392	6.413	0.021
35	342	6.412	6.411	-0.001
35	345	6.356	6.395	0.039
35	346	6.321	6.336	0.015
35	347	6.354	6.446	0.092
35	348	6.265	6.280	0.015
50	349	6.421	6.462	0.041
50	350	6.468	6.586	0.118
50	351	6.333	6.406	0.073
50	353	6.395	6.409	0.014
50	354	6.261	6.271	0.010
55	355	6.257	6.232	-0.025
55	357	6.483	6.749	0.266
55	359	6.376	6.429	0.053
55	362	6.337	6.333	-0.004
55	363	6.463	6.577	0.114
100	1	6.324	6.561	0.237
100	3	6.292	6.258	-0.034
100	4	6.411	6.613	0.202
100	5	6.362	6.596	0.234
100	6	6.285	6.523	0.238
100	7	6.383	6.485	0.102
100	8	6.304	6.281	-0.023
100	9	6.334	6.398	0.064
100	10	6.406	6.655	0.249
100	11	6.540	6.707	0.167
100	12	6.173	6.195	0.022
100	13	6.425	6.581	0.156
100	14	6.331	6.521	0.190
100	15	6.464	6.747	0.283
100	16	6.375	6.436	0.061
100	17	6.127	6.238	0.111
100	18	6.266	6.355	0.089
100	19	6.327	6.365	0.038
100	20	6.409	6.662	0.253
100	21	6.288	6.300	0.012
100	22	6.268	6.637	0.369
100	24	6.339	6.466	0.127
105	25	6.527	6.495	-0.032
105	26	6.399	6.476	0.077
105	27	6.369	6.452	0.083
105	28	6.356	6.447	0.091
105	29	6.503	6.755	0.252
	Max	6.540	6.755	0.369
	Average	6.363	6.432	0.069
	Min	6.099	6.111	-0.034
	Std Dev	0.093	0.141	0.093

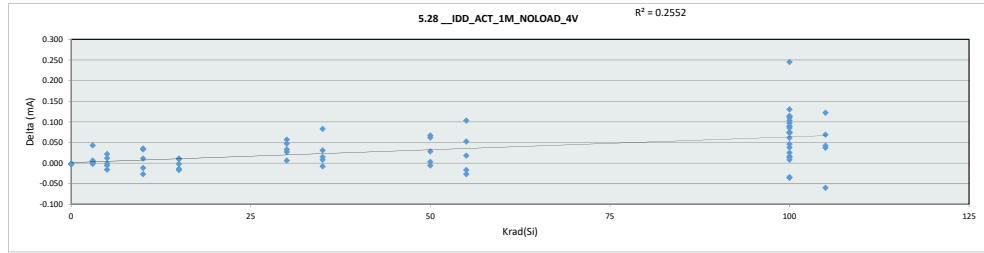


		5.27 IDD ACT 500K NOLO										
Test Site												
Tester												
Test Number												
Max Limit		8	mA									
Min Limit			mA									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		6.238	6.254	6.270	6.368	6.111	6.362	6.280	6.271	6.232	6.195	6.447
Average		6.323	6.382	6.346	6.443	6.315	6.459	6.374	6.427	6.464	6.481	6.525
Max		6.370	6.495	6.462	6.483	6.452	6.579	6.446	6.586	6.749	6.747	6.755
UL		8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000

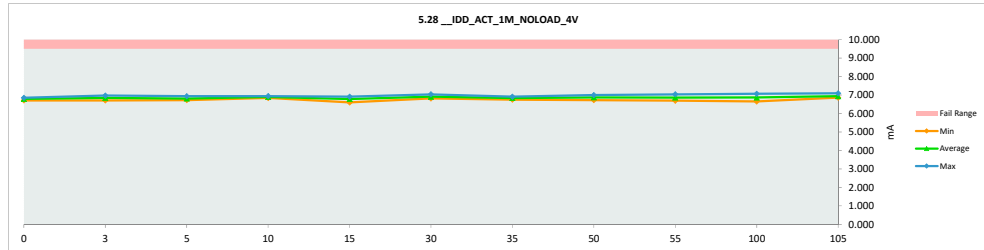


HDR TID Report
TPS7H5001-SP

		5.28 IDD_ACT_1M_NOLOAD_4V		
Test Site	Tester			
Test Number	Unit	mA	mA	
Max Limit	9.5		9.5	
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	6.713	6.710	-0.003
0	118	6.810	6.809	-0.001
0	122	6.852	6.849	-0.003
3	301	6.819	6.825	0.006
3	306	6.774	6.772	-0.002
3	307	6.950	6.952	0.002
3	308	6.667	6.710	0.043
3	309	6.970	6.971	0.001
5	310	6.782	6.766	-0.016
5	311	6.726	6.725	-0.001
5	312	6.859	6.871	0.012
5	314	6.916	6.938	0.022
5	316	6.759	6.753	-0.006
10	317	6.932	6.943	0.011
10	318	6.954	6.927	-0.027
10	319	6.807	6.842	0.035
10	321	6.893	6.881	-0.012
10	322	6.844	6.877	0.033
15	323	6.592	6.601	0.009
15	325	6.729	6.715	-0.014
15	326	6.820	6.818	-0.002
15	328	6.870	6.881	0.011
15	333	6.928	6.911	-0.017
30	334	7.006	7.039	0.033
30	335	6.790	6.817	0.027
30	337	6.778	6.835	0.057
30	339	6.942	6.989	0.047
30	341	6.871	6.877	0.006
35	342	6.884	6.876	-0.008
35	345	6.816	6.847	0.031
35	346	6.792	6.807	0.015
35	347	6.828	6.911	0.083
35	348	6.742	6.750	0.008
50	349	6.873	6.901	0.028
50	350	6.935	6.997	0.062
50	351	6.817	6.884	0.067
50	353	6.868	6.862	-0.006
50	354	6.725	6.728	0.003
55	355	6.721	6.694	-0.027
55	357	6.932	7.035	0.103
55	359	6.832	6.884	0.052
55	362	6.792	6.775	-0.017
55	363	6.924	6.942	0.018
100	1	6.786	6.875	0.089
100	3	6.753	6.717	-0.036
100	4	6.874	6.947	0.073
100	5	6.824	6.938	0.114
100	6	6.745	6.848	0.103
100	7	6.833	6.894	0.061
100	8	6.782	6.748	-0.034
100	9	6.814	6.860	0.046
100	10	6.870	6.955	0.085
100	11	6.976	7.066	0.090
100	12	6.639	6.653	0.014
100	13	6.870	6.895	0.025
100	14	6.797	6.871	0.074
100	15	6.922	7.052	0.130
100	16	6.852	6.868	0.016
100	17	6.593	6.690	0.097
100	18	6.718	6.793	0.075
100	19	6.782	6.820	0.038
100	20	6.867	6.981	0.114
100	21	6.763	6.771	0.008
100	22	6.735	6.980	0.245
100	24	6.789	6.899	0.110
105	25	6.974	6.914	-0.060
105	26	6.855	6.924	0.069
105	27	6.849	6.891	0.042
105	28	6.820	6.857	0.037
105	29	6.972	7.094	0.122
	Max	7.006	7.094	0.245
	Average	6.827	6.861	0.034
	Min	6.592	6.601	-0.060
	Std Dev	0.090	0.102	0.050

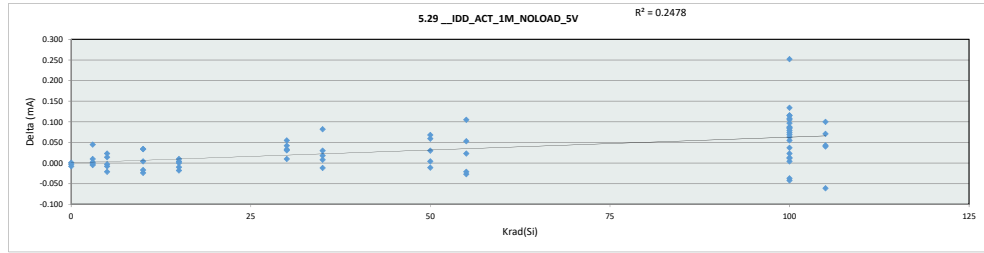


		5.28 IDD_ACT_1M_NOLOAD									
Test Site	Tester										
Test Number	Unit	mA	mA								
Max Limit	9.5										
Min Limit											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.710	6.710	6.725	6.842	6.601	6.817	6.750	6.728	6.694	6.653	6.857
Average	6.789	6.846	6.811	6.894	6.785	6.911	6.838	6.874	6.866	6.869	6.936
Max	6.849	6.971	6.938	6.943	6.911	7.039	6.911	6.997	7.035	7.066	7.094
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

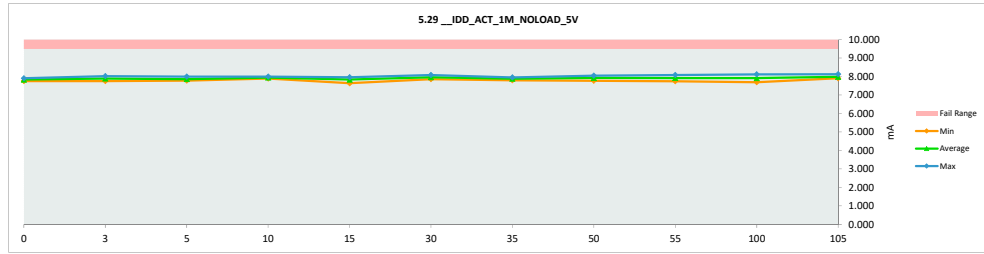


HDR TID Report
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		5.29 IDD_ACT_1M_NOLOAD_5V		
Test Site	Tester			
Test Number	Unit	mA	mA	
Max Limit	9.5	9.5		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	7.763	7.755	-0.008
0	118	7.855	7.856	0.001
0	122	7.910	7.907	-0.003
3	301	7.867	7.877	0.010
3	306	7.825	7.820	-0.005
3	307	8.009	8.011	0.002
3	308	7.711	7.756	0.045
3	309	8.026	8.024	-0.002
5	310	7.834	7.813	-0.021
5	311	7.784	7.781	-0.003
5	312	7.916	7.930	0.014
5	314	7.976	7.999	0.023
5	316	7.807	7.799	-0.008
10	317	7.993	7.997	0.004
10	318	8.012	7.988	-0.024
10	319	7.863	7.897	0.034
10	321	7.942	7.925	-0.017
10	322	7.893	7.927	0.034
15	323	7.636	7.640	0.004
15	325	7.775	7.765	-0.010
15	326	7.868	7.868	0.000
15	328	7.931	7.941	0.010
15	333	7.982	7.964	-0.018
30	334	8.056	8.089	0.033
30	335	7.833	7.864	0.031
30	337	7.830	7.885	0.055
30	339	7.997	8.039	0.042
30	341	7.921	7.931	0.010
35	342	7.937	7.925	-0.012
35	345	7.864	7.894	0.030
35	346	7.848	7.866	0.018
35	347	7.873	7.955	0.082
35	348	7.795	7.803	0.008
50	349	7.922	7.952	0.030
50	350	7.996	8.055	0.059
50	351	7.866	7.934	0.068
50	353	7.925	7.914	-0.011
50	354	7.773	7.777	0.004
55	355	7.772	7.745	-0.027
55	357	7.983	8.088	0.105
55	359	7.881	7.934	0.053
55	362	7.849	7.828	-0.021
55	363	7.978	8.001	0.023
100	1	7.835	7.922	0.087
100	3	7.799	7.757	-0.042
100	4	7.914	7.993	0.079
100	5	7.870	7.986	0.116
100	6	7.785	7.890	0.105
100	7	7.883	7.946	0.063
100	8	7.836	7.799	-0.037
100	9	7.863	7.918	0.055
100	10	7.915	8.002	0.087
100	11	8.036	8.120	0.084
100	12	7.685	7.697	0.012
100	13	7.922	7.945	0.023
100	14	7.845	7.919	0.074
100	15	7.974	8.108	0.134
100	16	7.909	7.922	0.013
100	17	7.635	7.732	0.097
100	18	7.768	7.837	0.069
100	19	7.823	7.860	0.037
100	20	7.921	8.036	0.115
100	21	7.819	7.823	0.004
100	22	7.775	8.027	0.252
100	24	7.846	7.954	0.108
105	25	8.033	7.972	-0.061
105	26	7.906	7.977	0.071
105	27	7.900	7.943	0.043
105	28	7.869	7.909	0.040
105	29	8.027	8.127	0.100
Max		8.056	8.127	0.252
Average		7.878	7.912	0.034
Min		7.635	7.640	-0.061
Std Dev		0.093	0.104	0.051

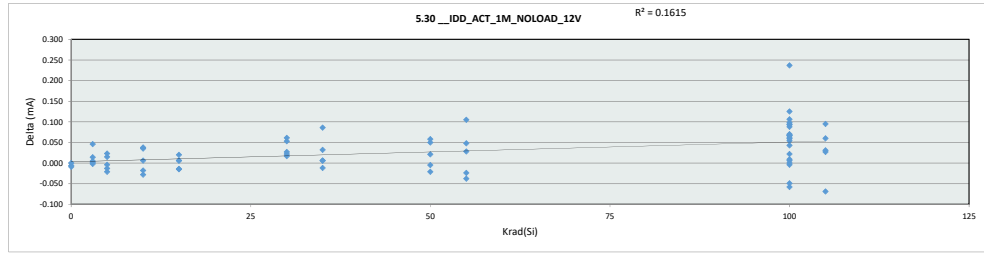


		5.29 IDD_ACT_1M_NOLOAD										
Test Site	Tester											
Test Number	Unit	9.5	mA									
Max Limit	9.5	mA										
Min Limit												
Krad(Si)	LL	Min	Average	Max	UL							
0		7.755	7.756	7.781	7.897	7.640	7.864	7.803	7.777	7.745	7.697	7.909
3		7.839	7.898	7.864	7.947	7.836	7.962	7.889	7.926	7.919	7.918	7.986
5		7.907	8.024	7.999	7.997	7.964	8.089	7.955	8.055	8.088	8.120	8.127
10		9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

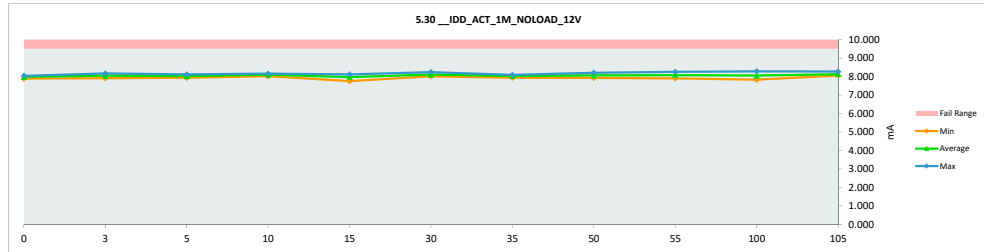


HDR TID Report
TPS7H5001-SP

5.30 IDD_ACT_1M_NOLOAD_12V				
Test Site				
Tester				
Test Number				
Unit	mA		mA	
Max Limit	9.5		9.5	
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	7.899	7.890	-0.009
0	118	8.023	8.023	0.000
0	122	8.047	8.041	-0.006
3	301	8.035	8.049	0.014
3	306	7.973	7.971	-0.002
3	307	8.140	8.141	0.001
3	308	7.863	7.909	0.046
3	309	8.164	8.169	0.005
5	310	7.974	7.953	-0.021
5	311	7.957	7.953	-0.004
5	312	8.102	8.117	0.015
5	314	8.094	8.117	0.023
5	316	7.944	7.931	-0.013
10	317	8.147	8.153	0.006
10	318	8.157	8.129	-0.028
10	319	7.982	8.020	0.038
10	321	8.127	8.109	-0.018
10	322	8.055	8.090	0.035
15	323	7.747	7.753	0.006
15	325	7.907	7.892	-0.015
15	326	8.006	8.012	0.006
15	328	8.077	8.097	0.020
15	333	8.130	8.116	-0.014
30	334	8.205	8.232	0.027
30	335	7.985	8.007	0.022
30	337	7.952	8.013	0.061
30	339	8.155	8.208	0.053
30	341	8.048	8.065	0.017
35	342	8.075	8.063	-0.012
35	345	8.017	8.049	0.032
35	346	7.987	7.993	0.006
35	347	8.005	8.091	0.086
35	348	7.930	7.936	0.006
50	349	8.079	8.100	0.021
50	350	8.154	8.204	0.050
50	351	7.980	8.038	0.058
50	353	8.066	8.045	-0.021
50	354	7.925	7.920	-0.005
55	355	7.920	7.896	-0.024
55	357	8.150	8.255	0.105
55	359	8.040	8.088	0.048
55	362	8.010	7.972	-0.038
55	363	8.133	8.161	0.028
100	1	7.992	8.062	0.070
100	3	7.942	7.884	-0.058
100	4	8.056	8.115	0.059
100	5	8.033	8.139	0.106
100	6	7.939	8.032	0.093
100	7	8.062	8.116	0.054
100	8	7.967	7.918	-0.049
100	9	7.986	8.029	0.043
100	10	8.052	8.119	0.067
100	11	8.217	8.283	0.066
100	12	7.822	7.828	0.006
100	13	8.086	8.095	0.009
100	14	7.990	8.058	0.068
100	15	8.134	8.259	0.125
100	16	8.031	8.031	0.000
100	17	7.794	7.888	0.094
100	18	7.923	7.984	0.061
100	19	7.983	8.005	0.022
100	20	8.073	8.171	0.098
100	21	7.956	7.952	-0.004
100	22	7.911	8.148	0.237
100	24	8.018	8.106	0.088
105	25	8.199	8.130	-0.069
105	26	8.063	8.123	0.060
105	27	8.031	8.058	0.027
105	28	8.028	8.059	0.031
105	29	8.178	8.273	0.095
	Max	8.217	8.283	0.237
	Average	8.026	8.055	0.029
	Min	7.747	7.753	-0.069
	Std Dev	0.097	0.109	0.049

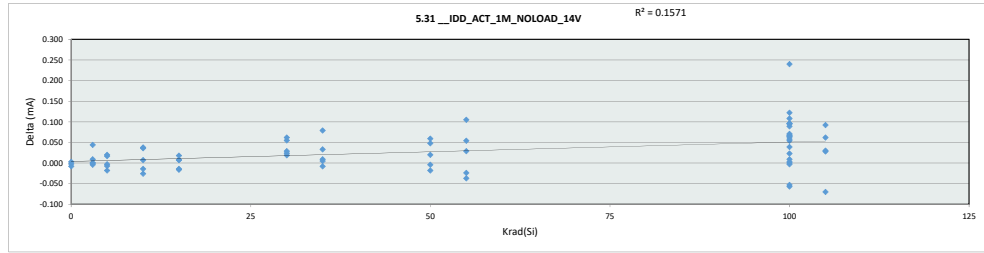


5.30 IDD_ACT_1M_NOLOAD											
Test Site											
Tester											
Test Number											
Max Limit	9.5		mA								
Min Limit			mA								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.890	7.909	7.931	8.020	7.753	8.007	7.936	7.920	7.896	7.828	8.058
Average	7.985	8.048	8.014	8.100	7.974	8.105	8.026	8.061	8.074	8.056	8.129
Max	8.041	8.169	8.117	8.153	8.116	8.232	8.091	8.204	8.255	8.283	8.273
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

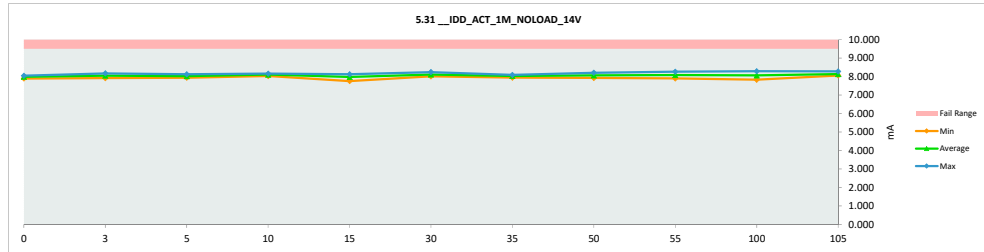


HDR TID Report
TPS7H5001-SP

		5.31 IDD_ACT_1M_NOLOAD_14V		
Test Site				
Tester				
Test Number				
Unit		mA	mA	
Max Limit		9.5	9.5	
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	7.902	7.894	-0.008
0	118	8.026	8.029	0.003
0	122	8.048	8.046	-0.002
3	301	8.040	8.049	0.009
3	306	7.974	7.974	0.000
3	307	8.147	8.143	-0.004
3	308	7.869	7.913	0.044
3	309	8.170	8.170	0.000
5	310	7.975	7.957	-0.018
5	311	7.960	7.957	-0.003
5	312	8.102	8.122	0.020
5	314	8.100	8.117	0.017
5	316	7.945	7.938	-0.007
10	317	8.148	8.155	0.007
10	318	8.160	8.134	-0.026
10	319	7.988	8.026	0.038
10	321	8.131	8.117	-0.014
10	322	8.060	8.096	0.036
15	323	7.749	7.757	0.008
15	325	7.909	7.895	-0.014
15	326	8.009	8.016	0.007
15	328	8.080	8.098	0.018
15	333	8.136	8.120	-0.016
30	334	8.210	8.239	0.029
30	335	7.988	8.012	0.024
30	337	7.957	8.019	0.062
30	339	8.159	8.214	0.055
30	341	8.053	8.072	0.019
35	342	8.078	8.070	-0.008
35	345	8.021	8.054	0.033
35	346	7.991	8.000	0.009
35	347	8.011	8.090	0.079
35	348	7.936	7.941	0.005
50	349	8.083	8.103	0.020
50	350	8.157	8.205	0.048
50	351	7.983	8.042	0.059
50	353	8.069	8.051	-0.018
50	354	7.927	7.923	-0.004
55	355	7.923	7.899	-0.024
55	357	8.154	8.259	0.105
55	359	8.040	8.094	0.054
55	362	8.013	7.976	-0.037
55	363	8.136	8.165	0.029
100	1	7.995	8.063	0.068
100	3	7.945	7.888	-0.057
100	4	8.061	8.119	0.058
100	5	8.037	8.145	0.108
100	6	7.943	8.038	0.095
100	7	8.067	8.121	0.054
100	8	7.972	7.919	-0.053
100	9	7.993	8.032	0.039
100	10	8.058	8.121	0.063
100	11	8.220	8.286	0.066
100	12	7.826	7.829	0.003
100	13	8.090	8.099	0.009
100	14	7.992	8.063	0.071
100	15	8.140	8.262	0.122
100	16	8.037	8.037	0.000
100	17	7.798	7.894	0.096
100	18	7.925	7.990	0.065
100	19	7.986	8.009	0.023
100	20	8.080	8.176	0.096
100	21	7.958	7.955	-0.003
100	22	7.913	8.153	0.240
100	24	8.020	8.109	0.089
105	25	8.201	8.131	-0.070
105	26	8.067	8.129	0.062
105	27	8.032	8.060	0.028
105	28	8.032	8.062	0.030
105	29	8.184	8.276	0.092
	Max	8.220	8.286	0.240
	Average	8.030	8.059	0.029
	Min	7.749	7.757	-0.070
	Std Dev	0.098	0.109	0.049

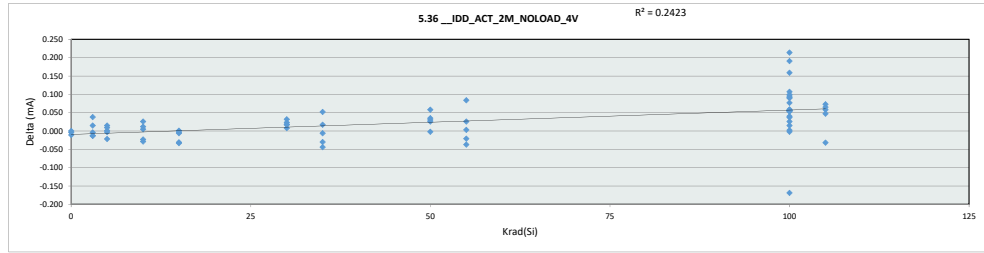


		5.31 IDD_ACT_1M_NOLOAD									
Test Site											
Tester											
Test Number											
Max Limit		9.5	mA								
Min Limit			mA								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.894	7.913	7.938	8.026	7.757	8.012	7.941	7.923	7.899	7.829	8.060
Average	7.990	8.050	8.018	8.106	7.977	8.111	8.031	8.065	8.079	8.059	8.132
Max	8.046	8.170	8.122	8.155	8.120	8.239	8.090	8.205	8.259	8.286	8.276
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

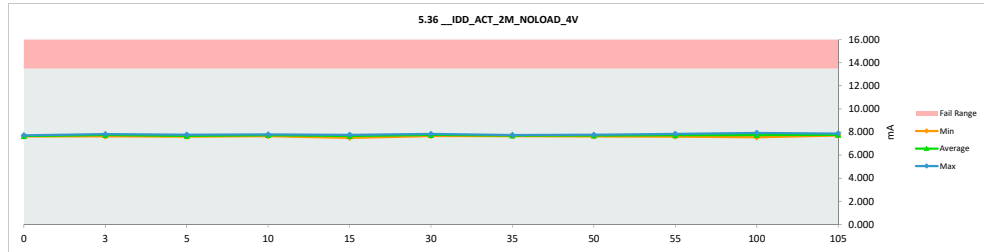


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5.36 IDD_ACT_2M_NOLOAD_4V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	13.5	13.5		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	7.616	7.605	-0.011
0	118	7.638	7.634	-0.004
0	122	7.709	7.709	0.000
3	301	7.774	7.789	0.015
3	306	7.637	7.624	-0.013
3	307	7.785	7.772	-0.013
3	308	7.622	7.660	0.038
3	309	7.818	7.814	-0.004
5	310	7.654	7.632	-0.022
5	311	7.601	7.598	-0.003
5	312	7.676	7.685	0.009
5	314	7.749	7.764	0.015
5	316	7.635	7.636	0.001
10	317	7.782	7.787	0.005
10	318	7.817	7.788	-0.029
10	319	7.685	7.697	0.012
10	321	7.667	7.644	-0.023
10	322	7.673	7.699	0.026
15	323	7.486	7.487	0.001
15	325	7.674	7.641	-0.033
15	326	7.649	7.646	-0.003
15	328	7.707	7.701	-0.006
15	333	7.788	7.757	-0.031
30	334	7.805	7.828	0.023
30	335	7.638	7.655	0.017
30	337	7.670	7.688	0.018
30	339	7.791	7.823	0.032
30	341	7.732	7.740	0.008
35	342	7.728	7.698	-0.030
35	345	7.679	7.696	0.017
35	346	7.746	7.740	-0.006
35	347	7.657	7.709	0.052
35	348	7.686	7.642	-0.044
50	349	7.677	7.703	0.026
50	350	7.723	7.758	0.035
50	351	7.661	7.719	0.058
50	353	7.706	7.704	-0.002
50	354	7.581	7.611	0.030
55	355	7.625	7.604	-0.021
55	357	7.755	7.839	0.084
55	359	7.710	7.736	0.026
55	362	7.705	7.668	-0.037
55	363	7.748	7.751	0.003
100	1	7.654	7.713	0.059
100	3	7.639	7.830	0.191
100	4	7.730	7.785	0.055
100	5	7.647	7.738	0.091
100	6	7.582	7.677	0.095
100	7	7.722	7.778	0.056
100	8	7.709	7.540	-0.169
100	9	7.701	7.741	0.040
100	10	7.668	7.745	0.077
100	11	7.881	7.918	0.037
100	12	7.631	7.790	0.159
100	13	7.704	7.730	0.026
100	14	7.662	7.719	0.057
100	15	7.749	7.856	0.107
100	16	7.694	7.709	0.015
100	17	7.484	7.539	0.055
100	18	7.589	7.642	0.053
100	19	7.673	7.675	0.002
100	20	7.709	7.808	0.099
100	21	7.643	7.641	-0.002
100	22	7.578	7.792	0.214
100	24	7.694	7.784	0.090
105	25	7.754	7.722	-0.032
105	26	7.706	7.771	0.065
105	27	7.665	7.723	0.058
105	28	7.651	7.698	0.047
105	29	7.787	7.860	0.073
	Max	7.881	7.918	0.214
	Average	7.688	7.714	0.026
	Min	7.484	7.487	-0.169
	Std Dev	0.072	0.081	0.055

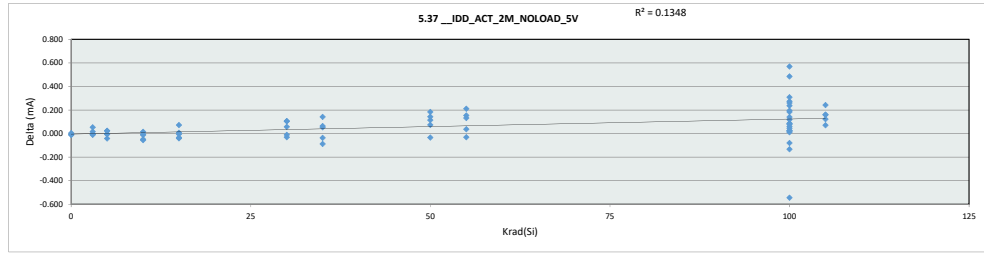


5.36 IDD_ACT_2M_NOLOAD											
Test Site											
Tester											
Test Number											
Max Limit	13.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.605	7.624	7.598	7.644	7.487	7.655	7.642	7.611	7.604	7.539	7.698
Average	7.649	7.732	7.663	7.723	7.646	7.747	7.697	7.699	7.720	7.734	7.755
Max	7.709	7.814	7.764	7.788	7.757	7.828	7.740	7.758	7.839	7.918	7.860
UL	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500

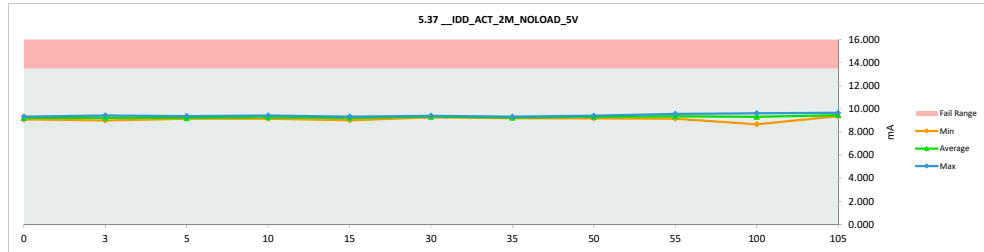


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5.37 IDD_ACT_2M_NOLOAD_5V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	13.5	13.5		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	9.102	9.091	-0.011
0	118	9.230	9.221	-0.009
0	122	9.332	9.337	0.005
3	301	9.157	9.174	0.017
3	306	9.173	9.163	-0.010
3	307	9.415	9.413	-0.002
3	308	8.950	9.003	0.053
3	309	9.434	9.428	-0.006
5	310	9.220	9.179	-0.041
5	311	9.154	9.148	-0.006
5	312	9.295	9.319	0.024
5	314	9.356	9.379	0.023
5	316	9.177	9.175	-0.002
10	317	9.435	9.430	-0.005
10	318	9.393	9.338	-0.055
10	319	9.230	9.245	0.015
10	321	9.203	9.155	-0.048
10	322	9.260	9.249	-0.011
15	323	9.017	9.012	-0.005
15	325	9.107	9.179	0.072
15	326	9.259	9.219	-0.040
15	328	9.327	9.334	0.007
15	333	9.353	9.320	-0.033
30	334	9.371	9.340	-0.031
30	335	9.166	9.271	0.105
30	337	9.226	9.332	0.106
30	339	9.413	9.403	-0.010
30	341	9.293	9.351	0.058
35	342	9.336	9.248	-0.088
35	345	9.261	9.325	0.064
35	346	9.135	9.277	0.142
35	347	9.229	9.193	-0.036
35	348	9.172	9.226	0.054
50	349	9.268	9.411	0.143
50	350	9.247	9.213	-0.034
50	351	9.214	9.398	0.184
50	353	9.276	9.390	0.114
50	354	9.105	9.179	0.074
55	355	9.115	9.152	0.037
55	357	9.356	9.367	0.011
55	359	9.263	9.417	0.154
55	362	9.238	9.207	-0.031
55	363	9.351	9.484	0.133
100	1	9.194	9.255	0.061
100	3	9.108	9.593	0.485
100	4	9.259	9.340	0.081
100	5	9.224	9.497	0.273
100	6	9.108	9.303	0.195
100	7	9.280	9.301	0.021
100	8	9.210	8.666	-0.544
100	9	9.258	9.179	-0.079
100	10	9.259	9.496	0.237
100	11	9.416	9.442	0.026
100	12	9.012	9.582	0.570
100	13	9.337	9.456	0.119
100	14	9.208	9.348	0.140
100	15	9.366	9.626	0.260
100	16	9.323	9.410	0.087
100	17	8.984	8.851	-0.133
100	18	9.104	9.127	0.023
100	19	9.184	9.266	0.082
100	20	9.248	9.432	0.184
100	21	9.221	9.232	0.011
100	22	9.130	9.439	0.309
100	24	9.281	9.326	0.045
105	25	9.305	9.376	0.071
105	26	9.301	9.462	0.161
105	27	9.254	9.413	0.159
105	28	9.250	9.373	0.123
105	29	9.415	9.657	0.242
	Max	9.435	9.657	0.570
	Average	9.241	9.305	0.064
	Min	8.950	8.666	-0.544
	Std Dev	0.108	0.167	0.142

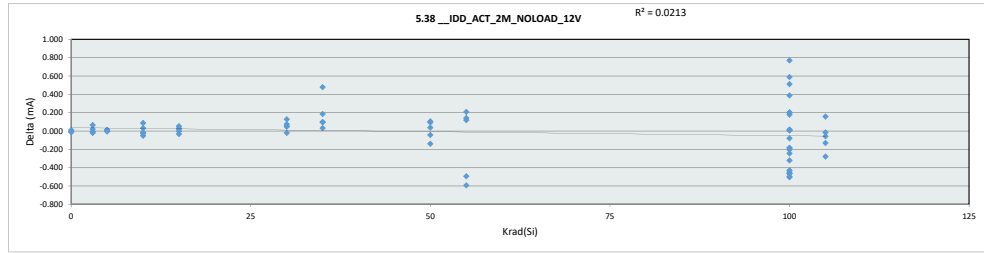


5.37 IDD_ACT_2M_NOLOAD											
Test Site											
Tester											
Test Number											
Max Limit	13.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.091	9.003	9.148	9.155	9.012	9.271	9.193	9.179	9.152	8.666	9.373
Average	9.216	9.236	9.240	9.283	9.213	9.339	9.254	9.318	9.365	9.326	9.456
Max	9.337	9.428	9.379	9.430	9.334	9.403	9.325	9.411	9.567	9.626	9.657
UL	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500

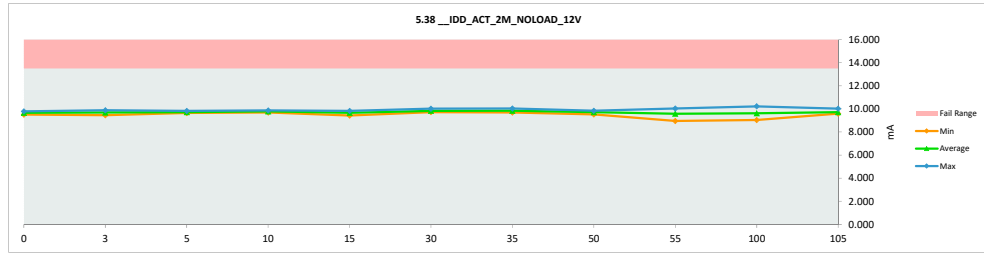


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5.38 IDD_ACT_2M_NOLOAD_12V				
Test Site	Serial #	PRE_HDR	POST_HDR	Delta
0	117	9.516	9.502	-0.014
0	118	9.703	9.696	-0.007
0	122	9.771	9.781	0.010
3	301	9.630	9.651	0.021
3	306	9.668	9.658	-0.010
3	307	9.809	9.788	-0.021
3	308	9.385	9.450	0.065
3	309	9.882	9.875	-0.007
5	310	9.646	9.658	0.012
5	311	9.713	9.711	-0.002
5	312	9.815	9.825	0.010
5	314	9.733	9.743	0.010
5	316	9.650	9.645	-0.005
10	317	9.895	9.868	-0.027
10	318	9.824	9.811	-0.013
10	319	9.591	9.679	0.088
10	321	9.828	9.775	-0.053
10	322	9.756	9.788	0.032
15	323	9.389	9.419	0.030
15	325	9.495	9.492	-0.003
15	326	9.694	9.747	0.053
15	328	9.767	9.788	0.021
15	333	9.854	9.821	-0.033
30	334	9.887	9.877	-0.010
30	335	9.675	9.723	0.048
30	337	9.670	9.743	0.073
30	339	9.891	10.018	0.127
30	341	9.750	9.803	0.053
35	342	9.769	9.800	0.031
35	345	9.722	9.818	0.096
35	346	9.548	10.026	0.478
35	347	9.633	9.819	0.186
35	348	9.587	9.685	0.098
50	349	9.735	9.840	0.105
50	350	9.839	9.700	-0.139
50	351	9.634	9.726	0.092
50	353	9.741	9.777	0.036
50	354	9.559	9.515	-0.044
55	355	9.548	8.954	-0.594
55	357	9.821	10.029	0.208
55	359	9.730	9.849	0.119
55	362	9.619	9.125	-0.494
55	363	9.807	9.948	0.141
100	1	9.717	9.288	-0.429
100	3	9.520	9.907	0.387
100	4	9.667	9.462	-0.205
100	5	9.702	9.907	0.205
100	6	9.614	9.534	-0.080
100	7	9.783	9.279	-0.504
100	8	9.597	10.185	0.588
100	9	9.636	9.173	-0.463
100	10	9.671	9.850	0.179
100	11	9.879	9.413	-0.466
100	12	9.423	9.935	0.512
100	13	9.806	9.821	0.015
100	14	9.703	9.518	-0.185
100	15	9.831	10.028	0.197
100	16	9.704	9.709	0.005
100	17	9.445	10.216	0.771
100	18	9.533	9.035	-0.498
100	19	9.642	9.457	-0.185
100	20	9.757	9.513	-0.244
100	21	9.641	9.320	-0.321
100	22	9.596	9.606	0.010
100	24	9.748	9.299	-0.449
105	25	9.871	9.591	-0.280
105	26	9.794	9.778	-0.016
105	27	9.661	9.603	-0.058
105	28	9.725	9.595	-0.130
105	29	9.857	10.014	0.157
Max		9.897	10.216	0.771
Average		9.696	9.685	-0.010
Min		9.385	8.954	-0.594
Std Dev		0.124	0.252	0.248

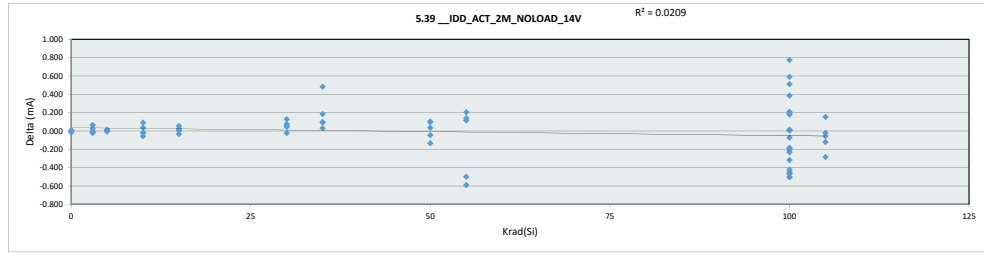


5.38 __IDD_ACT_2M_NOLOAD												
Test Site	Serial #	PRE_HDR	POST_HDR	Delta								
0	117	9.516	9.502	-0.014								
0	118	9.703	9.696	-0.007								
0	122	9.771	9.781	0.010								
3	301	9.630	9.651	0.021								
3	306	9.668	9.658	-0.010								
3	307	9.809	9.788	-0.021								
3	308	9.385	9.450	0.065								
3	309	9.882	9.875	-0.007								
5	310	9.646	9.658	0.012								
5	311	9.713	9.711	-0.002								
5	312	9.815	9.825	0.010								
5	314	9.733	9.743	0.010								
5	316	9.650	9.645	-0.005								
10	317	9.895	9.868	-0.027								
10	318	9.824	9.811	-0.013								
10	319	9.591	9.679	0.088								
10	321	9.828	9.775	-0.053								
10	322	9.756	9.788	0.032								
15	323	9.389	9.419	0.030								
15	325	9.495	9.492	-0.003								
15	326	9.694	9.747	0.053								
15	328	9.767	9.788	0.021								
15	333	9.854	9.821	-0.033								
30	334	9.887	9.877	-0.010								
30	335	9.675	9.723	0.048								
30	337	9.670	9.743	0.073								
30	339	9.891	10.018	0.127								
30	341	9.750	9.803	0.053								
35	342	9.769	9.800	0.031								
35	345	9.722	9.818	0.096								
35	346	9.548	10.026	0.478								
35	347	9.633	9.819	0.186								
35	348	9.587	9.685	0.098								
50	349	9.735	9.840	0.105								
50	350	9.839	9.700	-0.139								
50	351	9.634	9.726	0.092								
50	353	9.741	9.777	0.036								
50	354	9.559	9.515	-0.044								
55	355	9.548	8.954	-0.594								
55	357	9.821	10.029	0.208								
55	359	9.730	9.849	0.119								
55	362	9.619	9.125	-0.494								
55	363	9.807	9.948	0.141								
100	1	9.717	9.288	-0.429								
100	3	9.520	9.907	0.387								
100	4	9.667	9.462	-0.205								
100	5	9.702	9.907	0.205								
100	6	9.614	9.534	-0.080								
100	7	9.783	9.279	-0.504								
100	8	9.597	10.185	0.588								
100	9	9.636	9.173	-0.463								
100	10	9.671	9.850	0.179								
100	11	9.879	9.413	-0.466								
100	12	9.423	9.935	0.512								
100	13	9.806	9.821	0.015								
100	14	9.703	9.518	-0.185								
100	15	9.831	10.028	0.197								
100	16	9.704	9.709	0.005								
100	17	9.445	10.216	0.771								
100	18	9.533	9.035	-0.498								
100	19	9.642	9.457	-0.185								
100	20	9.757	9.513	-0.244								
100	21	9.641	9.320	-0.321								
100	22	9.596	9.606	0.010								
100	24	9.748	9.299	-0.449								
105	25	9.871	9.591	-0.280								
105	26	9.794	9.778	-0.016								
105	27	9.661	9.603	-0.058								
105	28	9.725	9.595	-0.130								
105	29	9.857	10.014	0.157								
LL		9.502	9.450	9.645	9.679	9.419	9.723	9.685	9.515	8.954	9.035	9.591
Min		9.502	9.450	9.645	9.679	9.419	9.723	9.685	9.515	8.954	9.035	9.591
Average		9.660	9.684	9.716	9.784	9.653	9.833	9.830	9.712	9.581	9.612	9.716
Max		9.781	9.875	9.825	9.868	9.821	10.018	10.026	9.840	10.029	10.216	10.014
UL		13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500

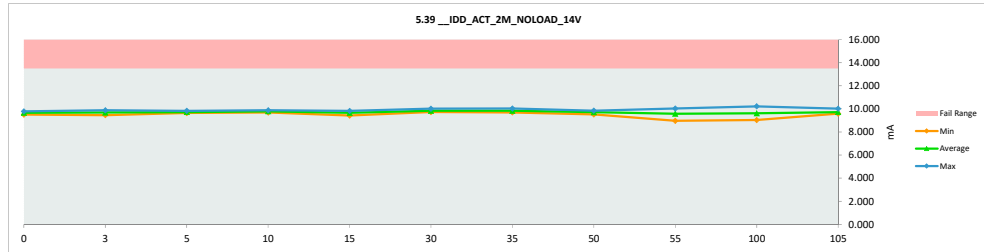


HDR TID Report
TPS7H5001-SP

5.39 IDD_ACT_2M_NOLOAD_14V				
Test Site				
Tester				
Test Number				
Unit	mA		mA	
Max Limit	13.5		13.5	
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	9.520	9.505	-0.015
0	118	9.704	9.701	-0.003
0	122	9.773	9.782	0.009
3	301	9.633	9.656	0.023
3	306	9.674	9.661	-0.013
3	307	9.810	9.790	-0.020
3	308	9.390	9.455	0.065
3	309	9.885	9.879	-0.006
5	310	9.649	9.659	0.010
5	311	9.719	9.716	-0.003
5	312	9.817	9.830	0.013
5	314	9.735	9.747	0.012
5	316	9.652	9.647	-0.005
10	317	9.893	9.874	-0.019
10	318	9.830	9.813	-0.017
10	319	9.593	9.684	0.091
10	321	9.829	9.772	-0.057
10	322	9.760	9.795	0.035
15	323	9.404	9.426	0.022
15	325	9.496	9.498	0.002
15	326	9.695	9.749	0.054
15	328	9.771	9.789	0.018
15	333	9.856	9.823	-0.033
30	334	9.889	9.876	-0.023
30	335	9.682	9.727	0.045
30	337	9.674	9.750	0.076
30	339	9.893	10.021	0.128
30	341	9.751	9.809	0.058
35	342	9.773	9.802	0.029
35	345	9.726	9.819	0.093
35	346	9.550	10.033	0.483
35	347	9.636	9.821	0.185
35	348	9.592	9.690	0.098
50	349	9.739	9.843	0.104
50	350	9.841	9.707	-0.134
50	351	9.637	9.731	0.094
50	353	9.744	9.779	0.035
50	354	9.566	9.520	-0.046
55	355	9.553	8.963	-0.590
55	357	9.827	10.032	0.205
55	359	9.735	9.851	0.116
55	362	9.626	9.126	-0.500
55	363	9.812	9.951	0.139
100	1	9.717	9.295	-0.422
100	3	9.525	9.911	0.386
100	4	9.673	9.465	-0.208
100	5	9.707	9.916	0.209
100	6	9.617	9.543	-0.074
100	7	9.789	9.288	-0.501
100	8	9.598	10.188	0.590
100	9	9.638	9.170	-0.468
100	10	9.672	9.852	0.180
100	11	9.883	9.416	-0.467
100	12	9.428	9.939	0.511
100	13	9.809	9.819	0.010
100	14	9.703	9.517	-0.186
100	15	9.832	10.028	0.196
100	16	9.705	9.711	0.006
100	17	9.446	10.222	0.776
100	18	9.536	9.032	-0.504
100	19	9.644	9.458	-0.186
100	20	9.761	9.529	-0.232
100	21	9.649	9.331	-0.318
100	22	9.600	9.613	0.013
100	24	9.752	9.305	-0.447
105	25	9.875	9.591	-0.284
105	26	9.800	9.780	-0.020
105	27	9.663	9.608	-0.055
105	28	9.727	9.607	-0.120
105	29	9.860	10.014	0.154
	Max	9.899	10.222	0.776
	Average	9.699	9.689	-0.010
	Min	9.390	8.963	-0.590
	Std Dev	0.124	0.252	0.248

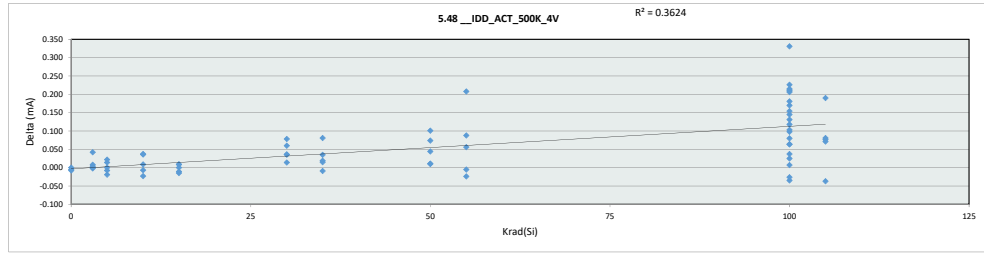


5.39 IDD_ACT_2M_NOLOAD											
Test Site											
Tester											
Test Number											
Max Limit	13.5		mA								
Min Limit			mA								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.505	9.455	9.647	9.684	9.426	9.727	9.690	9.520	8.963	9.032	9.591
Average	9.663	9.688	9.720	9.788	9.657	9.837	9.833	9.716	9.585	9.616	9.720
Max	9.782	9.879	9.830	9.874	9.823	10.021	10.033	9.843	10.032	10.222	10.014
UL	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500

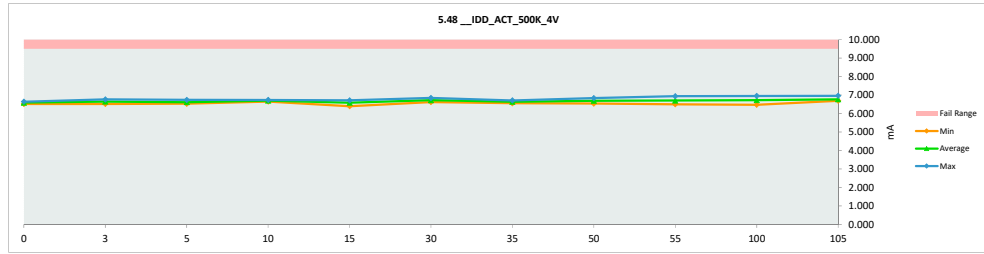


HDR TID Report
TPS7H5001-SP

5.48 IDD_ACT_500K_4V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	9.5	9.5		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	6.521	6.516	-0.005
0	118	6.615	6.615	0.000
0	122	6.646	6.638	-0.008
3	301	6.621	6.629	0.008
3	306	6.576	6.574	-0.002
3	307	6.752	6.756	0.004
3	308	6.475	6.517	0.042
3	309	6.766	6.766	0.000
5	310	6.582	6.563	-0.019
5	311	6.531	6.531	0.000
5	312	6.661	6.675	0.014
5	314	6.718	6.740	0.022
5	316	6.554	6.546	-0.008
10	317	6.725	6.734	0.009
10	318	6.755	6.732	-0.023
10	319	6.613	6.649	0.036
10	321	6.716	6.709	-0.007
10	322	6.642	6.679	0.037
15	323	6.388	6.397	0.009
15	325	6.528	6.517	-0.011
15	326	6.616	6.616	0.000
15	328	6.673	6.682	0.009
15	333	6.728	6.713	-0.015
30	334	6.804	6.840	0.036
30	335	6.587	6.623	0.036
30	337	6.580	6.640	0.060
30	339	6.743	6.821	0.078
30	341	6.668	6.682	0.014
35	342	6.686	6.677	-0.009
35	345	6.610	6.645	0.035
35	346	6.592	6.611	0.019
35	347	6.628	6.709	0.081
35	348	6.541	6.556	0.015
50	349	6.676	6.720	0.044
50	350	6.731	6.832	0.101
50	351	6.618	6.692	0.074
50	353	6.668	6.678	0.010
50	354	6.528	6.539	0.011
55	355	6.525	6.501	-0.024
55	357	6.730	6.938	0.208
55	359	6.630	6.686	0.056
55	362	6.595	6.590	-0.005
55	363	6.723	6.811	0.088
100	1	6.579	6.760	0.181
100	3	6.564	6.529	-0.035
100	4	6.672	6.842	0.170
100	5	6.615	6.830	0.215
100	6	6.544	6.753	0.209
100	7	6.631	6.729	0.098
100	8	6.577	6.551	-0.026
100	9	6.614	6.678	0.064
100	10	6.673	6.885	0.212
100	11	6.775	6.929	0.154
100	12	6.449	6.474	0.025
100	13	6.678	6.809	0.131
100	14	6.595	6.740	0.145
100	15	6.725	6.951	0.226
100	16	6.654	6.718	0.064
100	17	6.393	6.496	0.103
100	18	6.524	6.604	0.080
100	19	6.588	6.625	0.037
100	20	6.666	6.872	0.206
100	21	6.563	6.570	0.007
100	22	6.535	6.866	0.331
100	24	6.596	6.714	0.118
105	25	6.781	6.744	-0.037
105	26	6.654	6.725	0.071
105	27	6.648	6.729	0.081
105	28	6.613	6.690	0.077
105	29	6.767	6.957	0.190
	Max	6.804	6.957	0.331
	Average	6.628	6.687	0.059
	Min	6.388	6.397	-0.037
	Std Dev	0.089	0.124	0.079

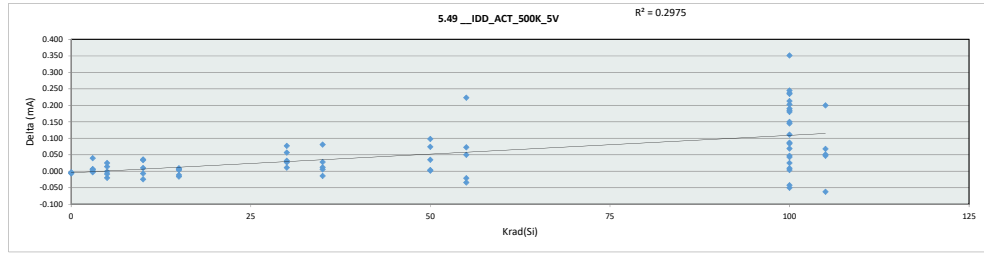


5.48 IDD_ACT_500K_4V											
Test Site											
Tester											
Test Number											
Max Limit	9.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.516	6.517	6.531	6.649	6.397	6.623	6.556	6.539	6.501	6.474	6.690
Average	6.590	6.648	6.611	6.701	6.585	6.721	6.640	6.692	6.705	6.724	6.769
Max	6.638	6.766	6.740	6.734	6.713	6.840	6.709	6.832	6.938	6.951	6.957
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

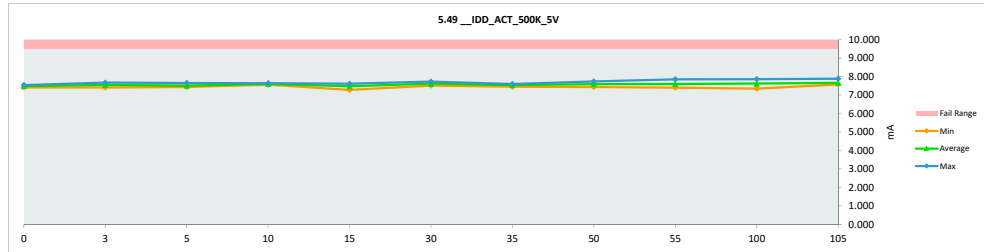


HDR TID Report
TPS7H5001-SP

5.49 IDD_ACT_500K_5V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	9.5	9.5		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	7.418	7.411	-0.007
0	118	7.508	7.505	-0.003
0	122	7.546	7.541	-0.005
3	301	7.522	7.529	0.007
3	306	7.470	7.467	-0.003
3	307	7.668	7.674	0.006
3	308	7.371	7.411	0.040
3	309	7.668	7.669	0.001
5	310	7.482	7.462	-0.020
5	311	7.437	7.436	-0.001
5	312	7.563	7.577	0.014
5	314	7.630	7.655	0.025
5	316	7.454	7.446	-0.008
10	317	7.626	7.636	0.010
10	318	7.671	7.647	-0.024
10	319	7.528	7.562	0.034
10	321	7.595	7.588	-0.007
10	322	7.536	7.572	0.036
15	323	7.273	7.279	0.006
15	325	7.430	7.419	-0.011
15	326	7.504	7.508	0.004
15	328	7.572	7.581	0.009
15	333	7.629	7.613	-0.016
30	334	7.700	7.727	0.027
30	335	7.480	7.512	0.032
30	337	7.482	7.539	0.057
30	339	7.632	7.709	0.077
30	341	7.569	7.580	0.011
35	342	7.590	7.576	-0.014
35	345	7.515	7.543	0.028
35	346	7.502	7.514	0.012
35	347	7.515	7.596	0.081
35	348	7.445	7.451	0.006
50	349	7.572	7.607	0.035
50	350	7.636	7.734	0.098
50	351	7.515	7.589	0.074
50	353	7.573	7.575	0.002
50	354	7.430	7.434	0.004
55	355	7.433	7.399	-0.034
55	357	7.631	7.854	0.223
55	359	7.521	7.571	0.050
55	362	7.510	7.489	-0.021
55	363	7.628	7.701	0.073
100	1	7.486	7.676	0.190
100	3	7.463	7.413	-0.050
100	4	7.567	7.747	0.180
100	5	7.500	7.703	0.203
100	6	7.442	7.677	0.235
100	7	7.525	7.609	0.084
100	8	7.485	7.443	-0.042
100	9	7.523	7.570	0.047
100	10	7.573	7.818	0.245
100	11	7.687	7.832	0.145
100	12	7.343	7.352	0.009
100	13	7.557	7.742	0.185
100	14	7.497	7.647	0.150
100	15	7.621	7.858	0.237
100	16	7.552	7.595	0.043
100	17	7.294	7.381	0.087
100	18	7.419	7.488	0.069
100	19	7.477	7.502	0.025
100	20	7.580	7.793	0.213
100	21	7.460	7.464	0.004
100	22	7.423	7.774	0.351
100	24	7.498	7.609	0.111
105	25	7.690	7.628	-0.062
105	26	7.562	7.613	0.051
105	27	7.551	7.598	0.047
105	28	7.505	7.573	0.068
105	29	7.680	7.880	0.200
	Max	7.700	7.880	0.351
	Average	7.528	7.584	0.056
	Min	7.273	7.279	-0.062
	Std Dev	0.091	0.130	0.085

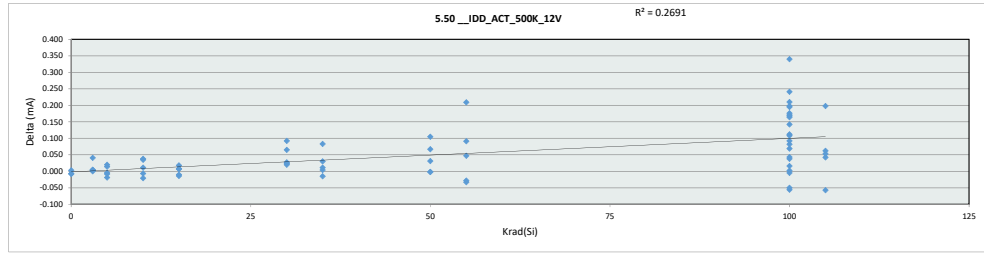


5.49 IDD_ACT_500K_5V											
Test Site											
Tester											
Test Number											
Max Limit	9.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.411	7.411	7.436	7.562	7.279	7.512	7.451	7.434	7.399	7.352	7.573
Average	7.486	7.550	7.515	7.601	7.480	7.613	7.536	7.588	7.603	7.622	7.658
Max	7.541	7.674	7.655	7.647	7.613	7.727	7.596	7.734	7.854	7.858	7.880
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

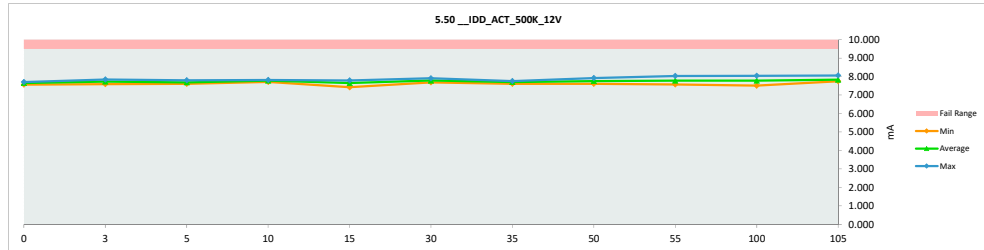


HDR TID Report
TPS7H5001-SP

		5.50 IDD_ACT_500K_12V		
Test Site	Tester			
Test Number	Unit	mA	mA	
Max Limit	Min Limit	9.5	9.5	
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	7.579	7.571	-0.008
0	118	7.695	7.698	0.003
0	122	7.709	7.702	-0.007
3	301	7.723	7.726	0.003
3	306	7.646	7.650	0.004
3	307	7.835	7.835	0.000
3	308	7.554	7.595	0.041
3	309	7.836	7.841	0.005
5	310	7.647	7.628	-0.019
5	311	7.639	7.634	-0.005
5	312	7.773	7.787	0.014
5	314	7.781	7.801	0.020
5	316	7.617	7.609	-0.008
10	317	7.808	7.819	0.011
10	318	7.843	7.822	-0.021
10	319	7.679	7.714	0.035
10	321	7.809	7.802	-0.007
10	322	7.724	7.762	0.038
15	323	7.417	7.424	0.007
15	325	7.590	7.576	-0.014
15	326	7.672	7.680	0.008
15	328	7.746	7.764	0.018
15	333	7.805	7.795	-0.010
30	334	7.872	7.899	0.027
30	335	7.662	7.686	0.024
30	337	7.638	7.703	0.065
30	339	7.817	7.909	0.092
30	341	7.724	7.744	0.020
35	342	7.755	7.740	-0.015
35	345	7.701	7.731	0.030
35	346	7.670	7.681	0.011
35	347	7.673	7.756	0.083
35	348	7.609	7.613	0.004
50	349	7.751	7.782	0.031
50	350	7.819	7.924	0.105
50	351	7.664	7.731	0.067
50	353	7.746	7.744	-0.002
50	354	7.612	7.610	-0.002
55	355	7.611	7.578	-0.033
55	357	7.829	8.038	0.209
55	359	7.706	7.753	0.047
55	362	7.701	7.673	-0.028
55	363	7.800	7.891	0.091
100	1	7.673	7.844	0.171
100	3	7.624	7.568	-0.056
100	4	7.735	7.903	0.168
100	5	7.695	7.905	0.210
100	6	7.620	7.796	0.176
100	7	7.724	7.806	0.082
100	8	7.646	7.596	-0.050
100	9	7.685	7.728	0.043
100	10	7.738	7.933	0.195
100	11	7.889	8.031	0.142
100	12	7.509	7.511	0.002
100	13	7.748	7.860	0.112
100	14	7.660	7.824	0.164
100	15	7.802	8.043	0.241
100	16	7.704	7.742	0.038
100	17	7.478	7.570	0.092
100	18	7.597	7.666	0.069
100	19	7.655	7.671	0.016
100	20	7.761	7.959	0.198
100	21	7.622	7.617	-0.005
100	22	7.581	7.921	0.340
100	24	7.695	7.803	0.108
105	25	7.875	7.818	-0.057
105	26	7.741	7.783	0.042
105	27	7.706	7.759	0.053
105	28	7.686	7.748	0.062
105	29	7.862	8.060	0.198
	Max	7.889	8.060	0.340
	Average	7.703	7.756	0.053
	Min	7.417	7.424	-0.057
	Std Dev	0.095	0.132	0.080

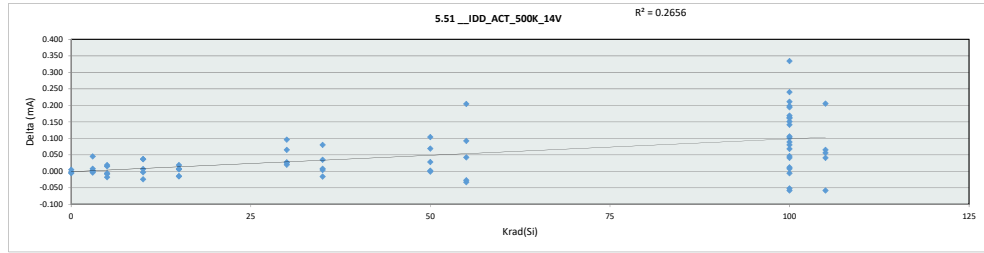


		5.50 IDD_ACT_500K_12V										
Test Site	Tester											
Test Number	Unit	9.5	mA									
Max Limit	Min Limit	9.5	mA									
Krad(Si)	LL	3	5	10	15	30	35	50	55	100	105	
Min	7.571	7.595	7.609	7.714	7.424	7.686	7.613	7.610	7.578	7.511	7.748	
Average	7.657	7.729	7.692	7.784	7.648	7.788	7.704	7.758	7.787	7.786	7.834	
Max	7.702	7.841	7.801	7.822	7.795	7.909	7.756	7.924	8.038	8.043	8.060	
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	

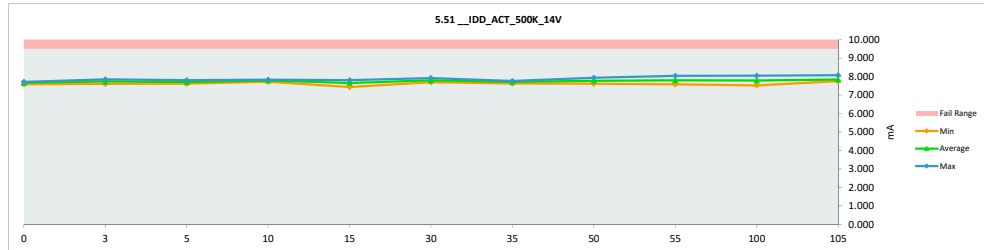


HDR TID Report
TPS7H5001-SP

5.51 IDD_ACT_500K_14V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	9.5	9.5		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	7.585	7.579	-0.006
0	118	7.699	7.704	0.005
0	122	7.711	7.708	-0.003
3	301	7.728	7.736	0.008
3	306	7.656	7.652	-0.004
3	307	7.841	7.840	-0.001
3	308	7.555	7.600	0.045
3	309	7.842	7.844	0.002
5	310	7.647	7.629	-0.018
5	311	7.645	7.639	-0.006
5	312	7.777	7.792	0.015
5	314	7.785	7.804	0.019
5	316	7.622	7.614	-0.008
10	317	7.815	7.822	0.007
10	318	7.850	7.826	-0.024
10	319	7.682	7.719	0.037
10	321	7.812	7.809	-0.003
10	322	7.730	7.767	0.037
15	323	7.423	7.429	0.006
15	325	7.596	7.581	-0.015
15	326	7.676	7.684	0.008
15	328	7.748	7.767	0.019
15	333	7.814	7.799	-0.015
30	334	7.877	7.903	0.026
30	335	7.664	7.691	0.027
30	337	7.644	7.709	0.065
30	339	7.820	7.916	0.096
30	341	7.730	7.750	0.020
35	342	7.760	7.744	-0.016
35	345	7.702	7.737	0.035
35	346	7.672	7.680	0.008
35	347	7.680	7.760	0.080
35	348	7.614	7.618	0.004
50	349	7.760	7.788	0.028
50	350	7.826	7.930	0.104
50	351	7.666	7.735	0.069
50	353	7.752	7.754	0.002
50	354	7.614	7.613	-0.001
55	355	7.613	7.580	-0.033
55	357	7.834	8.038	0.204
55	359	7.712	7.754	0.042
55	362	7.706	7.679	-0.027
55	363	7.806	7.898	0.092
100	1	7.678	7.839	0.161
100	3	7.631	7.573	-0.058
100	4	7.743	7.894	0.151
100	5	7.701	7.912	0.211
100	6	7.624	7.787	0.163
100	7	7.730	7.810	0.080
100	8	7.651	7.599	-0.052
100	9	7.689	7.730	0.041
100	10	7.740	7.938	0.198
100	11	7.897	8.038	0.141
100	12	7.513	7.521	0.008
100	13	7.753	7.859	0.106
100	14	7.664	7.833	0.169
100	15	7.808	8.048	0.240
100	16	7.707	7.753	0.046
100	17	7.485	7.574	0.089
100	18	7.601	7.669	0.068
100	19	7.662	7.674	0.012
100	20	7.765	7.959	0.194
100	21	7.629	7.623	-0.006
100	22	7.588	7.922	0.334
100	24	7.702	7.803	0.101
105	25	7.882	7.824	-0.058
105	26	7.748	7.789	0.041
105	27	7.709	7.765	0.056
105	28	7.688	7.753	0.065
105	29	7.866	8.071	0.205
	Max	7.897	8.071	0.334
	Average	7.708	7.760	0.052
	Min	7.423	7.429	-0.058
	Std Dev	0.096	0.131	0.079

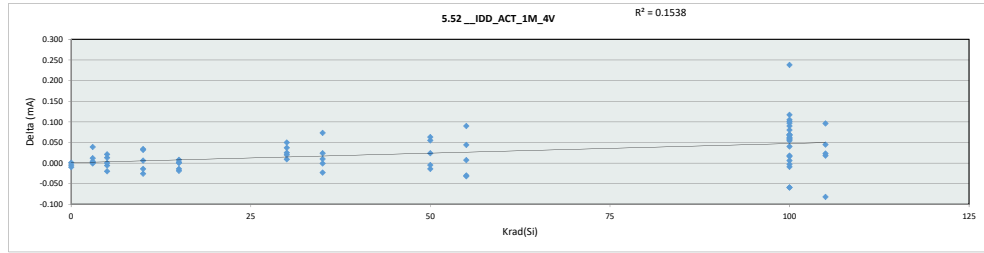


5.51 IDD_ACT_500K_14V											
Test Site											
Tester											
Test Number											
Max Limit	9.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.579	7.600	7.614	7.719	7.429	7.691	7.618	7.613	7.580	7.521	7.753
Average	7.664	7.734	7.696	7.789	7.652	7.794	7.708	7.764	7.790	7.789	7.840
Max	7.708	7.844	7.804	7.826	7.799	7.916	7.760	7.930	8.038	8.048	8.071
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

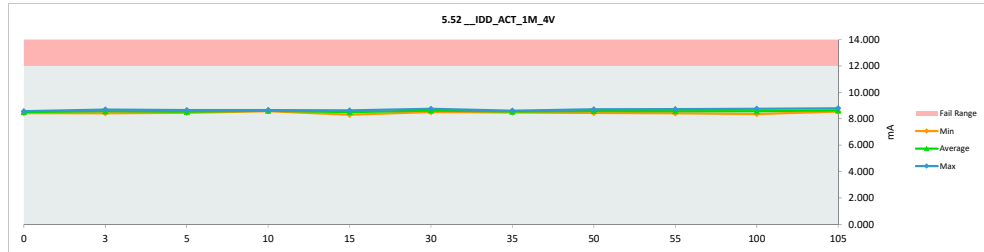


HDR TID Report
TPS7H5001-SP

5.52 IDD_ACT_1M_4V				
Test Site	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.439	8.429	-0.010
0	118	8.529	8.530	0.001
0	122	8.575	8.570	-0.005
3	301	8.538	8.550	0.012
3	306	8.494	8.493	-0.001
3	307	8.684	8.685	0.001
3	308	8.381	8.420	0.039
3	309	8.694	8.698	0.004
5	310	8.505	8.485	-0.020
5	311	8.459	8.459	0.000
5	312	8.593	8.606	0.013
5	314	8.638	8.659	0.021
5	316	8.466	8.460	-0.006
10	317	8.658	8.664	0.006
10	318	8.685	8.659	-0.026
10	319	8.536	8.570	0.034
10	321	8.603	8.589	-0.014
10	322	8.561	8.593	0.032
15	323	8.304	8.306	0.002
15	325	8.448	8.434	-0.014
15	326	8.539	8.539	0.000
15	328	8.603	8.611	0.008
15	333	8.652	8.633	-0.019
30	334	8.732	8.732	0.000
30	335	8.488	8.513	0.025
30	337	8.501	8.551	0.050
30	339	8.668	8.705	0.037
30	341	8.590	8.599	0.009
35	342	8.612	8.589	-0.023
35	345	8.525	8.549	0.024
35	346	8.516	8.526	0.010
35	347	8.539	8.612	0.073
35	348	8.474	8.473	-0.001
50	349	8.583	8.607	0.024
50	350	8.665	8.720	0.055
50	351	8.533	8.596	0.063
50	353	8.602	8.588	-0.014
50	354	8.446	8.441	-0.005
55	355	8.446	8.414	-0.032
55	357	8.640	8.730	0.090
55	359	8.547	8.591	0.044
55	362	8.517	8.487	-0.030
55	363	8.650	8.657	0.007
100	1	8.499	8.568	0.069
100	3	8.476	8.417	-0.059
100	4	8.574	8.634	0.060
100	5	8.532	8.637	0.105
100	6	8.451	8.541	0.090
100	7	8.553	8.607	0.054
100	8	8.501	8.442	-0.059
100	9	8.534	8.574	0.040
100	10	8.582	8.651	0.069
100	11	8.695	8.762	0.067
100	12	8.361	8.358	-0.003
100	13	8.584	8.600	0.016
100	14	8.511	8.573	0.062
100	15	8.645	8.762	0.117
100	16	8.574	8.580	0.006
100	17	8.313	8.393	0.080
100	18	8.428	8.485	0.057
100	19	8.496	8.514	0.018
100	20	8.585	8.686	0.101
100	21	8.492	8.483	-0.009
100	22	8.440	8.678	0.238
100	24	8.522	8.619	0.097
105	25	8.701	8.619	-0.082
105	26	8.574	8.619	0.045
105	27	8.572	8.595	0.023
105	28	8.533	8.551	0.018
105	29	8.699	8.795	0.096
Max		8.732	8.795	0.238
Average		8.547	8.573	0.026
Min		8.304	8.306	-0.082
Std Dev		0.092	0.102	0.049

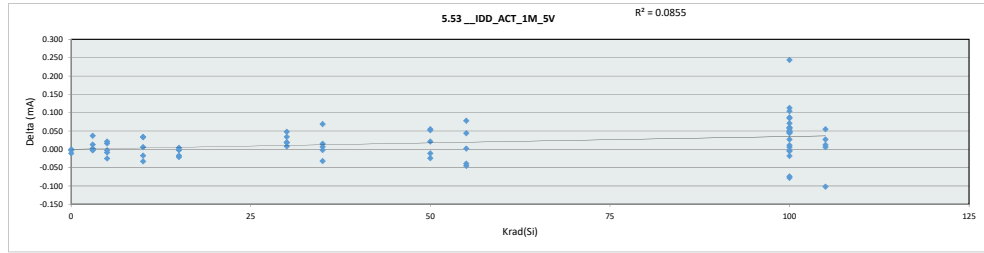


5.52 IDD_ACT_1M_4V												
Test Site	Serial #	PRE_HDR	POST_HDR	Delta	LL	Min	Average	Max	UL			
0	3	8.429	8.420	8.459	8.570	8.306	8.513	8.473	8.441	8.414	8.358	8.551
0	5	8.510	8.569	8.534	8.615	8.505	8.624	8.550	8.590	8.576	8.571	8.636
0	10	8.570	8.698	8.659	8.664	8.633	8.752	8.612	8.720	8.730	8.762	8.795
0	15	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000

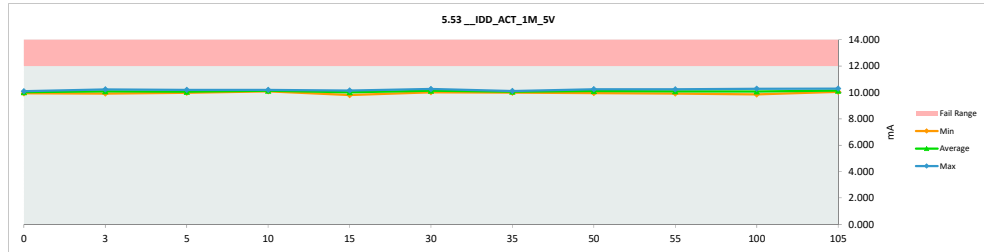


HDR TID Report
TPS7H5001-SP

5.53 IDD_ACT_1M_5V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	12	12		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	9.942	9.931	-0.011
0	118	10.026	10.026	0.000
0	122	10.094	10.091	-0.003
3	301	10.045	10.058	0.013
3	306	9.999	9.996	-0.003
3	307	10.228	10.227	-0.001
3	308	9.875	9.912	0.037
3	309	10.213	10.215	0.002
5	310	10.022	9.997	-0.025
5	311	9.979	9.976	-0.003
5	312	10.113	10.129	0.016
5	314	10.172	10.193	0.021
5	316	9.981	9.972	-0.009
10	317	10.178	10.184	0.006
10	318	10.225	10.192	-0.033
10	319	10.066	10.099	0.033
10	321	10.100	10.083	-0.017
10	322	10.061	10.095	0.034
15	323	9.797	9.796	-0.001
15	325	9.957	9.940	-0.017
15	326	10.039	10.037	-0.002
15	328	10.123	10.127	0.004
15	333	10.168	10.147	-0.021
30	334	10.241	10.260	0.019
30	335	9.989	10.007	0.018
30	337	10.011	10.059	0.048
30	339	10.176	10.210	0.034
30	341	10.101	10.109	0.008
35	342	10.134	10.102	-0.032
35	345	10.040	10.055	0.015
35	346	10.035	10.042	0.007
35	347	10.035	10.104	0.069
35	348	9.981	9.979	-0.002
50	349	10.086	10.107	0.021
50	350	10.188	10.240	0.052
50	351	10.043	10.098	0.055
50	353	10.128	10.104	-0.024
50	354	9.960	9.949	-0.011
55	355	9.961	9.916	-0.045
55	357	10.158	10.236	0.078
55	359	10.043	10.087	0.044
55	362	10.039	10.000	-0.039
55	363	10.166	10.168	0.002
100	1	10.023	10.068	0.045
100	3	9.976	9.902	-0.074
100	4	10.073	10.122	0.049
100	5	10.029	10.133	0.104
100	6	9.959	10.030	0.071
100	7	10.057	10.102	0.045
100	8	10.017	9.939	-0.078
100	9	10.055	10.082	0.027
100	10	10.100	10.151	0.051
100	11	10.216	10.275	0.059
100	12	9.852	9.847	-0.005
100	13	10.091	10.097	0.006
100	14	10.024	10.071	0.047
100	15	10.156	10.269	0.113
100	16	10.084	10.081	-0.003
100	17	9.827	9.886	0.059
100	18	9.923	9.980	0.057
100	19	9.988	9.999	0.011
100	20	10.115	10.200	0.085
100	21	9.999	9.981	-0.018
100	22	9.926	10.170	0.244
100	24	10.044	10.131	0.087
105	25	10.228	10.126	-0.102
105	26	10.093	10.120	0.027
105	27	10.093	10.099	0.006
105	28	10.032	10.044	0.012
105	29	10.231	10.286	0.055
Max	10.241	10.286	0.244	
Average	10.059	10.078	0.019	
Min	9.797	9.796	-0.102	
Std Dev	0.099	0.107	0.049	

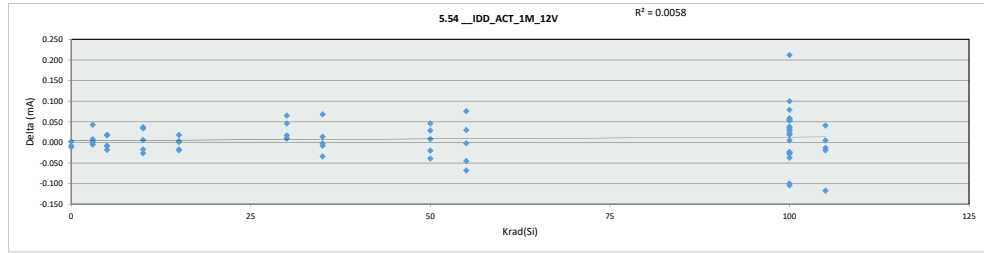


5.53 IDD_ACT_1M_5V											
Test Site											
Tester											
Test Number											
Max Limit	12	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.931	9.912	9.972	10.083	9.796	10.007	9.979	9.949	9.916	9.847	10.044
Average	10.016	10.082	10.053	10.131	10.009	10.129	10.056	10.100	10.081	10.069	10.135
Max	10.091	10.227	10.193	10.192	10.147	10.260	10.104	10.240	10.236	10.275	10.286
UL	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000

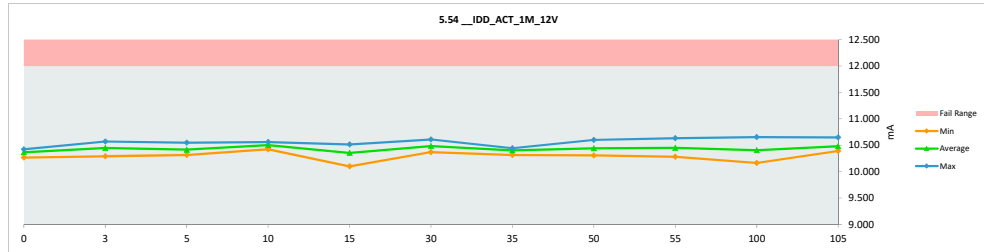


HDR TID Report
TPS7H5001-SP

		5.54 IDD_ACT_1M_12V		
Test Site	Tester			
Test Number	Unit	mA	mA	
Max Limit	12	12		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	10.278	10.267	-0.011
0	118	10.407	10.409	0.002
0	122	10.430	10.422	-0.008
3	301	10.444	10.452	0.008
3	306	10.363	10.362	-0.001
3	307	10.571	10.566	-0.005
3	308	10.246	10.289	0.043
3	309	10.568	10.570	0.002
5	310	10.354	10.336	-0.018
5	311	10.386	10.378	-0.008
5	312	10.532	10.549	0.017
5	314	10.485	10.504	0.019
5	316	10.323	10.314	-0.009
10	317	10.553	10.559	0.006
10	318	10.582	10.556	-0.026
10	319	10.388	10.422	0.034
10	321	10.511	10.494	-0.017
10	322	10.440	10.477	0.037
15	323	10.097	10.098	0.001
15	325	10.292	10.273	-0.019
15	326	10.382	10.385	0.003
15	328	10.477	10.495	0.018
15	333	10.533	10.516	-0.017
30	334	10.598	10.608	0.010
30	335	10.368	10.368	0.010
30	337	10.339	10.404	0.065
30	339	10.550	10.596	0.046
30	341	10.430	10.447	0.017
35	342	10.476	10.442	-0.034
35	345	10.422	10.436	0.014
35	346	10.381	10.379	-0.002
35	347	10.366	10.434	0.068
35	348	10.324	10.316	-0.008
50	349	10.451	10.459	0.008
50	350	10.571	10.600	0.029
50	351	10.358	10.404	0.046
50	353	10.482	10.443	-0.039
50	354	10.327	10.307	-0.020
55	355	10.327	10.282	-0.045
55	357	10.556	10.632	0.076
55	359	10.418	10.448	0.030
55	362	10.434	10.366	-0.068
55	363	10.523	10.521	-0.002
100	1	10.399	10.429	0.030
100	3	10.316	10.212	-0.104
100	4	10.421	10.445	0.024
100	5	10.420	10.499	0.079
100	6	10.325	10.381	0.056
100	7	10.455	10.474	0.019
100	8	10.356	10.256	-0.100
100	9	10.396	10.401	0.005
100	10	10.436	10.455	0.019
100	11	10.622	10.654	0.032
100	12	10.188	10.164	-0.024
100	13	10.465	10.442	-0.023
100	14	10.360	10.397	0.037
100	15	10.523	10.623	0.100
100	16	10.405	10.377	-0.028
100	17	10.207	10.260	0.053
100	18	10.288	10.326	0.038
100	19	10.357	10.332	-0.025
100	20	10.491	10.550	0.059
100	21	10.336	10.299	-0.037
100	22	10.263	10.475	0.212
100	24	10.441	10.495	0.054
105	25	10.611	10.494	-0.117
105	26	10.466	10.471	0.005
105	27	10.421	10.402	-0.019
105	28	10.404	10.391	-0.013
105	29	10.607	10.648	0.041
	Max	10.622	10.654	0.212
	Average	10.419	10.428	0.009
	Min	10.097	10.098	-0.117
	Std Dev	0.107	0.115	0.047

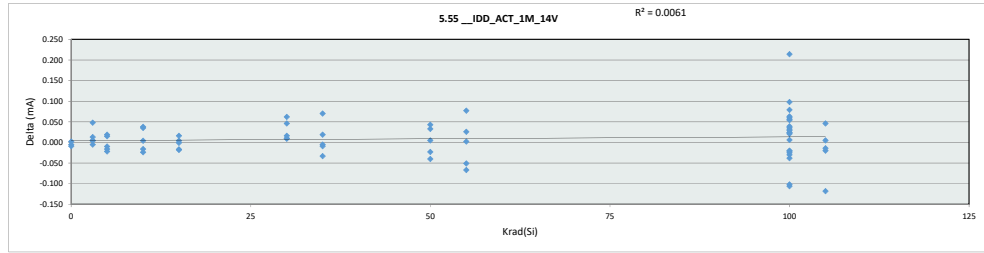


		5.54 IDD_ACT_1M_12V									
Test Site	Tester										
Test Number	Unit	12	mA								
Max Limit	12	12									
Min Limit											
Krad(Si)	LL	3	5	10	15	30	35	50	55	100	105
Min	10.267	10.289	10.314	10.422	10.098	10.368	10.316	10.307	10.282	10.164	10.391
Average	10.366	10.448	10.416	10.502	10.353	10.485	10.401	10.443	10.450	10.407	10.481
Max	10.422	10.570	10.549	10.559	10.516	10.608	10.442	10.600	10.632	10.654	10.648
UL	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000

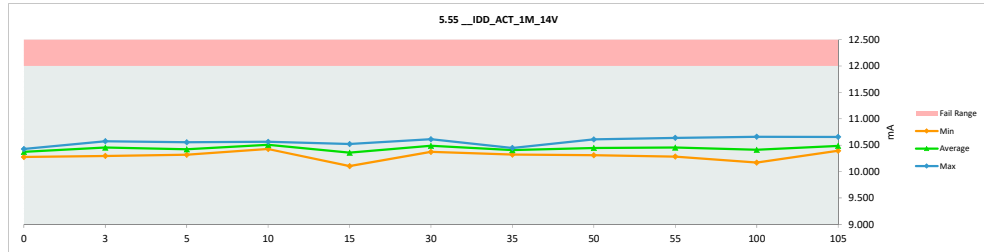


HDR TID Report
TPS7H5001-SP

		5.55 IDD ACT 1M 14V		
Test Site	Tester			
Test Number	Unit	mA	mA	
Max Limit	Min Limit	12	12	
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	10.280	10.275	-0.005
0	118	10.414	10.416	0.002
0	122	10.436	10.427	-0.009
3	301	10.446	10.459	0.013
3	306	10.365	10.369	0.004
3	307	10.576	10.571	-0.005
3	308	10.247	10.295	0.048
3	309	10.569	10.574	0.005
5	310	10.361	10.339	-0.022
5	311	10.396	10.380	-0.016
5	312	10.539	10.554	0.015
5	314	10.491	10.510	0.019
5	316	10.329	10.319	-0.010
10	317	10.559	10.563	0.004
10	318	10.587	10.563	-0.024
10	319	10.392	10.427	0.035
10	321	10.513	10.497	-0.016
10	322	10.444	10.482	0.038
15	323	10.104	10.103	-0.001
15	325	10.298	10.281	-0.017
15	326	10.384	10.388	0.004
15	328	10.484	10.500	0.016
15	333	10.538	10.520	-0.018
30	334	10.401	10.611	0.010
30	335	10.363	10.372	0.009
30	337	10.346	10.408	0.062
30	339	10.552	10.598	0.046
30	341	10.435	10.451	0.016
35	342	10.480	10.447	-0.033
35	345	10.422	10.441	0.019
35	346	10.389	10.384	-0.005
35	347	10.368	10.438	0.070
35	348	10.330	10.321	-0.009
50	349	10.458	10.463	0.005
50	350	10.575	10.608	0.033
50	351	10.364	10.407	0.043
50	353	10.487	10.447	-0.040
50	354	10.331	10.308	-0.023
55	355	10.334	10.283	-0.051
55	357	10.561	10.638	0.077
55	359	10.425	10.451	0.026
55	362	10.439	10.372	-0.067
55	363	10.526	10.528	0.002
100	1	10.403	10.432	0.029
100	3	10.322	10.216	-0.106
100	4	10.425	10.449	0.024
100	5	10.421	10.500	0.079
100	6	10.330	10.390	0.060
100	7	10.459	10.480	0.021
100	8	10.362	10.260	-0.102
100	9	10.400	10.406	0.006
100	10	10.440	10.463	0.023
100	11	10.627	10.659	0.032
100	12	10.191	10.170	-0.021
100	13	10.471	10.446	-0.025
100	14	10.365	10.404	0.039
100	15	10.527	10.625	0.098
100	16	10.409	10.379	-0.030
100	17	10.212	10.266	0.054
100	18	10.295	10.331	0.036
100	19	10.361	10.341	-0.020
100	20	10.494	10.557	0.063
100	21	10.342	10.304	-0.038
100	22	10.267	10.481	0.214
100	24	10.445	10.501	0.056
105	25	10.615	10.497	-0.118
105	26	10.473	10.478	0.005
105	27	10.426	10.406	-0.020
105	28	10.407	10.393	-0.014
105	29	10.610	10.656	0.046
Max		10.627	10.659	0.214
Average		10.423	10.433	0.009
Min		10.104	10.103	-0.118
Std Dev		0.107	0.115	0.048

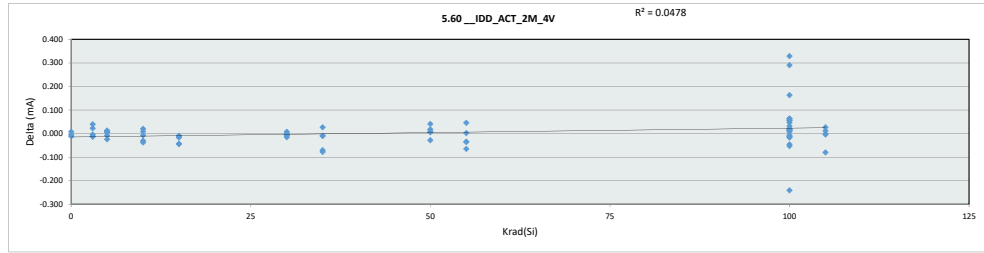


		5.55 IDD ACT 1M 14V										
Test Site	Tester											
Test Number	Unit	12	mA									
Max Limit	Min Limit	12	mA									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		10.275	10.295	10.319	10.427	10.103	10.372	10.321	10.308	10.283	10.170	10.393
Average		10.373	10.454	10.420	10.506	10.358	10.488	10.406	10.447	10.454	10.412	10.486
Max		10.427	10.574	10.554	10.563	10.520	10.611	10.447	10.608	10.638	10.659	10.656
UL		12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000

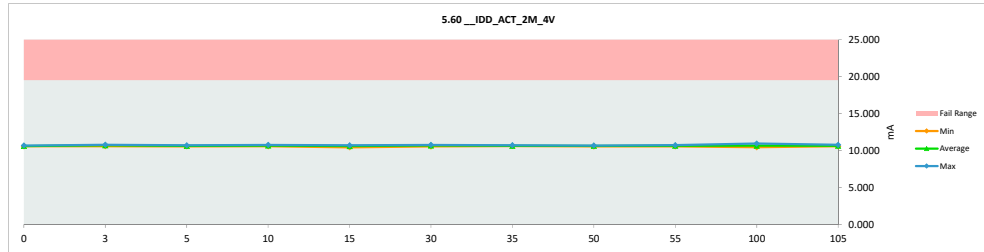


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		5.60 IDD_ACT_2M_4V		
Test Site	Tester			
Test Number	Unit	mA	mA	
Max Limit	19.5	19.5		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	10.574	10.569	-0.005
0	118	10.578	10.567	-0.011
0	122	10.651	10.659	0.008
3	301	10.749	10.772	0.023
3	306	10.585	10.572	-0.013
3	307	10.744	10.733	-0.011
3	308	10.580	10.620	0.040
3	309	10.771	10.767	-0.004
5	310	10.605	10.581	-0.024
5	311	10.565	10.557	-0.008
5	312	10.631	10.641	0.010
5	314	10.681	10.694	0.013
5	316	10.563	10.568	0.005
10	317	10.739	10.732	-0.007
10	318	10.784	10.746	-0.038
10	319	10.646	10.655	0.009
10	321	10.588	10.558	-0.030
10	322	10.602	10.623	0.021
15	323	10.431	10.422	-0.009
15	325	10.645	10.601	-0.044
15	326	10.589	10.572	-0.017
15	328	10.664	10.651	-0.013
15	333	10.741	10.698	-0.043
30	334	10.753	10.744	-0.009
30	335	10.551	10.548	-0.003
30	337	10.627	10.612	-0.015
30	339	10.744	10.752	0.008
30	341	10.683	10.684	0.001
35	342	10.690	10.620	-0.070
35	345	10.614	10.605	-0.009
35	346	10.713	10.703	-0.010
35	347	10.578	10.605	0.027
35	348	10.678	10.600	-0.078
50	349	10.586	10.595	0.009
50	350	10.659	10.665	0.006
50	351	10.599	10.640	0.041
50	353	10.669	10.641	-0.028
50	354	10.529	10.547	0.018
55	355	10.592	10.557	-0.035
55	357	10.674	10.720	0.046
55	359	10.658	10.660	0.002
55	362	10.665	10.600	-0.065
55	363	10.699	10.664	-0.035
100	1	10.591	10.599	0.008
100	3	10.599	10.928	0.329
100	4	10.658	10.651	-0.007
100	5	10.562	10.609	0.047
100	6	10.506	10.562	0.056
100	7	10.678	10.691	0.013
100	8	10.669	10.428	-0.241
100	9	10.658	10.674	0.016
100	10	10.580	10.612	0.032
100	11	10.824	10.811	-0.013
100	12	10.618	10.908	0.290
100	13	10.641	10.630	-0.011
100	14	10.605	10.619	0.014
100	15	10.688	10.752	0.064
100	16	10.640	10.623	-0.017
100	17	10.439	10.461	0.022
100	18	10.517	10.534	0.017
100	19	10.634	10.581	-0.053
100	20	10.634	10.691	0.057
100	21	10.605	10.559	-0.046
100	22	10.505	10.668	0.163
100	24	10.673	10.736	0.063
105	25	10.683	10.603	-0.080
105	26	10.656	10.655	-0.001
105	27	10.598	10.610	0.012
105	28	10.571	10.567	-0.004
105	29	10.727	10.754	0.027
Max		10.824	10.928	0.329
Average		10.635	10.641	0.006
Min		10.431	10.422	-0.241
Std Dev		0.076	0.091	0.070

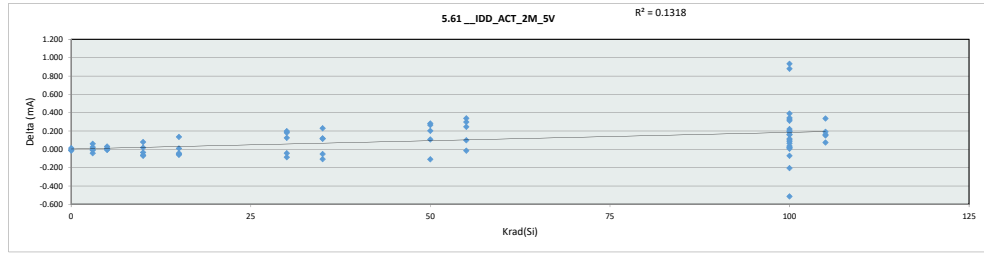


		5.60 IDD_ACT_2M_4V									
Test Site	Tester										
Test Number	Unit	19.5	mA								
Max Limit	19.5	19.5	mA								
Min Limit											
Krad(Si)	LL	3	5	10	15	30	35	50	55	100	105
Min	10.567	10.572	10.557	10.558	10.422	10.548	10.600	10.547	10.557	10.428	10.567
Average	10.598	10.693	10.608	10.663	10.589	10.668	10.627	10.618	10.640	10.651	10.638
Max	10.659	10.772	10.694	10.746	10.698	10.752	10.703	10.665	10.720	10.928	10.754
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500

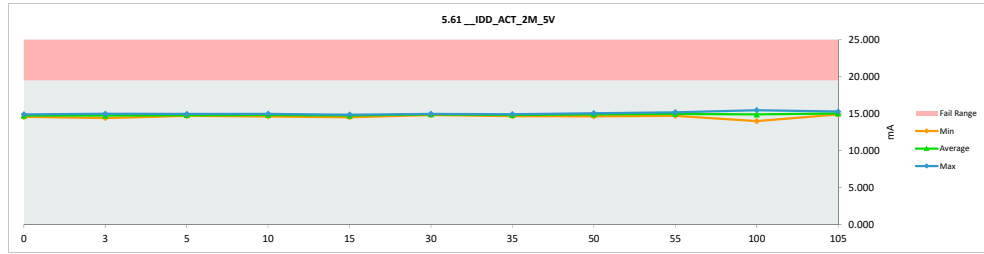


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		5.61 IDD_ACT_2M_5V		
Test Site	Tester			
Test Number	Unit	mA	mA	
Max Limit	19.5	19.5		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	14.561	14.553	-0.008
0	118	14.731	14.716	-0.015
0	122	14.871	14.885	0.014
3	301	14.596	14.620	0.024
3	306	14.667	14.666	-0.001
3	307	15.004	14.961	-0.043
3	308	14.333	14.393	0.060
3	309	14.972	14.963	-0.009
5	310	14.722	14.717	-0.005
5	311	14.678	14.677	-0.001
5	312	14.835	14.830	-0.005
5	314	14.909	14.940	0.031
5	316	14.693	14.705	0.012
10	317	14.985	14.951	-0.034
10	318	14.956	14.885	-0.071
10	319	14.708	14.788	0.080
10	321	14.675	14.611	-0.064
10	322	14.766	14.785	0.019
15	323	14.493	14.503	0.010
15	325	14.564	14.700	0.136
15	326	14.781	14.738	-0.043
15	328	14.878	14.825	-0.053
15	333	14.905	14.845	-0.060
30	334	14.891	14.885	-0.006
30	335	14.647	14.828	0.181
30	337	14.752	14.951	0.199
30	339	14.967	14.925	-0.042
30	341	14.822	14.947	0.125
35	342	14.905	14.797	-0.108
35	345	14.799	14.913	0.114
35	346	14.582	14.811	0.229
35	347	14.703	14.651	-0.052
35	348	14.668	14.788	0.120
50	349	14.752	15.016	0.264
50	350	14.725	14.616	-0.109
50	351	14.734	15.016	0.282
50	353	14.837	15.038	0.201
50	354	14.627	14.734	0.107
55	355	14.595	14.693	0.098
55	357	14.831	15.169	0.338
55	359	14.732	15.030	0.298
55	362	14.738	14.724	-0.014
55	363	14.879	15.125	0.246
100	1	14.701	14.719	0.018
100	3	14.563	15.443	0.880
100	4	14.697	14.811	0.114
100	5	14.678	15.069	0.391
100	6	14.601	14.822	0.221
100	7	14.760	14.766	0.006
100	8	14.672	14.601	-0.071
100	9	14.782	14.577	-0.205
100	10	14.761	15.091	0.330
100	11	14.842	14.929	0.087
100	12	14.471	15.404	0.933
100	13	14.867	15.067	0.200
100	14	14.722	14.885	0.163
100	15	14.884	15.227	0.343
100	16	14.839	14.997	0.158
100	17	14.481	13.966	-0.515
100	18	14.551	14.587	0.036
100	19	14.669	14.771	0.102
100	20	14.778	14.971	0.193
100	21	14.703	14.769	0.066
100	22	14.633	14.945	0.312
100	24	14.850	14.874	0.024
105	25	14.800	14.875	0.075
105	26	14.870	15.021	0.151
105	27	14.784	14.976	0.192
105	28	14.732	14.893	0.161
105	29	14.927	15.264	0.337
Max		15.004	15.443	0.933
Average		14.744	14.845	0.101
Min		14.333	13.966	-0.515
Std Dev		0.136	0.224	0.204

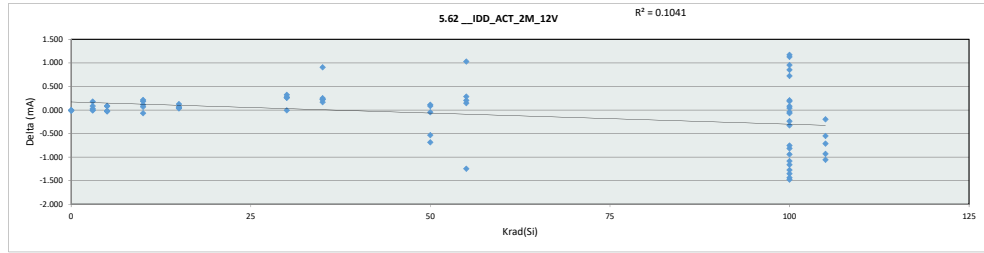


		5.61 IDD_ACT_2M_5V										
Test Site	Tester											
Test Number	Unit	19.5	mA									
Max Limit	19.5	mA										
Min Limit		mA										
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		14.553	14.393	14.677	14.611	14.503	14.805	14.651	14.616	14.693	13.966	14.875
Average		14.718	14.721	14.774	14.804	14.722	14.891	14.792	14.884	14.948	14.877	15.006
Max		14.885	14.963	14.940	14.951	14.845	14.951	14.913	15.038	15.169	15.443	15.264
UL		19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500

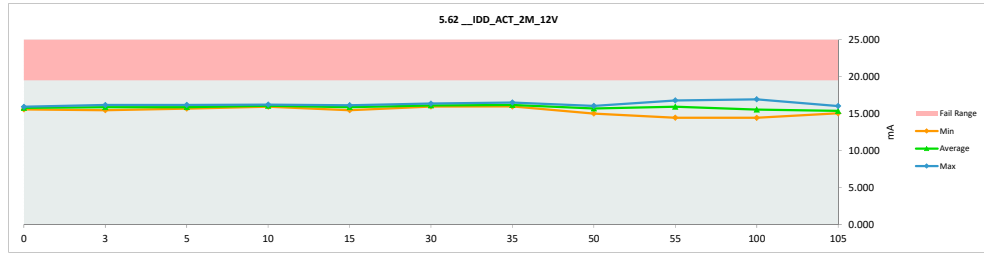


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		5.62 IDD_ACT_2M_12V		
Test Site	Tester			
Test Number	Unit	mA	mA	
Max Limit	19.5	19.5		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	15.579	15.564	-0.015
0	118	15.848	15.842	-0.006
0	122	15.917	15.917	0.000
3	301	15.720	15.747	0.027
3	306	15.723	15.904	0.181
3	307	16.156	16.155	-0.001
3	308	15.375	15.461	0.086
3	309	16.055	16.046	-0.009
5	310	15.752	15.721	-0.031
5	311	15.806	15.785	-0.021
5	312	16.073	16.162	0.089
5	314	15.911	15.989	0.078
5	316	15.691	15.672	-0.019
10	317	16.139	16.206	0.067
10	318	16.026	16.137	0.111
10	319	15.718	15.933	0.215
10	321	16.058	15.992	-0.066
10	322	15.883	16.067	0.184
15	323	15.383	15.446	0.063
15	325	15.552	15.593	0.041
15	326	15.964	16.025	0.061
15	328	16.060	16.095	0.035
15	333	16.007	16.132	0.125
30	334	16.120	16.115	-0.005
30	335	15.677	15.948	0.271
30	337	15.698	16.022	0.324
30	339	16.092	16.352	0.260
30	341	15.838	16.108	0.270
35	342	15.944	16.112	0.168
35	345	15.923	16.151	0.228
35	346	15.591	16.497	0.906
35	347	15.830	16.052	0.222
35	348	15.703	15.956	0.253
50	349	15.939	16.025	0.086
50	350	16.093	15.559	-0.534
50	351	15.883	15.835	-0.048
50	353	15.916	16.030	0.114
50	354	15.685	14.998	-0.687
55	355	15.678	14.434	-1.244
55	357	16.003	16.289	0.286
55	359	15.841	15.988	0.147
55	362	15.744	16.775	1.031
55	363	15.971	16.177	0.206
100	1	15.770	14.418	-1.352
100	3	15.575	16.298	0.723
100	4	15.743	14.661	-1.082
100	5	15.900	15.981	0.081
100	6	15.684	14.931	-0.753
100	7	15.918	15.959	0.041
100	8	15.679	16.633	0.954
100	9	15.790	16.919	1.129
100	10	15.919	15.880	-0.039
100	11	15.996	16.184	0.188
100	12	15.474	16.327	0.853
100	13	15.980	15.744	-0.236
100	14	15.734	14.794	-0.940
100	15	16.101	16.034	-0.067
100	16	15.846	15.519	-0.327
100	17	15.594	16.767	1.173
100	18	15.550	15.757	0.207
100	19	15.754	14.594	-1.160
100	20	15.887	14.452	-1.435
100	21	15.745	14.473	-1.272
100	22	15.824	15.010	-0.814
100	24	15.991	14.515	-1.476
105	25	16.135	15.080	-1.055
105	26	15.996	15.446	-0.550
105	27	15.918	15.206	-0.712
105	28	15.962	15.031	-0.931
105	29	16.209	16.013	-0.196
Max	16.209	16.919	1.713	
Average	15.846	15.766	-0.080	
Min	15.375	14.418	-1.476	
Std Dev	0.189	0.595	0.600	

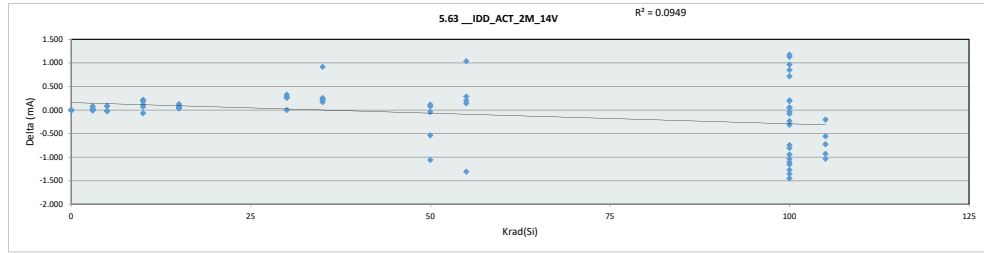


		5.62 IDD_ACT_2M_12V									
Test Site	Tester										
Test Number	Unit	19.5	mA								
Max Limit	19.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	15.564	15.461	15.672	15.933	15.446	15.948	15.956	14.998	14.434	14.418	15.031
Average	15.774	15.863	15.866	16.067	15.858	16.109	16.154	15.689	15.933	15.539	15.355
Max	15.917	16.155	16.162	16.206	16.132	16.352	16.497	16.030	16.775	16.919	16.013
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500

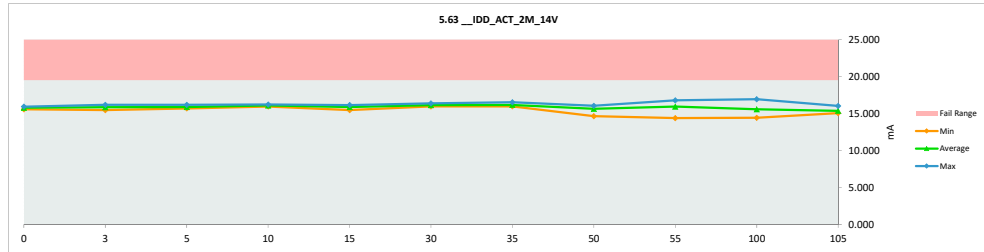


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5.63 IDD_ACT_2M_14V				
Test Site	Tester	Test Number	Unit	Max Limit
			mA	19.5
			mA	19.5
			Min Limit	
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	15.580	15.570	-0.010
0	118	15.854	15.849	-0.005
0	122	15.920	15.923	0.003
3	301	15.724	15.752	0.028
3	306	15.733	15.746	0.013
3	307	16.166	16.161	-0.005
3	308	15.385	15.463	0.078
3	309	16.058	16.053	-0.005
5	310	15.757	15.731	-0.026
5	311	15.809	15.790	-0.019
5	312	16.081	16.166	0.085
5	314	15.915	15.996	0.081
5	316	15.698	15.681	-0.017
10	317	16.143	16.212	0.069
10	318	16.033	16.144	0.111
10	319	15.723	15.941	0.218
10	321	16.062	15.996	-0.066
10	322	15.887	16.075	0.188
15	323	15.385	15.460	0.075
15	325	15.559	15.597	0.038
15	326	15.971	16.030	0.059
15	328	16.069	16.102	0.033
15	333	16.012	16.137	0.125
30	334	16.122	16.121	-0.001
30	335	15.678	15.954	0.276
30	337	15.703	16.031	0.328
30	339	16.100	16.360	0.260
30	341	15.843	16.113	0.270
35	342	15.953	16.120	0.167
35	345	15.931	16.153	0.222
35	346	15.598	16.514	0.916
35	347	15.840	16.060	0.220
35	348	15.708	15.964	0.256
50	349	15.947	16.026	0.079
50	350	16.103	15.568	-0.535
50	351	15.886	15.846	-0.040
50	353	15.922	16.038	0.116
50	354	15.689	14.630	-1.059
55	355	15.683	14.375	-1.308
55	357	16.007	16.293	0.286
55	359	15.847	15.991	0.144
55	362	15.747	16.784	1.037
55	363	15.977	16.179	0.202
100	1	15.775	14.420	-1.355
100	3	15.581	16.297	0.716
100	4	15.749	14.647	-1.102
100	5	15.910	15.966	0.056
100	6	15.686	14.944	-0.742
100	7	15.924	15.970	0.046
100	8	15.684	16.648	0.964
100	9	15.796	16.926	1.130
100	10	15.927	15.890	-0.037
100	11	16.002	16.193	0.191
100	12	15.481	16.329	0.848
100	13	15.984	15.752	-0.232
100	14	15.741	14.799	-0.942
100	15	16.104	16.026	-0.078
100	16	15.852	15.540	-0.312
100	17	15.600	16.776	1.176
100	18	15.556	15.762	0.206
100	19	15.757	14.604	-1.153
100	20	15.890	14.440	-1.450
100	21	15.750	14.480	-1.270
100	22	15.829	15.022	-0.807
100	24	16.000	14.965	-1.035
105	25	16.139	15.104	-1.035
105	26	16.002	15.445	-0.557
105	27	15.924	15.197	-0.727
105	28	15.965	15.036	-0.929
105	29	16.211	16.009	-0.202
	Max	16.211	16.926	1.176
	Average	15.852	15.770	-0.082
	Min	15.385	14.375	-1.450
	Std Dev	0.189	0.595	0.596

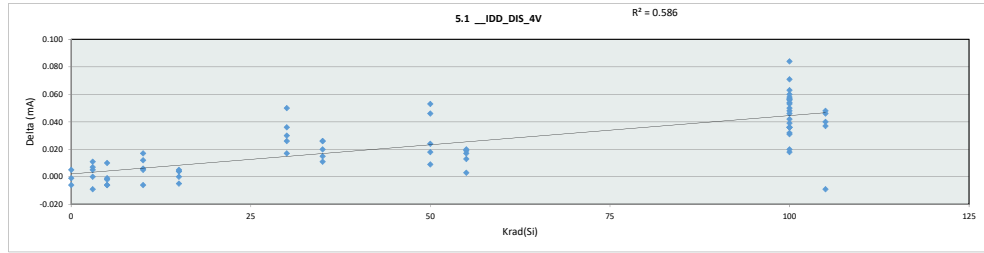


5.63 IDD_ACT_2M_14V											
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit						
			mA	19.5							
			mA								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	15.570	15.463	15.681	15.941	15.460	15.954	15.964	14.630	14.375	14.420	15.036
Average	15.781	15.835	15.873	16.074	15.865	16.116	16.162	15.622	15.924	15.563	15.358
Max	15.923	16.161	16.166	16.212	16.137	16.360	16.514	16.038	16.784	16.926	16.009
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500

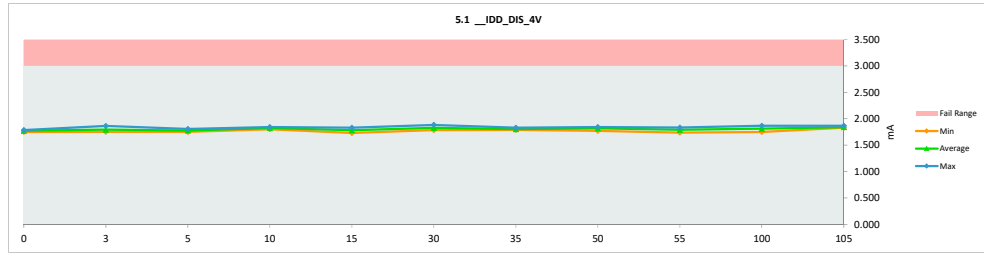


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5.1 IDD_DIS_4V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	3			
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.751	1.750	-0.001
0	118	1.789	1.783	-0.006
0	122	1.781	1.786	0.005
3	301	1.776	1.783	0.007
3	306	1.785	1.776	-0.009
3	307	1.858	1.863	0.005
3	308	1.742	1.753	0.011
3	309	1.798	1.798	0.000
5	310	1.774	1.768	-0.006
5	311	1.755	1.754	-0.001
5	312	1.797	1.807	0.010
5	314	1.812	1.806	-0.006
5	316	1.753	1.751	-0.002
10	317	1.828	1.834	0.006
10	318	1.837	1.842	0.005
10	319	1.821	1.833	0.012
10	321	1.819	1.813	-0.006
10	322	1.785	1.802	0.017
15	323	1.731	1.731	0.000
15	325	1.750	1.745	-0.005
15	326	1.791	1.795	0.004
15	328	1.826	1.831	0.005
15	333	1.803	1.807	0.004
30	334	1.858	1.884	0.026
30	335	1.750	1.786	0.036
30	337	1.768	1.818	0.050
30	339	1.799	1.829	0.030
30	341	1.804	1.821	0.017
35	342	1.805	1.831	0.026
35	345	1.784	1.795	0.011
35	346	1.775	1.790	0.015
35	347	1.782	1.808	0.026
35	348	1.770	1.790	0.020
50	349	1.792	1.816	0.024
50	350	1.790	1.843	0.053
50	351	1.788	1.834	0.046
50	353	1.812	1.830	0.018
50	354	1.762	1.771	0.009
55	355	1.718	1.738	0.020
55	357	1.815	1.834	0.019
55	359	1.792	1.805	0.013
55	362	1.747	1.750	0.003
55	363	1.815	1.832	0.017
100	1	1.743	1.797	0.054
100	3	1.748	1.768	0.020
100	4	1.786	1.839	0.053
100	5	1.759	1.815	0.056
100	6	1.750	1.798	0.048
100	7	1.765	1.821	0.056
100	8	1.752	1.783	0.031
100	9	1.776	1.815	0.039
100	10	1.801	1.851	0.050
100	11	1.810	1.867	0.057
100	12	1.735	1.767	0.032
100	13	1.757	1.793	0.036
100	14	1.765	1.828	0.063
100	15	1.806	1.866	0.060
100	16	1.796	1.814	0.018
100	17	1.714	1.785	0.071
100	18	1.714	1.750	0.036
100	19	1.763	1.799	0.036
100	20	1.747	1.793	0.046
100	21	1.748	1.790	0.042
100	22	1.778	1.862	0.084
100	24	1.803	1.861	0.058
105	25	1.840	1.831	-0.009
105	26	1.800	1.846	0.046
105	27	1.792	1.832	0.040
105	28	1.785	1.833	0.048
105	29	1.830	1.867	0.037
Max		1.858	1.884	0.084
Average		1.782	1.807	0.025
Min		1.714	1.731	-0.009
Std Dev		0.032	0.035	0.023

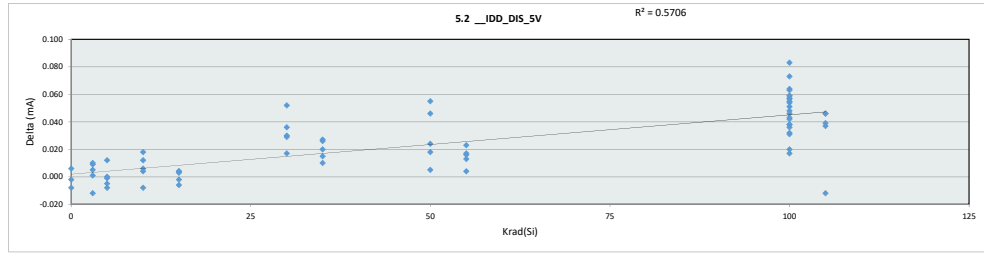


5.1 IDD_DIS_4V											
Test Site											
Tester											
Test Number											
Max Limit	3	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.750	1.753	1.751	1.802	1.731	1.786	1.790	1.771	1.738	1.750	1.831
Average	1.773	1.795	1.777	1.825	1.782	1.828	1.803	1.819	1.792	1.812	1.842
Max	1.786	1.863	1.807	1.842	1.831	1.884	1.831	1.843	1.834	1.867	1.867
UL	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000

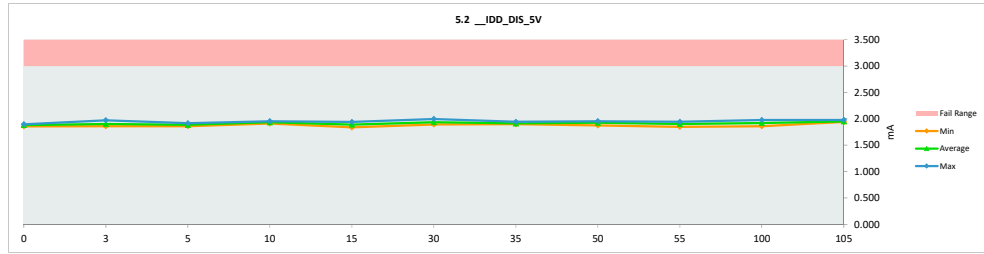


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5.2 IDD_DIS_5V				
Test Site	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.859	1.857	-0.002
0	118	1.899	1.891	-0.008
0	122	1.890	1.896	0.006
3	301	1.883	1.892	0.009
3	306	1.895	1.883	-0.012
3	307	1.967	1.972	0.005
3	308	1.849	1.859	0.010
3	309	1.907	1.908	0.001
5	310	1.880	1.875	-0.005
5	311	1.861	1.861	0.000
5	312	1.904	1.916	0.012
5	314	1.923	1.915	-0.008
5	316	1.860	1.859	-0.001
10	317	1.938	1.944	0.006
10	318	1.949	1.953	0.004
10	319	1.930	1.942	0.012
10	321	1.929	1.921	-0.008
10	322	1.894	1.912	0.018
15	323	1.839	1.837	-0.002
15	325	1.858	1.852	-0.006
15	326	1.898	1.902	0.004
15	328	1.937	1.940	0.003
15	333	1.911	1.915	0.004
30	334	1.966	1.996	0.030
30	335	1.857	1.893	0.036
30	337	1.876	1.928	0.052
30	339	1.906	1.935	0.029
30	341	1.911	1.928	0.017
35	342	1.916	1.943	0.027
35	345	1.894	1.904	0.010
35	346	1.883	1.898	0.015
35	347	1.890	1.916	0.026
35	348	1.880	1.900	0.020
50	349	1.900	1.924	0.024
50	350	1.898	1.953	0.055
50	351	1.897	1.943	0.046
50	353	1.920	1.938	0.018
50	354	1.870	1.875	0.005
55	355	1.824	1.847	0.023
55	357	1.927	1.943	0.016
55	359	1.900	1.913	0.013
55	362	1.853	1.857	0.004
55	363	1.926	1.943	0.017
100	1	1.849	1.906	0.057
100	3	1.857	1.877	0.020
100	4	1.894	1.948	0.054
100	5	1.864	1.921	0.057
100	6	1.858	1.906	0.048
100	7	1.873	1.928	0.055
100	8	1.859	1.891	0.032
100	9	1.882	1.925	0.043
100	10	1.907	1.958	0.051
100	11	1.918	1.977	0.059
100	12	1.844	1.875	0.031
100	13	1.864	1.902	0.038
100	14	1.872	1.936	0.064
100	15	1.914	1.977	0.063
100	16	1.903	1.920	0.017
100	17	1.819	1.892	0.073
100	18	1.821	1.859	0.038
100	19	1.870	1.906	0.036
100	20	1.853	1.899	0.046
100	21	1.858	1.900	0.042
100	22	1.888	1.971	0.083
100	24	1.911	1.970	0.059
105	25	1.952	1.940	-0.012
105	26	1.909	1.955	0.046
105	27	1.901	1.940	0.039
105	28	1.894	1.940	0.046
105	29	1.941	1.978	0.037
Max		1.967	1.996	0.083
Average		1.890	1.915	0.025
Min		1.819	1.837	-0.012
Std Dev		0.033	0.036	0.023

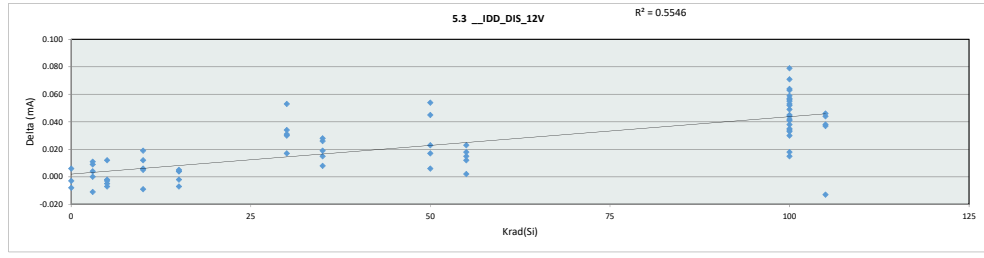


5.2 IDD_DIS_5V											
Test Site	Serial #	PRE_HDR	POST_HDR	Delta	LL	Min	Average	Max	UL		
0	3	5	10	15	30	35	50	55	100	105	
1.857	1.859	1.859	1.912	1.837	1.893	1.898	1.875	1.847	1.859	1.940	
1.881	1.903	1.885	1.934	1.889	1.936	1.912	1.927	1.901	1.920	1.951	
1.896	1.972	1.916	1.953	1.940	1.996	1.943	1.953	1.943	1.977	1.978	
3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	

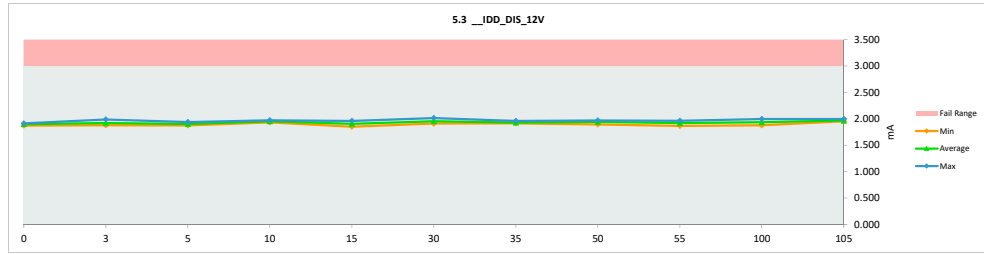


HDR TID Report
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5.3 IDD_DIS_12V				
Test Site	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.876	1.873	-0.003
0	118	1.918	1.910	-0.008
0	122	1.906	1.912	0.006
3	301	1.902	1.911	0.009
3	306	1.913	1.902	-0.011
3	307	1.984	1.988	0.004
3	308	1.867	1.878	0.011
3	309	1.924	1.924	0.000
5	310	1.896	1.891	-0.005
5	311	1.882	1.880	-0.002
5	312	1.926	1.938	0.012
5	314	1.937	1.930	-0.007
5	316	1.877	1.874	-0.003
10	317	1.958	1.964	0.006
10	318	1.966	1.971	0.005
10	319	1.945	1.957	0.012
10	321	1.952	1.943	-0.009
10	322	1.913	1.932	0.019
15	323	1.853	1.851	-0.002
15	325	1.874	1.867	-0.007
15	326	1.915	1.919	0.004
15	328	1.955	1.959	0.004
15	333	1.928	1.933	0.005
30	334	1.984	2.014	0.030
30	335	1.876	1.910	0.034
30	337	1.891	1.944	0.053
30	339	1.924	1.955	0.031
30	341	1.927	1.944	0.017
35	342	1.932	1.960	0.028
35	345	1.913	1.921	0.008
35	346	1.899	1.914	0.015
35	347	1.906	1.932	0.026
35	348	1.897	1.916	0.019
50	349	1.919	1.942	0.023
50	350	1.916	1.970	0.054
50	351	1.911	1.956	0.045
50	353	1.937	1.954	0.017
50	354	1.887	1.893	0.006
55	355	1.842	1.865	0.023
55	357	1.947	1.962	0.015
55	359	1.919	1.931	0.012
55	362	1.873	1.875	0.002
55	363	1.944	1.962	0.018
100	1	1.866	1.922	0.056
100	3	1.874	1.892	0.018
100	4	1.911	1.963	0.052
100	5	1.883	1.938	0.055
100	6	1.877	1.922	0.045
100	7	1.895	1.948	0.053
100	8	1.874	1.907	0.033
100	9	1.897	1.938	0.041
100	10	1.923	1.972	0.049
100	11	1.939	1.996	0.057
100	12	1.861	1.891	0.030
100	13	1.883	1.918	0.035
100	14	1.889	1.952	0.063
100	15	1.933	1.997	0.064
100	16	1.918	1.933	0.015
100	17	1.839	1.910	0.071
100	18	1.839	1.877	0.038
100	19	1.890	1.924	0.034
100	20	1.871	1.915	0.044
100	21	1.874	1.916	0.042
100	22	1.905	1.984	0.079
100	24	1.930	1.989	0.059
105	25	1.971	1.958	-0.013
105	26	1.928	1.974	0.046
105	27	1.916	1.953	0.037
105	28	1.913	1.957	0.044
105	29	1.958	1.996	0.038
Max		1.984	2.014	0.079
Average		1.908	1.932	0.024
Min		1.839	1.851	-0.013
Std Dev		0.033	0.036	0.023

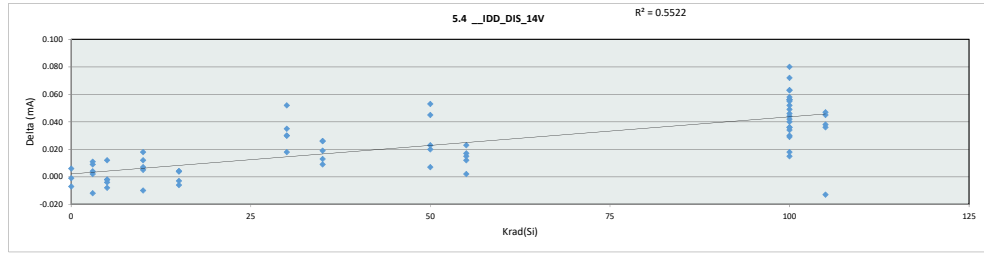


5.3 IDD_DIS_12V												
Test Site	Serial #	PRE_HDR	POST_HDR	Delta								
0	117	1.876	1.873	-0.003								
0	118	1.918	1.910	-0.008								
0	122	1.906	1.912	0.006								
3	301	1.902	1.911	0.009								
3	306	1.913	1.902	-0.011								
3	307	1.984	1.988	0.004								
3	308	1.867	1.878	0.011								
3	309	1.924	1.924	0.000								
5	310	1.896	1.891	-0.005								
5	311	1.882	1.880	-0.002								
5	312	1.926	1.938	0.012								
5	314	1.937	1.930	-0.007								
5	316	1.877	1.874	-0.003								
10	317	1.958	1.964	0.006								
10	318	1.966	1.971	0.005								
10	319	1.945	1.957	0.012								
10	321	1.952	1.943	-0.009								
10	322	1.913	1.932	0.019								
15	323	1.853	1.851	-0.002								
15	325	1.874	1.867	-0.007								
15	326	1.915	1.919	0.004								
15	328	1.955	1.959	0.004								
15	333	1.928	1.933	0.005								
30	334	1.984	2.014	0.030								
30	335	1.876	1.910	0.034								
30	337	1.891	1.944	0.053								
30	339	1.924	1.955	0.031								
30	341	1.927	1.944	0.017								
35	342	1.932	1.960	0.028								
35	345	1.913	1.921	0.008								
35	346	1.899	1.914	0.015								
35	347	1.906	1.932	0.026								
35	348	1.897	1.916	0.019								
50	349	1.919	1.942	0.023								
50	350	1.916	1.970	0.054								
50	351	1.911	1.956	0.045								
50	353	1.937	1.954	0.017								
50	354	1.887	1.893	0.006								
55	355	1.842	1.865	0.023								
55	357	1.947	1.962	0.015								
55	359	1.919	1.931	0.012								
55	362	1.873	1.875	0.002								
55	363	1.944	1.962	0.018								
100	1	1.866	1.922	0.056								
100	3	1.874	1.892	0.018								
100	4	1.911	1.963	0.052								
100	5	1.883	1.938	0.055								
100	6	1.877	1.922	0.045								
100	7	1.895	1.948	0.053								
100	8	1.874	1.907	0.033								
100	9	1.897	1.938	0.041								
100	10	1.923	1.972	0.049								
100	11	1.939	1.996	0.057								
100	12	1.861	1.891	0.030								
100	13	1.883	1.918	0.035								
100	14	1.889	1.952	0.063								
100	15	1.933	1.997	0.064								
100	16	1.918	1.933	0.015								
100	17	1.839	1.910	0.071								
100	18	1.839	1.877	0.038								
100	19	1.890	1.924	0.034								
100	20	1.871	1.915	0.044								
100	21	1.874	1.916	0.042								
100	22	1.905	1.984	0.079								
100	24	1.930	1.989	0.059								
105	25	1.971	1.958	-0.013								
105	26	1.928	1.974	0.046								
105	27	1.916	1.953	0.037								
105	28	1.913	1.957	0.044								
105	29	1.958	1.996	0.038								
LL		1.873	1.878	1.874	1.932	1.851	1.910	1.914	1.893	1.865	1.877	1.953
Min		1.898	1.921	1.903	1.953	1.906	1.953	1.929	1.943	1.919	1.937	1.968
Average		1.912	1.988	1.938	1.971	1.959	2.014	1.960	1.970	1.962	1.997	1.996
Max		3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000
UL		3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000

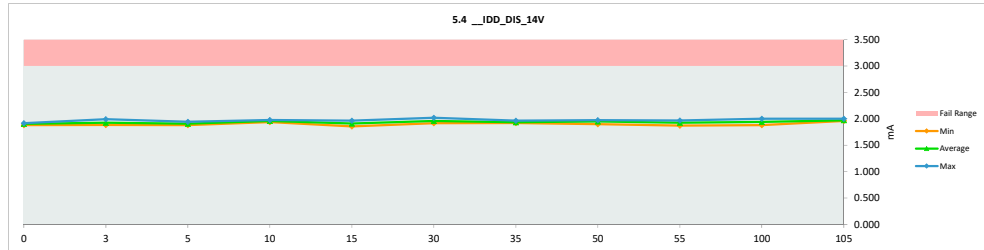


HDR TID Report
TPS7H5001-SP

5.4 IDD_DIS_14V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	3	3		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.879	1.878	-0.001
0	118	1.922	1.915	-0.007
0	122	1.910	1.916	0.006
3	301	1.906	1.915	0.009
3	306	1.918	1.906	-0.012
3	307	1.988	1.992	0.004
3	308	1.871	1.882	0.011
3	309	1.927	1.929	0.002
5	310	1.900	1.896	-0.004
5	311	1.886	1.884	-0.002
5	312	1.930	1.942	0.012
5	314	1.942	1.934	-0.008
5	316	1.881	1.879	-0.002
10	317	1.961	1.968	0.007
10	318	1.970	1.975	0.005
10	319	1.949	1.961	0.012
10	321	1.956	1.946	-0.010
10	322	1.917	1.935	0.018
15	323	1.858	1.855	-0.003
15	325	1.878	1.872	-0.006
15	326	1.919	1.923	0.004
15	328	1.960	1.964	0.004
15	333	1.933	1.937	0.004
30	334	1.988	2.018	0.030
30	335	1.879	1.914	0.035
30	337	1.896	1.948	0.052
30	339	1.929	1.959	0.030
30	341	1.930	1.948	0.018
35	342	1.937	1.963	0.026
35	345	1.916	1.925	0.009
35	346	1.904	1.917	0.013
35	347	1.910	1.936	0.026
35	348	1.902	1.921	0.019
50	349	1.923	1.946	0.023
50	350	1.920	1.973	0.053
50	351	1.915	1.960	0.045
50	353	1.940	1.960	0.020
50	354	1.891	1.898	0.007
55	355	1.846	1.869	0.023
55	357	1.951	1.966	0.015
55	359	1.924	1.936	0.012
55	362	1.877	1.879	0.002
55	363	1.948	1.965	0.017
100	1	1.871	1.927	0.056
100	3	1.878	1.896	0.018
100	4	1.916	1.968	0.052
100	5	1.887	1.943	0.056
100	6	1.881	1.927	0.046
100	7	1.898	1.953	0.055
100	8	1.879	1.909	0.030
100	9	1.901	1.941	0.040
100	10	1.928	1.977	0.049
100	11	1.944	2.000	0.056
100	12	1.866	1.895	0.029
100	13	1.887	1.923	0.036
100	14	1.893	1.956	0.063
100	15	1.937	2.000	0.063
100	16	1.922	1.937	0.015
100	17	1.842	1.914	0.072
100	18	1.844	1.880	0.036
100	19	1.894	1.928	0.034
100	20	1.874	1.918	0.044
100	21	1.878	1.920	0.042
100	22	1.909	1.989	0.080
100	24	1.935	1.993	0.058
105	25	1.976	1.963	-0.013
105	26	1.932	1.977	0.045
105	27	1.921	1.959	0.038
105	28	1.916	1.963	0.047
105	29	1.963	1.999	0.036
Max		1.988	2.018	0.080
Average		1.912	1.937	0.024
Min		1.842	1.855	-0.013
Std Dev		0.034	0.036	0.023

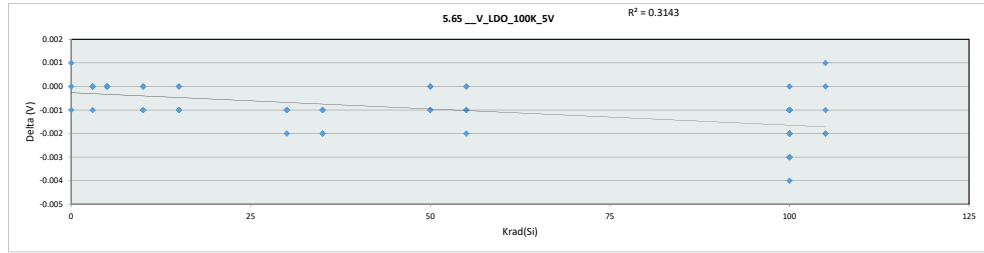


5.4 IDD_DIS_14V											
Test Site											
Tester											
Test Number											
Max Limit	3	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.878	1.882	1.879	1.935	1.855	1.914	1.917	1.898	1.869	1.880	1.959
Average	1.903	1.925	1.907	1.957	1.910	1.957	1.932	1.947	1.923	1.941	1.972
Max	1.916	1.992	1.942	1.975	1.964	2.018	1.963	1.973	1.966	2.000	1.999
UL	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000

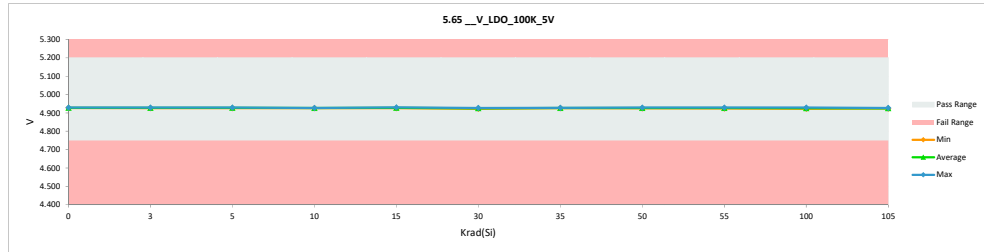


HDR TID Report
TPS7H5001-SP

5.65 V_LDO_100K_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	4.929	4.929	0.000
0	118	4.928	4.927	-0.001
0	122	4.927	4.928	0.001
3	301	4.928	4.928	0.000
3	306	4.928	4.928	0.000
3	307	4.926	4.926	0.000
3	308	4.930	4.929	-0.001
3	309	4.926	4.926	0.000
5	310	4.929	4.929	0.000
5	311	4.929	4.929	0.000
5	312	4.928	4.928	0.000
5	314	4.926	4.926	0.000
5	316	4.928	4.928	0.000
10	317	4.927	4.926	-0.001
10	318	4.926	4.926	0.000
10	319	4.928	4.927	-0.001
10	321	4.927	4.927	0.000
10	322	4.927	4.927	0.000
15	323	4.931	4.930	-0.001
15	325	4.930	4.929	-0.001
15	326	4.928	4.928	0.000
15	328	4.928	4.927	-0.001
15	333	4.926	4.926	0.000
30	334	4.925	4.923	-0.002
30	335	4.928	4.927	-0.001
30	337	4.928	4.927	-0.001
30	339	4.925	4.924	-0.001
30	341	4.927	4.926	-0.001
35	342	4.927	4.926	-0.001
35	345	4.928	4.927	-0.001
35	346	4.929	4.928	-0.001
35	347	4.928	4.926	-0.002
35	348	4.929	4.927	-0.002
50	349	4.926	4.926	0.000
50	350	4.926	4.925	-0.001
50	351	4.928	4.927	-0.001
50	353	4.927	4.927	0.000
50	354	4.929	4.929	0.000
55	355	4.929	4.929	0.000
55	357	4.926	4.924	-0.002
55	359	4.927	4.926	-0.001
55	362	4.928	4.928	0.000
55	363	4.927	4.926	-0.001
100	1	4.928	4.926	-0.002
100	3	4.929	4.928	-0.001
100	4	4.926	4.924	-0.002
100	5	4.928	4.925	-0.003
100	6	4.929	4.927	-0.002
100	7	4.928	4.926	-0.002
100	8	4.928	4.928	0.000
100	9	4.927	4.926	-0.001
100	10	4.926	4.924	-0.002
100	11	4.925	4.923	-0.002
100	12	4.930	4.929	-0.001
100	13	4.927	4.926	-0.001
100	14	4.928	4.926	-0.002
100	15	4.926	4.923	-0.003
100	16	4.927	4.926	-0.001
100	17	4.931	4.929	-0.002
100	18	4.929	4.927	-0.002
100	19	4.928	4.927	-0.001
100	20	4.927	4.924	-0.003
100	21	4.929	4.928	-0.001
100	22	4.929	4.925	-0.004
100	24	4.929	4.926	-0.003
105	25	4.925	4.926	0.001
105	26	4.927	4.925	-0.002
105	27	4.927	4.927	0.000
105	28	4.927	4.925	-0.002
105	29	4.925	4.924	-0.001
Max	4.931	4.930	0.001	
Average	4.928	4.927	-0.001	
Min	4.925	4.923	-0.004	
Std Dev	0.001	0.002	0.001	

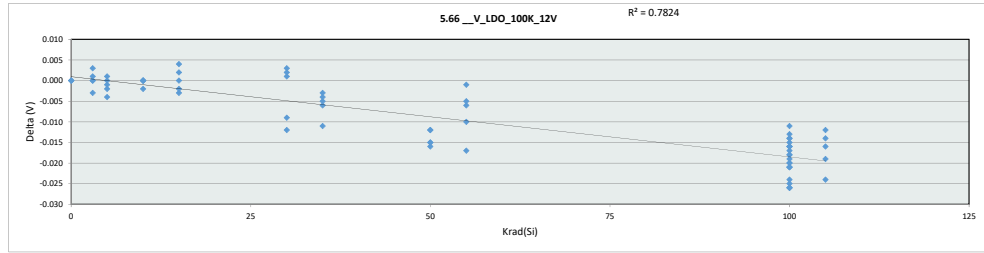


5.65 V_LDO_100K_5V											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	4.927	4.926	4.926	4.926	4.926	4.923	4.926	4.925	4.924	4.923	4.924
Average	4.928	4.927	4.928	4.927	4.928	4.925	4.927	4.927	4.927	4.926	4.925
Max	4.929	4.929	4.929	4.927	4.930	4.927	4.928	4.929	4.929	4.929	4.927
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

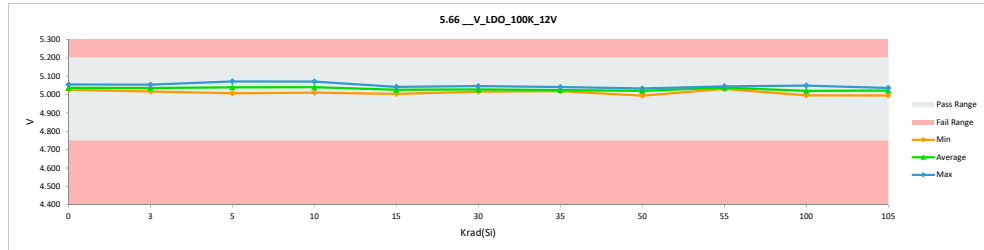


HDR TID Report
TPS7H5001-SP

5.66 V_LDO_100K_12V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	5.029	5.029	0.000
0	118	5.055	5.055	0.000
0	122	5.026	5.026	0.000
3	301	5.054	5.054	0.000
3	306	5.036	5.039	0.003
3	307	5.020	5.017	-0.003
3	308	5.042	5.043	0.001
3	309	5.028	5.028	0.000
5	310	5.032	5.033	0.001
5	311	5.063	5.059	-0.004
5	312	5.072	5.072	0.000
5	314	5.009	5.007	-0.002
5	316	5.027	5.026	-0.001
10	317	5.040	5.040	0.000
10	318	5.028	5.028	0.000
10	319	5.013	5.011	-0.002
10	321	5.071	5.071	0.000
10	322	5.051	5.051	0.000
15	323	5.007	5.004	-0.003
15	325	5.021	5.019	-0.002
15	326	5.030	5.032	0.002
15	328	5.038	5.042	0.004
15	333	5.035	5.035	0.000
30	334	5.035	5.026	-0.009
30	335	5.044	5.032	-0.012
30	337	5.015	5.016	0.001
30	339	5.044	5.046	0.002
30	341	5.017	5.020	0.003
35	342	5.027	5.024	-0.003
35	345	5.045	5.041	-0.004
35	346	5.029	5.018	-0.011
35	347	5.024	5.018	-0.006
35	348	5.027	5.022	-0.005
50	349	5.045	5.033	-0.012
50	350	5.042	5.026	-0.016
50	351	5.009	4.994	-0.015
50	353	5.031	5.016	-0.015
50	354	5.045	5.033	-0.012
55	355	5.041	5.036	-0.005
55	357	5.051	5.045	-0.006
55	359	5.048	5.038	-0.010
55	362	5.048	5.031	-0.017
55	363	5.041	5.040	-0.001
100	1	5.043	5.022	-0.021
100	3	5.031	5.011	-0.020
100	4	5.027	5.007	-0.020
100	5	5.051	5.032	-0.019
100	6	5.044	5.028	-0.016
100	7	5.067	5.049	-0.018
100	8	5.020	5.003	-0.017
100	9	5.012	4.997	-0.015
100	10	5.025	5.000	-0.025
100	11	5.062	5.038	-0.024
100	12	5.031	5.017	-0.014
100	13	5.053	5.032	-0.021
100	14	5.035	5.022	-0.013
100	15	5.044	5.030	-0.014
100	16	5.014	4.996	-0.018
100	17	5.054	5.043	-0.011
100	18	5.044	5.028	-0.016
100	19	5.050	5.029	-0.021
100	20	5.041	5.015	-0.026
100	21	5.026	5.010	-0.016
100	22	5.028	5.002	-0.026
100	24	5.060	5.034	-0.026
105	25	5.050	5.036	-0.014
105	26	5.046	5.027	-0.019
105	27	5.019	4.995	-0.024
105	28	5.044	5.028	-0.016
105	29	5.036	5.024	-0.012
	Max	5.072	5.072	0.004
	Average	5.037	5.028	-0.009
	Min	5.007	4.994	-0.026
	Std Dev	0.015	0.017	0.009

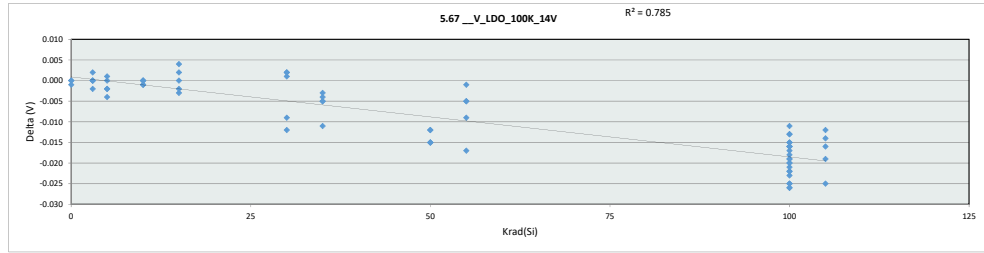


5.66 V_LDO_100K_12V											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.026	5.017	5.007	5.011	5.004	5.016	5.018	4.994	5.031	4.996	4.995
Average	5.037	5.036	5.039	5.040	5.026	5.028	5.025	5.020	5.038	5.020	5.022
Max	5.055	5.054	5.072	5.071	5.042	5.046	5.041	5.033	5.045	5.049	5.036
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

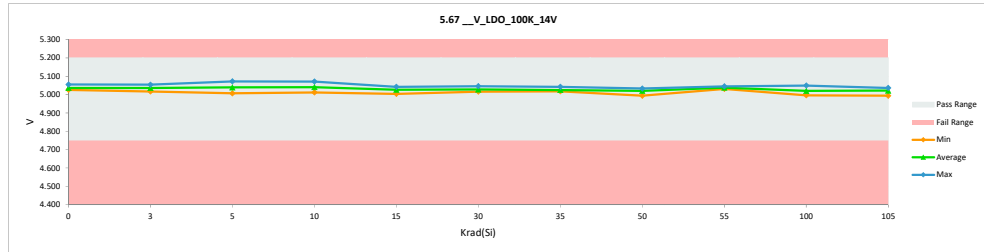


HDR TID Report
TPS7H5001-SP

5.67 V_LDO_100K_14V				
Test Site	Tester	Test Number	Unit	Max Limit
			V	5.2
			V	4.75
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	5.029	5.028	-0.001
0	118	5.055	5.055	0.000
0	122	5.026	5.026	0.000
3	301	5.054	5.054	0.000
3	306	5.037	5.039	0.002
3	307	5.019	5.017	-0.002
3	308	5.043	5.043	0.000
3	309	5.028	5.028	0.000
5	310	5.032	5.033	0.001
5	311	5.063	5.059	-0.004
5	312	5.072	5.072	0.000
5	314	5.009	5.007	-0.002
5	316	5.027	5.025	-0.002
10	317	5.040	5.040	0.000
10	318	5.028	5.028	0.000
10	319	5.013	5.012	-0.001
10	321	5.072	5.071	-0.001
10	322	5.051	5.051	0.000
15	323	5.007	5.004	-0.003
15	325	5.021	5.019	-0.002
15	326	5.030	5.032	0.002
15	328	5.038	5.042	0.004
15	333	5.035	5.035	0.000
30	334	5.035	5.026	-0.009
30	335	5.044	5.032	-0.012
30	337	5.015	5.016	0.001
30	339	5.044	5.046	0.002
30	341	5.017	5.019	0.002
35	342	5.027	5.023	-0.004
35	345	5.045	5.042	-0.003
35	346	5.029	5.018	-0.011
35	347	5.023	5.018	-0.005
35	348	5.027	5.022	-0.005
50	349	5.045	5.033	-0.012
50	350	5.041	5.026	-0.015
50	351	5.009	4.994	-0.015
50	353	5.031	5.016	-0.015
50	354	5.045	5.033	-0.012
55	355	5.041	5.036	-0.005
55	357	5.050	5.045	-0.005
55	359	5.047	5.038	-0.009
55	362	5.048	5.031	-0.017
55	363	5.041	5.040	-0.001
100	1	5.043	5.021	-0.022
100	3	5.031	5.011	-0.020
100	4	5.027	5.007	-0.020
100	5	5.051	5.032	-0.019
100	6	5.044	5.028	-0.016
100	7	5.068	5.049	-0.019
100	8	5.020	5.003	-0.017
100	9	5.012	4.997	-0.015
100	10	5.025	4.999	-0.026
100	11	5.062	5.039	-0.023
100	12	5.031	5.018	-0.013
100	13	5.053	5.032	-0.021
100	14	5.035	5.022	-0.013
100	15	5.044	5.029	-0.015
100	16	5.014	4.996	-0.018
100	17	5.054	5.043	-0.011
100	18	5.044	5.028	-0.016
100	19	5.050	5.028	-0.022
100	20	5.041	5.015	-0.026
100	21	5.026	5.010	-0.016
100	22	5.027	5.002	-0.025
100	24	5.059	5.034	-0.025
105	25	5.050	5.036	-0.014
105	26	5.046	5.027	-0.019
105	27	5.019	4.994	-0.025
105	28	5.044	5.028	-0.016
105	29	5.036	5.024	-0.012
	Max	5.072	5.072	0.004
	Average	5.037	5.028	-0.009
	Min	5.007	4.994	-0.026
	Std Dev	0.015	0.017	0.009

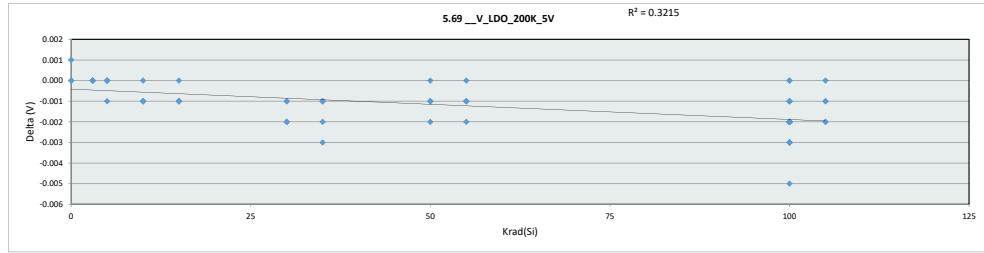


5.67 V_LDO_100K_14V											
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit						
			V	5.2	4.75						
			V	5.2	4.75						
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.026	5.017	5.007	5.012	5.004	5.016	5.018	4.994	5.031	4.996	4.994
Average	5.036	5.036	5.039	5.040	5.026	5.028	5.025	5.020	5.038	5.020	5.022
Max	5.055	5.054	5.072	5.071	5.042	5.046	5.042	5.033	5.045	5.049	5.036
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

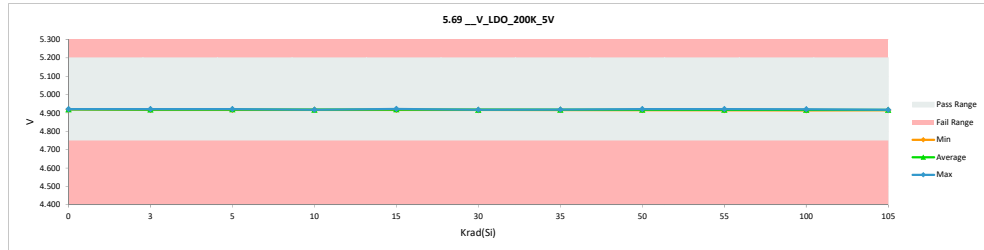


HDR TID Report
TPS7H5001-SP

5.69 V_LDO_200K_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	4.920	4.921	0.001
0	118	4.919	4.919	0.000
0	122	4.919	4.919	0.000
3	301	4.920	4.920	0.000
3	306	4.920	4.920	0.000
3	307	4.917	4.917	0.000
3	308	4.921	4.921	0.000
3	309	4.917	4.917	0.000
5	310	4.921	4.921	0.000
5	311	4.921	4.921	0.000
5	312	4.920	4.919	-0.001
5	314	4.917	4.917	0.000
5	316	4.920	4.920	0.000
10	317	4.918	4.917	-0.001
10	318	4.918	4.918	0.000
10	319	4.919	4.918	-0.001
10	321	4.918	4.917	-0.001
10	322	4.919	4.918	-0.001
15	323	4.923	4.922	-0.001
15	325	4.921	4.921	0.000
15	326	4.920	4.919	-0.001
15	328	4.919	4.918	-0.001
15	333	4.918	4.917	-0.001
30	334	4.916	4.915	-0.001
30	335	4.920	4.918	-0.002
30	337	4.920	4.918	-0.002
30	339	4.916	4.915	-0.001
30	341	4.919	4.917	-0.002
35	342	4.919	4.918	-0.001
35	345	4.920	4.918	-0.002
35	346	4.920	4.919	-0.001
35	347	4.920	4.917	-0.003
35	348	4.920	4.919	-0.001
50	349	4.918	4.917	-0.001
50	350	4.917	4.916	-0.001
50	351	4.920	4.918	-0.002
50	353	4.919	4.918	-0.001
50	354	4.921	4.921	0.000
55	355	4.921	4.921	0.000
55	357	4.917	4.915	-0.002
55	359	4.918	4.917	-0.001
55	362	4.920	4.919	-0.001
55	363	4.918	4.917	-0.001
100	1	4.920	4.917	-0.003
100	3	4.920	4.920	0.000
100	4	4.918	4.915	-0.003
100	5	4.919	4.916	-0.003
100	6	4.920	4.918	-0.002
100	7	4.919	4.917	-0.002
100	8	4.920	4.919	-0.001
100	9	4.918	4.916	-0.002
100	10	4.917	4.916	-0.001
100	11	4.916	4.914	-0.002
100	12	4.921	4.920	-0.001
100	13	4.918	4.917	-0.001
100	14	4.919	4.917	-0.002
100	15	4.917	4.914	-0.003
100	16	4.919	4.917	-0.002
100	17	4.922	4.920	-0.002
100	18	4.920	4.918	-0.002
100	19	4.920	4.918	-0.002
100	20	4.918	4.915	-0.003
100	21	4.920	4.920	0.000
100	22	4.921	4.916	-0.005
100	24	4.920	4.918	-0.002
105	25	4.917	4.917	0.000
105	26	4.918	4.917	-0.001
105	27	4.919	4.917	-0.002
105	28	4.918	4.917	-0.001
105	29	4.917	4.915	-0.002
	Max	4.923	4.922	0.001
	Average	4.919	4.918	-0.001
	Min	4.916	4.914	-0.005
	Std Dev	0.001	0.002	0.001

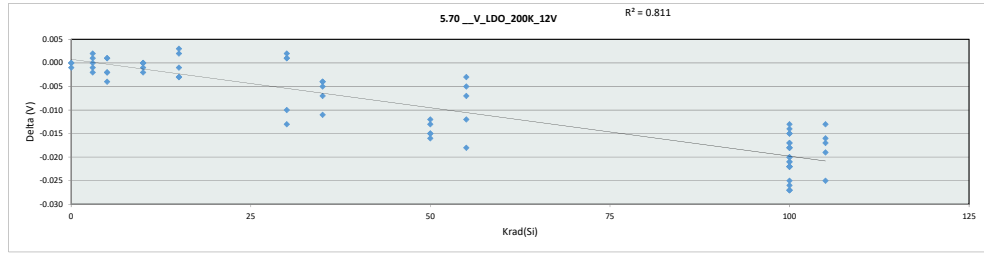


5.69 V_LDO_200K_5V											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	4.919	4.917	4.917	4.917	4.917	4.915	4.917	4.916	4.915	4.914	4.915
Average	4.920	4.919	4.920	4.918	4.919	4.917	4.918	4.918	4.918	4.917	4.917
Max	4.921	4.921	4.921	4.918	4.922	4.918	4.919	4.921	4.921	4.920	4.917
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

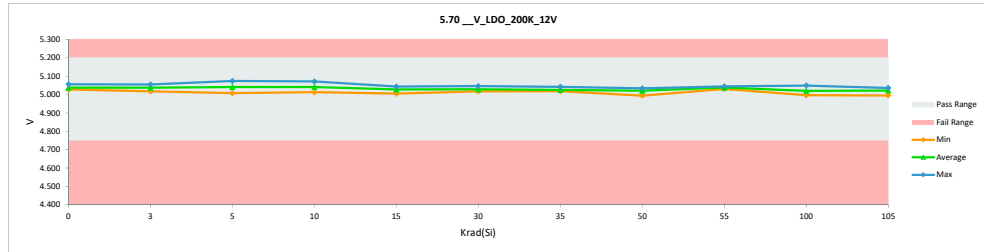


HDR TID Report
TPS7H5001-SP

5.70 V_LDO_200K_12V				
Test Site	Serial #	PRE_HDR	POST_HDR	Delta
0	117	5.030	5.029	-0.001
0	118	5.056	5.056	0.000
0	122	5.028	5.028	0.000
3	301	5.056	5.055	-0.001
3	306	5.038	5.040	0.002
3	307	5.019	5.017	-0.002
3	308	5.043	5.044	0.001
3	309	5.029	5.029	0.000
5	310	5.034	5.035	0.001
5	311	5.065	5.061	-0.004
5	312	5.073	5.074	0.001
5	314	5.010	5.008	-0.002
5	316	5.029	5.027	-0.002
10	317	5.042	5.041	-0.001
10	318	5.030	5.030	0.000
10	319	5.015	5.013	-0.002
10	321	5.071	5.071	0.000
10	322	5.052	5.052	0.000
15	323	5.008	5.005	-0.003
15	325	5.023	5.020	-0.003
15	326	5.032	5.034	0.002
15	328	5.040	5.043	0.003
15	333	5.037	5.036	-0.001
30	334	5.036	5.026	-0.010
30	335	5.046	5.033	-0.013
30	337	5.016	5.017	0.001
30	339	5.045	5.046	0.001
30	341	5.019	5.021	0.002
35	342	5.028	5.024	-0.004
35	345	5.046	5.042	-0.004
35	346	5.030	5.019	-0.011
35	347	5.025	5.018	-0.007
35	348	5.028	5.023	-0.005
50	349	5.047	5.034	-0.013
50	350	5.042	5.027	-0.015
50	351	5.010	4.994	-0.016
50	353	5.032	5.017	-0.015
50	354	5.046	5.034	-0.012
55	355	5.042	5.037	-0.005
55	357	5.051	5.044	-0.007
55	359	5.050	5.038	-0.012
55	362	5.049	5.031	-0.018
55	363	5.042	5.039	-0.003
100	1	5.044	5.022	-0.022
100	3	5.032	5.011	-0.021
100	4	5.029	5.007	-0.022
100	5	5.053	5.032	-0.021
100	6	5.045	5.027	-0.018
100	7	5.069	5.049	-0.020
100	8	5.021	5.003	-0.018
100	9	5.014	4.997	-0.017
100	10	5.025	4.999	-0.026
100	11	5.064	5.039	-0.025
100	12	5.032	5.018	-0.014
100	13	5.053	5.031	-0.022
100	14	5.036	5.021	-0.015
100	15	5.044	5.029	-0.015
100	16	5.016	4.996	-0.020
100	17	5.056	5.043	-0.013
100	18	5.046	5.029	-0.017
100	19	5.051	5.029	-0.022
100	20	5.042	5.015	-0.027
100	21	5.028	5.010	-0.018
100	22	5.029	5.002	-0.027
100	24	5.061	5.034	-0.027
105	25	5.052	5.036	-0.016
105	26	5.047	5.028	-0.019
105	27	5.020	4.995	-0.025
105	28	5.045	5.028	-0.017
105	29	5.036	5.023	-0.013
Max		5.073	5.074	0.003
Average		5.038	5.028	-0.010
Min		5.008	4.994	-0.027
Std Dev		0.015	0.017	0.009

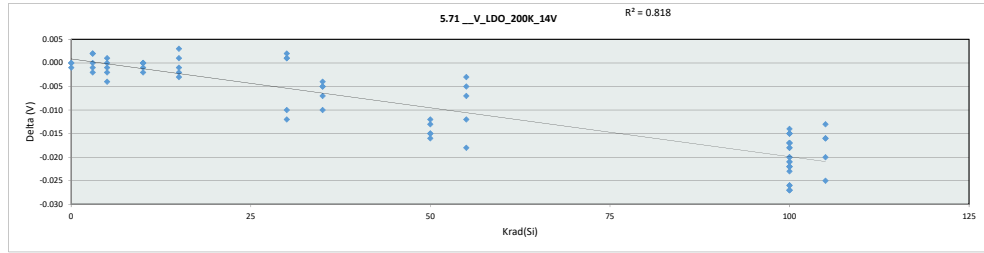


5.70 V_LDO_200K_12V											
Test Site	Serial #	PRE_HDR	POST_HDR	Delta							
0	117	5.030	5.029	-0.001							
0	118	5.056	5.056	0.000							
0	122	5.028	5.028	0.000							
3	301	5.056	5.055	-0.001							
3	306	5.038	5.040	0.002							
3	307	5.019	5.017	-0.002							
3	308	5.043	5.044	0.001							
3	309	5.029	5.029	0.000							
5	310	5.034	5.035	0.001							
5	311	5.065	5.061	-0.004							
5	312	5.073	5.074	0.001							
5	314	5.010	5.008	-0.002							
5	316	5.029	5.027	-0.002							
10	317	5.042	5.041	-0.001							
10	318	5.030	5.030	0.000							
10	319	5.015	5.013	-0.002							
10	321	5.071	5.071	0.000							
10	322	5.052	5.052	0.000							
15	323	5.008	5.005	-0.003							
15	325	5.023	5.020	-0.003							
15	326	5.032	5.034	0.002							
15	328	5.040	5.043	0.003							
15	333	5.037	5.036	-0.001							
30	334	5.036	5.026	-0.010							
30	335	5.046	5.033	-0.013							
30	337	5.016	5.017	0.001							
30	339	5.045	5.046	0.001							
30	341	5.019	5.021	0.002							
35	342	5.028	5.024	-0.004							
35	345	5.046	5.042	-0.004							
35	346	5.030	5.019	-0.011							
35	347	5.025	5.018	-0.007							
35	348	5.028	5.023	-0.005							
50	349	5.047	5.034	-0.013							
50	350	5.042	5.027	-0.015							
50	351	5.010	4.994	-0.016							
50	353	5.032	5.017	-0.015							
50	354	5.046	5.034	-0.012							
55	355	5.042	5.037	-0.005							
55	357	5.051	5.044	-0.007							
55	359	5.050	5.038	-0.012							
55	362	5.049	5.031	-0.018							
55	363	5.042	5.039	-0.003							
100	1	5.044	5.022	-0.022							
100	3	5.032	5.011	-0.021							
100	4	5.029	5.007	-0.022							
100	5	5.053	5.032	-0.021							
100	6	5.045	5.027	-0.018							
100	7	5.069	5.049	-0.020							
100	8	5.021	5.003	-0.018							
100	9	5.014	4.997	-0.017							
100	10	5.025	4.999	-0.026							
100	11	5.064	5.039	-0.025							
100	12	5.032	5.018	-0.014							
100	13	5.053	5.031	-0.022							
100	14	5.036	5.021	-0.015							
100	15	5.044	5.029	-0.015							
100	16	5.016	4.996	-0.020							
100	17	5.056	5.043	-0.013							
100	18	5.046	5.029	-0.017							
100	19	5.051	5.029	-0.022							
100	20	5.042	5.015	-0.027							
100	21	5.028	5.010	-0.018							
100	22	5.029	5.002	-0.027							
100	24	5.061	5.034	-0.027							
105	25	5.052	5.036	-0.016							
105	26	5.047	5.028	-0.019							
105	27	5.020	4.995	-0.025							
105	28	5.045	5.028	-0.017							
105	29	5.036	5.023	-0.013							
Max		5.073	5.074	0.003							
Average		5.038	5.028	-0.010							
Min		5.008	4.994	-0.027							
UL		5.200	5.200	5.200							

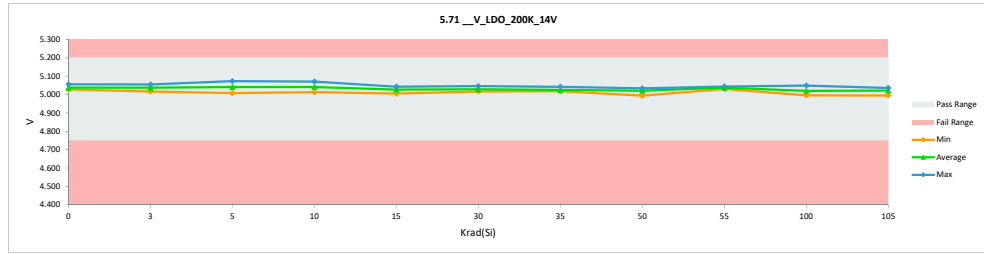


HDR TID Report
TPS7H5001-SP

5.71 V_LDO_200K_14V				
Test Site	Tester	Test Number	Unit	
			V	V
Max Limit			5.2	5.2
Min Limit			4.75	4.75
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	5.030	5.029	-0.001
0	118	5.056	5.056	0.000
0	122	5.028	5.028	0.000
3	301	5.056	5.055	-0.001
3	306	5.038	5.040	0.002
3	307	5.019	5.017	-0.002
3	308	5.043	5.045	0.002
3	309	5.029	5.029	0.000
5	310	5.034	5.035	0.001
5	311	5.065	5.061	-0.004
5	312	5.073	5.073	0.000
5	314	5.010	5.008	-0.002
5	316	5.029	5.028	-0.001
10	317	5.042	5.041	-0.001
10	318	5.030	5.030	0.000
10	319	5.015	5.013	-0.002
10	321	5.071	5.071	0.000
10	322	5.052	5.052	0.000
15	323	5.008	5.005	-0.003
15	325	5.022	5.020	-0.002
15	326	5.032	5.033	0.001
15	328	5.040	5.043	0.003
15	333	5.037	5.036	-0.001
30	334	5.036	5.026	-0.010
30	335	5.046	5.034	-0.012
30	337	5.016	5.017	0.001
30	339	5.045	5.046	0.001
30	341	5.019	5.021	0.002
35	342	5.028	5.024	-0.004
35	345	5.047	5.042	-0.005
35	346	5.030	5.020	-0.010
35	347	5.025	5.018	-0.007
35	348	5.028	5.023	-0.005
50	349	5.047	5.034	-0.013
50	350	5.042	5.027	-0.015
50	351	5.010	4.994	-0.016
50	353	5.032	5.017	-0.015
50	354	5.046	5.034	-0.012
55	355	5.042	5.037	-0.005
55	357	5.051	5.044	-0.007
55	359	5.050	5.038	-0.012
55	362	5.049	5.031	-0.018
55	363	5.042	5.039	-0.003
100	1	5.044	5.021	-0.023
100	3	5.032	5.011	-0.021
100	4	5.029	5.007	-0.022
100	5	5.053	5.032	-0.021
100	6	5.045	5.028	-0.017
100	7	5.069	5.049	-0.020
100	8	5.021	5.003	-0.018
100	9	5.014	4.997	-0.017
100	10	5.025	4.999	-0.026
100	11	5.064	5.038	-0.026
100	12	5.032	5.017	-0.015
100	13	5.053	5.031	-0.022
100	14	5.036	5.021	-0.015
100	15	5.044	5.029	-0.015
100	16	5.016	4.996	-0.020
100	17	5.056	5.042	-0.014
100	18	5.046	5.029	-0.017
100	19	5.051	5.029	-0.022
100	20	5.042	5.015	-0.027
100	21	5.028	5.010	-0.018
100	22	5.029	5.002	-0.027
100	24	5.061	5.034	-0.027
105	25	5.052	5.036	-0.016
105	26	5.047	5.027	-0.020
105	27	5.020	4.995	-0.025
105	28	5.045	5.029	-0.016
105	29	5.036	5.023	-0.013
	Max	5.073	5.073	0.003
	Average	5.038	5.028	-0.010
	Min	5.008	4.994	-0.027
	Std Dev	0.015	0.017	0.009

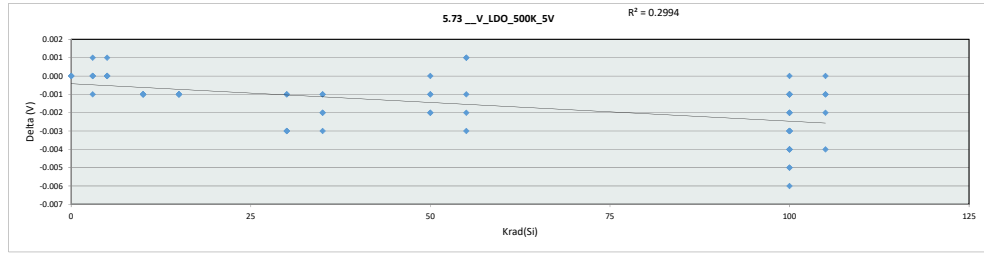


5.71 V_LDO_200K_14V											
Test Site	Tester	Test Number	Unit								
			V	V							
Max Limit			5.2	5.2							
Min Limit			4.75	4.75							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.028	5.017	5.008	5.013	5.005	5.017	5.018	4.994	5.031	4.996	4.995
Average	5.038	5.037	5.041	5.041	5.027	5.029	5.025	5.021	5.038	5.020	5.022
Max	5.056	5.055	5.073	5.071	5.043	5.046	5.042	5.034	5.044	5.049	5.036
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

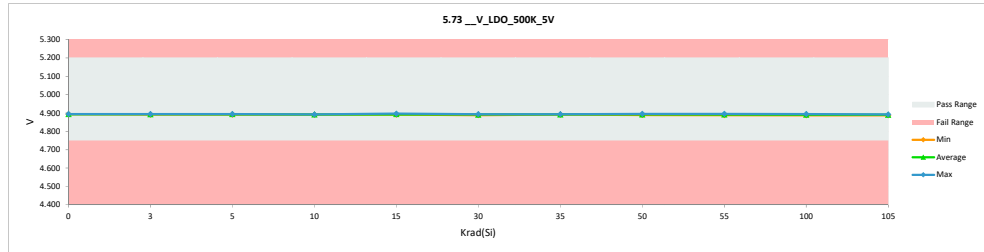


HDR TID Report
TPS7H5001-SP

5.73 V_LDO_500K_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	4.894	4.894	0.000
0	118	4.893	4.893	0.000
0	122	4.893	4.893	0.000
3	301	4.893	4.894	0.001
3	306	4.894	4.894	0.000
3	307	4.891	4.891	0.000
3	308	4.895	4.894	-0.001
3	309	4.891	4.891	0.000
5	310	4.894	4.894	0.000
5	311	4.894	4.894	0.000
5	312	4.893	4.893	0.000
5	314	4.891	4.891	0.000
5	316	4.893	4.894	0.001
10	317	4.892	4.891	-0.001
10	318	4.892	4.891	-0.001
10	319	4.893	4.892	-0.001
10	321	4.893	4.892	-0.001
10	322	4.893	4.892	-0.001
15	323	4.897	4.896	-0.001
15	325	4.895	4.894	-0.001
15	326	4.894	4.893	-0.001
15	328	4.893	4.892	-0.001
15	333	4.892	4.891	-0.001
30	334	4.891	4.888	-0.003
30	335	4.894	4.893	-0.001
30	337	4.894	4.891	-0.003
30	339	4.891	4.888	-0.003
30	341	4.893	4.892	-0.001
35	342	4.893	4.891	-0.002
35	345	4.894	4.892	-0.002
35	346	4.894	4.893	-0.001
35	347	4.894	4.891	-0.003
35	348	4.893	4.892	-0.001
50	349	4.892	4.891	-0.001
50	350	4.891	4.889	-0.002
50	351	4.894	4.892	-0.002
50	353	4.893	4.892	-0.001
50	354	4.895	4.895	0.000
55	355	4.894	4.895	0.001
55	357	4.891	4.888	-0.003
55	359	4.892	4.891	-0.001
55	362	4.893	4.894	0.001
55	363	4.893	4.891	-0.002
100	1	4.893	4.890	-0.003
100	3	4.894	4.894	0.000
100	4	4.892	4.888	-0.004
100	5	4.893	4.889	-0.004
100	6	4.895	4.891	-0.004
100	7	4.893	4.891	-0.002
100	8	4.894	4.893	-0.001
100	9	4.891	4.890	-0.001
100	10	4.892	4.888	-0.004
100	11	4.891	4.888	-0.003
100	12	4.895	4.894	-0.001
100	13	4.893	4.890	-0.003
100	14	4.893	4.890	-0.003
100	15	4.892	4.887	-0.005
100	16	4.892	4.891	-0.001
100	17	4.896	4.894	-0.002
100	18	4.894	4.893	-0.001
100	19	4.894	4.892	-0.002
100	20	4.893	4.888	-0.005
100	21	4.894	4.893	-0.001
100	22	4.895	4.889	-0.006
100	24	4.894	4.891	-0.003
105	25	4.891	4.891	0.000
105	26	4.892	4.890	-0.002
105	27	4.893	4.892	-0.001
105	28	4.891	4.890	-0.001
105	29	4.891	4.887	-0.004
	Max	4.897	4.896	0.001
	Average	4.893	4.892	-0.002
	Min	4.891	4.887	-0.006
	Std Dev	0.001	0.002	0.002

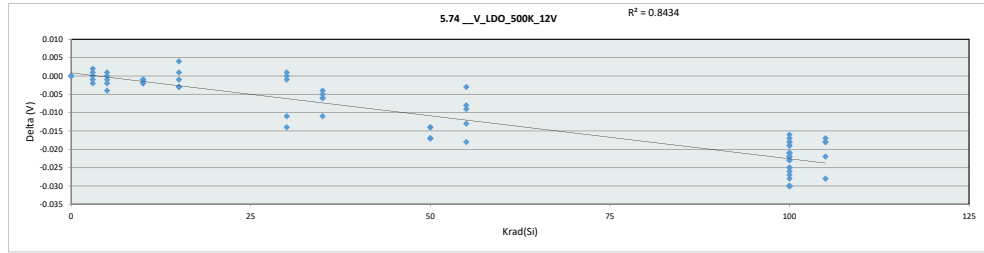


5.73 V_LDO_500K_5V											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	4.893	4.891	4.891	4.891	4.891	4.888	4.891	4.889	4.888	4.887	4.887
Average	4.893	4.893	4.893	4.892	4.893	4.890	4.892	4.892	4.892	4.891	4.890
Max	4.894	4.894	4.894	4.892	4.896	4.893	4.893	4.895	4.895	4.894	4.892
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

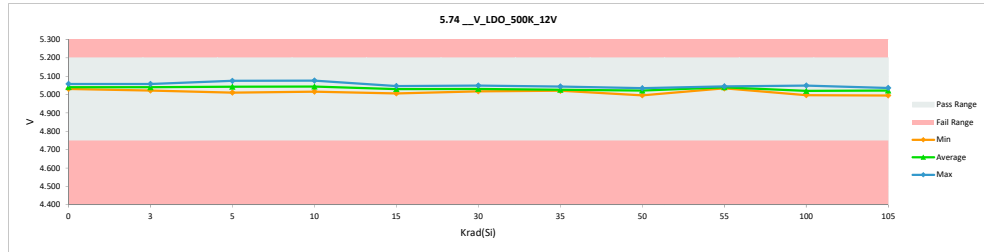


HDR TID Report
TPS7H5001-SP

5.74 V_LDO_500K_12V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	5.032	5.032	0.000
0	118	5.058	5.058	0.000
0	122	5.031	5.031	0.000
3	301	5.058	5.058	0.000
3	306	5.041	5.043	0.002
3	307	5.024	5.022	-0.002
3	308	5.045	5.046	0.001
3	309	5.033	5.032	-0.001
5	310	5.036	5.037	0.001
5	311	5.067	5.063	-0.004
5	312	5.075	5.075	0.000
5	314	5.013	5.011	-0.002
5	316	5.032	5.031	-0.001
10	317	5.044	5.043	-0.001
10	318	5.034	5.032	-0.002
10	319	5.018	5.016	-0.002
10	321	5.077	5.076	-0.001
10	322	5.054	5.053	-0.001
15	323	5.010	5.007	-0.003
15	325	5.025	5.022	-0.003
15	326	5.034	5.035	0.001
15	328	5.043	5.047	0.004
15	333	5.042	5.041	-0.001
30	334	5.040	5.029	-0.011
30	335	5.049	5.035	-0.014
30	337	5.019	5.018	-0.001
30	339	5.049	5.049	0.000
30	341	5.022	5.023	0.001
35	342	5.029	5.025	-0.004
35	345	5.049	5.044	-0.005
35	346	5.032	5.021	-0.011
35	347	5.027	5.021	-0.006
35	348	5.030	5.024	-0.006
50	349	5.049	5.035	-0.014
50	350	5.046	5.029	-0.017
50	351	5.013	4.996	-0.017
50	353	5.035	5.018	-0.017
50	354	5.049	5.035	-0.014
55	355	5.045	5.037	-0.008
55	357	5.054	5.045	-0.009
55	359	5.053	5.040	-0.013
55	362	5.052	5.034	-0.018
55	363	5.045	5.042	-0.003
100	1	5.047	5.022	-0.025
100	3	5.034	5.012	-0.022
100	4	5.032	5.007	-0.025
100	5	5.055	5.032	-0.023
100	6	5.049	5.027	-0.022
100	7	5.072	5.049	-0.023
100	8	5.024	5.003	-0.021
100	9	5.017	4.998	-0.019
100	10	5.029	4.999	-0.030
100	11	5.066	5.039	-0.027
100	12	5.034	5.017	-0.017
100	13	5.058	5.030	-0.028
100	14	5.039	5.021	-0.018
100	15	5.047	5.029	-0.018
100	16	5.018	4.997	-0.021
100	17	5.058	5.042	-0.016
100	18	5.048	5.029	-0.019
100	19	5.054	5.028	-0.026
100	20	5.046	5.016	-0.030
100	21	5.031	5.010	-0.021
100	22	5.032	5.002	-0.030
100	24	5.064	5.034	-0.030
105	25	5.054	5.036	-0.018
105	26	5.050	5.028	-0.022
105	27	5.023	4.995	-0.028
105	28	5.047	5.029	-0.018
105	29	5.041	5.024	-0.017
	Max	5.077	5.076	0.004
	Average	5.041	5.030	-0.012
	Min	5.010	4.995	-0.030
	Std Dev	0.015	0.017	0.010

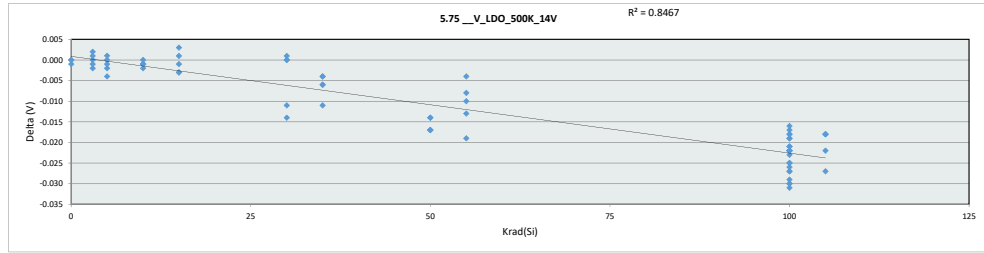


5.74 V_LDO_500K_12V											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.031	5.022	5.011	5.016	5.007	5.018	5.021	4.996	5.034	4.997	4.995
Average	5.040	5.040	5.043	5.044	5.030	5.031	5.027	5.023	5.040	5.020	5.022
Max	5.058	5.058	5.075	5.076	5.047	5.049	5.044	5.035	5.045	5.049	5.036
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

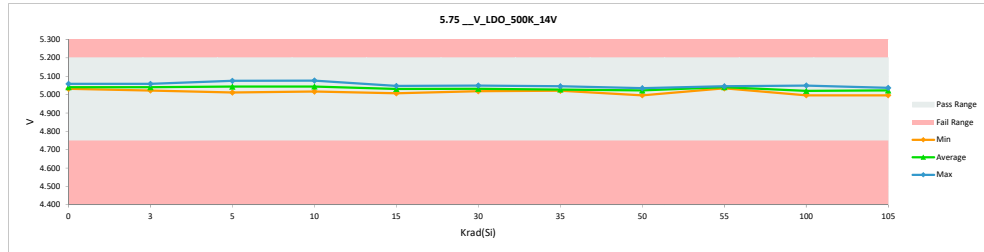


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5.75 V LDO 500K 14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	5.032	5.031	-0.001
0	118	5.058	5.058	0.000
0	122	5.031	5.031	0.000
3	301	5.058	5.058	0.000
3	306	5.041	5.043	0.002
3	307	5.024	5.022	-0.002
3	308	5.045	5.046	0.001
3	309	5.033	5.032	-0.001
5	310	5.036	5.037	0.001
5	311	5.067	5.063	-0.004
5	312	5.075	5.075	0.000
5	314	5.013	5.011	-0.002
5	316	5.032	5.031	-0.001
10	317	5.044	5.043	-0.001
10	318	5.033	5.032	-0.001
10	319	5.018	5.016	-0.002
10	321	5.077	5.076	-0.001
10	322	5.053	5.053	0.000
15	323	5.010	5.007	-0.003
15	325	5.025	5.022	-0.003
15	326	5.034	5.035	0.001
15	328	5.044	5.047	0.003
15	333	5.041	5.040	-0.001
30	334	5.040	5.029	-0.011
30	335	5.049	5.035	-0.014
30	337	5.018	5.018	0.000
30	339	5.049	5.049	0.000
30	341	5.022	5.023	0.001
35	342	5.029	5.025	-0.004
35	345	5.049	5.045	-0.004
35	346	5.032	5.021	-0.011
35	347	5.027	5.021	-0.006
35	348	5.030	5.024	-0.006
50	349	5.049	5.035	-0.014
50	350	5.045	5.028	-0.017
50	351	5.013	4.996	-0.017
50	353	5.035	5.018	-0.017
50	354	5.049	5.035	-0.014
55	355	5.045	5.037	-0.008
55	357	5.055	5.045	-0.010
55	359	5.053	5.040	-0.013
55	362	5.053	5.034	-0.019
55	363	5.046	5.042	-0.004
100	1	5.047	5.022	-0.025
100	3	5.034	5.012	-0.022
100	4	5.032	5.007	-0.025
100	5	5.054	5.032	-0.022
100	6	5.049	5.027	-0.022
100	7	5.072	5.049	-0.023
100	8	5.024	5.003	-0.021
100	9	5.017	4.998	-0.019
100	10	5.029	4.999	-0.030
100	11	5.066	5.039	-0.027
100	12	5.034	5.017	-0.017
100	13	5.057	5.030	-0.027
100	14	5.039	5.021	-0.018
100	15	5.047	5.029	-0.018
100	16	5.018	4.996	-0.022
100	17	5.058	5.042	-0.016
100	18	5.048	5.029	-0.019
100	19	5.054	5.028	-0.026
100	20	5.046	5.015	-0.031
100	21	5.031	5.010	-0.021
100	22	5.032	5.002	-0.030
100	24	5.063	5.034	-0.029
105	25	5.054	5.036	-0.018
105	26	5.050	5.028	-0.022
105	27	5.023	4.996	-0.027
105	28	5.047	5.029	-0.018
105	29	5.041	5.023	-0.018
	Max	5.077	5.076	0.003
	Average	5.041	5.029	-0.012
	Min	5.010	4.996	-0.031
	Std Dev	0.015	0.017	0.010

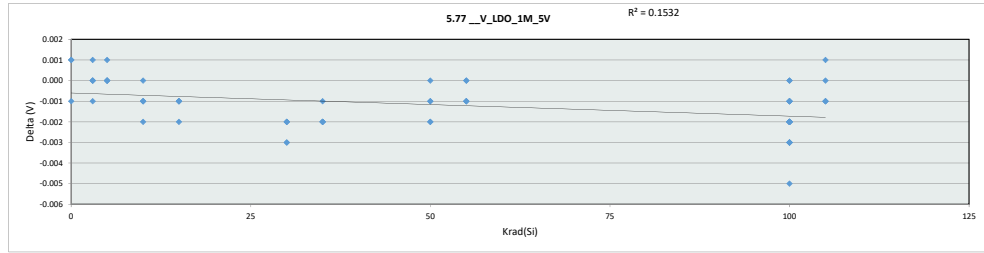


5.75 V LDO 500K 14V											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.031	5.022	5.011	5.016	5.007	5.018	5.021	4.996	5.034	4.996	4.996
Average	5.040	5.040	5.043	5.044	5.030	5.031	5.027	5.022	5.040	5.020	5.022
Max	5.058	5.058	5.075	5.076	5.047	5.049	5.045	5.035	5.045	5.049	5.036
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

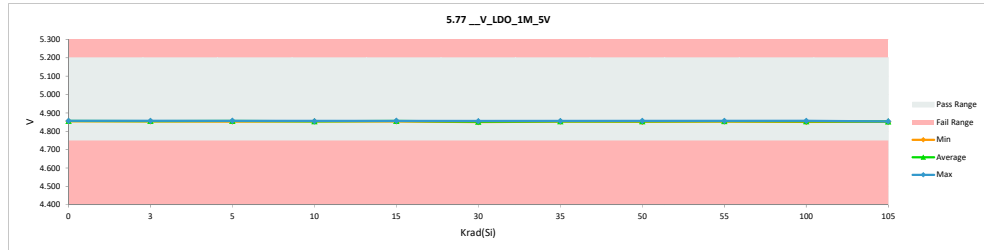


HDR TID Report
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5.77 V_LDO_1M_5V				
Test Site	Serial #	PRE_HDR	POST_HDR	Delta
0	117	4.856	4.857	0.001
0	118	4.856	4.855	-0.001
0	122	4.855	4.856	0.001
3	301	4.856	4.857	0.001
3	306	4.857	4.857	0.000
3	307	4.854	4.854	0.000
3	308	4.858	4.857	-0.001
3	309	4.854	4.854	0.000
5	310	4.857	4.858	0.001
5	311	4.857	4.857	0.000
5	312	4.856	4.856	0.000
5	314	4.853	4.853	0.000
5	316	4.856	4.856	0.000
10	317	4.855	4.854	-0.001
10	318	4.854	4.854	0.000
10	319	4.856	4.854	-0.002
10	321	4.856	4.855	-0.001
10	322	4.856	4.855	-0.001
15	323	4.860	4.858	-0.002
15	325	4.858	4.857	-0.001
15	326	4.857	4.856	-0.001
15	328	4.856	4.855	-0.001
15	333	4.855	4.854	-0.001
30	334	4.853	4.851	-0.002
30	335	4.857	4.855	-0.002
30	337	4.857	4.854	-0.003
30	339	4.853	4.850	-0.003
30	341	4.856	4.854	-0.002
35	342	4.855	4.853	-0.002
35	345	4.857	4.855	-0.002
35	346	4.857	4.856	-0.001
35	347	4.857	4.855	-0.002
35	348	4.856	4.854	-0.002
50	349	4.855	4.854	-0.001
50	350	4.854	4.852	-0.002
50	351	4.857	4.855	-0.002
50	353	4.855	4.855	0.000
50	354	4.858	4.857	-0.001
55	355	4.857	4.857	0.000
55	357	4.854	4.853	-0.001
55	359	4.855	4.854	-0.001
55	362	4.856	4.856	0.000
55	363	4.856	4.855	-0.001
100	1	4.856	4.854	-0.002
100	3	4.857	4.857	0.000
100	4	4.855	4.852	-0.003
100	5	4.856	4.853	-0.003
100	6	4.858	4.856	-0.002
100	7	4.856	4.854	-0.002
100	8	4.856	4.856	0.000
100	9	4.854	4.853	-0.001
100	10	4.855	4.853	-0.002
100	11	4.853	4.851	-0.002
100	12	4.858	4.856	-0.002
100	13	4.856	4.855	-0.001
100	14	4.856	4.854	-0.002
100	15	4.854	4.851	-0.003
100	16	4.855	4.854	-0.001
100	17	4.859	4.856	-0.003
100	18	4.857	4.855	-0.002
100	19	4.856	4.855	-0.001
100	20	4.855	4.852	-0.003
100	21	4.857	4.856	-0.001
100	22	4.858	4.853	-0.005
100	24	4.856	4.854	-0.002
105	25	4.853	4.854	0.001
105	26	4.854	4.853	-0.001
105	27	4.855	4.854	-0.001
105	28	4.853	4.853	0.000
105	29	4.853	4.852	-0.001
Max		4.860	4.858	0.001
Average		4.856	4.855	-0.001
Min		4.853	4.850	-0.005
Std Dev		0.002	0.002	0.001

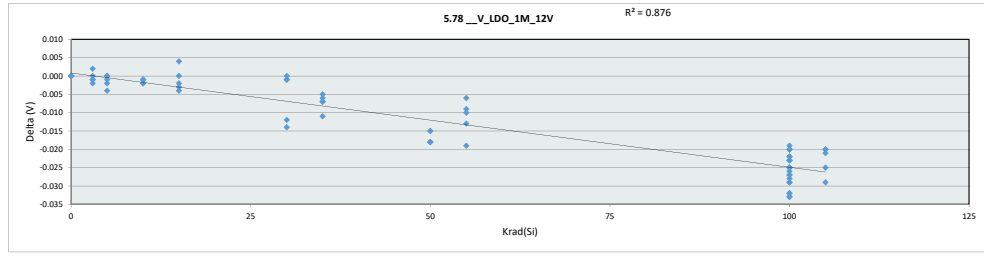


5.77 V_LDO_1M_5V											
Test Site	Serial #	PRE_HDR	POST_HDR	Delta							
0	117	4.856	4.857	0.001							
0	118	4.856	4.855	-0.001							
0	122	4.855	4.856	0.001							
3	301	4.856	4.857	0.001							
3	306	4.857	4.857	0.000							
3	307	4.854	4.854	0.000							
3	308	4.858	4.857	-0.001							
3	309	4.854	4.854	0.000							
5	310	4.857	4.858	0.001							
5	311	4.857	4.857	0.000							
5	312	4.856	4.856	0.000							
5	314	4.853	4.853	0.000							
5	316	4.856	4.856	0.000							
10	317	4.855	4.854	-0.001							
10	318	4.854	4.854	0.000							
10	319	4.856	4.854	-0.002							
10	321	4.856	4.855	-0.001							
10	322	4.856	4.855	-0.001							
15	323	4.860	4.858	-0.002							
15	325	4.858	4.857	-0.001							
15	326	4.857	4.856	-0.001							
15	328	4.856	4.855	-0.001							
15	333	4.855	4.854	-0.001							
30	334	4.853	4.851	-0.002							
30	335	4.857	4.855	-0.002							
30	337	4.857	4.854	-0.003							
30	339	4.853	4.850	-0.003							
30	341	4.856	4.854	-0.002							
35	342	4.855	4.853	-0.002							
35	345	4.857	4.855	-0.002							
35	346	4.857	4.856	-0.001							
35	347	4.857	4.855	-0.002							
35	348	4.856	4.854	-0.002							
50	349	4.855	4.854	-0.001							
50	350	4.854	4.852	-0.002							
50	351	4.857	4.855	-0.002							
50	353	4.855	4.855	0.000							
50	354	4.858	4.857	-0.001							
55	355	4.857	4.857	0.000							
55	357	4.854	4.853	-0.001							
55	359	4.855	4.854	-0.001							
55	362	4.856	4.856	0.000							
55	363	4.856	4.855	-0.001							
100	1	4.856	4.854	-0.002							
100	3	4.857	4.857	0.000							
100	4	4.855	4.852	-0.003							
100	5	4.856	4.853	-0.003							
100	6	4.858	4.856	-0.002							
100	7	4.856	4.854	-0.002							
100	8	4.856	4.856	0.000							
100	9	4.854	4.853	-0.001							
100	10	4.855	4.853	-0.002							
100	11	4.853	4.851	-0.002							
100	12	4.858	4.856	-0.002							
100	13	4.856	4.855	-0.001							
100	14	4.856	4.854	-0.002							
100	15	4.854	4.851	-0.003							
100	16	4.855	4.854	-0.001							
100	17	4.859	4.856	-0.003							
100	18	4.857	4.855	-0.002							
100	19	4.856	4.855	-0.001							
100	20	4.855	4.852	-0.003							
100	21	4.857	4.856	-0.001							
100	22	4.858	4.853	-0.005							
100	24	4.856	4.854	-0.002							
105	25	4.853	4.854	0.001							
105	26	4.854	4.853	-0.001							
105	27	4.855	4.854	-0.001							
105	28	4.853	4.853	0.000							
105	29	4.853	4.852	-0.001							
Max		4.860	4.858	0.001							
Average		4.856	4.855	-0.001							
Min		4.853	4.850	-0.005							
Std Dev		0.002	0.002	0.001							

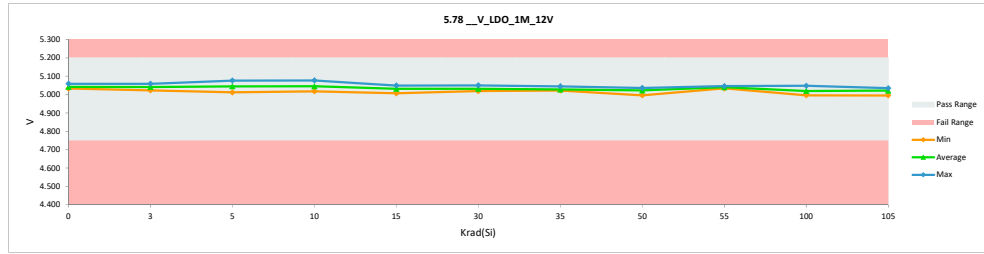


HDR TID Report
TPS7H5001-SP

5.78 V_LDO_1M_12V				
Test Site	Tester	Test Number	Unit	
			V	V
Max Limit			5.2	5.2
Min Limit			4.75	4.75
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	5.033	5.033	0.000
0	118	5.059	5.059	0.000
0	122	5.033	5.033	0.000
3	301	5.060	5.059	-0.001
3	306	5.042	5.044	0.002
3	307	5.025	5.023	-0.002
3	308	5.046	5.046	0.000
3	309	5.035	5.034	-0.001
5	310	5.038	5.038	0.000
5	311	5.068	5.064	-0.004
5	312	5.076	5.076	0.000
5	314	5.014	5.012	-0.002
5	316	5.035	5.034	-0.001
10	317	5.045	5.044	-0.001
10	318	5.036	5.034	-0.002
10	319	5.020	5.018	-0.002
10	321	5.078	5.077	-0.001
10	322	5.054	5.053	-0.001
15	323	5.011	5.007	-0.004
15	325	5.027	5.024	-0.003
15	326	5.036	5.036	0.000
15	328	5.045	5.049	0.004
15	333	5.045	5.043	-0.002
30	334	5.041	5.029	-0.012
30	335	5.051	5.037	-0.014
30	337	5.020	5.019	-0.001
30	339	5.051	5.050	-0.001
30	341	5.024	5.024	0.000
35	342	5.030	5.025	-0.005
35	345	5.051	5.045	-0.006
35	346	5.034	5.023	-0.011
35	347	5.029	5.022	-0.007
35	348	5.031	5.024	-0.007
50	349	5.050	5.035	-0.015
50	350	5.047	5.029	-0.018
50	351	5.014	4.996	-0.018
50	353	5.036	5.018	-0.018
50	354	5.051	5.036	-0.015
55	355	5.046	5.037	-0.009
55	357	5.056	5.046	-0.010
55	359	5.054	5.041	-0.013
55	362	5.053	5.034	-0.019
55	363	5.048	5.042	-0.006
100	1	5.048	5.021	-0.027
100	3	5.036	5.011	-0.025
100	4	5.034	5.007	-0.027
100	5	5.056	5.031	-0.025
100	6	5.051	5.028	-0.023
100	7	5.074	5.048	-0.026
100	8	5.025	5.002	-0.023
100	9	5.020	4.998	-0.022
100	10	5.031	4.998	-0.033
100	11	5.068	5.039	-0.029
100	12	5.035	5.015	-0.020
100	13	5.058	5.029	-0.029
100	14	5.039	5.019	-0.020
100	15	5.047	5.027	-0.020
100	16	5.021	4.996	-0.025
100	17	5.061	5.042	-0.019
100	18	5.050	5.028	-0.022
100	19	5.056	5.028	-0.028
100	20	5.048	5.015	-0.033
100	21	5.033	5.010	-0.023
100	22	5.034	5.002	-0.032
100	24	5.065	5.033	-0.032
105	25	5.056	5.035	-0.021
105	26	5.053	5.028	-0.025
105	27	5.024	4.995	-0.029
105	28	5.049	5.029	-0.020
105	29	5.042	5.022	-0.020
	Max	5.078	5.077	0.004
	Average	5.043	5.030	-0.013
	Min	5.011	4.995	-0.033
	Std Dev	0.015	0.018	0.011

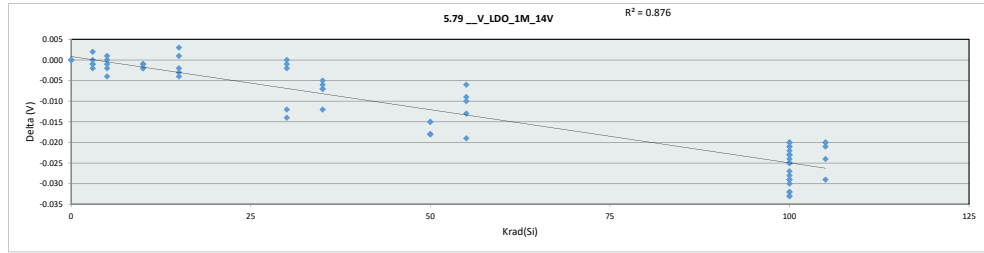


5.78 V_LDO_1M_12V											
Test Site	Tester	Test Number	Unit								
			V	V							
Max Limit			5.2	5.2							
Min Limit			4.75	4.75							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.033	5.023	5.012	5.018	5.018	5.007	5.019	5.022	4.996	5.034	4.996
Average	5.042	5.041	5.045	5.045	5.032	5.032	5.028	5.023	5.040	5.019	5.022
Max	5.059	5.059	5.076	5.077	5.049	5.050	5.045	5.036	5.046	5.048	5.035
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

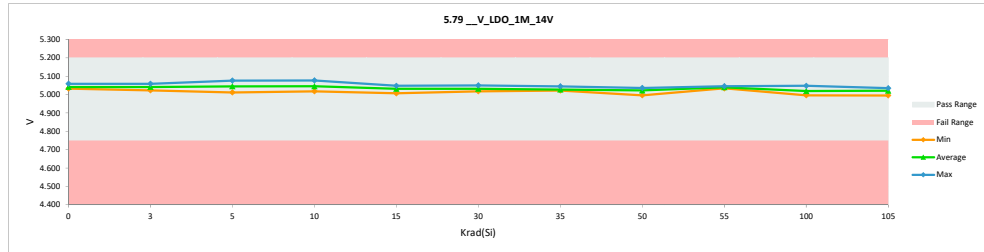


HDR TID Report
TPS7H5001-SP

5.79 V_LDO_1M_14V				
Test Site	Tester	Test Number	Unit	Max Limit
			V	5.2
			V	5.2
				4.75
				4.75
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	5.033	5.033	0.000
0	118	5.059	5.059	0.000
0	122	5.033	5.033	0.000
3	301	5.060	5.059	-0.001
3	306	5.042	5.044	0.002
3	307	5.025	5.023	-0.002
3	308	5.046	5.046	0.000
3	309	5.035	5.034	-0.001
5	310	5.038	5.039	0.001
5	311	5.068	5.064	-0.004
5	312	5.076	5.076	0.000
5	314	5.014	5.012	-0.002
5	316	5.035	5.034	-0.001
10	317	5.045	5.044	-0.001
10	318	5.036	5.034	-0.002
10	319	5.020	5.018	-0.002
10	321	5.078	5.077	-0.001
10	322	5.054	5.053	-0.001
15	323	5.011	5.007	-0.004
15	325	5.027	5.024	-0.003
15	326	5.035	5.036	0.001
15	328	5.045	5.048	0.003
15	333	5.045	5.043	-0.002
30	334	5.041	5.029	-0.012
30	335	5.051	5.037	-0.014
30	337	5.020	5.018	-0.002
30	339	5.051	5.050	-0.001
30	341	5.024	5.024	0.000
35	342	5.030	5.025	-0.005
35	345	5.051	5.045	-0.006
35	346	5.034	5.022	-0.012
35	347	5.029	5.022	-0.007
35	348	5.031	5.024	-0.007
50	349	5.050	5.035	-0.015
50	350	5.047	5.029	-0.018
50	351	5.014	4.996	-0.018
50	353	5.036	5.018	-0.018
50	354	5.051	5.036	-0.015
55	355	5.046	5.037	-0.009
55	357	5.056	5.046	-0.010
55	359	5.054	5.041	-0.013
55	362	5.053	5.034	-0.019
55	363	5.048	5.042	-0.006
100	1	5.048	5.021	-0.027
100	3	5.036	5.011	-0.025
100	4	5.034	5.006	-0.028
100	5	5.056	5.031	-0.025
100	6	5.051	5.028	-0.023
100	7	5.073	5.048	-0.025
100	8	5.025	5.002	-0.023
100	9	5.019	4.998	-0.021
100	10	5.031	4.998	-0.033
100	11	5.068	5.038	-0.030
100	12	5.035	5.015	-0.020
100	13	5.058	5.029	-0.029
100	14	5.039	5.019	-0.020
100	15	5.048	5.026	-0.022
100	16	5.020	4.996	-0.024
100	17	5.061	5.041	-0.020
100	18	5.049	5.028	-0.021
100	19	5.056	5.027	-0.029
100	20	5.048	5.015	-0.033
100	21	5.033	5.010	-0.023
100	22	5.034	5.002	-0.032
100	24	5.065	5.033	-0.032
105	25	5.055	5.035	-0.020
105	26	5.052	5.028	-0.024
105	27	5.024	4.995	-0.029
105	28	5.049	5.028	-0.021
105	29	5.042	5.022	-0.020
	Max	5.078	5.077	0.003
	Average	5.043	5.030	-0.013
	Min	5.011	4.995	-0.033
	Std Dev	0.015	0.018	0.011

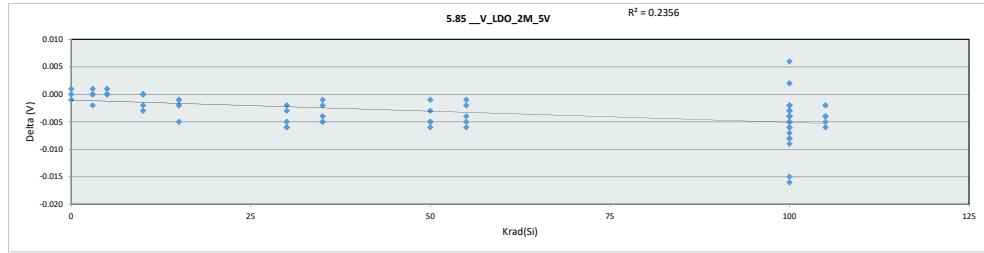


5.79 V_LDO_1M_14V											
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit						
			V	5.2	4.75						
			V	5.2	4.75						
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.033	5.023	5.012	5.018	5.007	5.018	5.022	4.996	5.034	4.996	4.995
Average	5.042	5.041	5.045	5.045	5.032	5.032	5.028	5.023	5.040	5.019	5.022
Max	5.059	5.059	5.076	5.077	5.048	5.050	5.045	5.036	5.046	5.048	5.035
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

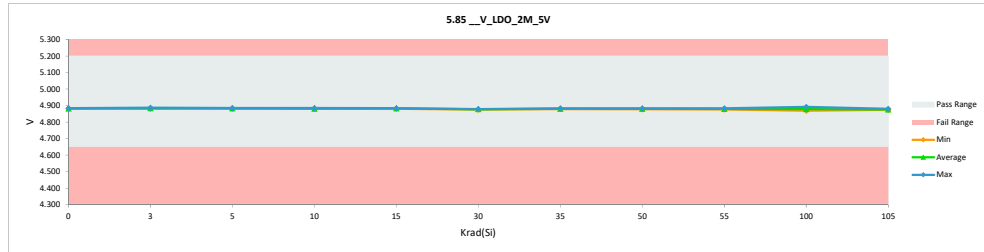


HDR TID Report
TPS7H5001-SP

5.85 V_LDO_2M_5V				
Test Site	Serial #	PRE_HDR	POST_HDR	Delta
0	117	4.884	4.884	0.000
0	118	4.882	4.881	-0.001
0	122	4.880	4.881	0.001
3	301	4.886	4.887	0.001
3	306	4.884	4.884	0.000
3	307	4.880	4.880	0.000
3	308	4.889	4.887	-0.002
3	309	4.880	4.880	0.000
5	310	4.885	4.885	0.000
5	311	4.884	4.884	0.000
5	312	4.883	4.883	0.000
5	314	4.880	4.880	0.000
5	316	4.882	4.883	0.001
10	317	4.880	4.880	0.000
10	318	4.881	4.881	0.000
10	319	4.884	4.881	-0.003
10	321	4.885	4.885	0.000
10	322	4.883	4.881	-0.002
15	323	4.886	4.884	-0.002
15	325	4.888	4.883	-0.005
15	326	4.885	4.883	-0.002
15	328	4.883	4.882	-0.001
15	333	4.882	4.881	-0.001
30	334	4.881	4.879	-0.002
30	335	4.885	4.879	-0.006
30	337	4.883	4.877	-0.006
30	339	4.878	4.875	-0.003
30	341	4.884	4.879	-0.005
35	342	4.881	4.880	-0.001
35	345	4.883	4.879	-0.004
35	346	4.886	4.881	-0.005
35	347	4.885	4.883	-0.002
35	348	4.883	4.878	-0.005
50	349	4.882	4.877	-0.005
50	350	4.884	4.883	-0.001
50	351	4.884	4.878	-0.006
50	353	4.883	4.878	-0.005
50	354	4.886	4.883	-0.003
55	355	4.885	4.883	-0.002
55	357	4.882	4.876	-0.006
55	359	4.881	4.876	-0.005
55	362	4.884	4.883	-0.001
55	363	4.883	4.879	-0.004
100	1	4.883	4.880	-0.003
100	3	4.886	4.871	-0.015
100	4	4.883	4.878	-0.005
100	5	4.884	4.875	-0.009
100	6	4.887	4.881	-0.006
100	7	4.883	4.880	-0.003
100	8	4.884	4.880	-0.004
100	9	4.879	4.881	0.002
100	10	4.882	4.875	-0.007
100	11	4.881	4.878	-0.003
100	12	4.885	4.869	-0.016
100	13	4.882	4.877	-0.005
100	14	4.882	4.878	-0.004
100	15	4.881	4.873	-0.008
100	16	4.881	4.877	-0.004
100	17	4.886	4.892	0.006
100	18	4.885	4.883	-0.002
100	19	4.884	4.880	-0.004
100	20	4.883	4.877	-0.006
100	21	4.884	4.880	-0.004
100	22	4.886	4.878	-0.008
100	24	4.882	4.880	-0.002
105	25	4.882	4.880	-0.002
105	26	4.879	4.875	-0.004
105	27	4.883	4.878	-0.005
105	28	4.879	4.875	-0.004
105	29	4.880	4.874	-0.006
Max		4.889	4.892	0.006
Average		4.883	4.880	-0.003
Min		4.878	4.869	-0.016
Std Dev		0.002	0.004	0.003

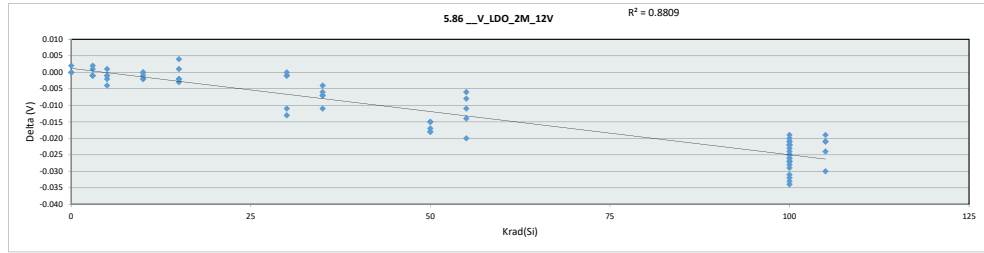


5.85 V_LDO_2M_5V													
Test Site	Serial #	PRE_HDR	POST_HDR	Delta									
0	117	4.884	4.884	0.000									
0	118	4.882	4.881	-0.001									
0	122	4.880	4.881	0.001									
3	301	4.886	4.887	0.001									
3	306	4.884	4.884	0.000									
3	307	4.880	4.880	0.000									
3	308	4.889	4.887	-0.002									
3	309	4.880	4.880	0.000									
5	310	4.885	4.885	0.000									
5	311	4.884	4.884	0.000									
5	312	4.883	4.883	0.000									
5	314	4.880	4.880	0.000									
5	316	4.882	4.883	0.001									
10	317	4.880	4.880	0.000									
10	318	4.881	4.881	0.000									
10	319	4.884	4.881	-0.003									
10	321	4.885	4.885	0.000									
10	322	4.883	4.881	-0.002									
15	323	4.886	4.884	-0.002									
15	325	4.888	4.883	-0.005									
15	326	4.885	4.883	-0.002									
15	328	4.883	4.882	-0.001									
15	333	4.882	4.881	-0.001									
30	334	4.881	4.879	-0.002									
30	335	4.885	4.879	-0.006									
30	337	4.883	4.877	-0.006									
30	339	4.878	4.875	-0.003									
30	341	4.884	4.879	-0.005									
35	342	4.881	4.880	-0.001									
35	345	4.883	4.879	-0.004									
35	346	4.886	4.881	-0.005									
35	347	4.885	4.883	-0.002									
35	348	4.883	4.878	-0.005									
50	349	4.882	4.877	-0.005									
50	350	4.884	4.883	-0.001									
50	351	4.884	4.878	-0.006									
50	353	4.883	4.878	-0.005									
50	354	4.886	4.883	-0.003									
55	355	4.885	4.883	-0.002									
55	357	4.882	4.876	-0.006									
55	359	4.881	4.876	-0.005									
55	362	4.884	4.883	-0.001									
55	363	4.883	4.879	-0.004									
100	1	4.883	4.880	-0.003									
100	3	4.886	4.871	-0.015									
100	4	4.883	4.878	-0.005									
100	5	4.884	4.875	-0.009									
100	6	4.887	4.881	-0.006									
100	7	4.883	4.880	-0.003									
100	8	4.884	4.880	-0.004									
100	9	4.879	4.881	0.002									
100	10	4.882	4.875	-0.007									
100	11	4.881	4.878	-0.003									
100	12	4.885	4.869	-0.016									
100	13	4.882	4.877	-0.005									
100	14	4.882	4.878	-0.004									
100	15	4.881	4.873	-0.008									
100	16	4.881	4.877	-0.004									
100	17	4.886	4.892	0.006									
100	18	4.885	4.883	-0.002									
100	19	4.884	4.880	-0.004									
100	20	4.883	4.877	-0.006									
100	21	4.884	4.880	-0.004									
100	22	4.886	4.878	-0.008									
100	24	4.882	4.880	-0.002									
105	25	4.882	4.880	-0.002									
105	26	4.879	4.875	-0.004									
105	27	4.883	4.878	-0.005									
105	28	4.879	4.875	-0.004									
105	29	4.880	4.874	-0.006									
LL		4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650
Min		4.881	4.880	4.880	4.880	4.881	4.875	4.878	4.877	4.876	4.869	4.874	
Average		4.882	4.884	4.883	4.882	4.883	4.878	4.880	4.880	4.879	4.878	4.876	
Max		4.884	4.887	4.885	4.885	4.884	4.879	4.883	4.883	4.883	4.892	4.880	
UL		5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	

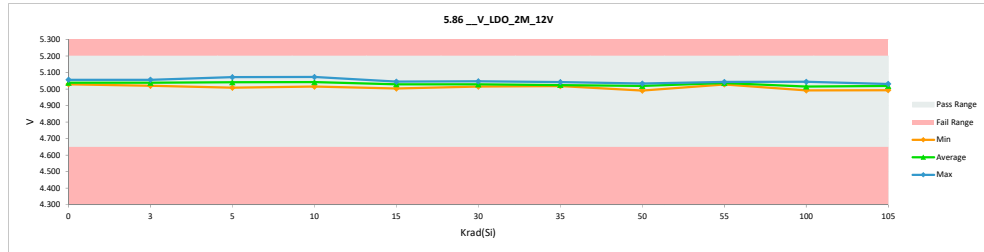


HDR TID Report
TPS7H5001-SP

5.86 V_LDO_2M_12V				
Test Site	Serial #	PRE_HDR	POST_HDR	Delta
0	117	5.029	5.029	0.000
0	118	5.054	5.056	0.002
0	122	5.030	5.030	0.000
3	301	5.057	5.056	-0.001
3	306	5.039	5.041	0.002
3	307	5.021	5.020	-0.001
3	308	5.041	5.042	0.001
3	309	5.032	5.031	-0.001
5	310	5.034	5.035	0.001
5	311	5.063	5.059	-0.004
5	312	5.073	5.072	-0.001
5	314	5.010	5.008	-0.002
5	316	5.032	5.031	-0.001
10	317	5.042	5.040	-0.002
10	318	5.033	5.032	-0.001
10	319	5.017	5.015	-0.002
10	321	5.073	5.073	0.000
10	322	5.050	5.050	0.000
15	323	5.006	5.003	-0.003
15	325	5.024	5.022	-0.002
15	326	5.032	5.033	0.001
15	328	5.041	5.045	0.004
15	333	5.044	5.042	-0.002
30	334	5.037	5.026	-0.011
30	335	5.048	5.035	-0.013
30	337	5.016	5.015	-0.001
30	339	5.048	5.047	-0.001
30	341	5.021	5.021	0.000
35	342	5.024	5.020	-0.004
35	345	5.048	5.042	-0.006
35	346	5.031	5.020	-0.011
35	347	5.027	5.020	-0.007
35	348	5.025	5.018	-0.007
50	349	5.046	5.031	-0.015
50	350	5.043	5.026	-0.017
50	351	5.009	4.991	-0.018
50	353	5.032	5.014	-0.018
50	354	5.048	5.033	-0.015
55	355	5.041	5.033	-0.008
55	357	5.054	5.043	-0.011
55	359	5.051	5.037	-0.014
55	362	5.048	5.028	-0.020
55	363	5.045	5.039	-0.006
100	1	5.045	5.019	-0.026
100	3	5.033	5.007	-0.026
100	4	5.031	5.004	-0.027
100	5	5.052	5.027	-0.025
100	6	5.047	5.025	-0.022
100	7	5.069	5.044	-0.025
100	8	5.020	4.996	-0.024
100	9	5.017	4.997	-0.020
100	10	5.028	4.994	-0.034
100	11	5.065	5.036	-0.029
100	12	5.029	5.008	-0.021
100	13	5.052	5.024	-0.028
100	14	5.034	5.015	-0.019
100	15	5.043	5.021	-0.022
100	16	5.019	4.992	-0.027
100	17	5.058	5.037	-0.021
100	18	5.046	5.024	-0.022
100	19	5.051	5.024	-0.027
100	20	5.046	5.013	-0.033
100	21	5.031	5.008	-0.023
100	22	5.031	5.000	-0.031
100	24	5.062	5.030	-0.032
105	25	5.050	5.031	-0.019
105	26	5.050	5.026	-0.024
105	27	5.023	4.993	-0.030
105	28	5.047	5.026	-0.021
105	29	5.040	5.019	-0.021
Max		5.073	5.073	0.004
Average		5.039	5.026	-0.013
Min		5.006	4.991	-0.034
Std Dev		0.015	0.018	0.011

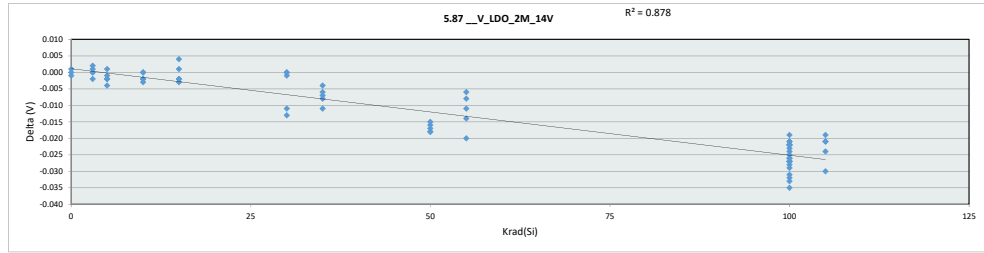


5.86 V_LDO_2M_12V											
Test Site	Serial #	PRE_HDR	POST_HDR	Delta							
0	117	5.029	5.029	0.000							
0	118	5.054	5.056	0.002							
0	122	5.030	5.030	0.000							
3	301	5.057	5.056	-0.001							
3	306	5.039	5.041	0.002							
3	307	5.021	5.020	-0.001							
3	308	5.041	5.042	0.001							
3	309	5.032	5.031	-0.001							
5	310	5.034	5.035	0.001							
5	311	5.063	5.059	-0.004							
5	312	5.073	5.072	-0.001							
5	314	5.010	5.008	-0.002							
5	316	5.032	5.031	-0.001							
10	317	5.042	5.040	-0.002							
10	318	5.033	5.032	-0.001							
10	319	5.017	5.015	-0.002							
10	321	5.073	5.073	0.000							
10	322	5.050	5.050	0.000							
15	323	5.006	5.003	-0.003							
15	325	5.024	5.022	-0.002							
15	326	5.032	5.033	0.001							
15	328	5.041	5.045	0.004							
15	333	5.044	5.042	-0.002							
30	334	5.037	5.026	-0.011							
30	335	5.048	5.035	-0.013							
30	337	5.016	5.015	-0.001							
30	339	5.048	5.047	-0.001							
30	341	5.021	5.021	0.000							
35	342	5.024	5.020	-0.004							
35	345	5.048	5.042	-0.006							
35	346	5.031	5.020	-0.011							
35	347	5.027	5.020	-0.007							
35	348	5.025	5.018	-0.007							
50	349	5.046	5.031	-0.015							
50	350	5.043	5.026	-0.017							
50	351	5.009	4.991	-0.018							
50	353	5.032	5.014	-0.018							
50	354	5.048	5.033	-0.015							
55	355	5.041	5.033	-0.008							
55	357	5.054	5.043	-0.011							
55	359	5.051	5.037	-0.014							
55	362	5.048	5.028	-0.020							
55	363	5.045	5.039	-0.006							
100	1	5.045	5.019	-0.026							
100	3	5.033	5.007	-0.026							
100	4	5.031	5.004	-0.027							
100	5	5.052	5.027	-0.025							
100	6	5.047	5.025	-0.022							
100	7	5.069	5.044	-0.025							
100	8	5.020	4.996	-0.024							
100	9	5.017	4.997	-0.020							
100	10	5.028	4.994	-0.034							
100	11	5.065	5.036	-0.029							
100	12	5.029	5.008	-0.021							
100	13	5.052	5.024	-0.028							
100	14	5.034	5.015	-0.019							
100	15	5.043	5.021	-0.022							
100	16	5.019	4.992	-0.027							
100	17	5.058	5.037	-0.021							
100	18	5.046	5.024	-0.022							
100	19	5.051	5.024	-0.027							
100	20	5.046	5.013	-0.033							
100	21	5.031	5.008	-0.023							
100	22	5.031	5.000	-0.031							
100	24	5.062	5.030	-0.032							
105	25	5.050	5.031	-0.019							
105	26	5.050	5.026	-0.024							
105	27	5.023	4.993	-0.030							
105	28	5.047	5.026	-0.021							
105	29	5.040	5.019	-0.021							
Max		5.073	5.073	0.004							
Average		5.039	5.026	-0.013							
Min		5.006	4.991	-0.034							
Std Dev		0.015	0.018	0.011							

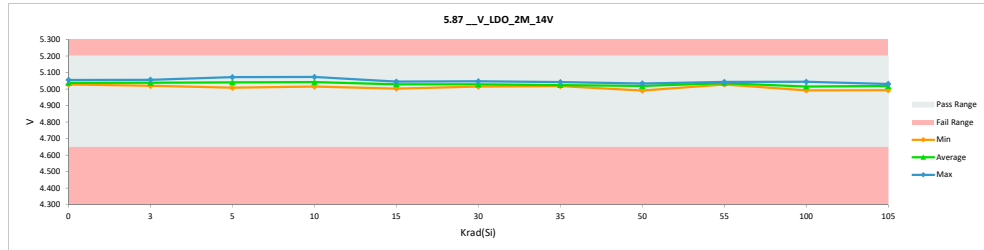


HDR TID Report
TPS7H5001-SP

5.87 V_LDO_2M_14V				
Test Site	Tester	Test Number	Unit	Max Limit
			V	5.2
			V	5.2
				4.65
				4.65
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	5.029	5.028	-0.001
0	118	5.054	5.055	0.001
0	122	5.030	5.030	0.000
3	301	5.056	5.056	0.000
3	306	5.039	5.041	0.002
3	307	5.022	5.020	-0.002
3	308	5.041	5.042	0.001
3	309	5.032	5.032	0.000
5	310	5.034	5.035	0.001
5	311	5.063	5.059	-0.004
5	312	5.073	5.072	-0.001
5	314	5.010	5.008	-0.002
5	316	5.032	5.030	-0.002
10	317	5.042	5.040	-0.002
10	318	5.034	5.032	-0.002
10	319	5.018	5.015	-0.003
10	321	5.073	5.073	0.000
10	322	5.050	5.050	0.000
15	323	5.006	5.003	-0.003
15	325	5.024	5.022	-0.002
15	326	5.032	5.033	0.001
15	328	5.041	5.045	0.004
15	333	5.044	5.042	-0.002
30	334	5.037	5.026	-0.011
30	335	5.047	5.034	-0.013
30	337	5.016	5.015	-0.001
30	339	5.047	5.047	0.000
30	341	5.021	5.021	0.000
35	342	5.024	5.020	-0.004
35	345	5.048	5.042	-0.006
35	346	5.031	5.020	-0.011
35	347	5.027	5.020	-0.007
35	348	5.026	5.018	-0.008
50	349	5.046	5.030	-0.016
50	350	5.043	5.026	-0.017
50	351	5.009	4.991	-0.018
50	353	5.032	5.014	-0.018
50	354	5.049	5.034	-0.015
55	355	5.041	5.033	-0.008
55	357	5.054	5.043	-0.011
55	359	5.051	5.037	-0.014
55	362	5.048	5.028	-0.020
55	363	5.045	5.039	-0.006
100	1	5.045	5.018	-0.027
100	3	5.033	5.007	-0.026
100	4	5.031	5.004	-0.027
100	5	5.052	5.026	-0.026
100	6	5.047	5.025	-0.022
100	7	5.069	5.044	-0.025
100	8	5.020	4.996	-0.024
100	9	5.018	4.997	-0.021
100	10	5.029	4.994	-0.035
100	11	5.065	5.036	-0.029
100	12	5.030	5.008	-0.022
100	13	5.052	5.024	-0.028
100	14	5.034	5.015	-0.019
100	15	5.043	5.021	-0.022
100	16	5.019	4.992	-0.027
100	17	5.058	5.037	-0.021
100	18	5.046	5.024	-0.022
100	19	5.051	5.024	-0.027
100	20	5.046	5.013	-0.033
100	21	5.031	5.008	-0.023
100	22	5.031	5.000	-0.031
100	24	5.062	5.030	-0.032
105	25	5.050	5.031	-0.019
105	26	5.050	5.026	-0.024
105	27	5.023	4.993	-0.030
105	28	5.047	5.026	-0.021
105	29	5.040	5.019	-0.021
	Max	5.073	5.073	0.004
	Average	5.039	5.026	-0.013
	Min	5.006	4.991	-0.035
	Std Dev	0.015	0.018	0.011



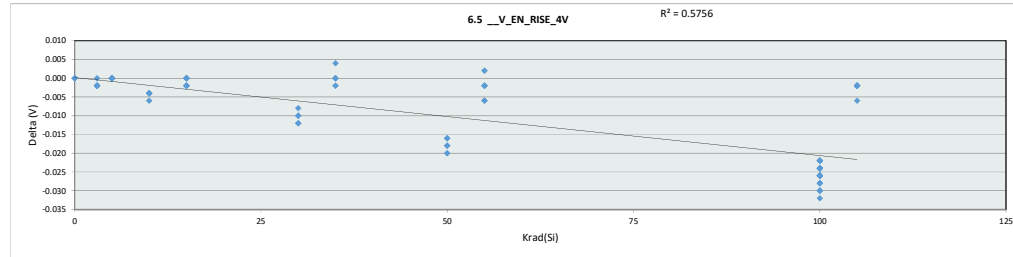
5.87 V_LDO_2M_14V											
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit						
			V	5.2	4.65						
			V	5.2	4.65						
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650
Min	5.028	5.020	5.008	5.015	5.003	5.015	5.018	4.991	5.028	4.992	4.993
Average	5.038	5.038	5.041	5.042	5.029	5.029	5.024	5.019	5.036	5.016	5.019
Max	5.055	5.056	5.072	5.073	5.045	5.047	5.042	5.034	5.043	5.044	5.031
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



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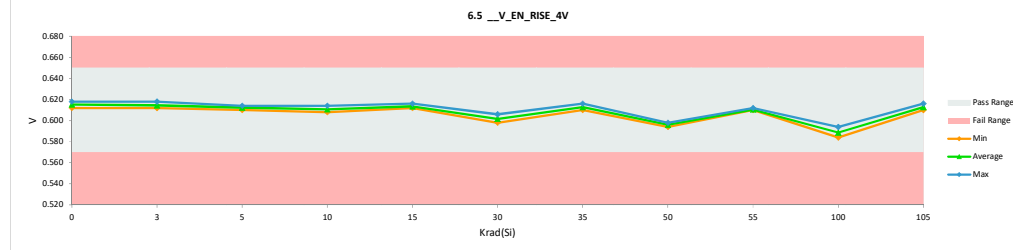
6.5 _V_EN_RISE_4V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	0.65 0.65
Min Limit	0.57 0.57

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	0.616	0.616	0.000
0	118	0.612	0.612	0.000
0	122	0.618	0.618	0.000
3	301	0.616	0.616	0.000
3	306	0.614	0.612	-0.002
3	307	0.620	0.618	-0.002
3	308	0.616	0.614	-0.002
3	309	0.614	0.612	-0.002
5	310	0.612	0.612	0.000
5	311	0.614	0.614	0.000
5	312	0.612	0.612	0.000
5	314	0.610	0.610	0.000
5	316	0.614	0.614	0.000
10	317	0.614	0.610	-0.004
10	318	0.614	0.610	-0.004
10	319	0.614	0.608	-0.006
10	321	0.616	0.612	-0.004
10	322	0.618	0.614	-0.004
15	323	0.614	0.612	-0.002
15	325	0.612	0.612	0.000
15	326	0.618	0.616	-0.002
15	328	0.614	0.614	0.000
15	333	0.614	0.614	0.000
30	334	0.610	0.598	-0.012
30	335	0.612	0.604	-0.008
30	337	0.616	0.606	-0.010
30	339	0.610	0.600	-0.010
30	341	0.612	0.600	-0.012
35	342	0.614	0.612	-0.002
35	345	0.616	0.616	0.000
35	346	0.610	0.610	0.000
35	347	0.610	0.610	0.000
35	348	0.612	0.616	0.004
50	349	0.614	0.598	-0.016
50	350	0.618	0.598	-0.020
50	351	0.612	0.594	-0.018
50	353	0.614	0.596	-0.018
50	354	0.610	0.594	-0.016
55	355	0.608	0.610	0.002
55	357	0.614	0.612	-0.002
55	359	0.616	0.610	-0.006
55	362	0.612	0.610	-0.002
55	363	0.616	0.610	-0.006
100	1	0.618	0.590	-0.028
100	3	0.618	0.586	-0.032
100	4	0.616	0.588	-0.028
100	5	0.614	0.584	-0.030
100	6	0.614	0.592	-0.022
100	7	0.616	0.592	-0.024
100	8	0.614	0.588	-0.026
100	9	0.614	0.588	-0.026
100	10	0.610	0.586	-0.024
100	11	0.614	0.592	-0.022
100	12	0.614	0.588	-0.026
100	13	0.616	0.590	-0.026
100	14	0.614	0.584	-0.030
100	15	0.616	0.590	-0.026
100	16	0.610	0.588	-0.022
100	17	0.616	0.590	-0.026
100	18	0.612	0.590	-0.022
100	19	0.618	0.594	-0.024
100	20	0.610	0.584	-0.026
100	21	0.616	0.592	-0.024
100	22	0.612	0.584	-0.028
100	24	0.612	0.590	-0.022
105	25	0.618	0.612	-0.006
105	26	0.612	0.610	-0.002
105	27	0.616	0.614	-0.002
105	28	0.614	0.612	-0.002
105	29	0.618	0.616	-0.002
	Max	0.620	0.618	0.004
	Average	0.614	0.603	-0.011
	Min	0.608	0.584	-0.032
	Std Dev	0.003	0.011	0.011



6.5 _V_EN_RISE_4V	
Test Site	
Tester	
Test Number	
Max Limit	0.65 V
Min Limit	0.57 V

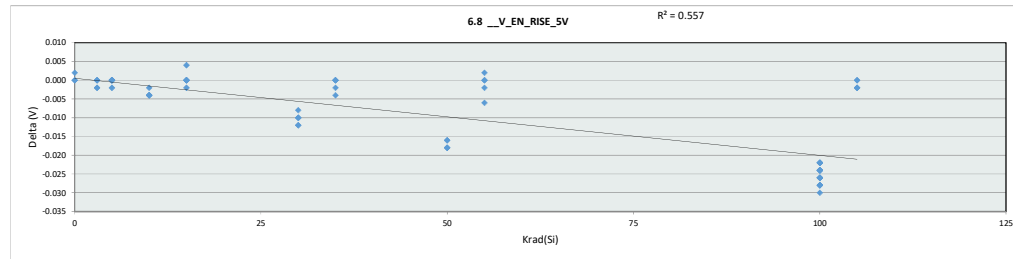
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.612	0.612	0.610	0.608	0.612	0.598	0.610	0.594	0.610	0.584	0.610
Average	0.615	0.614	0.612	0.611	0.614	0.602	0.613	0.596	0.610	0.589	0.613
Max	0.618	0.618	0.614	0.614	0.616	0.606	0.616	0.598	0.612	0.594	0.616
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



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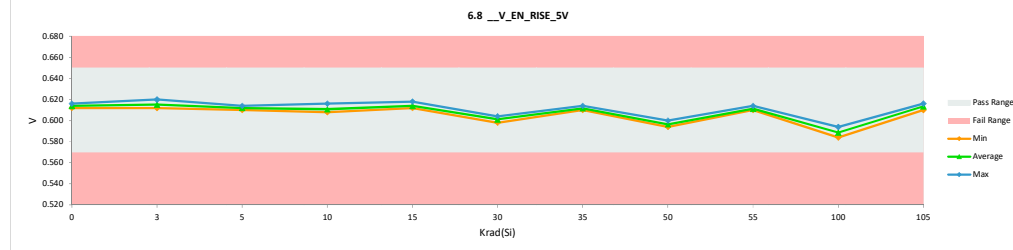
6.8 V_EN_RISE_5V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	0.65 0.65
Min Limit	0.57 0.57

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	0.614	0.616	0.002
0	118	0.612	0.612	0.000
0	122	0.614	0.614	0.000
3	301	0.616	0.616	0.000
3	306	0.614	0.612	-0.002
3	307	0.620	0.620	0.000
3	308	0.616	0.614	-0.002
3	309	0.614	0.614	0.000
5	310	0.612	0.612	0.000
5	311	0.612	0.610	-0.002
5	312	0.614	0.614	0.000
5	314	0.610	0.610	0.000
5	316	0.614	0.614	0.000
10	317	0.614	0.610	-0.004
10	318	0.614	0.610	-0.004
10	319	0.614	0.610	-0.004
10	321	0.612	0.608	-0.004
10	322	0.618	0.616	-0.002
15	323	0.614	0.612	-0.002
15	325	0.612	0.612	0.000
15	326	0.614	0.618	0.004
15	328	0.614	0.614	0.000
15	333	0.614	0.614	0.000
30	334	0.610	0.598	-0.012
30	335	0.612	0.604	-0.008
30	337	0.614	0.602	-0.012
30	339	0.610	0.600	-0.010
30	341	0.612	0.602	-0.010
35	342	0.614	0.614	0.000
35	345	0.616	0.612	-0.004
35	346	0.610	0.610	0.000
35	347	0.610	0.610	0.000
35	348	0.614	0.612	-0.002
50	349	0.614	0.598	-0.016
50	350	0.618	0.600	-0.018
50	351	0.612	0.594	-0.018
50	353	0.614	0.596	-0.018
50	354	0.610	0.594	-0.016
55	355	0.608	0.610	0.002
55	357	0.614	0.614	0.000
55	359	0.612	0.612	0.000
55	362	0.612	0.610	-0.002
55	363	0.616	0.610	-0.006
100	1	0.614	0.590	-0.024
100	3	0.614	0.586	-0.028
100	4	0.616	0.588	-0.028
100	5	0.614	0.584	-0.030
100	6	0.614	0.592	-0.022
100	7	0.616	0.588	-0.028
100	8	0.614	0.588	-0.026
100	9	0.610	0.588	-0.022
100	10	0.610	0.588	-0.022
100	11	0.614	0.592	-0.022
100	12	0.614	0.588	-0.026
100	13	0.616	0.590	-0.026
100	14	0.610	0.584	-0.026
100	15	0.616	0.590	-0.026
100	16	0.612	0.588	-0.024
100	17	0.616	0.590	-0.026
100	18	0.612	0.590	-0.022
100	19	0.618	0.594	-0.024
100	20	0.612	0.584	-0.028
100	21	0.618	0.594	-0.024
100	22	0.612	0.584	-0.028
100	24	0.614	0.590	-0.024
105	25	0.614	0.614	0.000
105	26	0.612	0.610	-0.002
105	27	0.616	0.616	0.000
105	28	0.614	0.612	-0.002
105	29	0.618	0.616	-0.002
	Max	0.620	0.620	0.004
	Average	0.614	0.603	-0.010
	Min	0.608	0.584	-0.030
	Std Dev	0.002	0.011	0.011



6.8 V_EN_RISE_5V	
Test Site	
Tester	
Test Number	
Max Limit	0.65 V
Min Limit	0.57 V

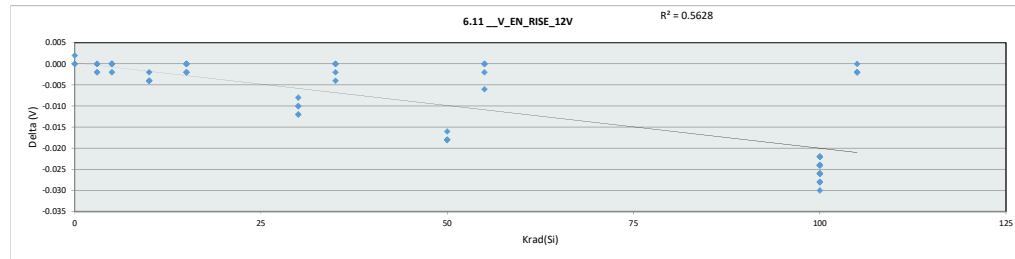
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.612	0.612	0.610	0.608	0.612	0.598	0.610	0.594	0.610	0.584	0.610
Average	0.614	0.615	0.612	0.611	0.614	0.601	0.612	0.596	0.611	0.589	0.614
Max	0.616	0.620	0.614	0.616	0.618	0.604	0.614	0.600	0.614	0.594	0.616
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



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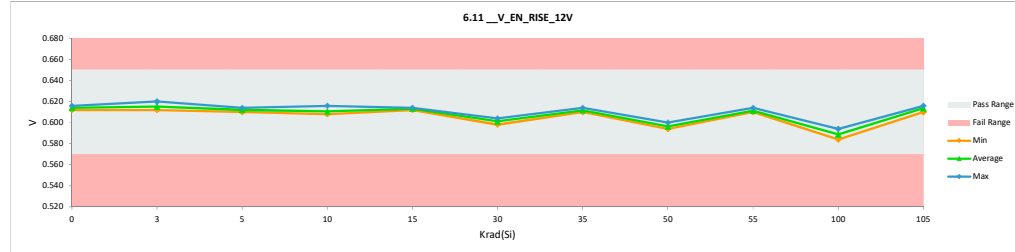
6.11 _V_EN_RISE_12V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	0.65 0.65
Min Limit	0.57 0.57

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	0.614	0.616	0.002
0	118	0.612	0.612	0.000
0	122	0.614	0.614	0.000
3	301	0.616	0.616	0.000
3	306	0.614	0.612	-0.002
3	307	0.620	0.620	0.000
3	308	0.616	0.614	-0.002
3	309	0.614	0.614	0.000
5	310	0.612	0.612	0.000
5	311	0.612	0.610	-0.002
5	312	0.614	0.614	0.000
5	314	0.610	0.610	0.000
5	316	0.614	0.614	0.000
10	317	0.614	0.610	-0.004
10	318	0.614	0.610	-0.004
10	319	0.614	0.610	-0.004
10	321	0.612	0.608	-0.004
10	322	0.618	0.616	-0.002
15	323	0.614	0.612	-0.002
15	325	0.612	0.612	0.000
15	326	0.614	0.614	0.000
15	328	0.616	0.614	-0.002
15	333	0.614	0.614	0.000
30	334	0.610	0.598	-0.012
30	335	0.612	0.604	-0.008
30	337	0.614	0.602	-0.012
30	339	0.610	0.600	-0.010
30	341	0.612	0.602	-0.010
35	342	0.614	0.614	0.000
35	345	0.616	0.612	-0.004
35	346	0.610	0.610	0.000
35	347	0.610	0.610	0.000
35	348	0.614	0.612	-0.002
50	349	0.614	0.598	-0.016
50	350	0.618	0.600	-0.018
50	351	0.612	0.594	-0.018
50	353	0.614	0.596	-0.018
50	354	0.612	0.594	-0.018
55	355	0.610	0.610	0.000
55	357	0.614	0.614	0.000
55	359	0.612	0.612	0.000
55	362	0.612	0.610	-0.002
55	363	0.616	0.610	-0.006
100	1	0.614	0.592	-0.022
100	3	0.614	0.586	-0.028
100	4	0.616	0.588	-0.028
100	5	0.614	0.584	-0.030
100	6	0.614	0.592	-0.022
100	7	0.612	0.588	-0.024
100	8	0.614	0.588	-0.026
100	9	0.610	0.588	-0.022
100	10	0.612	0.588	-0.024
100	11	0.614	0.592	-0.022
100	12	0.614	0.588	-0.026
100	13	0.616	0.590	-0.026
100	14	0.610	0.584	-0.026
100	15	0.616	0.590	-0.026
100	16	0.612	0.588	-0.024
100	17	0.616	0.590	-0.026
100	18	0.612	0.590	-0.022
100	19	0.618	0.594	-0.024
100	20	0.612	0.584	-0.028
100	21	0.618	0.594	-0.024
100	22	0.612	0.584	-0.028
100	24	0.614	0.590	-0.024
105	25	0.614	0.614	0.000
105	26	0.612	0.610	-0.002
105	27	0.618	0.616	-0.002
105	28	0.614	0.612	-0.002
105	29	0.618	0.616	-0.002
	Max	0.620	0.620	0.002
	Average	0.614	0.603	-0.011
	Min	0.610	0.584	-0.030
	Std Dev	0.002	0.011	0.011



6.11 _V_EN_RISE_12V	
Test Site	
Tester	
Test Number	
Max Limit	0.65 V
Min Limit	0.57 V

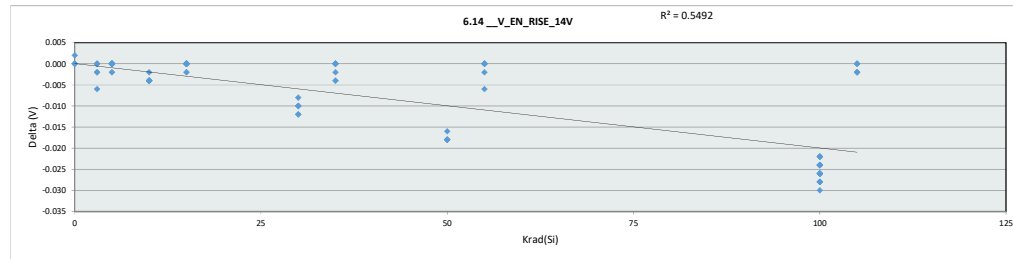
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.612	0.612	0.610	0.608	0.612	0.598	0.610	0.594	0.610	0.584	0.610
Average	0.614	0.615	0.612	0.611	0.613	0.601	0.612	0.596	0.611	0.589	0.614
Max	0.616	0.620	0.614	0.616	0.614	0.604	0.614	0.600	0.614	0.594	0.616
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



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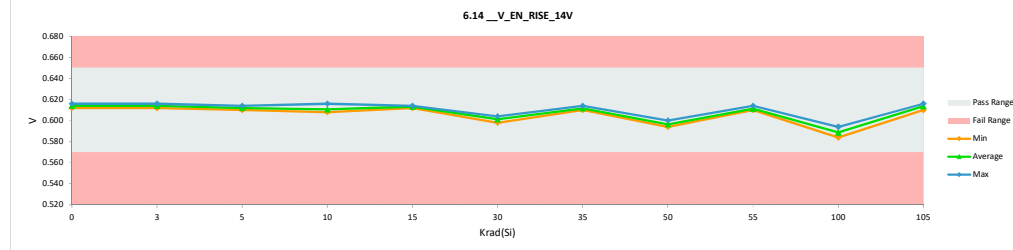
6.14 _V_EN_RISE_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.65
Min Limit	0.57

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	0.614	0.616	0.002
0	118	0.612	0.612	0.000
0	122	0.614	0.614	0.000
3	301	0.616	0.616	0.000
3	306	0.614	0.612	-0.002
3	307	0.620	0.614	-0.006
3	308	0.616	0.614	-0.002
3	309	0.614	0.614	0.000
5	310	0.612	0.612	0.000
5	311	0.612	0.610	-0.002
5	312	0.614	0.614	0.000
5	314	0.610	0.610	0.000
5	316	0.614	0.614	0.000
10	317	0.614	0.610	-0.004
10	318	0.614	0.610	-0.004
10	319	0.614	0.610	-0.004
10	321	0.612	0.608	-0.004
10	322	0.618	0.616	-0.002
15	323	0.614	0.612	-0.002
15	325	0.612	0.612	0.000
15	326	0.614	0.614	0.000
15	328	0.614	0.614	0.000
15	333	0.614	0.614	0.000
30	334	0.610	0.598	-0.012
30	335	0.612	0.604	-0.008
30	337	0.614	0.602	-0.012
30	339	0.610	0.600	-0.010
30	341	0.612	0.602	-0.010
35	342	0.614	0.614	0.000
35	345	0.616	0.612	-0.004
35	346	0.610	0.610	0.000
35	347	0.610	0.610	0.000
35	348	0.614	0.612	-0.002
50	349	0.614	0.598	-0.016
50	350	0.618	0.600	-0.018
50	351	0.612	0.594	-0.018
50	353	0.614	0.596	-0.018
50	354	0.612	0.594	-0.018
55	355	0.610	0.610	0.000
55	357	0.614	0.614	0.000
55	359	0.612	0.612	0.000
55	362	0.612	0.610	-0.002
55	363	0.616	0.610	-0.006
100	1	0.614	0.592	-0.022
100	3	0.614	0.586	-0.028
100	4	0.616	0.588	-0.028
100	5	0.614	0.584	-0.030
100	6	0.614	0.592	-0.022
100	7	0.612	0.588	-0.024
100	8	0.614	0.588	-0.026
100	9	0.610	0.588	-0.022
100	10	0.612	0.588	-0.024
100	11	0.614	0.592	-0.022
100	12	0.614	0.588	-0.026
100	13	0.616	0.590	-0.026
100	14	0.610	0.584	-0.026
100	15	0.616	0.590	-0.026
100	16	0.612	0.588	-0.024
100	17	0.616	0.590	-0.026
100	18	0.614	0.590	-0.024
100	19	0.618	0.594	-0.024
100	20	0.612	0.584	-0.028
100	21	0.618	0.594	-0.024
100	22	0.612	0.584	-0.028
100	24	0.614	0.590	-0.024
105	25	0.614	0.614	0.000
105	26	0.612	0.610	-0.002
105	27	0.616	0.616	0.000
105	28	0.614	0.612	-0.002
105	29	0.618	0.616	-0.002
	Max	0.620	0.616	0.002
	Average	0.614	0.603	-0.011
	Min	0.610	0.584	-0.030
	Std Dev	0.002	0.011	0.011



6.14 _V_EN_RISE_14V	
Test Site	
Tester	
Test Number	
Max Limit	0.65
Min Limit	0.57

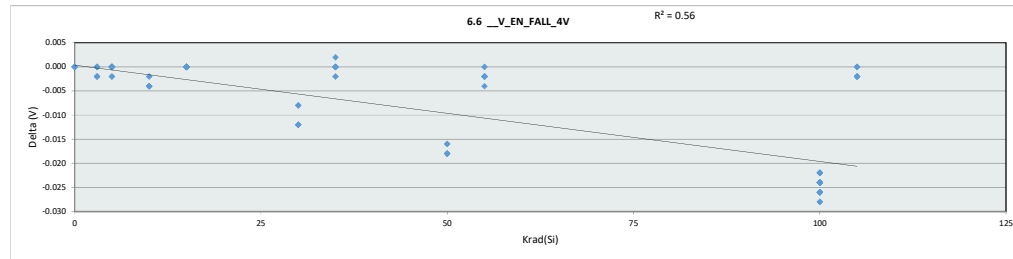
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.612	0.612	0.610	0.608	0.612	0.598	0.610	0.594	0.610	0.584	0.610
Average	0.614	0.614	0.612	0.611	0.613	0.601	0.612	0.596	0.611	0.589	0.614
Max	0.616	0.616	0.614	0.616	0.614	0.604	0.614	0.600	0.614	0.594	0.616
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



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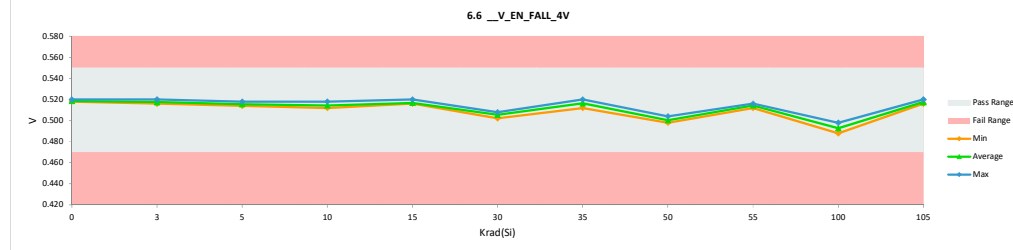
6.6 _V_EN_FALL_4V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	0.55 0.55
Min Limit	0.47 0.47

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	0.518	0.518	0.000
0	118	0.518	0.518	0.000
0	122	0.520	0.520	0.000
3	301	0.520	0.520	0.000
3	306	0.516	0.516	0.000
3	307	0.522	0.520	-0.002
3	308	0.518	0.516	-0.002
3	309	0.516	0.516	0.000
5	310	0.516	0.514	-0.002
5	311	0.516	0.516	0.000
5	312	0.516	0.516	0.000
5	314	0.514	0.514	0.000
5	316	0.518	0.518	0.000
10	317	0.518	0.514	-0.004
10	318	0.516	0.512	-0.004
10	319	0.518	0.514	-0.004
10	321	0.518	0.514	-0.004
10	322	0.520	0.518	-0.002
15	323	0.516	0.516	0.000
15	325	0.516	0.516	0.000
15	326	0.520	0.520	0.000
15	328	0.516	0.516	0.000
15	333	0.516	0.516	0.000
30	334	0.514	0.502	-0.012
30	335	0.518	0.506	-0.012
30	337	0.520	0.508	-0.012
30	339	0.512	0.504	-0.008
30	341	0.516	0.508	-0.008
35	342	0.518	0.518	0.000
35	345	0.518	0.520	0.002
35	346	0.512	0.512	0.000
35	347	0.516	0.514	-0.002
35	348	0.518	0.518	0.000
50	349	0.518	0.500	-0.018
50	350	0.520	0.504	-0.016
50	351	0.516	0.498	-0.018
50	353	0.518	0.500	-0.018
50	354	0.516	0.500	-0.016
55	355	0.514	0.512	-0.002
55	357	0.516	0.516	0.000
55	359	0.518	0.516	-0.002
55	362	0.516	0.514	-0.002
55	363	0.518	0.514	-0.004
100	1	0.522	0.494	-0.028
100	3	0.518	0.492	-0.026
100	4	0.518	0.494	-0.024
100	5	0.516	0.490	-0.026
100	6	0.518	0.494	-0.024
100	7	0.520	0.496	-0.024
100	8	0.518	0.492	-0.026
100	9	0.518	0.490	-0.028
100	10	0.514	0.490	-0.024
100	11	0.518	0.496	-0.022
100	12	0.516	0.494	-0.022
100	13	0.518	0.494	-0.024
100	14	0.516	0.490	-0.026
100	15	0.520	0.494	-0.026
100	16	0.514	0.490	-0.024
100	17	0.520	0.494	-0.026
100	18	0.516	0.494	-0.022
100	19	0.520	0.496	-0.024
100	20	0.514	0.488	-0.026
100	21	0.522	0.498	-0.024
100	22	0.514	0.490	-0.024
100	24	0.516	0.492	-0.024
105	25	0.520	0.518	-0.002
105	26	0.516	0.516	0.000
105	27	0.520	0.520	0.000
105	28	0.518	0.516	-0.002
105	29	0.520	0.518	-0.002
Max		0.522	0.520	0.002
Average		0.517	0.507	-0.010
Min		0.512	0.488	-0.028
Std Dev		0.002	0.011	0.011



6.6 _V_EN_FALL_4V	
Test Site	
Tester	
Test Number	
Max Limit	0.55 V
Min Limit	0.47 V

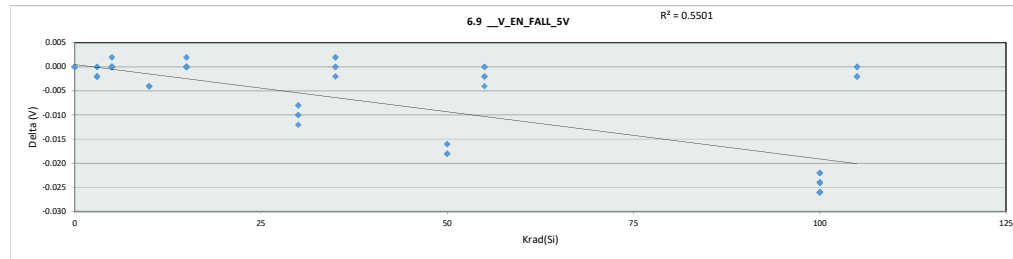
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.518	0.516	0.514	0.512	0.516	0.502	0.512	0.498	0.512	0.488	0.516
Average	0.519	0.518	0.516	0.514	0.517	0.506	0.516	0.500	0.514	0.493	0.518
Max	0.520	0.520	0.518	0.518	0.520	0.508	0.520	0.504	0.516	0.498	0.520
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



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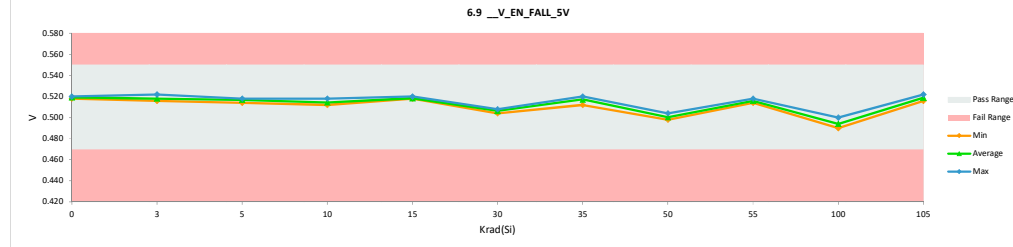
6.9 _V_EN_FALL_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.55
Min Limit	0.47

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	0.520	0.520	0.000
0	118	0.518	0.518	0.000
0	122	0.520	0.520	0.000
3	301	0.522	0.520	-0.002
3	306	0.518	0.516	-0.002
3	307	0.522	0.522	0.000
3	308	0.518	0.516	-0.002
3	309	0.516	0.516	0.000
5	310	0.516	0.516	0.000
5	311	0.518	0.518	0.000
5	312	0.516	0.518	0.002
5	314	0.514	0.514	0.000
5	316	0.518	0.518	0.000
10	317	0.518	0.514	-0.004
10	318	0.516	0.512	-0.004
10	319	0.518	0.514	-0.004
10	321	0.518	0.514	-0.004
10	322	0.522	0.518	-0.004
15	323	0.518	0.518	0.000
15	325	0.518	0.518	0.000
15	326	0.520	0.520	0.000
15	328	0.518	0.518	0.000
15	333	0.516	0.518	0.002
30	334	0.514	0.504	-0.010
30	335	0.518	0.508	-0.010
30	337	0.520	0.508	-0.012
30	339	0.512	0.504	-0.008
30	341	0.516	0.508	-0.008
35	342	0.518	0.518	0.000
35	345	0.518	0.520	0.002
35	346	0.514	0.512	-0.002
35	347	0.516	0.516	0.000
35	348	0.518	0.520	0.002
50	349	0.518	0.500	-0.018
50	350	0.520	0.504	-0.016
50	351	0.516	0.498	-0.018
50	353	0.518	0.500	-0.018
50	354	0.516	0.500	-0.016
55	355	0.514	0.514	0.000
55	357	0.518	0.518	0.000
55	359	0.520	0.518	-0.002
55	362	0.518	0.514	-0.004
55	363	0.518	0.516	-0.002
100	1	0.522	0.496	-0.026
100	3	0.518	0.494	-0.024
100	4	0.520	0.494	-0.026
100	5	0.516	0.492	-0.024
100	6	0.518	0.494	-0.024
100	7	0.520	0.496	-0.024
100	8	0.518	0.492	-0.026
100	9	0.518	0.492	-0.026
100	10	0.516	0.492	-0.024
100	11	0.520	0.496	-0.024
100	12	0.518	0.496	-0.022
100	13	0.520	0.496	-0.024
100	14	0.518	0.492	-0.026
100	15	0.520	0.494	-0.026
100	16	0.514	0.492	-0.022
100	17	0.520	0.496	-0.024
100	18	0.516	0.494	-0.022
100	19	0.520	0.498	-0.022
100	20	0.516	0.490	-0.026
100	21	0.522	0.500	-0.022
100	22	0.516	0.490	-0.026
100	24	0.516	0.494	-0.022
105	25	0.522	0.520	-0.002
105	26	0.516	0.516	0.000
105	27	0.522	0.522	0.000
105	28	0.518	0.516	-0.002
105	29	0.520	0.520	0.000
Max		0.522	0.522	0.002
Average		0.518	0.508	-0.010
Min		0.512	0.490	-0.026
Std Dev		0.002	0.011	0.011



6.9 _V_EN_FALL_5V	
Test Site	
Tester	
Test Number	
Max Limit	0.55
Min Limit	0.47

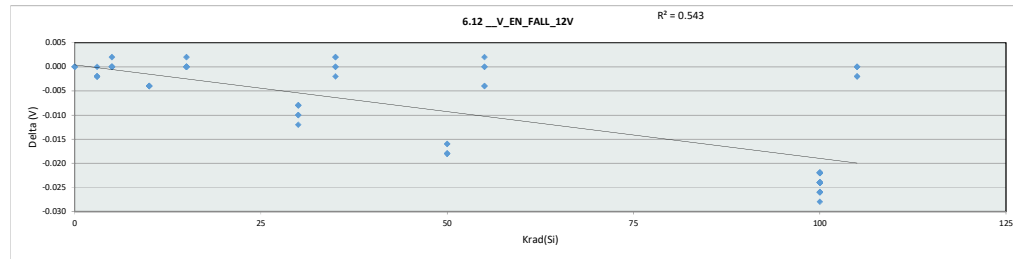
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.518	0.516	0.514	0.512	0.518	0.504	0.512	0.498	0.514	0.490	0.516
Average	0.519	0.518	0.517	0.514	0.518	0.506	0.517	0.500	0.516	0.494	0.519
Max	0.520	0.522	0.518	0.518	0.520	0.508	0.520	0.504	0.518	0.500	0.522
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



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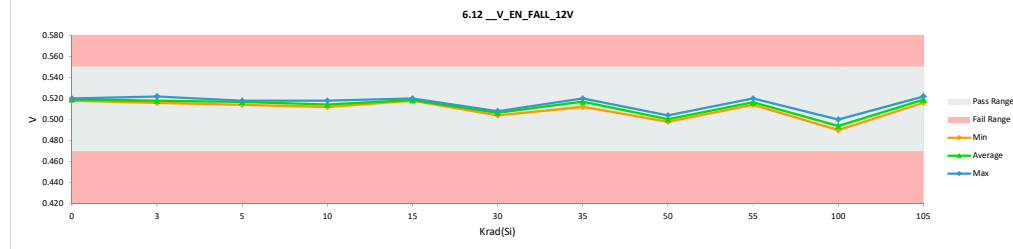
6.12 __V_EN_FALL_12V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	0.55 0.55
Min Limit	0.47 0.47

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	0.520	0.520	0.000
0	118	0.518	0.518	0.000
0	122	0.520	0.520	0.000
3	301	0.522	0.520	-0.002
3	306	0.518	0.516	-0.002
3	307	0.524	0.522	-0.002
3	308	0.518	0.516	-0.002
3	309	0.516	0.516	0.000
5	310	0.516	0.516	0.000
5	311	0.518	0.518	0.000
5	312	0.516	0.518	0.002
5	314	0.514	0.514	0.000
5	316	0.518	0.518	0.000
10	317	0.518	0.514	-0.004
10	318	0.516	0.512	-0.004
10	319	0.518	0.514	-0.004
10	321	0.518	0.514	-0.004
10	322	0.522	0.518	-0.004
15	323	0.518	0.518	0.000
15	325	0.518	0.518	0.000
15	326	0.520	0.520	0.000
15	328	0.518	0.518	0.000
15	333	0.516	0.518	0.002
30	334	0.514	0.504	-0.010
30	335	0.518	0.508	-0.010
30	337	0.520	0.508	-0.012
30	339	0.512	0.504	-0.008
30	341	0.516	0.508	-0.008
35	342	0.518	0.518	0.000
35	345	0.518	0.520	0.002
35	346	0.514	0.512	-0.002
35	347	0.516	0.516	0.000
35	348	0.518	0.520	0.002
50	349	0.518	0.500	-0.018
50	350	0.520	0.504	-0.016
50	351	0.516	0.498	-0.018
50	353	0.518	0.500	-0.018
50	354	0.516	0.500	-0.016
55	355	0.514	0.514	0.000
55	357	0.518	0.518	0.000
55	359	0.518	0.520	0.002
55	362	0.518	0.514	-0.004
55	363	0.520	0.516	-0.004
100	1	0.522	0.494	-0.028
100	3	0.518	0.494	-0.024
100	4	0.520	0.494	-0.026
100	5	0.516	0.492	-0.024
100	6	0.518	0.494	-0.024
100	7	0.520	0.496	-0.024
100	8	0.518	0.492	-0.026
100	9	0.518	0.494	-0.024
100	10	0.516	0.492	-0.024
100	11	0.520	0.496	-0.024
100	12	0.518	0.496	-0.022
100	13	0.520	0.496	-0.024
100	14	0.518	0.492	-0.026
100	15	0.520	0.494	-0.026
100	16	0.514	0.492	-0.022
100	17	0.520	0.496	-0.024
100	18	0.516	0.494	-0.022
100	19	0.520	0.498	-0.022
100	20	0.514	0.490	-0.024
100	21	0.522	0.500	-0.022
100	22	0.516	0.490	-0.026
100	24	0.516	0.494	-0.022
105	25	0.522	0.520	-0.002
105	26	0.516	0.516	0.000
105	27	0.522	0.522	0.000
105	28	0.518	0.516	-0.002
105	29	0.520	0.520	0.000
Max		0.524	0.522	0.002
Average		0.518	0.508	-0.010
Min		0.512	0.490	-0.028
Std Dev		0.002	0.011	0.011



6.12 __V_EN_FALL_12V	
Test Site	
Tester	
Test Number	
Max Limit	0.55 V
Min Limit	0.47 V

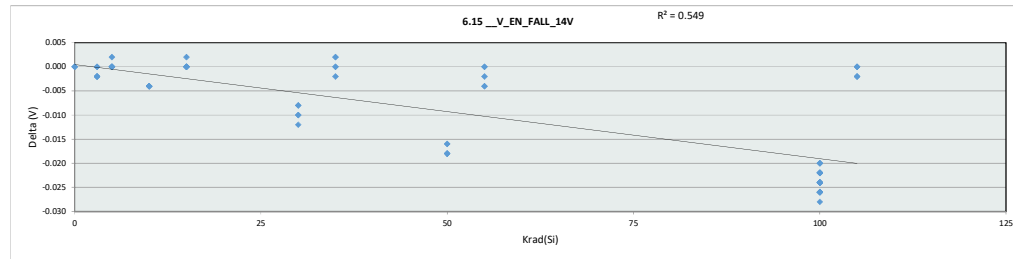
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.518	0.516	0.514	0.512	0.518	0.504	0.512	0.498	0.514	0.490	0.516
Average	0.519	0.518	0.517	0.514	0.518	0.506	0.517	0.500	0.516	0.494	0.519
Max	0.520	0.522	0.518	0.518	0.520	0.508	0.520	0.504	0.520	0.500	0.522
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



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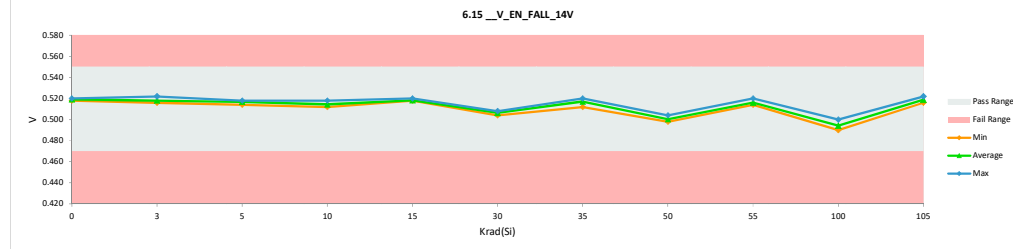
6.15 __V_EN_FALL_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.55
Min Limit	0.47

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	0.520	0.520	0.000
0	118	0.518	0.518	0.000
0	122	0.520	0.520	0.000
3	301	0.522	0.520	-0.002
3	306	0.518	0.516	-0.002
3	307	0.522	0.522	0.000
3	308	0.518	0.516	-0.002
3	309	0.516	0.516	0.000
5	310	0.516	0.516	0.000
5	311	0.518	0.518	0.000
5	312	0.516	0.518	0.002
5	314	0.514	0.514	0.000
5	316	0.518	0.518	0.000
10	317	0.518	0.514	-0.004
10	318	0.516	0.512	-0.004
10	319	0.518	0.514	-0.004
10	321	0.518	0.514	-0.004
10	322	0.522	0.518	-0.004
15	323	0.518	0.518	0.000
15	325	0.518	0.518	0.000
15	326	0.520	0.520	0.000
15	328	0.518	0.518	0.000
15	333	0.516	0.518	0.002
30	334	0.514	0.504	-0.010
30	335	0.518	0.508	-0.010
30	337	0.520	0.508	-0.012
30	339	0.512	0.504	-0.008
30	341	0.516	0.508	-0.008
35	342	0.518	0.518	0.000
35	345	0.518	0.520	0.002
35	346	0.514	0.512	-0.002
35	347	0.516	0.516	0.000
35	348	0.518	0.520	0.002
50	349	0.518	0.500	-0.018
50	350	0.520	0.504	-0.016
50	351	0.516	0.498	-0.018
50	353	0.518	0.500	-0.018
50	354	0.516	0.500	-0.016
55	355	0.514	0.514	0.000
55	357	0.518	0.516	-0.002
55	359	0.520	0.520	0.000
55	362	0.518	0.514	-0.004
55	363	0.520	0.516	-0.004
100	1	0.522	0.494	-0.028
100	3	0.518	0.494	-0.024
100	4	0.520	0.494	-0.026
100	5	0.516	0.492	-0.024
100	6	0.518	0.494	-0.024
100	7	0.520	0.496	-0.024
100	8	0.518	0.492	-0.026
100	9	0.518	0.492	-0.026
100	10	0.516	0.492	-0.024
100	11	0.520	0.496	-0.024
100	12	0.518	0.496	-0.022
100	13	0.520	0.496	-0.024
100	14	0.518	0.492	-0.026
100	15	0.520	0.494	-0.026
100	16	0.514	0.494	-0.020
100	17	0.520	0.496	-0.024
100	18	0.516	0.494	-0.022
100	19	0.520	0.500	-0.020
100	20	0.516	0.490	-0.026
100	21	0.522	0.500	-0.022
100	22	0.516	0.490	-0.026
100	24	0.516	0.494	-0.022
105	25	0.522	0.520	-0.002
105	26	0.516	0.516	0.000
105	27	0.522	0.522	0.000
105	28	0.518	0.516	-0.002
105	29	0.520	0.520	0.000
Max		0.522	0.522	0.002
Average		0.518	0.508	-0.010
Min		0.512	0.490	-0.028
Std Dev		0.002	0.011	0.011



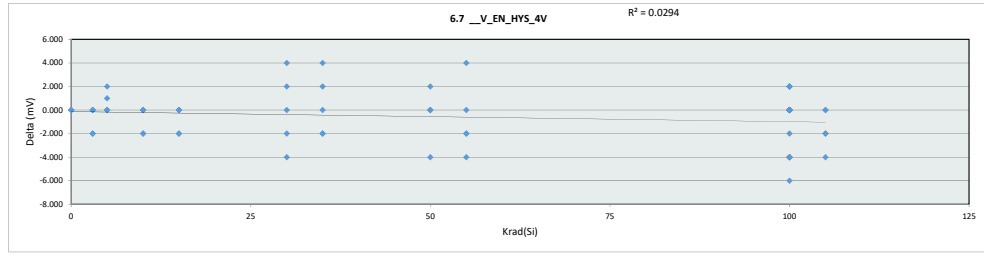
6.15 __V_EN_FALL_14V	
Test Site	
Tester	
Test Number	
Max Limit	0.55
Min Limit	0.47

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.518	0.516	0.514	0.512	0.518	0.504	0.512	0.498	0.514	0.490	0.516
Average	0.519	0.518	0.517	0.514	0.518	0.506	0.517	0.500	0.516	0.494	0.519
Max	0.520	0.522	0.518	0.518	0.520	0.508	0.520	0.504	0.520	0.500	0.522
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550

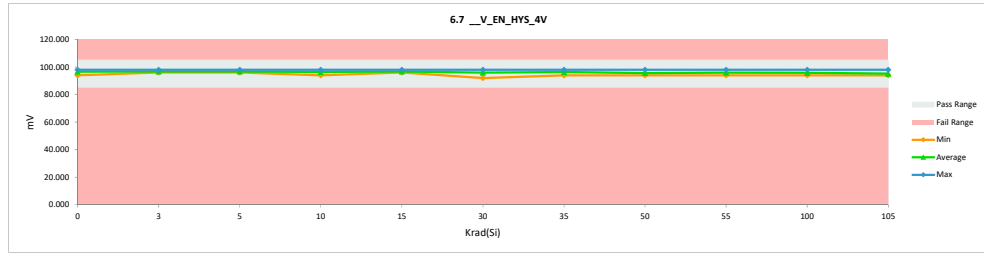


HDR TID Report
TPS7H5001-SP

6.7 V_EN_HYS_4V				
Test Site	Serial #	PRE_HDR	POST_HDR	Delta
Test Site				
Tester				
Test Number				
Unit		mV	mV	
Max Limit		105	105	
Min Limit		85	85	
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	98.000	98.000	0.000
0	118	94.000	94.000	0.000
0	122	98.000	98.000	0.000
3	301	96.000	96.000	0.000
3	306	98.000	96.000	-2.000
3	307	98.000	98.000	0.000
3	308	98.000	98.000	0.000
3	309	98.000	96.000	-2.000
5	310	96.000	98.000	2.000
5	311	97.000	98.000	1.000
5	312	96.000	96.000	0.000
5	314	96.000	96.000	0.000
5	316	96.000	96.000	0.000
10	317	96.000	96.000	0.000
10	318	98.000	98.000	0.000
10	319	96.000	94.000	-2.000
10	321	98.000	98.000	0.000
10	322	98.000	96.000	-2.000
15	323	98.000	96.000	-2.000
15	325	96.000	96.000	0.000
15	326	98.000	96.000	-2.000
15	328	98.000	98.000	0.000
15	333	98.000	98.000	0.000
30	334	96.000	96.000	0.000
30	335	94.000	98.000	4.000
30	337	96.000	98.000	2.000
30	339	98.000	96.000	-2.000
30	341	96.000	92.000	-4.000
35	342	96.000	94.000	-2.000
35	345	98.000	96.000	-2.000
35	346	98.000	98.000	0.000
35	347	94.000	96.000	2.000
35	348	94.000	98.000	4.000
50	349	96.000	98.000	2.000
50	350	98.000	94.000	-4.000
50	351	96.000	96.000	0.000
50	353	96.000	96.000	0.000
50	354	94.000	94.000	0.000
55	355	94.000	98.000	4.000
55	357	98.000	96.000	-2.000
55	359	98.000	94.000	-4.000
55	362	96.000	96.000	0.000
55	363	98.000	96.000	-2.000
100	1	96.000	96.000	0.000
100	3	100.000	94.000	-6.000
100	4	98.000	94.000	-4.000
100	5	98.000	94.000	-4.000
100	6	96.000	98.000	2.000
100	7	96.000	96.000	0.000
100	8	96.000	96.000	0.000
100	9	96.000	98.000	2.000
100	10	96.000	96.000	0.000
100	11	96.000	96.000	0.000
100	12	98.000	94.000	-4.000
100	13	98.000	96.000	-2.000
100	14	98.000	94.000	-4.000
100	15	96.000	96.000	0.000
100	16	96.000	98.000	2.000
100	17	96.000	96.000	0.000
100	18	96.000	96.000	0.000
100	19	98.000	98.000	0.000
100	20	96.000	96.000	0.000
100	21	94.000	94.000	0.000
100	22	98.000	94.000	-4.000
100	24	96.000	98.000	2.000
105	25	98.000	94.000	-4.000
105	26	96.000	94.000	-2.000
105	27	96.000	94.000	-2.000
105	28	96.000	96.000	0.000
105	29	98.000	98.000	0.000
Max	100.000	98.000	98.000	4.000
Average	96.700	96.114	96.114	-0.586
Min	94.000	92.000	92.000	-6.000
Std Dev	1.355	1.556	1.556	2.061

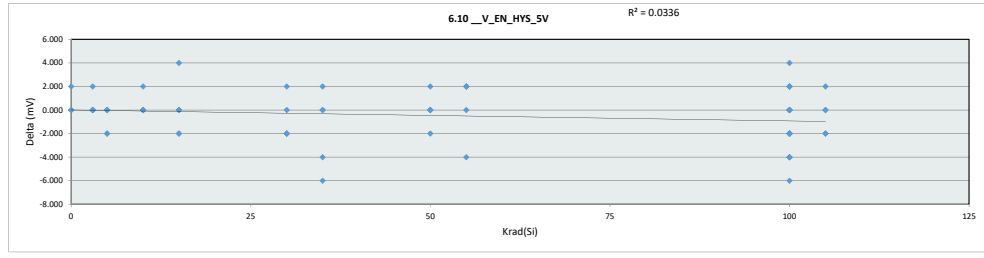


6.7 V_EN_HYS_4V											
Test Site	Serial #	PRE_HDR	POST_HDR	Delta							
Test Site											
Tester											
Test Number											
Unit		mV	mV								
Max Limit		105	105								
Min Limit		85	85								
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta							
LL	0	85.000	85.000	85.000							
Min	3	94.000	96.000	94.000							
Average	5	96.667	96.800	96.667							
Max	10	98.000	98.000	98.000							
UL	15	105.000	105.000	105.000							

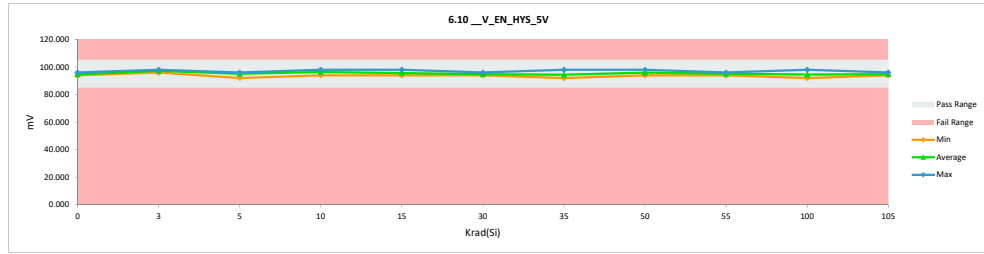


HDR TID Report
TPS7H5001-SP

6.10 V_EN_HYS_5V				
Test Site	Serial #	PRE_HDR	POST_HDR	Delta
Test Site				
Tester				
Test Number				
Unit	mV		mV	
Max Limit	105		105	
Min Limit	85		85	
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	94.000	96.000	2.000
0	118	94.000	94.000	0.000
0	122	94.000	94.000	0.000
3	301	94.000	96.000	2.000
3	306	96.000	96.000	0.000
3	307	98.000	98.000	0.000
3	308	98.000	98.000	0.000
3	309	98.000	98.000	0.000
5	310	96.000	96.000	0.000
5	311	94.000	92.000	-2.000
5	312	98.000	96.000	-2.000
5	314	96.000	96.000	0.000
5	316	96.000	96.000	0.000
10	317	96.000	96.000	0.000
10	318	98.000	98.000	0.000
10	319	96.000	96.000	0.000
10	321	94.000	94.000	0.000
10	322	96.000	98.000	2.000
15	323	96.000	94.000	-2.000
15	325	94.000	94.000	0.000
15	326	94.000	98.000	4.000
15	328	96.000	96.000	0.000
15	333	98.000	96.000	-2.000
30	334	96.000	94.000	-2.000
30	335	94.000	96.000	2.000
30	337	94.000	94.000	0.000
30	339	98.000	96.000	-2.000
30	341	96.000	94.000	-2.000
35	342	96.000	96.000	0.000
35	345	98.000	92.000	-6.000
35	346	96.000	98.000	2.000
35	347	94.000	94.000	0.000
35	348	96.000	92.000	-4.000
50	349	96.000	98.000	2.000
50	350	98.000	96.000	-2.000
50	351	96.000	96.000	0.000
50	353	96.000	96.000	0.000
50	354	94.000	94.000	0.000
55	355	94.000	96.000	2.000
55	357	96.000	96.000	0.000
55	359	92.000	94.000	2.000
55	362	94.000	96.000	2.000
55	363	98.000	94.000	-4.000
100	1	92.000	94.000	2.000
100	3	96.000	92.000	-4.000
100	4	96.000	94.000	-2.000
100	5	98.000	92.000	-6.000
100	6	96.000	98.000	2.000
100	7	96.000	92.000	-4.000
100	8	96.000	96.000	0.000
100	9	92.000	96.000	4.000
100	10	94.000	96.000	2.000
100	11	94.000	96.000	2.000
100	12	96.000	92.000	-4.000
100	13	96.000	94.000	-2.000
100	14	92.000	92.000	0.000
100	15	96.000	96.000	0.000
100	16	98.000	96.000	-2.000
100	17	96.000	94.000	-2.000
100	18	96.000	96.000	0.000
100	19	98.000	96.000	-2.000
100	20	96.000	94.000	-2.000
100	21	96.000	94.000	-2.000
100	22	96.000	94.000	-2.000
100	24	98.000	96.000	-2.000
105	25	92.000	94.000	2.000
105	26	96.000	94.000	-2.000
105	27	94.000	94.000	0.000
105	28	96.000	96.000	0.000
105	29	98.000	96.000	-2.000
Max		98.000	98.000	4.000
Average		95.657	95.171	-0.486
Min		92.000	92.000	-6.000
Std Dev		1.702	1.719	2.083

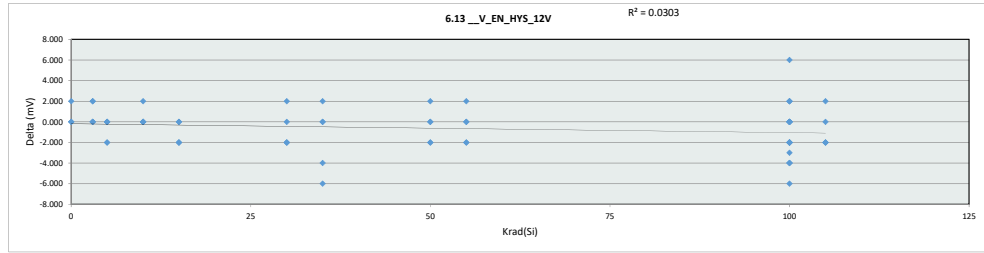


6.10_V_EN_HYS_5V											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.000	96.000	92.000	94.000	94.000	94.000	94.000	92.000	94.000	94.000	94.000
Average	94.667	97.200	95.200	96.400	95.600	94.800	94.400	96.000	95.200	94.545	94.800
Max	96.000	98.000	96.000	98.000	98.000	96.000	98.000	98.000	96.000	98.000	96.000
UL	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000

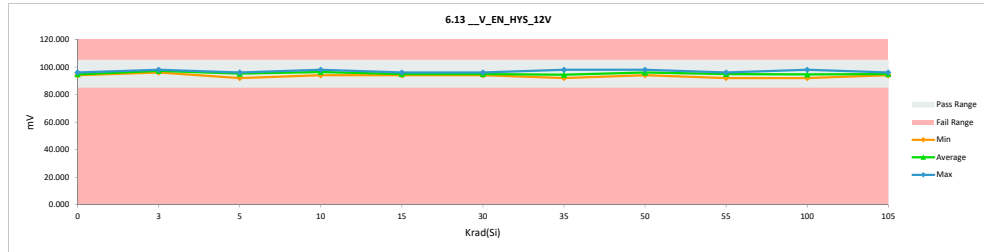


HDR TID Report
TPS7H5001-SP

6.13 V_EN_HYS_12V				
Test Site	Serial #	PRE_HDR	POST_HDR	Delta
Test Number				
Unit	mV		mV	
Max Limit	105	105		
Min Limit	85	85		
0	117	94.000	96.000	2.000
0	118	94.000	94.000	0.000
0	122	94.000	94.000	0.000
3	301	94.000	96.000	2.000
3	306	96.000	96.000	0.000
3	307	96.000	98.000	2.000
3	308	98.000	98.000	0.000
3	309	98.000	98.000	0.000
5	310	96.000	96.000	0.000
5	311	94.000	92.000	-2.000
5	312	98.000	96.000	-2.000
5	314	96.000	96.000	0.000
5	316	96.000	96.000	0.000
10	317	96.000	96.000	0.000
10	318	98.000	98.000	0.000
10	319	96.000	96.000	0.000
10	321	94.000	94.000	0.000
10	322	96.000	98.000	2.000
15	323	96.000	94.000	-2.000
15	325	94.000	94.000	0.000
15	326	94.000	94.000	0.000
15	328	98.000	96.000	-2.000
15	333	98.000	96.000	-2.000
30	334	96.000	94.000	-2.000
30	335	94.000	96.000	2.000
30	337	94.000	94.000	0.000
30	339	98.000	96.000	-2.000
30	341	96.000	94.000	-2.000
35	342	96.000	96.000	0.000
35	345	98.000	92.000	-6.000
35	346	96.000	98.000	2.000
35	347	94.000	94.000	0.000
35	348	96.000	92.000	-4.000
50	349	96.000	98.000	2.000
50	350	98.000	96.000	-2.000
50	351	96.000	96.000	0.000
50	353	96.000	96.000	0.000
50	354	96.000	94.000	-2.000
55	355	96.000	96.000	0.000
55	357	96.000	96.000	0.000
55	359	94.000	92.000	-2.000
55	362	94.000	96.000	2.000
55	363	96.000	94.000	-2.000
100	1	92.000	98.000	6.000
100	3	96.000	92.000	-4.000
100	4	96.000	94.000	-2.000
100	5	98.000	92.000	-6.000
100	6	96.000	98.000	2.000
100	7	92.000	92.000	0.000
100	8	96.000	96.000	0.000
100	9	92.000	94.000	2.000
100	10	96.000	96.000	0.000
100	11	94.000	96.000	2.000
100	12	96.000	92.000	-4.000
100	13	96.000	94.000	-2.000
100	14	92.000	92.000	0.000
100	15	96.000	96.000	0.000
100	16	98.000	96.000	-2.000
100	17	96.000	94.000	-2.000
100	18	96.000	96.000	0.000
100	19	98.000	96.000	-2.000
100	20	98.000	94.000	-4.000
100	21	96.000	93.000	-3.000
100	22	96.000	94.000	-2.000
100	24	98.000	96.000	-2.000
105	25	92.000	94.000	2.000
105	26	96.000	94.000	-2.000
105	27	96.000	94.000	-2.000
105	28	96.000	96.000	0.000
105	29	98.000	96.000	-2.000
Max		98.000	98.000	6.000
Average		95.743	95.100	-0.643
Min		92.000	92.000	-6.000
Std Dev		1.665	1.771	2.043

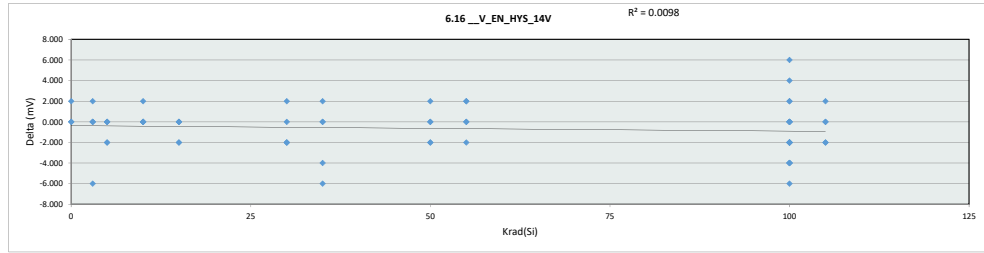


6.13 V_EN_HYS_12V												
Test Site	Serial #	PRE_HDR	POST_HDR	Delta								
Test Number												
Unit	mV		mV									
Max Limit	105	105										
Min Limit	85	85										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	
Min	94.000	96.000	92.000	94.000	94.000	94.000	94.000	92.000	94.000	92.000	94.000	
Average	94.667	97.200	95.200	96.400	94.800	94.800	94.400	96.000	94.800	94.591	94.800	
Max	96.000	98.000	96.000	98.000	96.000	96.000	98.000	98.000	98.000	96.000	96.000	
UL	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	

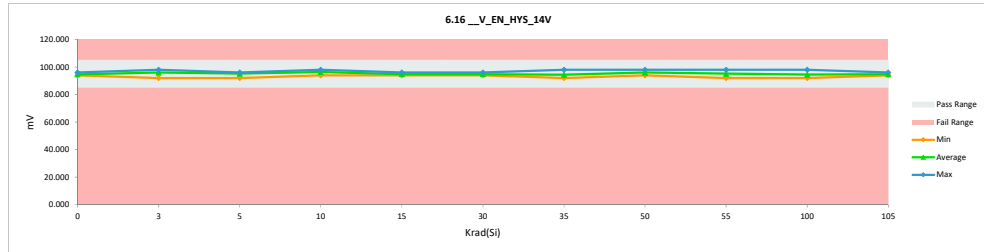


HDR TID Report
TPS7H5001-SP

6.16 V_EN_HYS_14V				
Test Site	Serial #	PRE_HDR	POST_HDR	Delta
0	117	94.000	96.000	2.000
0	118	94.000	94.000	0.000
0	122	94.000	94.000	0.000
3	301	94.000	96.000	2.000
3	306	96.000	96.000	0.000
3	307	98.000	92.000	-6.000
3	308	98.000	98.000	0.000
3	309	98.000	98.000	0.000
5	310	96.000	96.000	0.000
5	311	94.000	92.000	-2.000
5	312	98.000	96.000	-2.000
5	314	96.000	96.000	0.000
5	316	96.000	96.000	0.000
10	317	96.000	96.000	0.000
10	318	98.000	98.000	0.000
10	319	96.000	96.000	0.000
10	321	94.000	94.000	0.000
10	322	96.000	98.000	2.000
15	323	96.000	94.000	-2.000
15	325	94.000	94.000	0.000
15	326	94.000	94.000	0.000
15	328	96.000	96.000	0.000
15	333	98.000	96.000	-2.000
30	334	96.000	94.000	-2.000
30	335	94.000	96.000	2.000
30	337	94.000	94.000	0.000
30	339	98.000	96.000	-2.000
30	341	96.000	94.000	-2.000
35	342	96.000	96.000	0.000
35	345	98.000	92.000	-6.000
35	346	96.000	98.000	2.000
35	347	94.000	94.000	0.000
35	348	96.000	92.000	-4.000
50	349	96.000	98.000	2.000
50	350	98.000	96.000	-2.000
50	351	96.000	96.000	0.000
50	353	96.000	96.000	0.000
50	354	96.000	94.000	-2.000
55	355	96.000	96.000	0.000
55	357	96.000	98.000	2.000
55	359	92.000	92.000	0.000
55	362	94.000	96.000	2.000
55	363	96.000	94.000	-2.000
100	1	92.000	98.000	6.000
100	3	96.000	92.000	-4.000
100	4	96.000	94.000	-2.000
100	5	98.000	92.000	-6.000
100	6	96.000	98.000	2.000
100	7	92.000	92.000	0.000
100	8	96.000	96.000	0.000
100	9	92.000	96.000	4.000
100	10	96.000	96.000	0.000
100	11	94.000	96.000	2.000
100	12	96.000	92.000	-4.000
100	13	96.000	94.000	-2.000
100	14	92.000	92.000	0.000
100	15	96.000	96.000	0.000
100	16	94.000	94.000	0.000
100	17	96.000	94.000	-2.000
100	18	98.000	96.000	-2.000
100	19	98.000	94.000	-4.000
100	20	96.000	94.000	-2.000
100	21	96.000	94.000	-2.000
100	22	96.000	94.000	-2.000
100	24	98.000	96.000	-2.000
105	25	92.000	94.000	2.000
105	26	96.000	94.000	-2.000
105	27	94.000	94.000	0.000
105	28	96.000	96.000	0.000
105	29	98.000	96.000	-2.000
Max		98.000	98.000	6.000
Average		95.686	95.029	-0.657
Min		92.000	92.000	-6.000
Std Dev		1.724	1.793	2.199



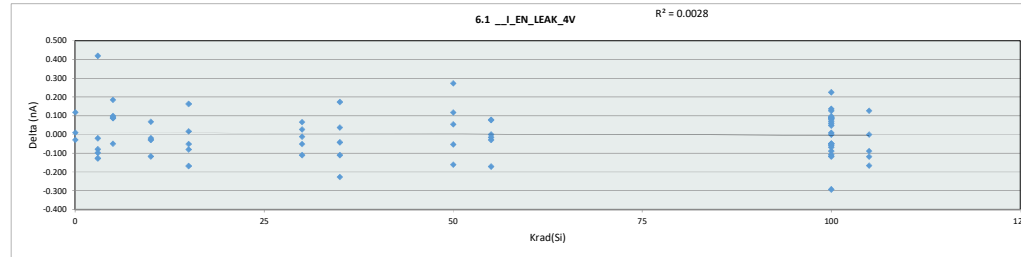
6.16 V_EN_HYS_14V											
Test Site	Serial #	PRE_HDR	POST_HDR	Delta							
0	117	94.000	96.000	2.000							
0	118	94.000	94.000	0.000							
0	122	94.000	94.000	0.000							
3	301	94.000	96.000	2.000							
3	306	96.000	96.000	0.000							
3	307	98.000	92.000	-6.000							
3	308	98.000	98.000	0.000							
3	309	98.000	98.000	0.000							
5	310	96.000	96.000	0.000							
5	311	94.000	92.000	-2.000							
5	312	98.000	96.000	-2.000							
5	314	96.000	96.000	0.000							
5	316	96.000	96.000	0.000							
10	317	96.000	96.000	0.000							
10	318	98.000	98.000	0.000							
10	319	96.000	96.000	0.000							
10	321	94.000	94.000	0.000							
10	322	96.000	98.000	2.000							
15	323	96.000	94.000	-2.000							
15	325	94.000	94.000	0.000							
15	326	94.000	94.000	0.000							
15	328	96.000	96.000	0.000							
15	333	98.000	96.000	-2.000							
30	334	96.000	94.000	-2.000							
30	335	94.000	96.000	2.000							
30	337	94.000	94.000	0.000							
30	339	98.000	96.000	-2.000							
30	341	96.000	94.000	-2.000							
35	342	96.000	96.000	0.000							
35	345	98.000	92.000	-6.000							
35	346	96.000	98.000	2.000							
35	347	94.000	94.000	0.000							
35	348	96.000	92.000	-4.000							
50	349	96.000	98.000	2.000							
50	350	98.000	96.000	-2.000							
50	351	96.000	96.000	0.000							
50	353	96.000	96.000	0.000							
50	354	96.000	94.000	-2.000							
55	355	96.000	96.000	0.000							
55	357	96.000	98.000	2.000							
55	359	92.000	92.000	0.000							
55	362	94.000	96.000	2.000							
55	363	96.000	94.000	-2.000							
100	1	92.000	98.000	6.000							
100	3	96.000	92.000	-4.000							
100	4	96.000	94.000	-2.000							
100	5	98.000	92.000	-6.000							
100	6	96.000	98.000	2.000							
100	7	92.000	92.000	0.000							
100	8	96.000	96.000	0.000							
100	9	92.000	96.000	4.000							
100	10	96.000	96.000	0.000							
100	11	94.000	96.000	2.000							
100	12	96.000	92.000	-4.000							
100	13	96.000	94.000	-2.000							
100	14	92.000	92.000	0.000							
100	15	96.000	96.000	0.000							
100	16	94.000	94.000	0.000							
100	17	96.000	94.000	-2.000							
100	18	98.000	96.000	-2.000							
100	19	98.000	94.000	-4.000							
100	20	96.000	94.000	-2.000							
100	21	96.000	94.000	-2.000							
100	22	96.000	94.000	-2.000							
100	24	98.000	96.000	-2.000							
105	25	92.000	94.000	2.000							
105	26	96.000	94.000	-2.000							
105	27	94.000	94.000	0.000							
105	28	96.000	96.000	0.000							
105	29	98.000	96.000	-2.000							
LL		85.000	85.000	85.000							
Min		94.000	92.000	94.000							
Average		94.667	95.200	96.400							
Max		96.000	98.000	96.000							
UL		105.000	105.000	105.000							



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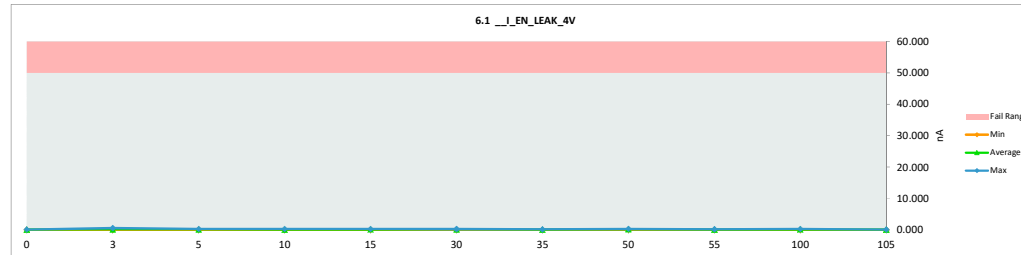
6.1_I_EN_LEAK_4V	
Test Site	
Tester	
Test Number	
Unit	nA nA
Max Limit	50
Min Limit	

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	0.024	0.142	0.118
0	118	0.073	0.044	-0.029
0	122	0.083	0.093	0.010
3	301	0.054	0.034	-0.020
3	306	0.132	0.005	-0.127
3	307	0.181	0.102	-0.079
3	308	0.161	0.063	-0.098
3	309	0.161	0.581	0.420
5	310	0.063	0.161	0.098
5	311	0.122	0.210	0.088
5	312	0.122	0.073	-0.049
5	314	0.142	0.327	0.185
5	316	0.122	0.210	0.088
10	317	0.063	0.044	-0.019
10	318	0.220	0.288	0.068
10	319	0.034	0.005	-0.029
10	321	0.073	0.044	-0.029
10	322	0.122	0.005	-0.117
15	323	0.154	0.317	0.163
15	325	0.056	0.073	0.017
15	326	0.124	0.044	-0.080
15	328	0.183	0.132	-0.051
15	323	0.261	0.093	-0.168
30	334	0.261	0.210	-0.051
30	335	0.027	0.093	0.066
30	337	0.056	0.044	-0.012
30	339	0.281	0.171	-0.110
30	341	0.251	0.278	0.027
35	342	0.242	0.015	-0.227
35	345	0.017	0.190	0.173
35	346	0.105	0.142	0.037
35	347	0.066	0.024	-0.042
35	348	0.183	0.073	-0.110
50	349	0.124	0.071	-0.053
50	350	0.217	0.334	0.117
50	351	0.261	0.100	-0.161
50	353	0.071	0.344	0.273
50	354	0.095	0.149	0.054
55	355	0.110	0.110	0.000
55	357	0.041	0.012	-0.029
55	359	0.022	0.007	-0.015
55	362	0.159	0.237	0.078
55	363	0.310	0.139	-0.171
100	1	0.102	0.327	0.225
100	3	0.151	0.102	-0.049
100	4	0.132	0.083	-0.049
100	5	0.044	0.112	0.068
100	6	0.132	0.132	0.000
100	7	0.015	0.063	0.048
100	8	0.102	0.044	-0.058
100	9	0.054	0.005	-0.049
100	10	0.093	0.181	0.088
100	11	0.005	0.063	0.058
100	12	0.200	0.083	-0.117
100	13	0.063	0.142	0.079
100	14	0.054	0.063	0.009
100	15	0.132	0.220	0.088
100	16	0.093	0.190	0.097
100	17	0.317	0.024	-0.293
100	18	0.181	0.073	-0.108
100	19	0.073	0.005	-0.068
100	20	0.073	0.210	0.137
100	21	0.054	0.181	0.127
100	22	0.132	0.044	-0.088
100	24	0.024	0.112	0.088
105	25	0.142	0.024	-0.118
105	26	0.190	0.024	-0.166
105	27	0.200	0.112	-0.088
105	28	0.024	0.151	0.127
105	29	0.151	0.151	0.000
	Max	0.317	0.581	0.420
	Average	0.122	0.125	0.003
	Min	0.005	0.005	-0.293
	Std Dev	0.077	0.107	0.119



6.1_I_EN_LEAK_4V	
Test Site	
Tester	
Test Number	
Max Limit	50 nA
Min Limit	nA

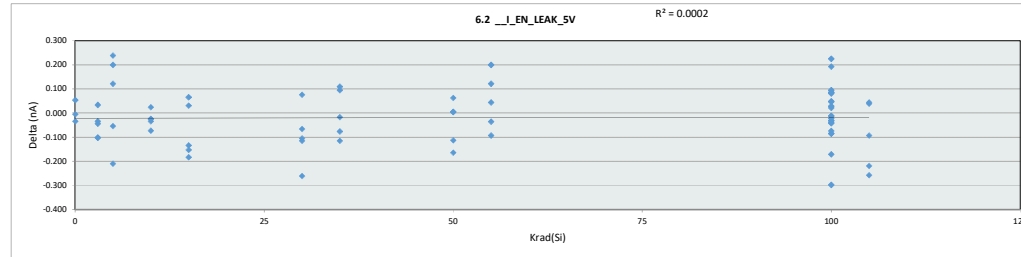
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	0.044	0.005	0.073	0.005	0.044	0.044	0.015	0.071	0.007	0.005	0.024
Average	0.093	0.157	0.196	0.077	0.132	0.159	0.089	0.200	0.101	0.112	0.092
Max	0.142	0.581	0.327	0.288	0.317	0.278	0.190	0.344	0.237	0.327	0.151
UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000



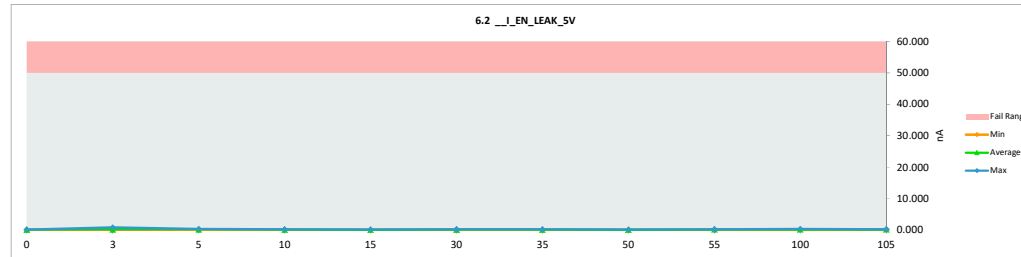
HDR TID Report
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6.2_I_EN_LEAK_5V	
Test Site	
Tester	
Test Number	
Unit	nA nA
Max Limit	50
Min Limit	

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	0.127	0.122	-0.005
0	118	0.088	0.142	0.054
0	122	0.068	0.034	-0.034
3	301	0.020	0.054	0.034
3	306	0.107	0.005	-0.102
3	307	0.156	0.054	-0.102
3	308	0.107	0.063	-0.044
3	309	0.810	0.776	-0.034
5	310	0.176	0.122	-0.054
5	311	0.234	0.024	-0.210
5	312	0.107	0.229	0.122
5	314	0.039	0.239	0.200
5	316	0.078	0.317	0.239
10	317	0.146	0.112	-0.034
10	318	0.166	0.190	0.024
10	319	0.127	0.054	-0.073
10	321	0.059	0.034	-0.025
10	322	0.166	0.142	-0.024
15	323	0.295	0.161	-0.134
15	325	0.032	0.063	0.031
15	326	0.168	0.015	-0.153
15	328	0.017	0.083	0.066
15	333	0.256	0.073	-0.183
30	334	0.227	0.112	-0.115
30	335	0.315	0.054	-0.261
30	337	0.017	0.093	0.076
30	339	0.305	0.200	-0.105
30	341	0.168	0.102	-0.066
35	342	0.007	0.102	0.095
35	345	0.227	0.112	-0.115
35	346	0.159	0.083	-0.076
35	347	0.041	0.024	-0.017
35	348	0.090	0.200	0.110
50	349	0.198	0.034	-0.164
50	350	0.068	0.073	0.005
50	351	0.039	0.102	0.063
50	353	0.107	0.112	0.005
50	354	0.137	0.024	-0.113
55	355	0.000	0.200	0.200
55	357	0.137	0.102	-0.035
55	359	0.117	0.024	-0.093
55	362	0.059	0.181	0.122
55	363	0.098	0.142	0.044
100	1	0.107	0.154	0.047
100	3	0.195	0.163	-0.032
100	4	0.010	0.203	0.193
100	5	0.146	0.061	-0.085
100	6	0.039	0.120	0.081
100	7	0.098	0.066	-0.032
100	8	0.117	0.212	0.095
100	9	0.029	0.007	-0.022
100	10	0.117	0.105	-0.012
100	11	0.088	0.046	-0.042
100	12	0.029	0.051	0.022
100	13	0.117	0.203	0.086
100	14	0.088	0.173	0.085
100	15	0.293	0.281	-0.012
100	16	0.107	0.022	-0.085
100	17	0.203	0.032	-0.171
100	18	0.056	0.281	0.225
100	19	0.300	0.002	-0.298
100	20	0.271	0.300	0.029
100	21	0.163	0.212	0.049
100	22	0.261	0.222	-0.039
100	24	0.115	0.041	-0.074
105	25	0.173	0.212	0.039
105	26	0.368	0.149	-0.219
105	27	0.339	0.081	-0.258
105	28	0.037	0.081	0.044
105	29	0.203	0.110	-0.093
Max		0.810	0.776	0.239
Average		0.145	0.125	-0.019
Min		0.000	0.002	-0.298
Std Dev		0.121	0.111	0.114



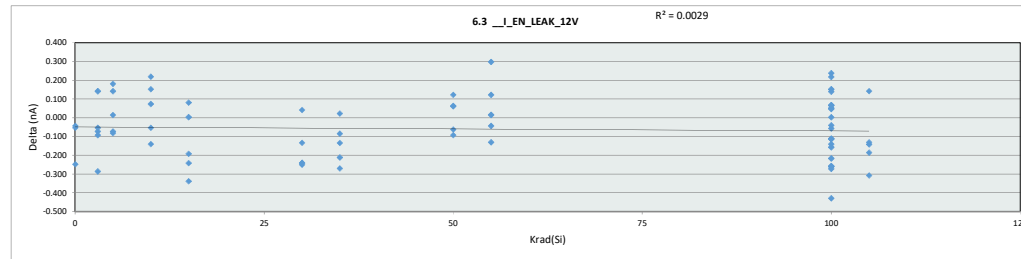
6.2_I_EN_LEAK_5V											
Test Site											
Tester											
Test Number											
Max Limit	50	nA									
Min Limit		nA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	0.034	0.005	0.024	0.034	0.015	0.054	0.024	0.024	0.024	0.002	0.081
Average	0.099	0.190	0.186	0.106	0.079	0.112	0.104	0.069	0.130	0.134	0.127
Max	0.142	0.776	0.317	0.190	0.161	0.200	0.200	0.112	0.200	0.300	0.212
UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000



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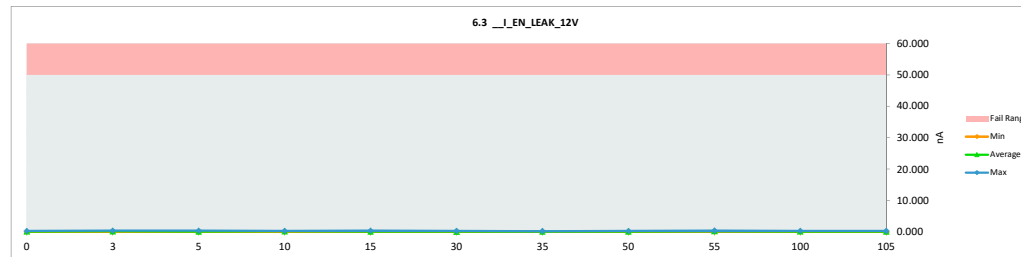
6.3 _I_EN_LEAK_12V	
Test Site	
Tester	
Test Number	
Unit	nA nA
Max Limit	50 50
Min Limit	

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	0.264	0.015	-0.249
0	118	0.049	0.005	-0.044
0	122	0.322	0.268	-0.054
3	301	0.303	0.210	-0.093
3	306	0.098	0.239	0.141
3	307	0.166	0.093	-0.073
3	308	0.088	0.034	-0.054
3	309	0.673	0.386	-0.287
5	310	0.020	0.161	0.141
5	311	0.010	0.024	0.014
5	312	0.205	0.386	0.181
5	314	0.273	0.190	-0.083
5	316	0.098	0.024	-0.074
10	317	0.244	0.190	-0.054
10	318	0.127	0.200	0.073
10	319	0.195	0.054	-0.141
10	321	0.195	0.347	0.152
10	322	0.088	0.307	0.219
15	323	0.198	0.005	-0.193
15	325	0.373	0.376	0.003
15	326	0.198	0.278	0.080
15	328	0.344	0.005	-0.339
15	333	0.403	0.161	-0.242
30	334	0.256	0.005	-0.251
30	335	0.237	0.278	0.041
30	337	0.188	0.054	-0.134
30	339	0.295	0.054	-0.241
30	341	0.295	0.054	-0.241
35	342	0.364	0.229	-0.135
35	345	0.383	0.112	-0.271
35	346	0.168	0.083	-0.085
35	347	0.217	0.239	0.022
35	348	0.227	0.015	-0.212
50	349	0.120	0.181	0.061
50	350	0.195	0.102	-0.093
50	351	0.146	0.268	0.122
50	353	0.039	0.102	0.063
50	354	0.107	0.044	-0.063
55	355	0.156	0.278	0.122
55	357	0.264	0.220	-0.044
55	359	0.059	0.356	0.297
55	362	0.068	0.083	0.015
55	363	0.351	0.220	-0.131
100	1	0.254	0.037	-0.217
100	3	0.273	0.012	-0.261
100	4	0.098	0.041	-0.057
100	5	0.020	0.159	0.139
100	6	0.107	0.344	0.237
100	7	0.039	0.041	0.002
100	8	0.312	0.154	-0.158
100	9	0.146	0.105	-0.041
100	10	0.000	0.217	0.217
100	11	0.059	0.110	0.051
100	12	0.146	0.032	-0.114
100	13	0.029	0.095	0.066
100	14	0.029	0.095	0.066
100	15	0.117	0.163	0.046
100	16	0.342	0.081	-0.261
100	17	0.417	0.305	-0.112
100	18	0.081	0.232	0.151
100	19	0.329	0.071	-0.258
100	20	0.173	0.032	-0.141
100	21	0.388	0.115	-0.273
100	22	0.554	0.124	-0.430
100	24	0.408	0.295	-0.113
105	25	0.476	0.168	-0.308
105	26	0.144	0.286	0.142
105	27	0.203	0.017	-0.186
105	28	0.329	0.198	-0.131
105	29	0.183	0.041	-0.142
Max	0.673	0.386	0.297	
Average	0.210	0.150	-0.060	
Min	0.000	0.005	-0.430	
Std Dev	0.138	0.112	0.160	



6.3 _I_EN_LEAK_12V	
Test Site	
Tester	
Test Number	
Max Limit	50 nA
Min Limit	nA

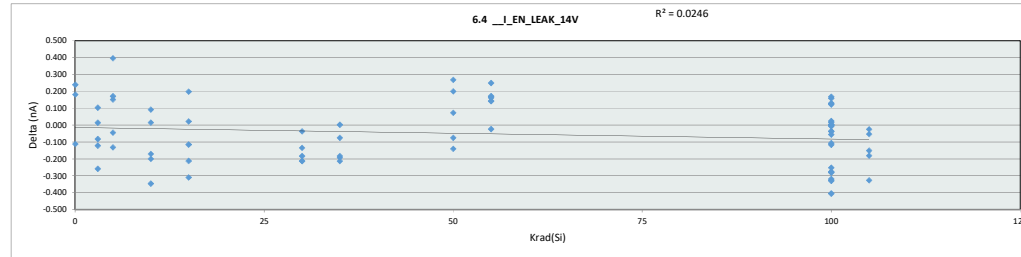
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	0.005	0.034	0.024	0.054	0.005	0.005	0.015	0.044	0.083	0.012	0.017
Average	0.096	0.192	0.157	0.220	0.165	0.089	0.136	0.139	0.231	0.130	0.142
Max	0.268	0.386	0.386	0.347	0.376	0.278	0.239	0.268	0.356	0.344	0.286
UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000



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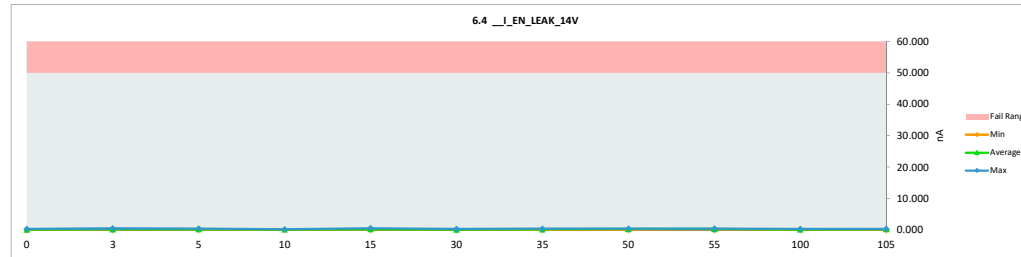
6.4_I_EN_LEAK_14V	
Test Site	
Tester	
Test Number	
Unit	nA nA
Max Limit	50
Min Limit	

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	0.000	0.181	0.181
0	118	0.098	0.337	0.239
0	122	0.117	0.005	-0.112
3	301	0.176	0.190	0.014
3	306	0.244	0.122	-0.122
3	307	0.244	0.161	-0.083
3	308	0.029	0.132	0.103
3	309	0.752	0.493	-0.259
5	310	0.244	0.112	-0.132
5	311	0.049	0.200	0.151
5	312	0.029	0.425	0.396
5	314	0.166	0.337	0.171
5	316	0.176	0.132	-0.044
10	317	0.068	0.083	0.015
10	318	0.371	0.024	-0.347
10	319	0.254	0.083	-0.171
10	321	0.137	0.229	0.092
10	322	0.342	0.142	-0.200
15	323	0.178	0.200	0.022
15	325	0.286	0.171	-0.115
15	326	0.373	0.161	-0.212
15	328	0.344	0.542	0.198
15	333	0.422	0.112	-0.310
30	334	0.286	0.102	-0.184
30	335	0.276	0.063	-0.213
30	337	0.286	0.151	-0.135
30	339	0.344	0.132	-0.212
30	341	0.344	0.307	-0.037
35	342	0.217	0.142	-0.075
35	345	0.422	0.239	-0.183
35	346	0.315	0.317	0.002
35	347	0.217	0.024	-0.193
35	348	0.608	0.395	-0.213
50	349	0.168	0.093	-0.075
50	350	0.400	0.259	-0.141
50	351	0.381	0.454	0.073
50	353	0.059	0.327	0.268
50	354	0.166	0.366	0.200
55	355	0.156	0.405	0.249
55	357	0.185	0.347	0.162
55	359	0.039	0.181	0.142
55	362	0.107	0.278	0.171
55	363	0.322	0.298	-0.024
100	1	0.410	0.090	-0.320
100	3	0.381	0.051	-0.330
100	4	0.127	0.090	-0.037
100	5	0.420	0.139	-0.281
100	6	0.010	0.139	0.129
100	7	0.039	0.198	0.159
100	8	0.127	0.120	-0.007
100	9	0.166	0.168	0.002
100	10	0.059	0.227	0.168
100	11	0.234	0.178	-0.056
100	12	0.303	0.051	-0.252
100	13	0.029	0.032	0.003
100	14	0.117	0.115	-0.002
100	15	0.068	0.085	0.017
100	16	0.127	0.090	-0.037
100	17	0.242	0.266	0.024
100	18	0.320	0.203	-0.117
100	19	0.378	0.100	-0.278
100	20	0.447	0.041	-0.406
100	21	0.163	0.056	-0.107
100	22	0.349	0.071	-0.278
100	24	0.242	0.364	0.122
105	25	0.290	0.237	-0.053
105	26	0.525	0.344	-0.181
105	27	0.525	0.198	-0.327
105	28	0.329	0.178	-0.151
105	29	0.281	0.256	-0.025
	Max	0.752	0.542	0.396
	Average	0.244	0.193	-0.051
	Min	0.000	0.005	-0.406
	Std Dev	0.152	0.123	0.176



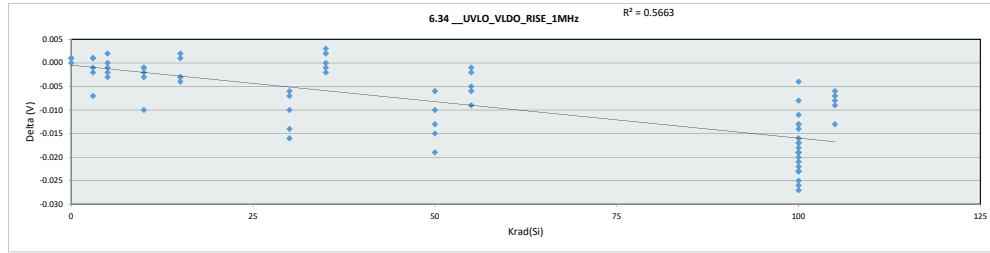
6.4_I_EN_LEAK_14V	
Test Site	
Tester	
Test Number	
Max Limit	50 nA
Min Limit	nA

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	0.005	0.122	0.112	0.024	0.112	0.063	0.024	0.093	0.181	0.032	0.178
Average	0.174	0.220	0.241	0.112	0.237	0.151	0.223	0.300	0.302	0.131	0.243
Max	0.337	0.493	0.425	0.229	0.542	0.307	0.395	0.454	0.405	0.364	0.344
UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000

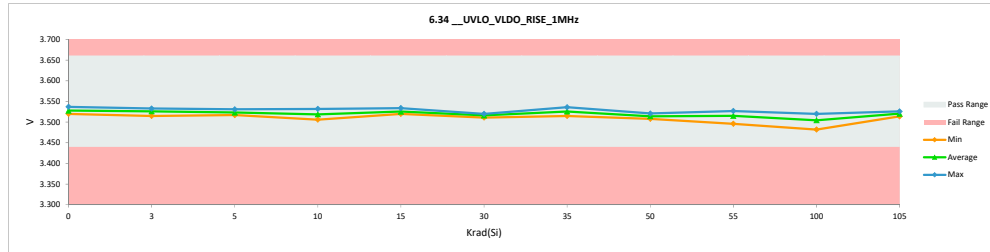


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6.34 UVLO_VLDO_RISE_1MHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.66	3.66		
Min Limit	3.44	3.44		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	3.519	3.520	0.001
0	118	3.527	3.527	0.000
0	122	3.536	3.537	0.001
3	301	3.531	3.532	0.001
3	306	3.533	3.531	-0.002
3	307	3.532	3.533	0.001
3	308	3.516	3.515	-0.001
3	309	3.527	3.520	-0.007
5	310	3.525	3.527	0.002
5	311	3.519	3.517	-0.002
5	312	3.523	3.522	-0.001
5	314	3.522	3.519	-0.003
5	316	3.531	3.531	0.000
10	317	3.524	3.522	-0.002
10	318	3.533	3.532	-0.001
10	319	3.520	3.517	-0.003
10	321	3.527	3.517	-0.010
10	322	3.507	3.506	-0.001
15	323	3.527	3.529	0.002
15	325	3.537	3.534	-0.003
15	326	3.528	3.524	-0.004
15	328	3.523	3.520	-0.003
15	333	3.520	3.521	0.001
30	334	3.525	3.518	-0.007
30	335	3.521	3.511	-0.010
30	337	3.528	3.512	-0.016
30	339	3.526	3.520	-0.006
30	341	3.534	3.520	-0.014
35	342	3.528	3.527	-0.001
35	345	3.538	3.536	-0.002
35	346	3.513	3.515	0.002
35	347	3.519	3.522	0.003
35	348	3.529	3.529	0.000
50	349	3.527	3.512	-0.015
50	350	3.526	3.516	-0.010
50	351	3.527	3.514	-0.013
50	353	3.527	3.521	-0.006
50	354	3.527	3.508	-0.019
55	355	3.497	3.496	-0.001
55	357	3.532	3.527	-0.005
55	359	3.520	3.511	-0.009
55	362	3.526	3.524	-0.002
55	363	3.524	3.518	-0.006
100	1	3.521	3.494	-0.027
100	3	3.529	3.518	-0.011
100	4	3.519	3.496	-0.023
100	5	3.525	3.517	-0.008
100	6	3.519	3.505	-0.014
100	7	3.528	3.507	-0.021
100	8	3.522	3.509	-0.013
100	9	3.526	3.504	-0.022
100	10	3.529	3.509	-0.020
100	11	3.521	3.502	-0.019
100	12	3.533	3.514	-0.019
100	13	3.519	3.502	-0.017
100	14	3.519	3.494	-0.025
100	15	3.517	3.513	-0.004
100	16	3.520	3.504	-0.016
100	17	3.521	3.498	-0.023
100	18	3.521	3.504	-0.017
100	19	3.508	3.482	-0.026
100	20	3.517	3.498	-0.019
100	21	3.538	3.520	-0.018
100	22	3.516	3.503	-0.013
100	24	3.530	3.507	-0.023
105	25	3.535	3.526	-0.009
105	26	3.522	3.516	-0.006
105	27	3.528	3.521	-0.007
105	28	3.522	3.514	-0.008
105	29	3.539	3.526	-0.013
	Max	3.539	3.537	0.003
	Average	3.525	3.516	-0.009
	Min	3.497	3.482	-0.027
	Std Dev	0.007	0.011	0.008

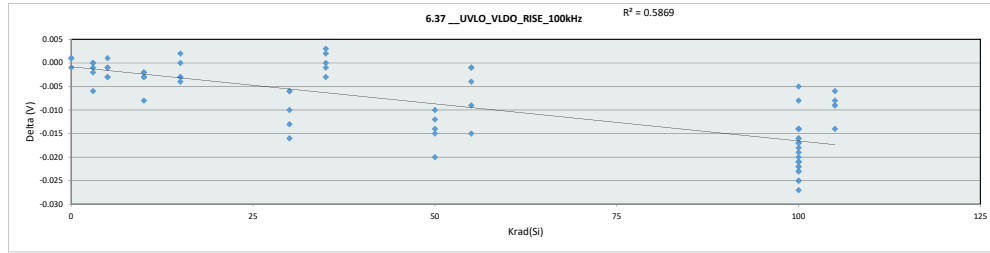


6.34 UVLO_VLDO_RISE_1M											
Test Site											
Tester											
Test Number											
Max Limit	3.66	V									
Min Limit	3.44	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.520	3.515	3.517	3.506	3.520	3.511	3.515	3.508	3.496	3.482	3.514
Average	3.528	3.526	3.523	3.519	3.526	3.516	3.526	3.514	3.515	3.505	3.521
Max	3.537	3.533	3.531	3.532	3.534	3.520	3.536	3.521	3.527	3.520	3.526
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660

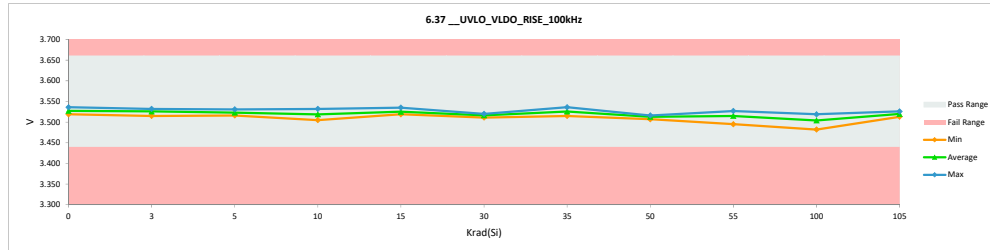


HDR TID Report
TPS7H5001-SP

6.37 UVLO_VLDO_RISE_100kHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.66	3.66		
Min Limit	3.44	3.44		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	3.518	3.519	0.001
0	118	3.526	3.527	0.001
0	122	3.537	3.536	-0.001
3	301	3.531	3.531	0.000
3	306	3.532	3.531	-0.001
3	307	3.532	3.532	0.000
3	308	3.517	3.515	-0.002
3	309	3.527	3.521	-0.006
5	310	3.525	3.526	0.001
5	311	3.519	3.516	-0.003
5	312	3.523	3.522	-0.001
5	314	3.523	3.520	-0.003
5	316	3.532	3.531	-0.001
10	317	3.524	3.522	-0.002
10	318	3.534	3.532	-0.002
10	319	3.520	3.517	-0.003
10	321	3.526	3.518	-0.008
10	322	3.508	3.505	-0.003
15	323	3.528	3.528	0.000
15	325	3.538	3.535	-0.003
15	326	3.527	3.523	-0.004
15	328	3.522	3.519	-0.003
15	333	3.519	3.521	0.002
30	334	3.525	3.519	-0.006
30	335	3.521	3.511	-0.010
30	337	3.528	3.512	-0.016
30	339	3.526	3.520	-0.006
30	341	3.533	3.520	-0.013
35	342	3.528	3.528	0.000
35	345	3.539	3.536	-0.003
35	346	3.513	3.515	0.002
35	347	3.519	3.522	0.003
35	348	3.529	3.528	-0.001
50	349	3.527	3.512	-0.015
50	350	3.526	3.516	-0.010
50	351	3.527	3.515	-0.012
50	353	3.529	3.515	-0.014
50	354	3.527	3.507	-0.020
55	355	3.496	3.495	-0.001
55	357	3.531	3.527	-0.004
55	359	3.520	3.511	-0.009
55	362	3.525	3.524	-0.001
55	363	3.533	3.518	-0.015
100	1	3.521	3.494	-0.027
100	3	3.530	3.516	-0.014
100	4	3.520	3.498	-0.022
100	5	3.524	3.516	-0.008
100	6	3.520	3.504	-0.016
100	7	3.529	3.506	-0.023
100	8	3.522	3.508	-0.014
100	9	3.525	3.504	-0.021
100	10	3.530	3.507	-0.023
100	11	3.521	3.501	-0.020
100	12	3.534	3.513	-0.021
100	13	3.518	3.501	-0.017
100	14	3.519	3.494	-0.025
100	15	3.517	3.512	-0.005
100	16	3.520	3.503	-0.017
100	17	3.521	3.499	-0.022
100	18	3.521	3.505	-0.016
100	19	3.507	3.482	-0.025
100	20	3.516	3.497	-0.019
100	21	3.537	3.519	-0.018
100	22	3.517	3.503	-0.014
100	24	3.529	3.508	-0.021
105	25	3.534	3.526	-0.008
105	26	3.521	3.515	-0.006
105	27	3.528	3.519	-0.009
105	28	3.522	3.513	-0.009
105	29	3.540	3.526	-0.014
Max	3.540	3.536	0.003	
Average	3.525	3.516	-0.009	
Min	3.496	3.482	-0.027	
Std Dev	0.008	0.011	0.008	

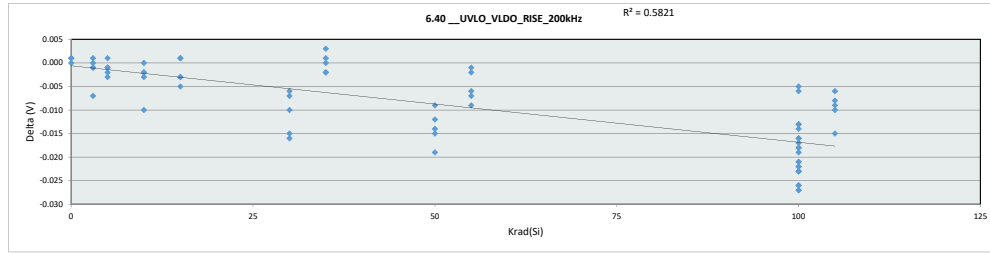


6.37 __UVLO_VLDO_RISE_100											
Test Site											
Tester											
Test Number											
Max Limit	3.66	V									
Min Limit	3.44	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.519	3.515	3.516	3.505	3.519	3.511	3.515	3.507	3.495	3.482	3.513
Average	3.527	3.526	3.523	3.519	3.525	3.516	3.526	3.513	3.515	3.504	3.520
Max	3.536	3.532	3.531	3.532	3.535	3.520	3.536	3.516	3.527	3.519	3.526
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660

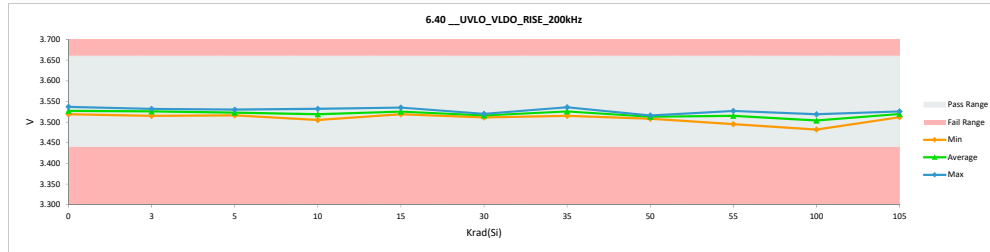


HDR TID Report
TPS7H5001-SP

6.40 UVLO_VLDO_RISE_200kHz				
Test Site				
Tester				
Test Number				
Unit	V		V	
Max Limit	3.66		3.66	
Min Limit	3.44		3.44	
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	3.518	3.519	0.001
0	118	3.526	3.526	0.000
0	122	3.536	3.537	0.001
3	301	3.531	3.531	0.000
3	306	3.531	3.532	0.001
3	307	3.533	3.532	-0.001
3	308	3.516	3.515	-0.001
3	309	3.527	3.520	-0.007
5	310	3.525	3.526	0.001
5	311	3.519	3.516	-0.003
5	312	3.523	3.522	-0.001
5	314	3.522	3.520	-0.002
5	316	3.531	3.530	-0.001
10	317	3.523	3.523	0.000
10	318	3.534	3.532	-0.002
10	319	3.520	3.517	-0.003
10	321	3.527	3.517	-0.010
10	322	3.507	3.505	-0.002
15	323	3.528	3.529	0.001
15	325	3.538	3.535	-0.003
15	326	3.528	3.523	-0.005
15	328	3.522	3.519	-0.003
15	333	3.520	3.521	0.001
30	334	3.524	3.518	-0.006
30	335	3.521	3.511	-0.010
30	337	3.528	3.512	-0.016
30	339	3.527	3.520	-0.007
30	341	3.534	3.519	-0.015
35	342	3.529	3.527	-0.002
35	345	3.538	3.536	-0.002
35	346	3.514	3.515	0.001
35	347	3.519	3.522	0.003
35	348	3.529	3.529	0.000
50	349	3.527	3.513	-0.014
50	350	3.525	3.516	-0.009
50	351	3.527	3.515	-0.012
50	353	3.528	3.513	-0.015
50	354	3.527	3.508	-0.019
55	355	3.496	3.495	-0.001
55	357	3.533	3.527	-0.006
55	359	3.520	3.511	-0.009
55	362	3.526	3.524	-0.002
55	363	3.525	3.518	-0.007
100	1	3.521	3.494	-0.027
100	3	3.530	3.516	-0.014
100	4	3.519	3.497	-0.022
100	5	3.524	3.518	-0.006
100	6	3.520	3.504	-0.016
100	7	3.529	3.502	-0.027
100	8	3.521	3.508	-0.013
100	9	3.527	3.504	-0.023
100	10	3.530	3.508	-0.022
100	11	3.521	3.500	-0.021
100	12	3.534	3.513	-0.021
100	13	3.519	3.501	-0.018
100	14	3.520	3.494	-0.026
100	15	3.517	3.512	-0.005
100	16	3.521	3.504	-0.017
100	17	3.521	3.498	-0.023
100	18	3.521	3.505	-0.016
100	19	3.508	3.482	-0.026
100	20	3.516	3.497	-0.019
100	21	3.537	3.519	-0.018
100	22	3.516	3.503	-0.013
100	24	3.529	3.506	-0.023
105	25	3.535	3.526	-0.009
105	26	3.521	3.515	-0.006
105	27	3.528	3.520	-0.008
105	28	3.522	3.512	-0.010
105	29	3.540	3.525	-0.015
Max		3.540	3.537	0.003
Average		3.525	3.515	-0.009
Min		3.496	3.482	-0.027
Std Dev		0.008	0.012	0.009

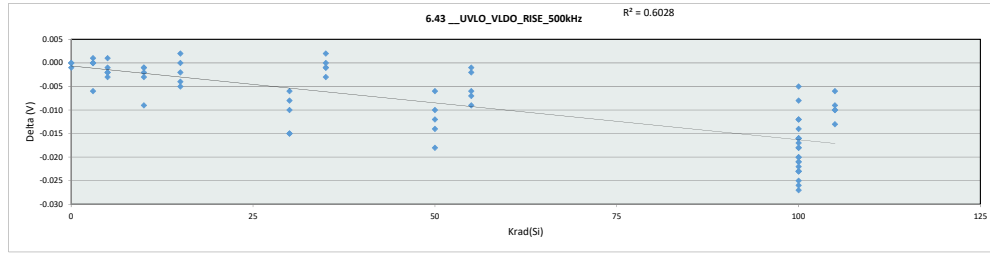


6.40 UVLO_VLDO_RISE_200											
Test Site											
Tester											
Test Number											
Max Limit	3.66		V								
Min Limit	3.44		V								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.519	3.515	3.516	3.505	3.519	3.511	3.515	3.508	3.495	3.482	3.512
Average	3.527	3.526	3.523	3.519	3.525	3.516	3.526	3.513	3.515	3.504	3.520
Max	3.537	3.532	3.530	3.532	3.535	3.520	3.536	3.516	3.527	3.519	3.526
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660

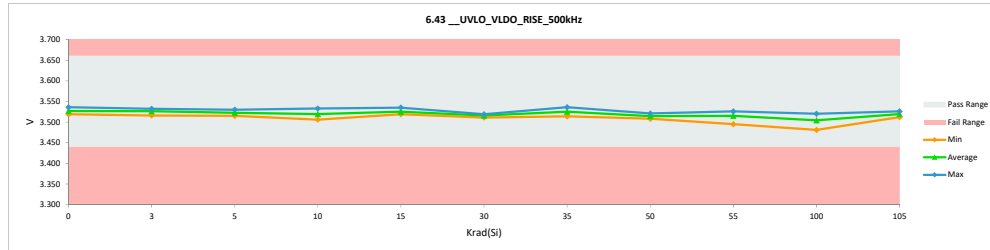


HDR TID Report
TPS7H5001-SP

6.43 UVLO_VLDO_RISE_500kHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.66	3.66		
Min Limit	3.44	3.44		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	3.519	3.519	0.000
0	118	3.527	3.526	-0.001
0	122	3.536	3.536	0.000
3	301	3.531	3.532	0.001
3	306	3.531	3.531	0.000
3	307	3.532	3.532	0.000
3	308	3.516	3.516	0.000
3	309	3.527	3.521	-0.006
5	310	3.525	3.526	0.001
5	311	3.518	3.515	-0.003
5	312	3.523	3.521	-0.002
5	314	3.522	3.520	-0.002
5	316	3.531	3.530	-0.001
10	317	3.524	3.522	-0.002
10	318	3.534	3.533	-0.001
10	319	3.520	3.517	-0.003
10	321	3.527	3.518	-0.009
10	322	3.507	3.506	-0.001
15	323	3.527	3.529	0.002
15	325	3.537	3.535	-0.002
15	326	3.528	3.523	-0.005
15	328	3.523	3.519	-0.004
15	333	3.520	3.520	0.000
30	334	3.524	3.518	-0.006
30	335	3.521	3.511	-0.010
30	337	3.527	3.512	-0.015
30	339	3.527	3.519	-0.008
30	341	3.534	3.519	-0.015
35	342	3.528	3.527	-0.001
35	345	3.539	3.536	-0.003
35	346	3.514	3.514	0.000
35	347	3.519	3.521	0.002
35	348	3.529	3.528	-0.001
50	349	3.526	3.512	-0.014
50	350	3.526	3.516	-0.010
50	351	3.527	3.515	-0.012
50	353	3.527	3.521	-0.006
50	354	3.526	3.508	-0.018
55	355	3.497	3.495	-0.002
55	357	3.532	3.526	-0.006
55	359	3.520	3.511	-0.009
55	362	3.526	3.525	-0.001
55	363	3.525	3.518	-0.007
100	1	3.521	3.494	-0.027
100	3	3.529	3.517	-0.012
100	4	3.520	3.497	-0.023
100	5	3.525	3.517	-0.008
100	6	3.520	3.504	-0.016
100	7	3.528	3.507	-0.021
100	8	3.521	3.509	-0.012
100	9	3.526	3.503	-0.023
100	10	3.529	3.508	-0.021
100	11	3.520	3.500	-0.020
100	12	3.533	3.513	-0.020
100	13	3.519	3.501	-0.018
100	14	3.520	3.495	-0.025
100	15	3.517	3.512	-0.005
100	16	3.520	3.504	-0.016
100	17	3.521	3.498	-0.023
100	18	3.520	3.504	-0.016
100	19	3.507	3.481	-0.026
100	20	3.516	3.498	-0.018
100	21	3.537	3.520	-0.017
100	22	3.517	3.503	-0.014
100	24	3.529	3.507	-0.022
105	25	3.536	3.526	-0.010
105	26	3.520	3.514	-0.006
105	27	3.528	3.519	-0.009
105	28	3.522	3.512	-0.010
105	29	3.539	3.526	-0.013
	Max	3.539	3.536	0.002
	Average	3.525	3.516	-0.009
	Min	3.497	3.481	-0.027
	Std Dev	0.007	0.011	0.008

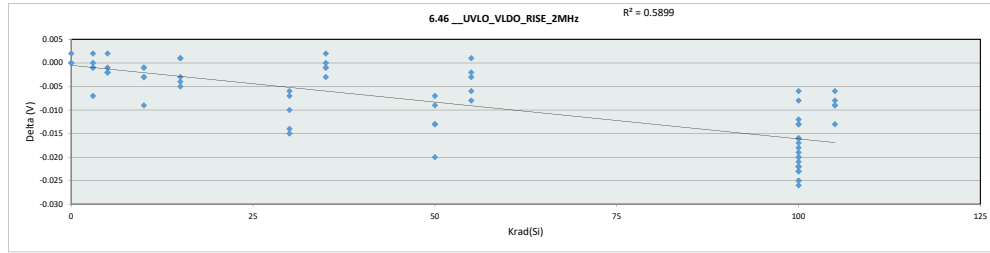


6.43 UVLO_VLDO_RISE_500											
Test Site											
Tester											
Test Number											
Max Limit	3.66	V									
Min Limit	3.44	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.519	3.516	3.515	3.506	3.519	3.511	3.514	3.508	3.495	3.481	3.512
Average	3.527	3.526	3.522	3.519	3.525	3.516	3.525	3.514	3.515	3.504	3.519
Max	3.536	3.532	3.530	3.533	3.535	3.519	3.536	3.521	3.526	3.520	3.526
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660

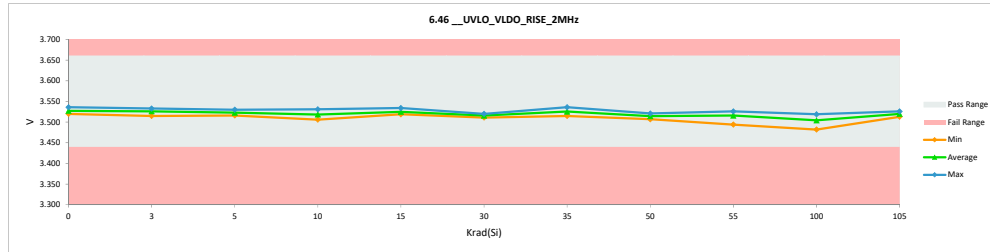


HDR TID Report
TPS7H5001-SP

6.46 UVLO_VLDO_RISE_2MHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.66	3.66		
Min Limit	3.44	3.44		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	3.518	3.520	0.002
0	118	3.526	3.526	0.000
0	122	3.536	3.536	0.000
3	301	3.530	3.532	0.002
3	306	3.532	3.531	-0.001
3	307	3.533	3.533	0.000
3	308	3.516	3.515	-0.001
3	309	3.527	3.520	-0.007
5	310	3.525	3.527	0.002
5	311	3.518	3.516	-0.002
5	312	3.523	3.521	-0.002
5	314	3.522	3.520	-0.002
5	316	3.531	3.530	-0.001
10	317	3.523	3.522	-0.001
10	318	3.534	3.531	-0.003
10	319	3.519	3.516	-0.003
10	321	3.526	3.517	-0.009
10	322	3.507	3.506	-0.001
15	323	3.527	3.528	0.001
15	325	3.537	3.534	-0.003
15	326	3.528	3.523	-0.005
15	328	3.523	3.519	-0.004
15	333	3.520	3.521	0.001
30	334	3.524	3.517	-0.007
30	335	3.521	3.511	-0.010
30	337	3.527	3.512	-0.015
30	339	3.526	3.520	-0.006
30	341	3.533	3.519	-0.014
35	342	3.528	3.527	-0.001
35	345	3.539	3.536	-0.003
35	346	3.515	3.515	0.000
35	347	3.519	3.521	0.002
35	348	3.530	3.529	-0.001
50	349	3.526	3.513	-0.013
50	350	3.526	3.517	-0.009
50	351	3.527	3.514	-0.013
50	353	3.528	3.521	-0.007
50	354	3.527	3.507	-0.020
55	355	3.496	3.494	-0.002
55	357	3.532	3.526	-0.006
55	359	3.519	3.511	-0.008
55	362	3.526	3.523	-0.003
55	363	3.524	3.525	0.001
100	1	3.520	3.494	-0.026
100	3	3.529	3.516	-0.013
100	4	3.520	3.497	-0.023
100	5	3.524	3.516	-0.008
100	6	3.520	3.504	-0.016
100	7	3.528	3.506	-0.022
100	8	3.522	3.510	-0.012
100	9	3.525	3.503	-0.022
100	10	3.529	3.508	-0.021
100	11	3.521	3.501	-0.020
100	12	3.533	3.513	-0.020
100	13	3.518	3.502	-0.016
100	14	3.519	3.494	-0.025
100	15	3.518	3.512	-0.006
100	16	3.520	3.503	-0.017
100	17	3.521	3.498	-0.023
100	18	3.521	3.505	-0.016
100	19	3.507	3.482	-0.025
100	20	3.516	3.498	-0.018
100	21	3.538	3.519	-0.019
100	22	3.516	3.503	-0.013
100	24	3.530	3.508	-0.022
105	25	3.535	3.526	-0.009
105	26	3.521	3.515	-0.006
105	27	3.528	3.520	-0.008
105	28	3.522	3.513	-0.009
105	29	3.539	3.526	-0.013
	Max	3.539	3.536	0.002
	Average	3.524	3.516	-0.009
	Min	3.496	3.482	-0.026
	Std Dev	0.008	0.011	0.008

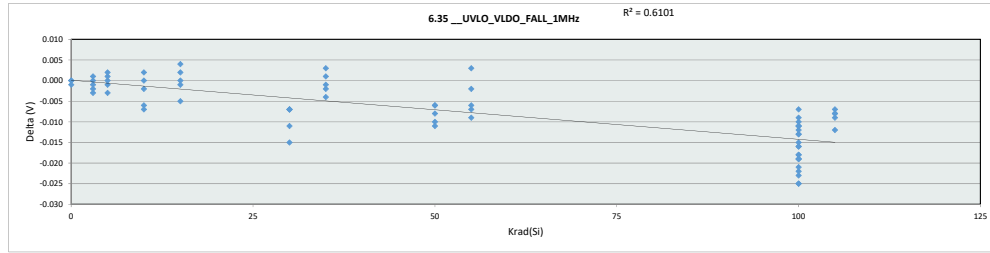


6.46 UVLO_VLDO_RISE_2M											
Test Site											
Tester											
Test Number											
Max Limit	3.66	V									
Min Limit	3.44	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.520	3.515	3.516	3.506	3.519	3.511	3.515	3.507	3.494	3.482	3.513
Average	3.527	3.526	3.523	3.518	3.525	3.516	3.526	3.514	3.516	3.504	3.520
Max	3.536	3.533	3.530	3.531	3.534	3.520	3.536	3.521	3.526	3.519	3.526
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660

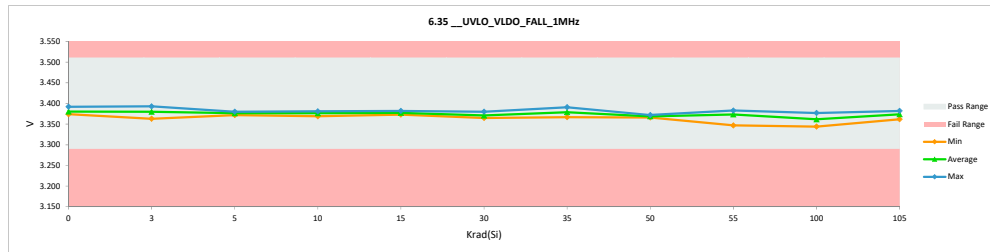


HDR TID Report
TPS7H5001-SP

6.35 UVLO_VLDO_FALL_1MHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.51	3.51		
Min Limit	3.29	3.29		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	3.374	3.374	0.000
0	118	3.375	3.375	0.000
0	122	3.393	3.392	-0.001
3	301	3.382	3.382	0.000
3	306	3.386	3.384	-0.002
3	307	3.396	3.393	-0.003
3	308	3.364	3.363	-0.001
3	309	3.378	3.379	0.001
5	310	3.379	3.379	0.000
5	311	3.370	3.372	0.002
5	312	3.381	3.378	-0.003
5	314	3.374	3.373	-0.001
5	316	3.379	3.380	0.001
10	317	3.377	3.379	0.002
10	318	3.388	3.381	-0.007
10	319	3.382	3.376	-0.006
10	321	3.377	3.377	0.000
10	322	3.371	3.369	-0.002
15	323	3.375	3.377	0.002
15	325	3.387	3.382	-0.005
15	326	3.378	3.378	0.000
15	328	3.374	3.373	-0.001
15	333	3.373	3.377	0.004
30	334	3.377	3.370	-0.007
30	335	3.376	3.365	-0.011
30	337	3.387	3.380	-0.007
30	339	3.375	3.368	-0.007
30	341	3.387	3.372	-0.015
35	342	3.380	3.376	-0.004
35	345	3.390	3.391	0.001
35	346	3.369	3.367	-0.002
35	347	3.368	3.371	0.003
35	348	3.391	3.390	-0.001
50	349	3.378	3.367	-0.011
50	350	3.374	3.368	-0.006
50	351	3.378	3.372	-0.006
50	353	3.379	3.371	-0.008
50	354	3.376	3.366	-0.010
55	355	3.353	3.347	-0.006
55	357	3.385	3.383	-0.002
55	359	3.376	3.379	0.003
55	362	3.391	3.382	-0.009
55	363	3.383	3.376	-0.007
100	1	3.369	3.344	-0.025
100	3	3.387	3.366	-0.021
100	4	3.378	3.366	-0.012
100	5	3.374	3.367	-0.007
100	6	3.371	3.358	-0.013
100	7	3.377	3.358	-0.019
100	8	3.376	3.360	-0.016
100	9	3.375	3.364	-0.011
100	10	3.384	3.369	-0.015
100	11	3.375	3.357	-0.018
100	12	3.384	3.366	-0.018
100	13	3.375	3.356	-0.019
100	14	3.370	3.360	-0.010
100	15	3.386	3.377	-0.009
100	16	3.383	3.360	-0.023
100	17	3.386	3.367	-0.019
100	18	3.373	3.360	-0.013
100	19	3.365	3.354	-0.011
100	20	3.375	3.350	-0.025
100	21	3.391	3.369	-0.022
100	22	3.378	3.367	-0.011
100	24	3.378	3.362	-0.016
105	25	3.387	3.379	-0.008
105	26	3.378	3.370	-0.008
105	27	3.389	3.382	-0.007
105	28	3.371	3.362	-0.009
105	29	3.389	3.377	-0.012
	Max	3.396	3.393	0.004
	Average	3.379	3.371	-0.008
	Min	3.353	3.344	-0.025
	Std Dev	0.008	0.010	0.008

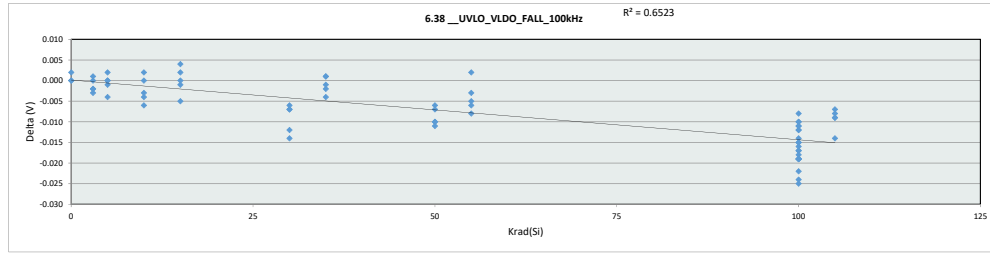


6.35 UVLO_VLDO_FALL_1M											
Test Site											
Tester											
Test Number											
Max Limit	3.51	V									
Min Limit	3.29	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.374	3.363	3.372	3.369	3.373	3.371	3.379	3.366	3.347	3.344	3.362
Average	3.380	3.380	3.376	3.376	3.377	3.371	3.379	3.369	3.373	3.362	3.374
Max	3.392	3.393	3.380	3.381	3.382	3.380	3.391	3.372	3.383	3.377	3.382
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510

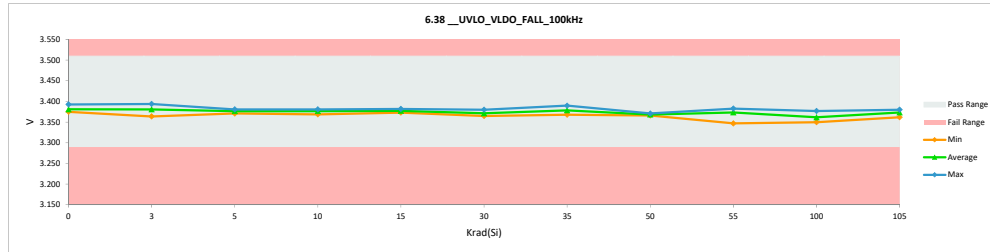


HDR TID Report
TPS7H5001-SP

6.38 UVLO_VLDO_FALL_100kHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.51	3.51		
Min Limit	3.29	3.29		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	3.373	3.375	0.002
0	118	3.375	3.375	0.000
0	122	3.393	3.393	0.000
3	301	3.383	3.383	0.000
3	306	3.387	3.384	-0.003
3	307	3.396	3.394	-0.002
3	308	3.366	3.364	-0.002
3	309	3.378	3.379	0.001
5	310	3.379	3.381	0.002
5	311	3.371	3.371	0.000
5	312	3.382	3.378	-0.004
5	314	3.374	3.373	-0.001
5	316	3.380	3.380	0.000
10	317	3.377	3.379	0.002
10	318	3.387	3.381	-0.006
10	319	3.381	3.377	-0.004
10	321	3.377	3.377	0.000
10	322	3.372	3.369	-0.003
15	323	3.375	3.377	0.002
15	325	3.387	3.382	-0.005
15	326	3.378	3.377	-0.001
15	328	3.373	3.373	0.000
15	333	3.374	3.378	0.004
30	334	3.378	3.372	-0.006
30	335	3.377	3.365	-0.012
30	337	3.387	3.380	-0.007
30	339	3.375	3.368	-0.007
30	341	3.386	3.372	-0.014
35	342	3.380	3.376	-0.004
35	345	3.389	3.390	0.001
35	346	3.369	3.368	-0.001
35	347	3.369	3.370	0.001
35	348	3.391	3.389	-0.002
50	349	3.378	3.368	-0.010
50	350	3.375	3.368	-0.007
50	351	3.377	3.371	-0.006
50	353	3.380	3.370	-0.010
50	354	3.377	3.366	-0.011
55	355	3.353	3.347	-0.006
55	357	3.386	3.383	-0.003
55	359	3.377	3.379	0.002
55	362	3.390	3.382	-0.008
55	363	3.382	3.377	-0.005
100	1	3.369	3.350	-0.019
100	3	3.386	3.367	-0.019
100	4	3.379	3.365	-0.014
100	5	3.375	3.367	-0.008
100	6	3.371	3.359	-0.012
100	7	3.377	3.358	-0.019
100	8	3.375	3.359	-0.016
100	9	3.375	3.364	-0.011
100	10	3.383	3.368	-0.015
100	11	3.375	3.356	-0.019
100	12	3.383	3.366	-0.017
100	13	3.375	3.356	-0.019
100	14	3.369	3.359	-0.010
100	15	3.387	3.377	-0.010
100	16	3.383	3.359	-0.024
100	17	3.386	3.368	-0.018
100	18	3.373	3.358	-0.015
100	19	3.365	3.354	-0.011
100	20	3.375	3.350	-0.025
100	21	3.391	3.369	-0.022
100	22	3.378	3.366	-0.012
100	24	3.379	3.362	-0.017
105	25	3.385	3.378	-0.007
105	26	3.379	3.371	-0.008
105	27	3.389	3.380	-0.009
105	28	3.371	3.362	-0.009
105	29	3.390	3.376	-0.014
	Max	3.396	3.394	0.004
	Average	3.379	3.371	-0.008
	Min	3.353	3.347	-0.025
	Std Dev	0.008	0.010	0.007

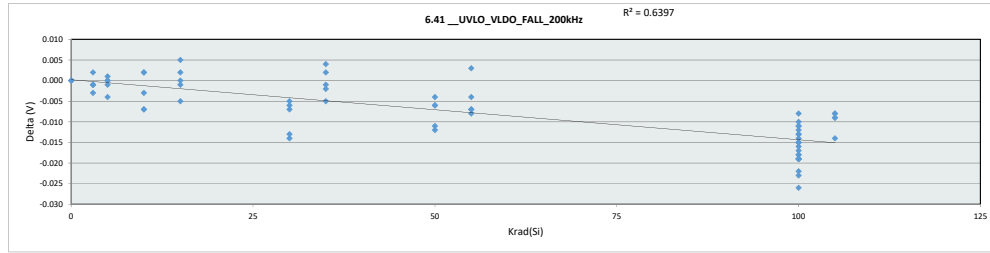


6.38 __UVLO_VLDO_FALL_100											
Test Site											
Tester											
Test Number											
Max Limit	3.51	V									
Min Limit	3.29	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.375	3.364	3.371	3.369	3.373	3.365	3.368	3.366	3.347	3.350	3.362
Average	3.381	3.381	3.377	3.377	3.377	3.371	3.379	3.369	3.374	3.362	3.373
Max	3.393	3.394	3.381	3.381	3.382	3.380	3.390	3.371	3.383	3.377	3.380
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510

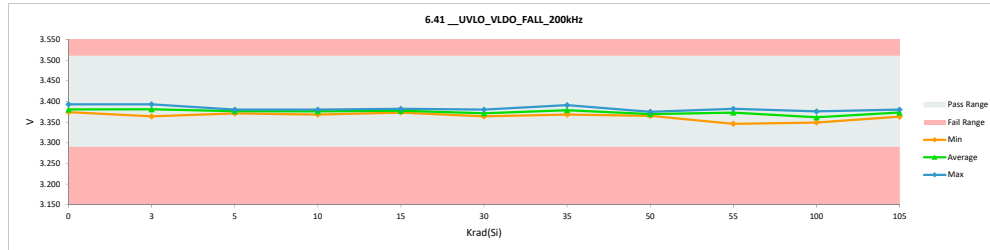


HDR TID Report
TPS7H5001-SP

6.41 UVLO_VLDO_FALL_200kHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.51	3.51		
Min Limit	3.29	3.29		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	3.374	3.374	0.000
0	118	3.375	3.375	0.000
0	122	3.393	3.393	0.000
3	301	3.384	3.383	-0.001
3	306	3.386	3.385	-0.001
3	307	3.396	3.393	-0.003
3	308	3.365	3.364	-0.001
3	309	3.378	3.380	0.002
5	310	3.379	3.380	0.001
5	311	3.371	3.371	0.000
5	312	3.382	3.378	-0.004
5	314	3.374	3.373	-0.001
5	316	3.380	3.380	0.000
10	317	3.377	3.379	0.002
10	318	3.387	3.380	-0.007
10	319	3.382	3.375	-0.007
10	321	3.376	3.378	0.002
10	322	3.371	3.368	-0.003
15	323	3.375	3.377	0.002
15	325	3.387	3.382	-0.005
15	326	3.378	3.378	0.000
15	328	3.374	3.373	-0.001
15	333	3.373	3.378	0.005
30	334	3.377	3.371	-0.006
30	335	3.377	3.364	-0.013
30	337	3.387	3.380	-0.007
30	339	3.374	3.369	-0.005
30	341	3.386	3.372	-0.014
35	342	3.380	3.375	-0.005
35	345	3.389	3.391	0.002
35	346	3.369	3.368	-0.001
35	347	3.367	3.371	0.004
35	348	3.391	3.389	-0.002
50	349	3.378	3.367	-0.011
50	350	3.374	3.368	-0.006
50	351	3.377	3.371	-0.006
50	353	3.379	3.375	-0.004
50	354	3.377	3.365	-0.012
55	355	3.353	3.346	-0.007
55	357	3.386	3.382	-0.004
55	359	3.376	3.379	0.003
55	362	3.390	3.382	-0.008
55	363	3.383	3.376	-0.007
100	1	3.369	3.350	-0.019
100	3	3.386	3.367	-0.019
100	4	3.379	3.365	-0.014
100	5	3.374	3.366	-0.008
100	6	3.370	3.358	-0.012
100	7	3.377	3.358	-0.019
100	8	3.376	3.360	-0.016
100	9	3.376	3.363	-0.013
100	10	3.383	3.368	-0.015
100	11	3.375	3.357	-0.018
100	12	3.382	3.365	-0.017
100	13	3.375	3.357	-0.018
100	14	3.370	3.360	-0.010
100	15	3.387	3.376	-0.011
100	16	3.383	3.360	-0.023
100	17	3.386	3.367	-0.019
100	18	3.373	3.358	-0.015
100	19	3.365	3.354	-0.011
100	20	3.375	3.349	-0.026
100	21	3.391	3.369	-0.022
100	22	3.378	3.365	-0.013
100	24	3.378	3.363	-0.015
105	25	3.386	3.378	-0.008
105	26	3.379	3.370	-0.009
105	27	3.389	3.380	-0.009
105	28	3.371	3.363	-0.008
105	29	3.389	3.375	-0.014
	Max	3.396	3.393	0.005
	Average	3.379	3.371	-0.008
	Min	3.353	3.346	-0.026
	Std Dev	0.008	0.010	0.007

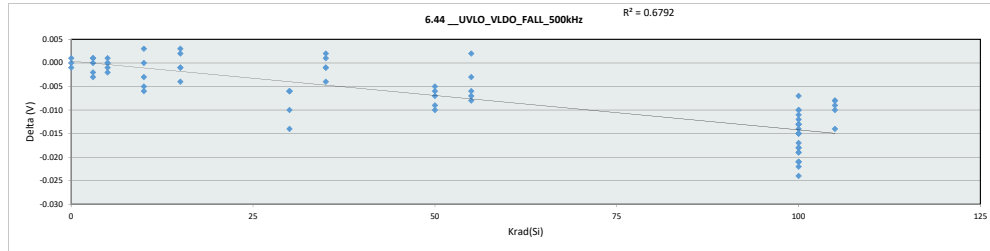


6.41 UVLO_VLDO_FALL_200											
Test Site											
Tester											
Test Number											
Max Limit	3.51	V									
Min Limit	3.29	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.374	3.364	3.371	3.368	3.373	3.364	3.368	3.365	3.346	3.349	3.363
Average	3.381	3.381	3.376	3.376	3.378	3.371	3.379	3.369	3.373	3.362	3.373
Max	3.393	3.393	3.380	3.380	3.382	3.380	3.391	3.375	3.382	3.376	3.380
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510

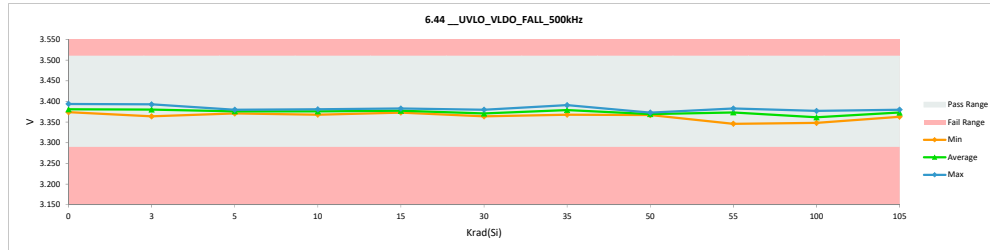


HDR TID Report
TPS7H5001-SP

6.44 UVLO_VLDO_FALL_500kHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.51	3.51		
Min Limit	3.29	3.29		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	3.374	3.374	0.000
0	118	3.376	3.375	-0.001
0	122	3.393	3.394	0.001
3	301	3.382	3.382	0.000
3	306	3.386	3.384	-0.002
3	307	3.396	3.393	-0.003
3	308	3.363	3.364	0.001
3	309	3.378	3.379	0.001
5	310	3.379	3.380	0.001
5	311	3.371	3.371	0.000
5	312	3.378	3.378	0.000
5	314	3.374	3.373	-0.001
5	316	3.381	3.379	-0.002
10	317	3.376	3.379	0.003
10	318	3.387	3.381	-0.006
10	319	3.381	3.376	-0.005
10	321	3.376	3.376	0.000
10	322	3.371	3.368	-0.003
15	323	3.375	3.377	0.002
15	325	3.387	3.383	-0.004
15	326	3.378	3.377	-0.001
15	328	3.374	3.373	-0.001
15	333	3.374	3.377	0.003
30	334	3.377	3.371	-0.006
30	335	3.378	3.364	-0.014
30	337	3.386	3.380	-0.006
30	339	3.374	3.368	-0.006
30	341	3.382	3.372	-0.010
35	342	3.380	3.376	-0.004
35	345	3.390	3.391	0.001
35	346	3.369	3.368	-0.001
35	347	3.369	3.371	0.002
35	348	3.391	3.390	-0.001
50	349	3.377	3.367	-0.010
50	350	3.373	3.368	-0.005
50	351	3.378	3.372	-0.006
50	353	3.380	3.373	-0.007
50	354	3.376	3.367	-0.009
55	355	3.353	3.346	-0.007
55	357	3.386	3.383	-0.003
55	359	3.377	3.379	0.002
55	362	3.390	3.382	-0.008
55	363	3.383	3.377	-0.006
100	1	3.369	3.348	-0.021
100	3	3.386	3.367	-0.019
100	4	3.379	3.366	-0.013
100	5	3.374	3.367	-0.007
100	6	3.371	3.358	-0.013
100	7	3.378	3.357	-0.021
100	8	3.376	3.359	-0.017
100	9	3.375	3.364	-0.011
100	10	3.383	3.368	-0.015
100	11	3.375	3.357	-0.018
100	12	3.382	3.367	-0.015
100	13	3.374	3.356	-0.018
100	14	3.369	3.359	-0.010
100	15	3.387	3.377	-0.010
100	16	3.381	3.360	-0.021
100	17	3.386	3.367	-0.019
100	18	3.373	3.359	-0.014
100	19	3.366	3.354	-0.012
100	20	3.374	3.350	-0.024
100	21	3.391	3.369	-0.022
100	22	3.379	3.366	-0.013
100	24	3.378	3.363	-0.015
105	25	3.386	3.378	-0.008
105	26	3.380	3.370	-0.010
105	27	3.389	3.380	-0.009
105	28	3.371	3.363	-0.008
105	29	3.389	3.375	-0.014
	Max	3.396	3.394	0.003
	Average	3.379	3.371	-0.007
	Min	3.353	3.346	-0.024
	Std Dev	0.008	0.010	0.007

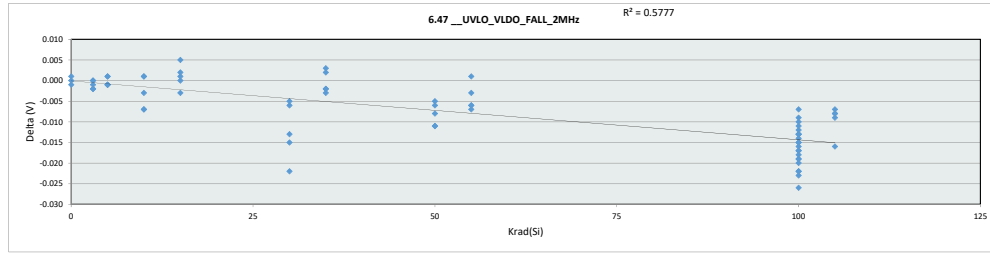


6.44 UVLO_VLDO_FALL_500											
Test Site											
Tester											
Test Number											
Max Limit	3.51	V									
Min Limit	3.29	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.374	3.364	3.371	3.368	3.373	3.364	3.368	3.367	3.346	3.348	3.363
Average	3.381	3.380	3.376	3.376	3.377	3.371	3.379	3.369	3.373	3.362	3.373
Max	3.394	3.393	3.380	3.381	3.383	3.380	3.391	3.373	3.383	3.377	3.380
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510

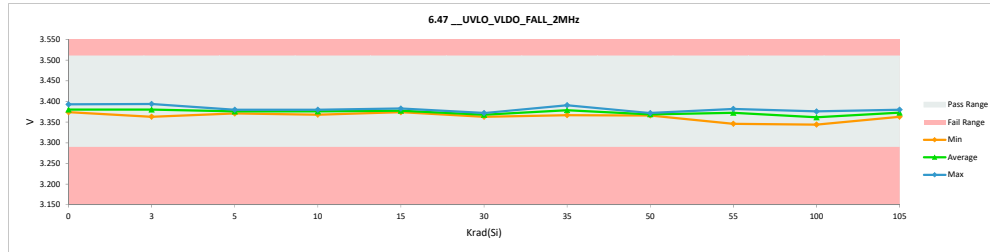


HDR TID Report
TPS7H5001-SP

6.47 UVLO_VLDO_FALL_2MHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.51	3.51		
Min Limit	3.29	3.29		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	3.373	3.374	0.001
0	118	3.375	3.374	-0.001
0	122	3.393	3.393	0.000
3	301	3.383	3.383	0.000
3	306	3.386	3.384	-0.002
3	307	3.395	3.394	-0.001
3	308	3.365	3.363	-0.002
3	309	3.378	3.378	0.000
5	310	3.379	3.380	0.001
5	311	3.370	3.371	0.001
5	312	3.378	3.377	-0.001
5	314	3.374	3.373	-0.001
5	316	3.380	3.379	-0.001
10	317	3.378	3.379	0.001
10	318	3.387	3.380	-0.007
10	319	3.382	3.375	-0.007
10	321	3.377	3.378	0.001
10	322	3.371	3.368	-0.003
15	323	3.375	3.377	0.002
15	325	3.386	3.383	-0.003
15	326	3.377	3.377	0.000
15	328	3.373	3.374	0.001
15	333	3.373	3.378	0.005
30	334	3.377	3.371	-0.006
30	335	3.377	3.364	-0.013
30	337	3.385	3.363	-0.022
30	339	3.374	3.369	-0.005
30	341	3.387	3.372	-0.015
35	342	3.379	3.376	-0.003
35	345	3.389	3.391	0.002
35	346	3.369	3.367	-0.002
35	347	3.368	3.371	0.003
35	348	3.391	3.389	-0.002
50	349	3.378	3.367	-0.011
50	350	3.374	3.368	-0.006
50	351	3.377	3.372	-0.005
50	353	3.379	3.371	-0.008
50	354	3.377	3.366	-0.011
55	355	3.352	3.346	-0.006
55	357	3.385	3.382	-0.003
55	359	3.377	3.378	0.001
55	362	3.389	3.382	-0.007
55	363	3.381	3.375	-0.006
100	1	3.370	3.344	-0.026
100	3	3.386	3.367	-0.019
100	4	3.378	3.365	-0.013
100	5	3.374	3.367	-0.007
100	6	3.371	3.358	-0.013
100	7	3.378	3.358	-0.020
100	8	3.375	3.359	-0.016
100	9	3.375	3.364	-0.011
100	10	3.383	3.368	-0.015
100	11	3.375	3.358	-0.017
100	12	3.382	3.367	-0.015
100	13	3.375	3.356	-0.019
100	14	3.369	3.360	-0.009
100	15	3.386	3.376	-0.010
100	16	3.382	3.360	-0.022
100	17	3.386	3.368	-0.018
100	18	3.372	3.358	-0.014
100	19	3.365	3.353	-0.012
100	20	3.374	3.351	-0.023
100	21	3.391	3.369	-0.022
100	22	3.379	3.366	-0.013
100	24	3.379	3.362	-0.017
105	25	3.385	3.378	-0.007
105	26	3.379	3.370	-0.009
105	27	3.388	3.380	-0.008
105	28	3.371	3.363	-0.008
105	29	3.390	3.374	-0.016
	Max	3.395	3.394	0.005
	Average	3.378	3.371	-0.008
	Min	3.352	3.344	-0.026
	Std Dev	0.007	0.010	0.008

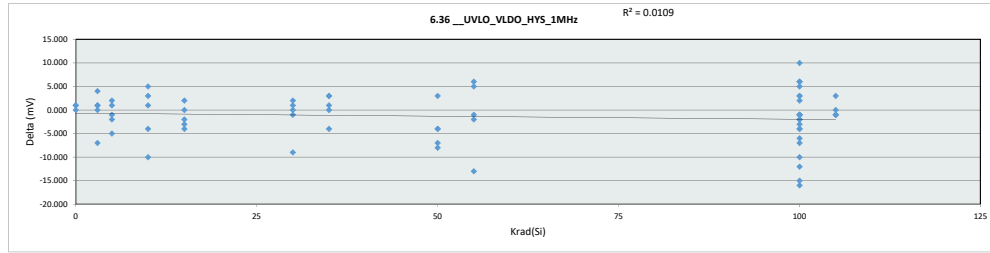


6.47 UVLO_VLDO_FALL_2MHz											
Test Site											
Tester											
Test Number											
Max Limit	3.51	V									
Min Limit	3.29	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.374	3.363	3.371	3.368	3.374	3.363	3.367	3.366	3.346	3.344	3.363
Average	3.380	3.380	3.376	3.376	3.378	3.368	3.379	3.369	3.373	3.362	3.373
Max	3.393	3.394	3.380	3.380	3.383	3.372	3.391	3.372	3.382	3.376	3.380
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510

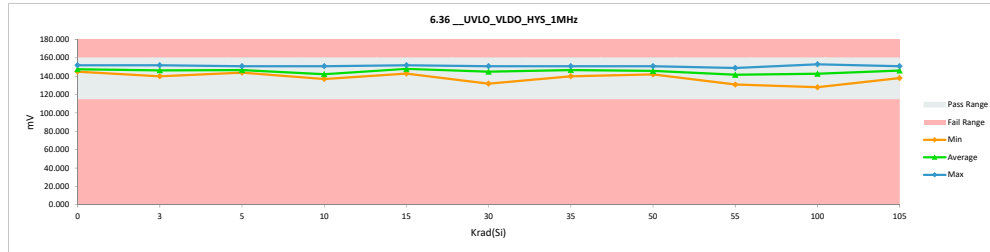


HDR TID Report
TPS7H5001-SP

6.36 UVLO_VLDO_HYS_1MHz				
Test Site				
Tester				
Test Number				
Unit	mV		mV	
Max Limit	160		160	
Min Limit	115		115	
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	145.000	146.000	1.000
0	118	152.000	152.000	0.000
0	122	144.000	145.000	1.000
3	301	149.000	150.000	1.000
3	306	147.000	148.000	1.000
3	307	136.000	140.000	4.000
3	308	152.000	152.000	0.000
3	309	149.000	142.000	-7.000
5	310	147.000	148.000	1.000
5	311	150.000	145.000	-5.000
5	312	142.000	144.000	2.000
5	314	148.000	146.000	-2.000
5	316	152.000	151.000	-1.000
10	317	147.000	143.000	-4.000
10	318	146.000	151.000	5.000
10	319	138.000	141.000	3.000
10	321	149.000	139.000	-10.000
10	322	136.000	137.000	1.000
15	323	152.000	152.000	0.000
15	325	150.000	152.000	2.000
15	326	149.000	146.000	-3.000
15	328	149.000	147.000	-2.000
15	333	147.000	143.000	-4.000
30	334	147.000	148.000	1.000
30	335	145.000	147.000	2.000
30	337	141.000	132.000	-9.000
30	339	151.000	151.000	0.000
30	341	148.000	147.000	-1.000
35	342	148.000	151.000	3.000
35	345	148.000	144.000	-4.000
35	346	145.000	148.000	3.000
35	347	151.000	151.000	0.000
35	348	139.000	140.000	1.000
50	349	149.000	145.000	-4.000
50	350	152.000	148.000	-4.000
50	351	149.000	142.000	-7.000
50	353	148.000	151.000	3.000
50	354	151.000	143.000	-8.000
55	355	144.000	149.000	5.000
55	357	147.000	145.000	-2.000
55	359	144.000	131.000	-13.000
55	362	136.000	142.000	6.000
55	363	142.000	141.000	-1.000
100	1	153.000	150.000	-3.000
100	3	143.000	153.000	10.000
100	4	141.000	131.000	-10.000
100	5	151.000	150.000	-1.000
100	6	148.000	147.000	-1.000
100	7	151.000	149.000	-2.000
100	8	146.000	149.000	3.000
100	9	151.000	139.000	-12.000
100	10	146.000	140.000	-6.000
100	11	146.000	145.000	-1.000
100	12	149.000	148.000	-1.000
100	13	144.000	146.000	2.000
100	14	150.000	134.000	-16.000
100	15	131.000	136.000	5.000
100	16	137.000	143.000	6.000
100	17	135.000	131.000	-4.000
100	18	148.000	144.000	-4.000
100	19	143.000	128.000	-15.000
100	20	142.000	148.000	6.000
100	21	147.000	150.000	3.000
100	22	138.000	136.000	-2.000
100	24	152.000	145.000	-7.000
105	25	148.000	147.000	-1.000
105	26	143.000	146.000	3.000
105	27	139.000	138.000	-1.000
105	28	151.000	151.000	0.000
105	29	150.000	149.000	-1.000
	Max	153.000	153.000	10.000
	Average	146.057	144.700	-1.357
	Min	131.000	128.000	-16.000
	Std Dev	4.981	5.928	5.056

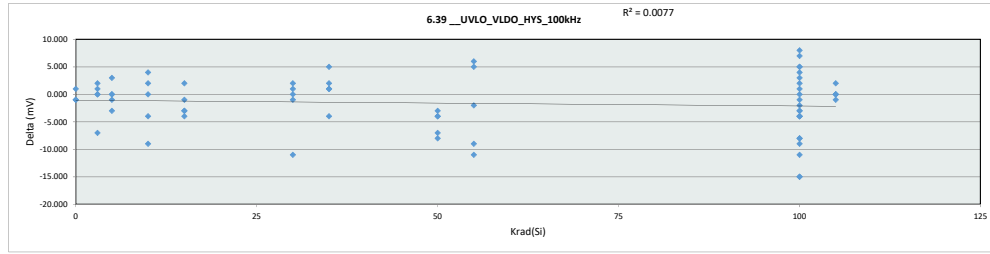


6.36 UVLO_VLDO_HYS_1MHz											
Test Site											
Tester											
Test Number											
Max Limit	160		mV								
Min Limit	115		mV								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min	145.000	140.000	144.000	137.000	143.000	132.000	140.000	142.000	131.000	128.000	138.000
Average	147.667	146.400	146.800	142.200	148.000	145.000	146.800	145.800	141.600	142.818	146.200
Max	152.000	152.000	151.000	151.000	152.000	151.000	151.000	151.000	149.000	153.000	151.000
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000

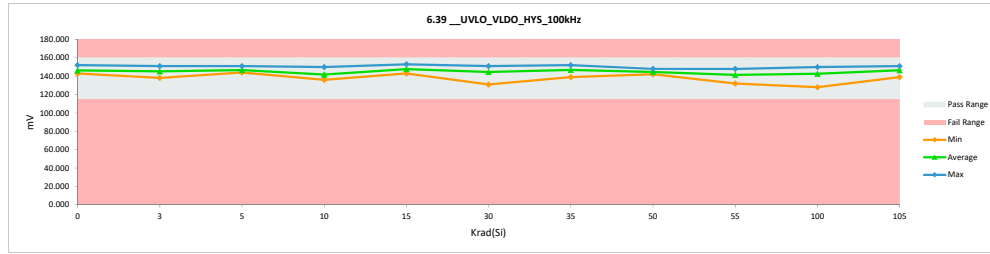


HDR TID Report
TPS7H5001-SP

6.39 UVLO_VLDO_HYS_100kHz				
Test Site				
Tester				
Test Number				
Unit	mV		mV	
Max Limit	160		160	
Min Limit	115		115	
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	145.000	144.000	-1.000
0	118	151.000	152.000	1.000
0	122	144.000	143.000	-1.000
3	301	148.000	148.000	0.000
3	306	146.000	147.000	1.000
3	307	136.000	138.000	2.000
3	308	151.000	151.000	0.000
3	309	149.000	142.000	-7.000
5	310	145.000	145.000	0.000
5	311	148.000	145.000	-3.000
5	312	141.000	144.000	3.000
5	314	149.000	148.000	-1.000
5	316	151.000	151.000	0.000
10	317	147.000	143.000	-4.000
10	318	146.000	150.000	4.000
10	319	138.000	140.000	2.000
10	321	149.000	140.000	-9.000
10	322	136.000	136.000	0.000
15	323	152.000	151.000	-1.000
15	325	151.000	153.000	2.000
15	326	150.000	146.000	-4.000
15	328	149.000	146.000	-3.000
15	333	146.000	143.000	-3.000
30	334	148.000	147.000	-1.000
30	335	144.000	146.000	2.000
30	337	142.000	131.000	-11.000
30	339	151.000	151.000	0.000
30	341	147.000	148.000	1.000
35	342	147.000	152.000	5.000
35	345	150.000	146.000	-4.000
35	346	145.000	146.000	1.000
35	347	150.000	151.000	1.000
35	348	137.000	139.000	2.000
50	349	149.000	145.000	-4.000
50	350	151.000	148.000	-3.000
50	351	150.000	143.000	-7.000
50	353	149.000	145.000	-4.000
50	354	150.000	142.000	-8.000
55	355	143.000	148.000	5.000
55	357	146.000	144.000	-2.000
55	359	143.000	132.000	-11.000
55	362	136.000	142.000	6.000
55	363	150.000	141.000	-9.000
100	1	152.000	144.000	-8.000
100	3	144.000	149.000	5.000
100	4	141.000	132.000	-9.000
100	5	149.000	150.000	1.000
100	6	149.000	146.000	-3.000
100	7	152.000	148.000	-4.000
100	8	147.000	149.000	2.000
100	9	150.000	139.000	-11.000
100	10	147.000	139.000	-8.000
100	11	146.000	144.000	-2.000
100	12	151.000	148.000	-3.000
100	13	142.000	146.000	4.000
100	14	150.000	135.000	-15.000
100	15	130.000	135.000	5.000
100	16	137.000	144.000	7.000
100	17	135.000	131.000	-4.000
100	18	148.000	147.000	-1.000
100	19	143.000	128.000	-15.000
100	20	140.000	148.000	8.000
100	21	147.000	150.000	3.000
100	22	138.000	138.000	0.000
100	24	150.000	146.000	-4.000
105	25	149.000	148.000	-1.000
105	26	142.000	144.000	2.000
105	27	139.000	139.000	0.000
105	28	151.000	151.000	0.000
105	29	150.000	150.000	0.000
Max		152.000	153.000	8.000
Average		145.929	144.300	-1.629
Min		130.000	128.000	-15.000
Std Dev		5.054	5.691	4.950

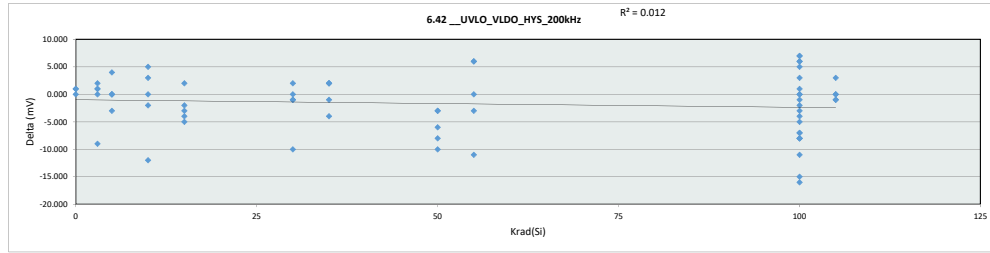


6.39 UVLO_VLDO_HYS_100											
Test Site											
Tester											
Test Number											
Max Limit	160										
Min Limit	115										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min	143.000	138.000	144.000	136.000	131.000	139.000	142.000	132.000	128.000	128.000	139.000
Average	146.333	145.200	146.600	141.800	147.800	144.600	146.800	144.600	141.400	142.545	146.400
Max	152.000	151.000	151.000	150.000	153.000	151.000	152.000	148.000	148.000	150.000	151.000
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000

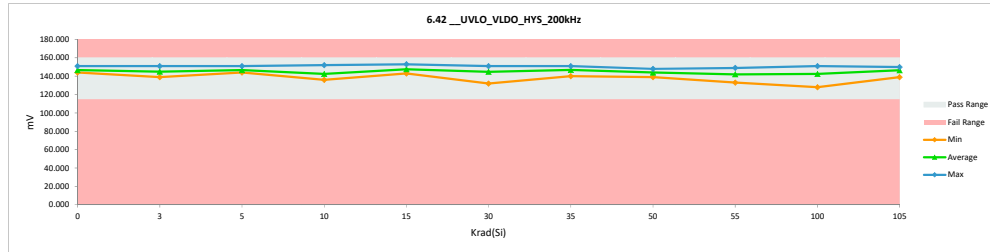


HDR TID Report
TPS7H5001-SP

6.42 UVLO_VLDO_HYS_200kHz				
Test Site				
Tester				
Test Number				
Unit	mV		mV	
Max Limit	160		160	
Min Limit	115		115	
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	144.000	145.000	1.000
0	118	151.000	151.000	0.000
0	122	143.000	144.000	1.000
3	301	147.000	148.000	1.000
3	306	146.000	147.000	1.000
3	307	137.000	139.000	2.000
3	308	151.000	151.000	0.000
3	309	149.000	140.000	-9.000
5	310	146.000	146.000	0.000
5	311	148.000	145.000	-3.000
5	312	140.000	144.000	4.000
5	314	147.000	147.000	0.000
5	316	151.000	151.000	0.000
10	317	146.000	144.000	-2.000
10	318	147.000	152.000	5.000
10	319	138.000	141.000	3.000
10	321	151.000	139.000	-12.000
10	322	136.000	136.000	0.000
15	323	153.000	151.000	-2.000
15	325	151.000	153.000	2.000
15	326	150.000	145.000	-5.000
15	328	149.000	146.000	-3.000
15	333	147.000	143.000	-4.000
30	334	147.000	147.000	0.000
30	335	145.000	147.000	2.000
30	337	142.000	132.000	-10.000
30	339	152.000	151.000	-1.000
30	341	148.000	147.000	-1.000
35	342	149.000	151.000	2.000
35	345	149.000	145.000	-4.000
35	346	145.000	147.000	2.000
35	347	152.000	151.000	-1.000
35	348	138.000	140.000	2.000
50	349	149.000	146.000	-3.000
50	350	151.000	148.000	-3.000
50	351	150.000	144.000	-6.000
50	353	149.000	139.000	-10.000
50	354	151.000	143.000	-8.000
55	355	143.000	149.000	6.000
55	357	147.000	144.000	-3.000
55	359	144.000	133.000	-11.000
55	362	136.000	142.000	6.000
55	363	142.000	142.000	0.000
100	1	152.000	145.000	-7.000
100	3	144.000	150.000	6.000
100	4	140.000	132.000	-8.000
100	5	150.000	151.000	1.000
100	6	150.000	146.000	-4.000
100	7	151.000	144.000	-7.000
100	8	145.000	148.000	3.000
100	9	151.000	140.000	-11.000
100	10	148.000	140.000	-8.000
100	11	146.000	144.000	-2.000
100	12	152.000	149.000	-3.000
100	13	144.000	144.000	0.000
100	14	150.000	134.000	-16.000
100	15	131.000	137.000	6.000
100	16	137.000	144.000	7.000
100	17	136.000	131.000	-5.000
100	18	148.000	147.000	-1.000
100	19	143.000	128.000	-15.000
100	20	141.000	148.000	7.000
100	21	146.000	151.000	5.000
100	22	138.000	138.000	0.000
100	24	151.000	143.000	-8.000
105	25	148.000	148.000	0.000
105	26	142.000	145.000	3.000
105	27	140.000	139.000	-1.000
105	28	151.000	150.000	-1.000
105	29	150.000	150.000	0.000
Max		153.000	153.000	7.000
Average		146.029	144.314	-1.714
Min		131.000	128.000	-16.000
Std Dev		5.036	5.607	5.201

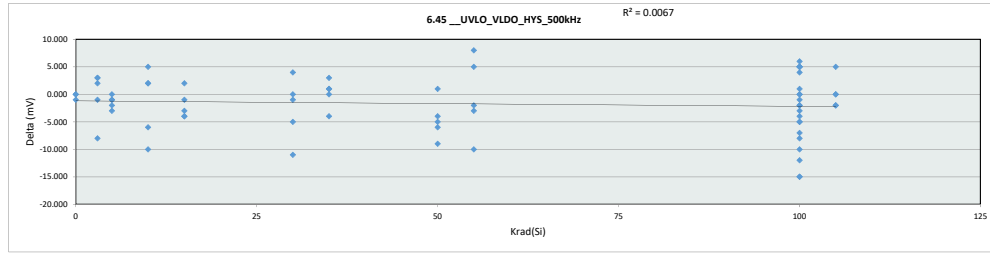


6.42 UVLO_VLDO_HYS_200											
Test Site											
Tester											
Test Number											
Max Limit	160		mV								
Min Limit	115		mV								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min	144.000	139.000	144.000	136.000	143.000	132.000	140.000	139.000	133.000	128.000	139.000
Average	146.667	145.000	146.600	142.400	147.600	144.800	146.800	144.000	142.000	142.455	146.400
Max	151.000	151.000	151.000	152.000	153.000	151.000	151.000	148.000	149.000	151.000	150.000
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000

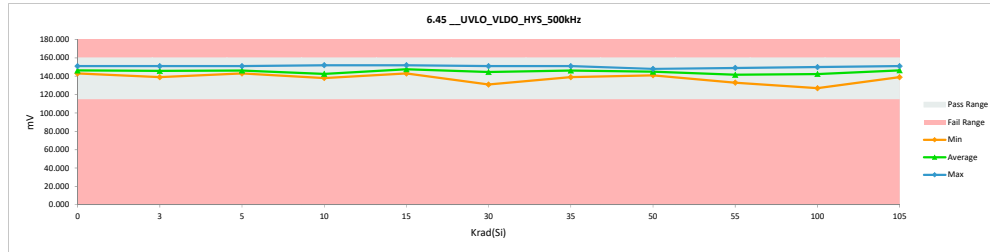


HDR TID Report
TPS7H5001-SP

6.45 UVLO_VLDO_HYS_500kHz				
Test Site				
Tester				
Test Number				
Unit	mV		mV	
Max Limit	160		160	
Min Limit	115		115	
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	146.000	145.000	-1.000
0	118	151.000	151.000	0.000
0	122	143.000	143.000	0.000
3	301	148.000	150.000	2.000
3	306	145.000	148.000	3.000
3	307	136.000	139.000	3.000
3	308	152.000	151.000	-1.000
3	309	149.000	141.000	-8.000
5	310	147.000	146.000	-1.000
5	311	147.000	144.000	-3.000
5	312	145.000	143.000	-2.000
5	314	148.000	147.000	-1.000
5	316	151.000	151.000	0.000
10	317	148.000	142.000	-6.000
10	318	147.000	152.000	5.000
10	319	138.000	140.000	2.000
10	321	151.000	141.000	-10.000
10	322	136.000	138.000	2.000
15	323	153.000	152.000	-1.000
15	325	150.000	152.000	2.000
15	326	150.000	146.000	-4.000
15	328	149.000	145.000	-4.000
15	333	146.000	143.000	-3.000
30	334	147.000	147.000	0.000
30	335	143.000	147.000	4.000
30	337	142.000	131.000	-11.000
30	339	152.000	151.000	-1.000
30	341	152.000	147.000	-5.000
35	342	148.000	151.000	3.000
35	345	149.000	145.000	-4.000
35	346	145.000	146.000	1.000
35	347	150.000	150.000	0.000
35	348	138.000	139.000	1.000
50	349	149.000	145.000	-4.000
50	350	153.000	148.000	-5.000
50	351	149.000	143.000	-6.000
50	353	147.000	148.000	1.000
50	354	150.000	141.000	-9.000
55	355	144.000	149.000	5.000
55	357	146.000	143.000	-3.000
55	359	143.000	133.000	-10.000
55	362	135.000	143.000	8.000
55	363	142.000	140.000	-2.000
100	1	151.000	146.000	-5.000
100	3	144.000	150.000	6.000
100	4	141.000	131.000	-10.000
100	5	151.000	150.000	-1.000
100	6	149.000	146.000	-3.000
100	7	150.000	150.000	0.000
100	8	145.000	150.000	5.000
100	9	151.000	139.000	-12.000
100	10	147.000	139.000	-8.000
100	11	146.000	144.000	-2.000
100	12	151.000	146.000	-5.000
100	13	144.000	145.000	1.000
100	14	151.000	136.000	-15.000
100	15	130.000	135.000	5.000
100	16	139.000	144.000	5.000
100	17	135.000	131.000	-4.000
100	18	147.000	145.000	-2.000
100	19	142.000	127.000	-15.000
100	20	142.000	147.000	5.000
100	21	146.000	150.000	4.000
100	22	137.000	137.000	0.000
100	24	151.000	144.000	-7.000
105	25	150.000	148.000	-2.000
105	26	140.000	145.000	5.000
105	27	139.000	139.000	0.000
105	28	151.000	149.000	-2.000
105	29	151.000	151.000	0.000
Max	153.000	152.000	8.000	
Average	146.014	144.300	-1.714	
Min	130.000	127.000	-15.000	
Std Dev	5.146	5.726	5.022	

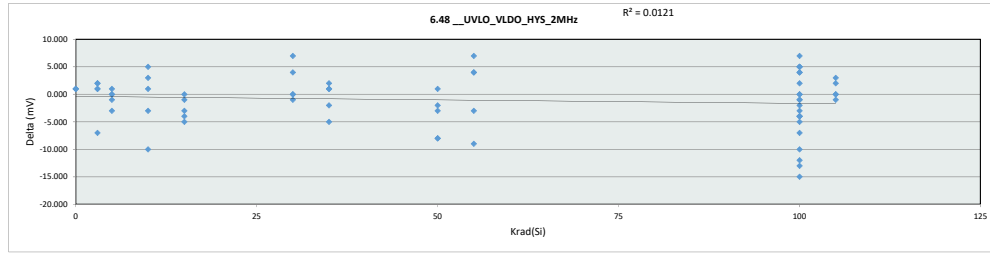


6.45 UVLO_VLDO_HYS_500											
Test Site											
Tester											
Test Number											
Max Limit	160		mV								
Min Limit	115		mV								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min	143.000	139.000	143.000	138.000	143.000	131.000	139.000	141.000	133.000	127.000	139.000
Average	146.333	145.800	146.200	142.600	147.600	144.600	146.200	145.000	141.600	142.364	146.400
Max	151.000	151.000	151.000	152.000	152.000	151.000	151.000	148.000	149.000	150.000	151.000
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000

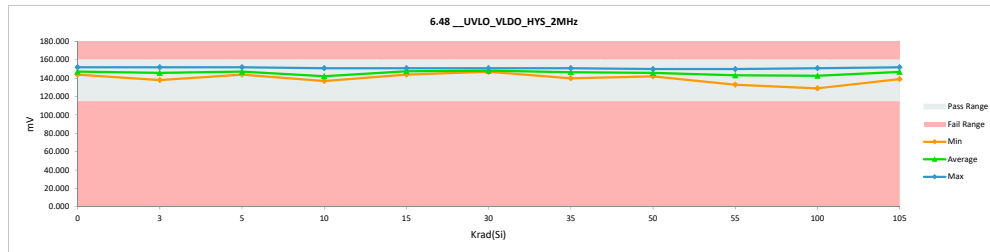


HDR TID Report
TPS7H5001-SP

6.48 UVLO_VLDO_HYS_2MHz				
Test Site				
Tester				
Test Number				
Unit	mV		mV	
Max Limit	160		160	
Min Limit	115		115	
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	145.000	146.000	1.000
0	118	151.000	152.000	1.000
0	122	143.000	144.000	1.000
3	301	147.000	149.000	2.000
3	306	146.000	148.000	2.000
3	307	137.000	138.000	1.000
3	308	151.000	152.000	1.000
3	309	149.000	142.000	-7.000
5	310	146.000	147.000	1.000
5	311	148.000	145.000	-3.000
5	312	145.000	144.000	-1.000
5	314	148.000	148.000	0.000
5	316	152.000	152.000	0.000
10	317	146.000	143.000	-3.000
10	318	148.000	151.000	3.000
10	319	136.000	141.000	5.000
10	321	149.000	139.000	-10.000
10	322	136.000	137.000	1.000
15	323	152.000	151.000	-1.000
15	325	151.000	151.000	0.000
15	326	151.000	147.000	-4.000
15	328	150.000	145.000	-5.000
15	333	147.000	144.000	-3.000
30	334	148.000	147.000	-1.000
30	335	143.000	147.000	4.000
30	337	142.000	149.000	7.000
30	339	151.000	151.000	0.000
30	341	147.000	147.000	0.000
35	342	149.000	151.000	2.000
35	345	150.000	145.000	-5.000
35	346	146.000	147.000	1.000
35	347	152.000	150.000	-2.000
35	348	139.000	140.000	1.000
50	349	148.000	146.000	-2.000
50	350	152.000	149.000	-3.000
50	351	150.000	142.000	-8.000
50	353	149.000	150.000	1.000
50	354	150.000	142.000	-8.000
55	355	144.000	148.000	4.000
55	357	147.000	144.000	-3.000
55	359	142.000	133.000	-9.000
55	362	137.000	141.000	4.000
55	363	143.000	150.000	7.000
100	1	150.000	150.000	0.000
100	3	143.000	150.000	7.000
100	4	142.000	132.000	-10.000
100	5	150.000	149.000	-1.000
100	6	150.000	146.000	-4.000
100	7	151.000	148.000	-3.000
100	8	146.000	151.000	5.000
100	9	150.000	138.000	-12.000
100	10	147.000	140.000	-7.000
100	11	146.000	144.000	-2.000
100	12	151.000	146.000	-5.000
100	13	144.000	146.000	2.000
100	14	149.000	134.000	-15.000
100	15	132.000	136.000	4.000
100	16	138.000	143.000	5.000
100	17	135.000	131.000	-4.000
100	18	149.000	148.000	-1.000
100	19	142.000	129.000	-13.000
100	20	142.000	147.000	5.000
100	21	146.000	150.000	4.000
100	22	137.000	137.000	0.000
100	24	151.000	147.000	-4.000
105	25	149.000	148.000	-1.000
105	26	142.000	145.000	3.000
105	27	139.000	139.000	0.000
105	28	151.000	151.000	0.000
105	29	150.000	152.000	2.000
Max		152.000	152.000	7.000
Average		146.071	145.029	-1.043
Min		132.000	129.000	-15.000
Std Dev		4.912	5.569	4.747

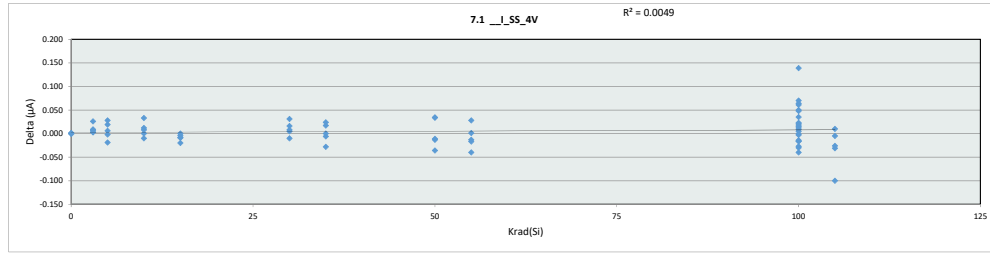


6.48 UVLO_VLDO_HYS_2MHz											
Test Site											
Tester											
Test Number											
Max Limit	160		mV								
Min Limit	115		mV								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min	144.000	138.000	144.000	137.000	144.000	147.000	140.000	142.000	133.000	129.000	139.000
Average	147.333	145.800	147.200	142.200	147.600	148.200	146.600	145.800	143.200	142.818	147.000
Max	152.000	152.000	152.000	151.000	151.000	151.000	151.000	150.000	150.000	151.000	152.000
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000

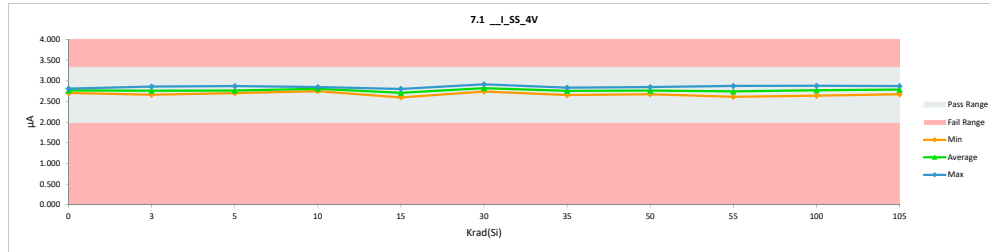


HDR TID Report
TPS7H5001-SP

7.1 I SS_4V				
Test Site				
Tester				
Test Number				
Unit	µA	µA		
Max Limit	3.32	3.32		
Min Limit	1.98	1.98		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	2.702	2.703	0.001
0	118	2.785	2.786	0.001
0	122	2.808	2.807	-0.001
3	301	2.683	2.692	0.009
3	306	2.724	2.731	0.007
3	307	2.855	2.858	0.003
3	308	2.637	2.663	0.026
3	309	2.839	2.842	0.003
5	310	2.739	2.720	-0.019
5	311	2.694	2.700	0.006
5	312	2.748	2.767	0.019
5	314	2.842	2.870	0.028
5	316	2.754	2.752	-0.002
10	317	2.840	2.848	0.008
10	318	2.804	2.794	-0.010
10	319	2.749	2.782	0.033
10	321	2.848	2.848	0.000
10	322	2.738	2.750	0.012
15	323	2.608	2.599	-0.009
15	325	2.667	2.659	-0.008
15	326	2.720	2.700	-0.020
15	328	2.786	2.786	0.000
15	333	2.808	2.804	-0.004
30	334	2.924	2.914	-0.010
30	335	2.724	2.740	0.016
30	337	2.750	2.781	0.031
30	339	2.837	2.845	0.008
30	341	2.819	2.824	0.005
35	342	2.833	2.805	-0.028
35	345	2.802	2.819	0.017
35	346	2.659	2.653	-0.006
35	347	2.807	2.831	0.024
35	348	2.666	2.666	0.000
50	349	2.815	2.802	-0.013
50	350	2.812	2.846	0.034
50	351	2.697	2.731	0.034
50	353	2.774	2.763	-0.011
50	354	2.710	2.674	-0.036
55	355	2.653	2.613	-0.040
55	357	2.845	2.873	0.028
55	359	2.754	2.755	0.001
55	362	2.732	2.715	-0.017
55	363	2.763	2.750	-0.013
100	1	2.674	2.695	0.021
100	3	2.750	2.710	-0.040
100	4	2.811	2.846	0.035
100	5	2.809	2.879	0.070
100	6	2.701	2.716	0.015
100	7	2.823	2.807	-0.016
100	8	2.692	2.662	-0.030
100	9	2.758	2.781	0.023
100	10	2.783	2.833	0.050
100	11	2.843	2.848	0.005
100	12	2.659	2.656	-0.003
100	13	2.818	2.802	-0.016
100	14	2.761	2.809	0.048
100	15	2.816	2.877	0.061
100	16	2.750	2.736	-0.014
100	17	2.635	2.699	0.064
100	18	2.639	2.638	-0.001
100	19	2.819	2.828	0.009
100	20	2.763	2.781	0.018
100	21	2.766	2.739	-0.027
100	22	2.672	2.811	0.139
100	24	2.764	2.776	0.012
105	25	2.947	2.847	-0.100
105	26	2.829	2.824	-0.005
105	27	2.705	2.674	-0.031
105	28	2.741	2.715	-0.026
105	29	2.860	2.870	0.010
	Max	2.947	2.914	0.139
	Average	2.762	2.767	0.005
	Min	2.608	2.599	-0.100
	Std Dev	0.071	0.074	0.031

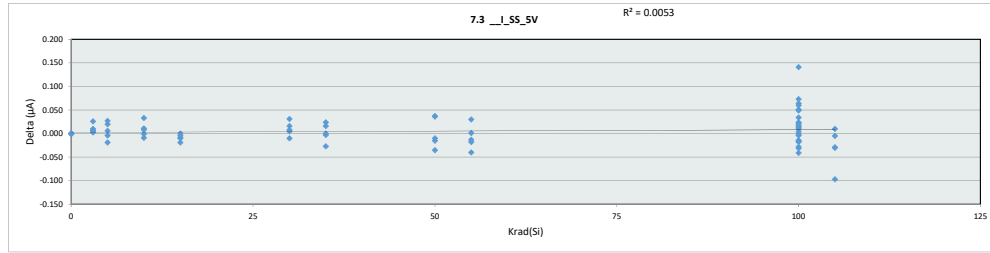


7.1 I SS_4V											
Test Site											
Tester											
Test Number											
Max Limit	3.32	µA									
Min Limit	1.98	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min	2.703	2.663	2.700	2.750	2.599	2.740	2.653	2.674	2.613	2.638	2.674
Average	2.765	2.757	2.762	2.804	2.710	2.821	2.755	2.763	2.741	2.770	2.786
Max	2.807	2.858	2.870	2.848	2.804	2.914	2.831	2.846	2.873	2.879	2.870
UL	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320

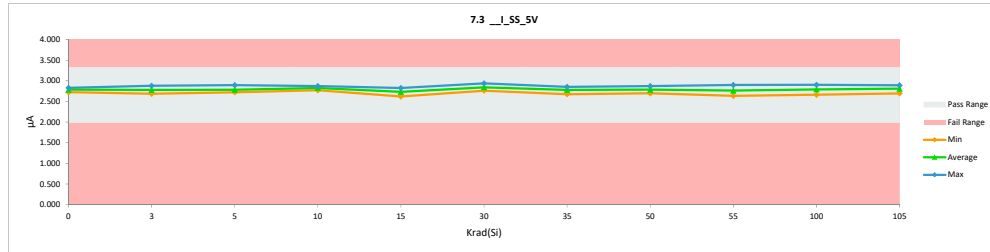


HDR TID Report
TPS7H5001-SP

7.3 I SS 5V				
Test Site				
Tester				
Test Number				
Unit	µA	µA		
Max Limit	3.32	3.32		
Min Limit	1.98	1.98		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	2.725	2.724	-0.001
0	118	2.808	2.809	0.001
0	122	2.830	2.829	-0.001
3	301	2.704	2.714	0.010
3	306	2.746	2.753	0.007
3	307	2.877	2.880	0.003
3	308	2.660	2.686	0.026
3	309	2.861	2.864	0.003
5	310	2.761	2.742	-0.019
5	311	2.716	2.722	0.006
5	312	2.770	2.790	0.020
5	314	2.866	2.893	0.027
5	316	2.777	2.773	-0.004
10	317	2.863	2.871	0.008
10	318	2.826	2.817	-0.009
10	319	2.772	2.805	0.033
10	321	2.871	2.870	-0.001
10	322	2.759	2.770	0.011
15	323	2.629	2.619	-0.010
15	325	2.688	2.680	-0.008
15	326	2.742	2.723	-0.019
15	328	2.808	2.808	0.000
15	333	2.830	2.826	-0.004
30	334	2.948	2.938	-0.010
30	335	2.745	2.761	0.016
30	337	2.773	2.804	0.031
30	339	2.860	2.868	0.008
30	341	2.841	2.846	0.005
35	342	2.855	2.828	-0.027
35	345	2.825	2.841	0.016
35	346	2.679	2.676	-0.003
35	347	2.828	2.852	0.024
35	348	2.687	2.687	0.000
50	349	2.838	2.823	-0.015
50	350	2.835	2.871	0.036
50	351	2.718	2.755	0.037
50	353	2.796	2.786	-0.010
50	354	2.731	2.696	-0.035
55	355	2.674	2.634	-0.040
55	357	2.867	2.897	0.030
55	359	2.775	2.776	0.001
55	362	2.755	2.737	-0.018
55	363	2.785	2.772	-0.013
100	1	2.695	2.717	0.022
100	3	2.772	2.731	-0.041
100	4	2.836	2.870	0.034
100	5	2.830	2.903	0.073
100	6	2.721	2.737	0.016
100	7	2.846	2.830	-0.016
100	8	2.714	2.683	-0.031
100	9	2.779	2.803	0.024
100	10	2.804	2.855	0.051
100	11	2.866	2.870	0.004
100	12	2.681	2.677	-0.004
100	13	2.840	2.822	-0.018
100	14	2.784	2.833	0.049
100	15	2.840	2.900	0.060
100	16	2.773	2.759	-0.014
100	17	2.656	2.720	0.064
100	18	2.662	2.661	-0.001
100	19	2.843	2.853	0.010
100	20	2.785	2.803	0.018
100	21	2.789	2.761	-0.028
100	22	2.693	2.834	0.141
100	24	2.787	2.800	0.013
105	25	2.969	2.872	-0.097
105	26	2.851	2.846	-0.005
105	27	2.725	2.695	-0.030
105	28	2.764	2.735	-0.029
105	29	2.881	2.891	0.010
	Max	2.969	2.938	0.141
	Average	2.784	2.790	0.006
	Min	2.629	2.619	-0.097
	Std Dev	0.072	0.074	0.032

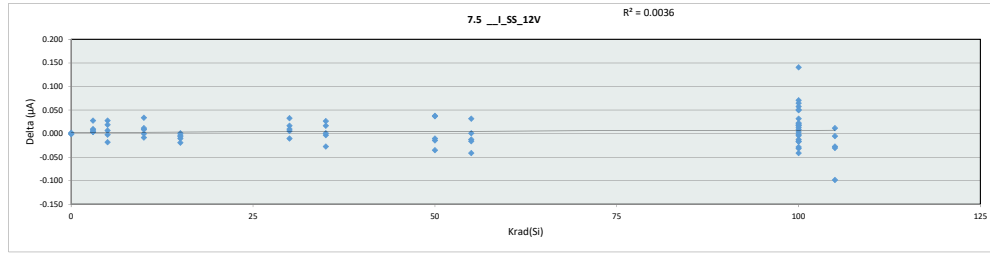


7.3 I SS 5V											
Test Site											
Tester											
Test Number											
Max Limit	3.32	µA									
Min Limit	1.98	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min	2.724	2.686	2.722	2.770	2.619	2.761	2.676	2.696	2.634	2.661	2.695
Average	2.787	2.779	2.784	2.827	2.731	2.843	2.777	2.786	2.763	2.792	2.808
Max	2.829	2.880	2.893	2.871	2.826	2.938	2.852	2.871	2.897	2.903	2.891
UL	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320

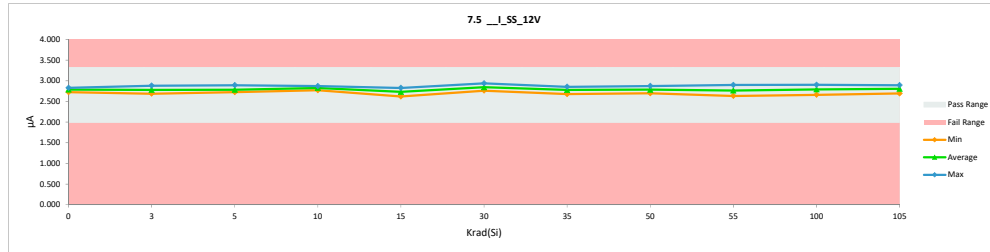


HDR TID Report
TPS7H5001-SP

7.5 I SS 12V				
Test Site				
Tester				
Test Number				
Unit	µA	µA		
Max Limit	3.32	3.32		
Min Limit	1.98	1.98		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	2.727	2.725	-0.002
0	118	2.809	2.810	0.001
0	122	2.831	2.830	-0.001
3	301	2.706	2.715	0.009
3	306	2.748	2.755	0.007
3	307	2.878	2.881	0.003
3	308	2.660	2.687	0.027
3	309	2.862	2.865	0.003
5	310	2.762	2.743	-0.019
5	311	2.718	2.724	0.006
5	312	2.773	2.791	0.018
5	314	2.866	2.893	0.027
5	316	2.777	2.774	-0.003
10	317	2.863	2.871	0.008
10	318	2.827	2.818	-0.009
10	319	2.773	2.806	0.033
10	321	2.873	2.872	-0.001
10	322	2.760	2.771	0.011
15	323	2.630	2.619	-0.011
15	325	2.688	2.681	-0.007
15	326	2.743	2.723	-0.020
15	328	2.809	2.809	0.000
15	333	2.831	2.827	-0.004
30	334	2.949	2.938	-0.011
30	335	2.746	2.762	0.016
30	337	2.773	2.805	0.032
30	339	2.861	2.870	0.009
30	341	2.842	2.847	0.005
35	342	2.856	2.828	-0.028
35	345	2.826	2.842	0.016
35	346	2.681	2.677	-0.004
35	347	2.828	2.854	0.026
35	348	2.688	2.688	0.000
50	349	2.839	2.824	-0.015
50	350	2.836	2.873	0.037
50	351	2.719	2.755	0.036
50	353	2.798	2.787	-0.011
50	354	2.733	2.697	-0.036
55	355	2.676	2.634	-0.042
55	357	2.868	2.899	0.031
55	359	2.776	2.776	0.000
55	362	2.756	2.739	-0.017
55	363	2.786	2.773	-0.013
100	1	2.697	2.717	0.020
100	3	2.773	2.731	-0.042
100	4	2.837	2.868	0.031
100	5	2.832	2.902	0.070
100	6	2.721	2.737	0.016
100	7	2.848	2.831	-0.017
100	8	2.716	2.684	-0.032
100	9	2.780	2.802	0.022
100	10	2.804	2.854	0.050
100	11	2.868	2.871	0.003
100	12	2.682	2.677	-0.005
100	13	2.841	2.823	-0.018
100	14	2.784	2.833	0.049
100	15	2.842	2.899	0.057
100	16	2.774	2.761	-0.013
100	17	2.657	2.721	0.064
100	18	2.663	2.661	-0.002
100	19	2.845	2.853	0.008
100	20	2.786	2.803	0.017
100	21	2.790	2.761	-0.029
100	22	2.694	2.834	0.140
100	24	2.789	2.801	0.012
105	25	2.971	2.872	-0.099
105	26	2.852	2.846	-0.006
105	27	2.725	2.694	-0.031
105	28	2.765	2.737	-0.028
105	29	2.882	2.893	0.011
	Max	2.971	2.938	0.140
	Average	2.785	2.790	0.005
	Min	2.630	2.619	-0.099
	Std Dev	0.072	0.074	0.032

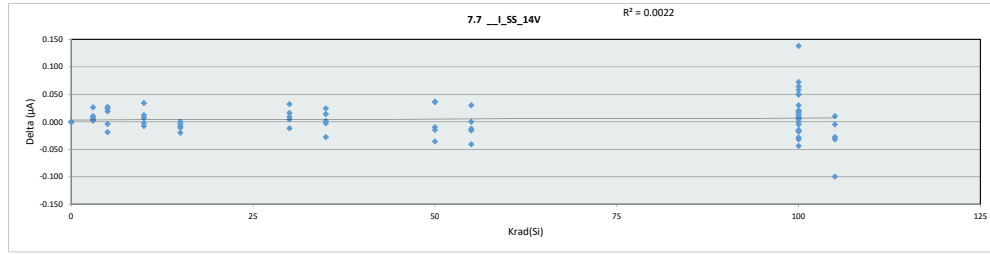


7.5 I SS 12V											
Test Site											
Tester											
Test Number											
Max Limit	3.32	µA									
Min Limit	1.98	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min	2.725	2.687	2.724	2.771	2.619	2.762	2.677	2.697	2.634	2.661	2.694
Average	2.788	2.781	2.785	2.828	2.732	2.844	2.778	2.787	2.764	2.792	2.808
Max	2.830	2.881	2.893	2.872	2.827	2.938	2.854	2.873	2.899	2.902	2.893
UL	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320

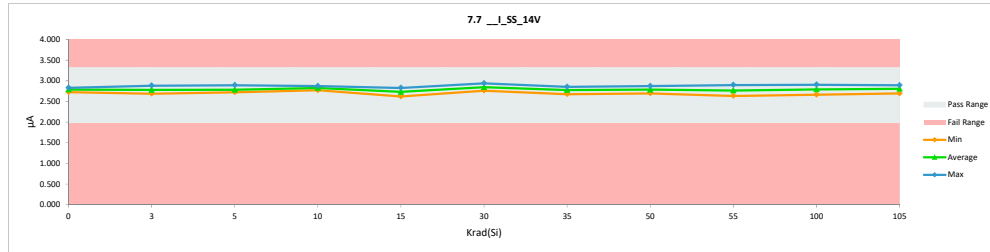


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7.7 I SS 14V				
Test Site				
Tester				
Test Number				
Unit	µA	µA		
Max Limit	3.32	3.32		
Min Limit	1.98	1.98		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	2.726	2.725	-0.001
0	118	2.810	2.810	0.000
0	122	2.831	2.830	-0.001
3	301	2.706	2.716	0.010
3	306	2.748	2.754	0.006
3	307	2.878	2.881	0.003
3	308	2.661	2.687	0.026
3	309	2.863	2.865	0.002
5	310	2.762	2.743	-0.019
5	311	2.699	2.723	0.024
5	312	2.772	2.791	0.019
5	314	2.867	2.894	0.027
5	316	2.778	2.774	-0.004
10	317	2.864	2.871	0.007
10	318	2.827	2.819	-0.008
10	319	2.773	2.807	0.034
10	321	2.874	2.872	-0.002
10	322	2.759	2.771	0.012
15	323	2.630	2.619	-0.011
15	325	2.689	2.681	-0.008
15	326	2.743	2.723	-0.020
15	328	2.810	2.810	0.000
15	333	2.831	2.827	-0.004
30	334	2.950	2.938	-0.012
30	335	2.746	2.762	0.016
30	337	2.774	2.806	0.032
30	339	2.861	2.870	0.009
30	341	2.842	2.846	0.004
35	342	2.856	2.828	-0.028
35	345	2.827	2.841	0.014
35	346	2.680	2.677	-0.003
35	347	2.829	2.853	0.024
35	348	2.688	2.689	0.001
50	349	2.839	2.824	-0.015
50	350	2.836	2.872	0.036
50	351	2.719	2.755	0.036
50	353	2.797	2.787	-0.010
50	354	2.733	2.697	-0.036
55	355	2.675	2.634	-0.041
55	357	2.868	2.898	0.030
55	359	2.777	2.777	0.000
55	362	2.756	2.740	-0.016
55	363	2.786	2.773	-0.013
100	1	2.697	2.716	0.019
100	3	2.773	2.729	-0.044
100	4	2.837	2.867	0.030
100	5	2.832	2.904	0.072
100	6	2.722	2.736	0.014
100	7	2.848	2.830	-0.018
100	8	2.715	2.683	-0.032
100	9	2.780	2.801	0.021
100	10	2.804	2.854	0.050
100	11	2.868	2.873	0.005
100	12	2.682	2.677	-0.005
100	13	2.841	2.824	-0.017
100	14	2.785	2.834	0.049
100	15	2.842	2.900	0.058
100	16	2.774	2.759	-0.015
100	17	2.657	2.721	0.064
100	18	2.663	2.663	0.000
100	19	2.845	2.852	0.007
100	20	2.786	2.803	0.017
100	21	2.790	2.761	-0.029
100	22	2.694	2.832	0.138
100	24	2.790	2.800	0.010
105	25	2.971	2.871	-0.100
105	26	2.852	2.847	-0.005
105	27	2.726	2.694	-0.032
105	28	2.765	2.737	-0.028
105	29	2.882	2.892	0.010
	Max	2.971	2.938	0.138
	Average	2.785	2.790	0.005
	Min	2.630	2.619	-0.100
	Std Dev	0.072	0.074	0.032



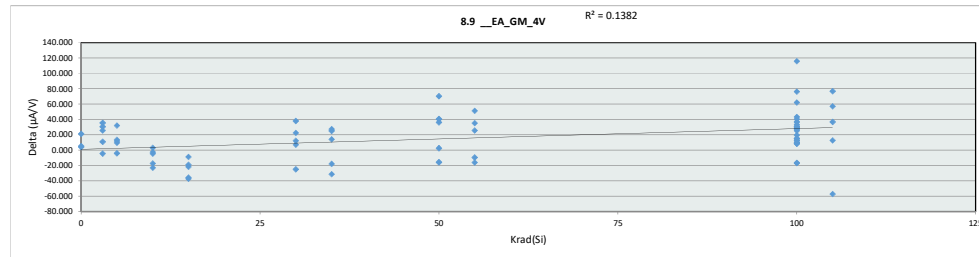
7.7 I SS 14V											
Test Site											
Tester											
Test Number											
Max Limit	3.32	µA									
Min Limit	1.98	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min	2.725	2.687	2.723	2.771	2.619	2.762	2.677	2.697	2.634	2.663	2.694
Average	2.788	2.781	2.785	2.828	2.732	2.844	2.778	2.787	2.764	2.792	2.808
Max	2.830	2.881	2.894	2.872	2.827	2.938	2.853	2.872	2.898	2.904	2.892
UL	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320



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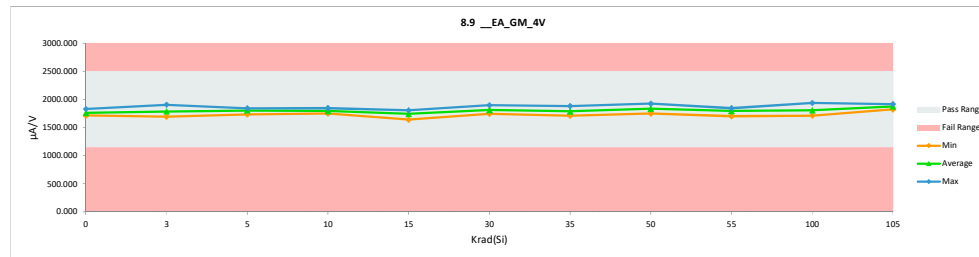
8.9_EA_GM_4V	
Test Site	
Tester	
Test Number	
Unit	µA/V
Max Limit	2500
Min Limit	1150

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1711.018	1716.190	5.172
0	118	1733.488	1737.554	4.066
0	122	1809.625	1830.724	21.099
3	301	1697.506	1693.112	-4.394
3	306	1748.228	1778.802	30.574
3	307	1870.693	1906.264	35.571
3	308	1727.375	1753.078	25.703
3	309	1795.834	1806.848	11.014
5	310	1844.927	1840.737	-4.190
5	311	1754.122	1763.572	9.450
5	312	1831.754	1843.620	11.866
5	314	1799.117	1831.194	32.077
5	316	1722.321	1735.855	13.534
10	317	1852.311	1834.989	-17.322
10	318	1794.000	1789.283	-4.717
10	319	1748.647	1751.650	3.003
10	321	1766.041	1763.572	-2.469
10	322	1867.258	1844.248	-23.010
15	323	1659.771	1640.763	-19.008
15	325	1755.986	1747.235	-8.751
15	326	1806.167	1768.862	-37.305
15	328	1784.439	1762.720	-21.739
15	333	1844.339	1808.708	-35.631
30	334	1887.923	1900.111	12.188
30	335	1768.862	1806.848	37.986
30	337	1791.060	1798.018	6.958
30	339	1796.834	1819.260	22.426
30	341	1768.862	1743.857	-25.005
35	342	1742.707	1724.844	-17.863
35	345	1817.447	1842.638	25.191
35	346	1806.858	1775.386	-31.472
35	347	1857.417	1884.900	27.483
35	348	1697.497	1711.760	14.263
50	349	1724.082	1764.630	40.548
50	350	1846.501	1882.753	36.252
50	351	1854.635	1924.942	70.307
50	353	1847.935	1850.381	2.446
50	354	1766.218	1750.462	-15.756
55	355	1711.847	1702.415	-9.432
55	357	1809.225	1844.481	35.256
55	359	1828.589	1812.411	-16.178
55	362	1784.269	1835.642	51.373
55	363	1762.526	1788.196	25.670
100	1	1774.203	1811.002	36.799
100	3	1706.632	1721.870	15.238
100	4	1860.486	1880.074	19.588
100	5	1891.392	1931.680	40.288
100	6	1829.297	1839.284	9.987
100	7	1708.761	1784.953	76.192
100	8	1699.045	1726.664	27.619
100	9	1805.467	1818.008	12.541
100	10	1848.994	1864.032	15.038
100	11	1904.208	1937.614	33.406
100	12	1707.839	1716.234	8.395
100	13	1782.876	1811.168	28.292
100	14	1784.061	1809.371	25.310
100	15	1844.694	1875.314	30.620
100	16	1770.206	1801.329	31.123
100	17	1675.510	1737.563	62.053
100	18	1682.956	1712.310	29.354
100	19	1786.017	1794.614	8.597
100	20	1741.676	1784.943	43.267
100	21	1772.675	1756.216	-16.459
100	22	1821.650	1937.614	115.964
100	24	1769.935	1753.078	-16.857
105	25	1921.184	1864.032	-57.152
105	26	1881.898	1918.679	36.781
105	27	1829.866	1886.925	57.059
105	28	1810.817	1823.640	12.823
105	29	1823.630	1900.444	76.814
	Max	1921.184	1937.614	115.964
	Average	1789.032	1804.460	15.427
	Min	1659.771	1640.763	-57.152
	Std Dev	60.111	66.650	29.788



8.9_EA_GM_4V	
Test Site	
Tester	
Test Number	
Max Limit	2500 µA/V
Min Limit	1150 µA/V

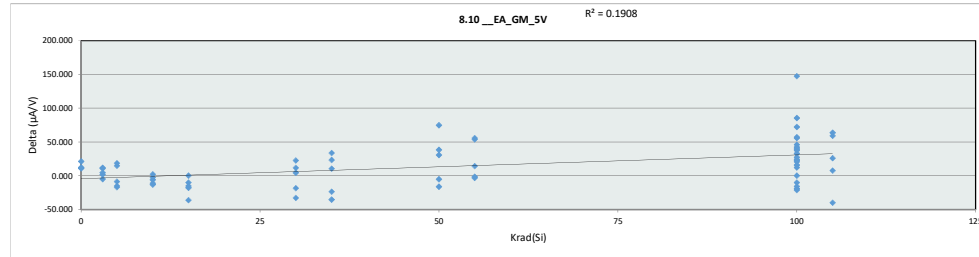
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min	1716.190	1693.112	1735.855	1751.650	1640.763	1743.857	1711.760	1750.462	1702.415	1712.310	1823.640
Average	1761.489	1787.621	1802.996	1796.748	1745.658	1813.619	1787.906	1834.634	1796.629	1809.315	1878.744
Max	1830.724	1906.264	1843.620	1844.248	1808.708	1900.111	1884.900	1924.942	1844.481	1937.614	1918.679
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000



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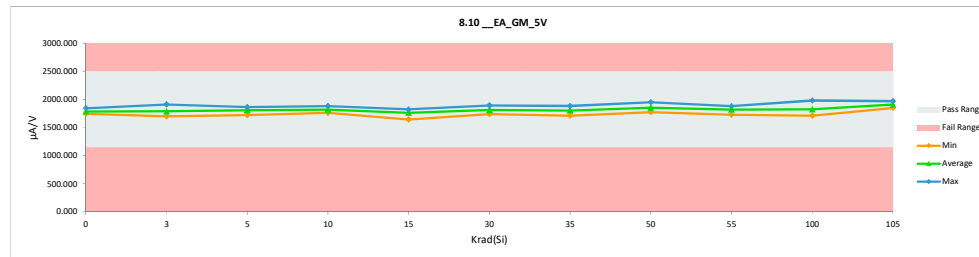
8.10 __EA_GM_5V	
Test Site	
Tester	
Test Number	
Unit	µA/V
Max Limit	2500
Min Limit	1150

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1727.829	1748.911	21.082
0	118	1753.078	1765.084	12.006
0	122	1831.474	1842.385	10.911
3	301	1697.497	1701.914	4.417
3	306	1758.065	1769.823	11.758
3	307	1901.855	1912.446	10.591
3	308	1747.345	1748.920	1.575
3	309	1817.496	1812.421	-5.075
5	310	1859.734	1842.648	-17.086
5	311	1784.953	1776.147	-8.806
5	312	1848.668	1863.297	14.629
5	314	1821.522	1839.778	18.256
5	316	1737.563	1722.321	-15.242
10	317	1887.923	1881.898	-6.025
10	318	1818.008	1806.858	-11.150
10	319	1776.871	1763.582	-13.289
10	321	1782.923	1784.953	2.030
10	322	1872.677	1869.953	-2.724
15	323	1660.584	1645.341	-15.243
15	325	1766.209	1755.977	-10.232
15	326	1820.820	1802.877	-17.943
15	328	1790.372	1790.295	-0.077
15	333	1859.888	1823.630	-36.258
30	334	1912.446	1893.997	-18.449
30	335	1783.672	1788.044	4.372
30	337	1795.834	1807.451	11.617
30	339	1812.284	1834.708	22.424
30	341	1773.125	1740.122	-33.003
35	342	1746.208	1710.652	-35.556
35	345	1852.710	1875.901	23.191
35	346	1825.088	1801.319	-23.769
35	347	1875.901	1886.257	10.356
35	348	1702.406	1736.017	33.611
50	349	1744.991	1775.536	30.545
50	350	1867.299	1905.636	38.337
50	351	1875.901	1950.442	74.541
50	353	1863.493	1858.148	-5.345
50	354	1801.319	1784.962	-16.357
55	355	1728.888	1727.375	-1.513
55	357	1824.652	1880.401	55.749
55	359	1849.820	1846.491	-3.329
55	362	1790.372	1844.197	53.825
55	363	1785.941	1799.947	14.006
100	1	1786.159	1828.480	42.321
100	3	1749.175	1729.770	-19.405
100	4	1874.717	1890.359	15.642
100	5	1909.953	1955.808	45.855
100	6	1828.261	1855.548	27.287
100	7	1724.206	1809.615	85.409
100	8	1713.683	1750.983	37.300
100	9	1789.484	1846.501	57.017
100	10	1875.891	1887.934	12.043
100	11	1909.583	1950.442	40.859
100	12	1737.554	1727.366	-10.188
100	13	1808.699	1829.297	20.598
100	14	1795.834	1828.729	32.895
100	15	1871.131	1871.131	0.000
100	16	1793.846	1832.544	38.698
100	17	1693.838	1765.920	72.082
100	18	1688.630	1712.756	24.126
100	19	1794.518	1817.388	22.870
100	20	1753.068	1808.708	55.640
100	21	1789.474	1767.986	-21.488
100	22	1834.989	1982.326	147.337
100	24	1774.193	1758.314	-15.879
105	25	1949.513	1909.355	-40.158
105	26	1912.457	1971.471	59.014
105	27	1848.821	1912.457	63.636
105	28	1843.022	1850.564	7.542
105	29	1881.898	1907.803	25.905
	Max	1949.513	1982.326	147.337
	Average	1806.690	1821.123	14.433
	Min	1660.584	1645.341	-40.158
	Std Dev	63.337	71.194	32.719



8.10 __EA_GM_5V	
Test Site	
Tester	
Test Number	
Max Limit	2500
Min Limit	1150

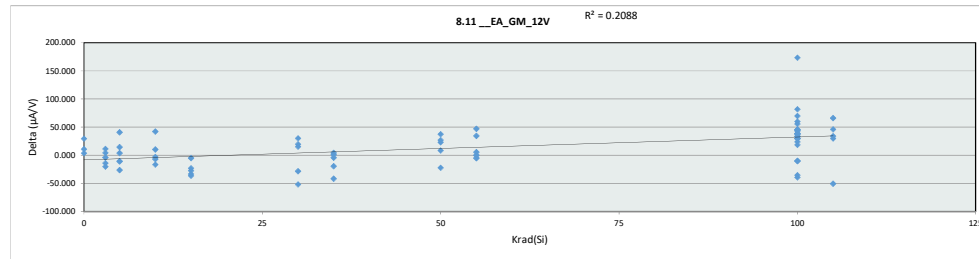
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min	1748.911	1701.914	1722.321	1763.582	1645.341	1740.122	1710.652	1775.536	1727.375	1712.756	1850.564
Average	1785.460	1789.105	1808.838	1821.449	1763.624	1812.864	1802.029	1854.945	1819.682	1827.632	1910.330
Max	1842.385	1912.446	1863.297	1881.898	1823.630	1893.997	1886.257	1950.442	1880.401	1982.326	1971.471
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000



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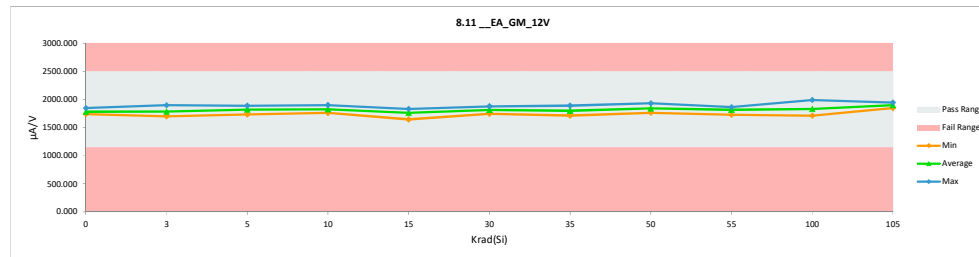
8.11 __EA_GM_12V			
Test Site			
Tester			
Test Number			
Unit	µA/V	µA/V	
Max Limit	2500	2500	
Min Limit	1150	1150	

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1730.029	1740.637	10.608
0	118	1754.654	1758.305	3.651
0	122	1817.309	1846.491	29.182
3	301	1699.139	1703.297	4.158
3	306	1767.846	1753.728	-14.118
3	307	1918.668	1898.272	-20.396
3	308	1758.305	1754.251	-4.054
3	309	1818.008	1829.297	11.289
5	310	1858.158	1862.066	3.908
5	311	1784.953	1774.193	-10.760
5	312	1847.080	1887.934	40.854
5	314	1826.211	1840.535	14.324
5	316	1759.356	1732.969	-26.387
10	317	1858.148	1900.121	41.973
10	318	1829.307	1822.511	-6.796
10	319	1778.218	1761.471	-16.747
10	321	1787.920	1784.962	-2.958
10	322	1853.754	1864.042	10.288
15	323	1682.956	1646.350	-36.606
15	325	1758.305	1753.078	-5.227
15	326	1823.630	1796.257	-27.373
15	328	1813.430	1790.018	-23.412
15	333	1866.261	1832.904	-33.357
30	334	1906.264	1877.906	-28.358
30	335	1787.282	1806.848	19.566
30	337	1797.334	1812.401	15.067
30	339	1806.858	1836.898	30.040
30	341	1794.623	1742.716	-51.907
35	342	1756.887	1715.286	-41.601
35	345	1865.866	1869.953	4.087
35	346	1824.523	1804.778	-19.745
35	347	1894.543	1890.124	-4.419
35	348	1721.305	1722.321	1.016
50	349	1740.565	1763.572	23.007
50	350	1870.745	1878.905	8.160
50	351	1893.997	1931.257	37.260
50	353	1845.628	1872.426	26.798
50	354	1784.953	1762.526	-22.427
55	355	1732.450	1727.375	-5.075
55	357	1818.008	1864.923	46.915
55	359	1846.501	1846.491	-0.010
55	362	1812.411	1846.501	34.090
55	363	1790.372	1795.824	5.452
100	1	1796.612	1826.161	29.549
100	3	1740.131	1729.904	-10.227
100	4	1884.922	1918.679	33.757
100	5	1886.829	1956.492	69.663
100	6	1824.146	1869.953	45.807
100	7	1752.501	1812.411	59.910
100	8	1711.821	1744.918	33.097
100	9	1774.193	1819.872	45.679
100	10	1852.301	1875.901	23.600
100	11	1908.041	1963.442	55.401
100	12	1747.872	1712.310	-35.562
100	13	1804.089	1840.737	36.648
100	14	1795.834	1834.367	38.533
100	15	1871.935	1890.146	18.211
100	16	1783.226	1826.459	43.233
100	17	1688.656	1770.206	81.550
100	18	1691.311	1720.952	29.641
100	19	1785.931	1830.325	44.394
100	20	1766.757	1804.836	38.079
100	21	1802.897	1763.572	-39.325
100	22	1818.008	1991.479	173.471
100	24	1784.412	1774.203	-10.209
105	25	1955.295	1904.500	-50.795
105	26	1909.974	1944.001	34.027
105	27	1846.501	1912.435	65.934
105	28	1816.758	1846.491	29.733
105	29	1848.149	1894.008	45.859
	Max	1955.295	1991.479	173.471
	Average	1807.598	1821.164	13.566
	Min	1682.956	1646.350	-51.907
	Std Dev	59.806	71.307	36.290



8.11 __EA_GM_12V			
Test Site			
Tester			
Test Number			
Max Limit	2500	µA/V	
Min Limit	1150	µA/V	

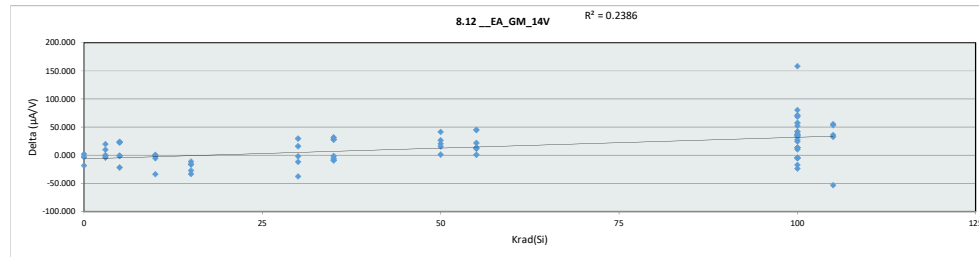
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min	1740.637	1703.297	1732.969	1761.471	1646.350	1742.716	1715.286	1762.526	1727.375	1712.310	1846.491
Average	1781.811	1787.769	1819.539	1826.621	1763.721	1815.354	1800.492	1841.737	1816.223	1830.788	1900.287
Max	1846.491	1898.272	1887.934	1900.121	1832.904	1877.906	1890.124	1931.257	1864.923	1991.479	1944.001
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000



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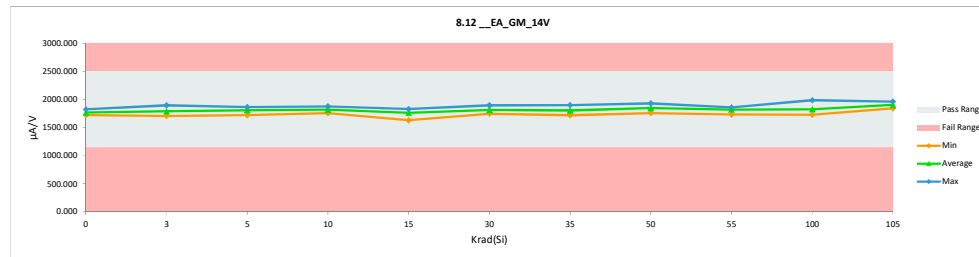
8.12 __EA_GM_14V	
Test Site	
Tester	
Test Number	
Unit	µA/V
Max Limit	2500
Min Limit	1150

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1742.707	1724.392	-18.315
0	118	1746.144	1748.337	2.193
0	122	1829.297	1825.903	-3.394
3	301	1711.664	1707.162	-4.502
3	306	1766.339	1764.825	-1.514
3	307	1886.723	1896.459	9.736
3	308	1731.600	1751.522	19.922
3	309	1834.999	1834.999	0.000
5	310	1864.032	1862.862	-1.170
5	311	1763.563	1762.813	-0.750
5	312	1842.577	1865.223	22.646
5	314	1819.408	1843.043	23.635
5	316	1744.175	1722.321	-21.854
10	317	1875.901	1876.762	0.861
10	318	1813.096	1812.411	-0.685
10	319	1765.335	1759.937	-5.398
10	321	1802.112	1768.340	-33.772
10	322	1870.474	1869.953	-0.521
15	323	1663.507	1630.170	-33.337
15	325	1774.794	1763.563	-11.231
15	326	1821.126	1805.380	-15.746
15	328	1805.623	1788.940	-16.683
15	333	1858.148	1831.074	-27.074
30	334	1897.660	1895.720	-1.940
30	335	1789.780	1805.749	15.969
30	337	1797.199	1785.361	-11.838
30	339	1804.768	1834.418	29.650
30	341	1785.447	1747.872	-37.575
35	342	1733.175	1727.366	-5.809
35	345	1842.162	1869.953	27.791
35	346	1814.646	1805.623	-9.023
35	347	1869.953	1901.715	31.762
35	348	1720.802	1719.312	-1.490
50	349	1742.698	1758.314	15.616
50	350	1866.967	1887.254	20.287
50	351	1889.943	1931.257	41.314
50	353	1842.962	1869.598	26.636
50	354	1784.943	1786.302	1.359
55	355	1731.806	1732.450	0.644
55	357	1845.354	1858.879	13.525
55	359	1840.737	1852.301	11.564
55	362	1812.411	1857.551	45.140
55	363	1783.880	1805.740	21.860
100	1	1784.193	1818.560	34.367
100	3	1749.604	1732.441	-17.163
100	4	1881.898	1892.459	10.561
100	5	1910.899	1952.883	41.984
100	6	1837.280	1869.953	32.673
100	7	1739.843	1811.012	71.169
100	8	1710.102	1747.872	37.770
100	9	1802.703	1816.316	13.613
100	10	1874.403	1869.953	-4.450
100	11	1912.467	1970.012	57.545
100	12	1732.450	1727.366	-5.084
100	13	1795.843	1831.194	35.351
100	14	1789.684	1858.137	68.453
100	15	1854.061	1867.320	13.259
100	16	1796.930	1821.067	24.137
100	17	1686.017	1766.516	80.499
100	18	1691.490	1728.104	36.614
100	19	1784.459	1811.794	27.335
100	20	1762.405	1814.508	52.103
100	21	1787.672	1783.150	-4.522
100	22	1829.277	1987.469	158.192
100	24	1781.948	1758.305	-23.643
105	25	1947.872	1894.607	-53.265
105	26	1927.485	1963.442	35.957
105	27	1846.501	1901.747	55.246
105	28	1810.318	1842.881	32.563
105	29	1859.219	1912.446	53.227
	Max	1947.872	1987.469	158.192
	Average	1805.967	1820.009	14.043
	Min	1663.507	1630.170	-53.265
	Std Dev	61.319	71.191	32.277



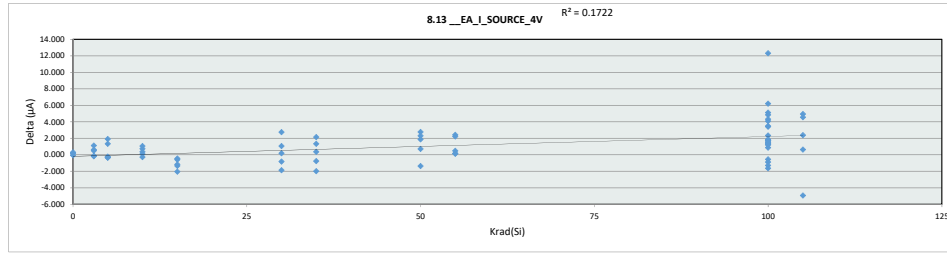
8.12 __EA_GM_14V	
Test Site	
Tester	
Test Number	
Max Limit	2500 µA/V
Min Limit	1150 µA/V

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min	1724.392	1707.162	1722.321	1759.937	1630.170	1747.872	1719.312	1758.314	1732.450	1727.366	1842.881
Average	1766.211	1790.993	1811.252	1817.481	1763.825	1813.824	1804.794	1846.545	1821.384	1828.927	1903.025
Max	1825.903	1896.459	1865.223	1876.762	1831.074	1895.720	1901.715	1931.257	1858.879	1987.469	1963.442
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000

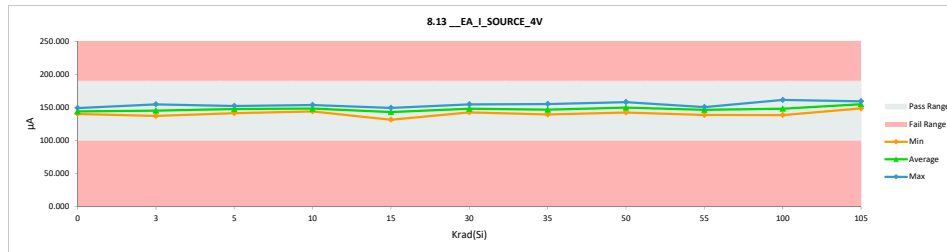


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8.13 EA_I_SOURCE_4V				
Test Site				
Tester				
Test Number				
Unit	µA	µA		
Max Limit	190	190		
Min Limit	100	100		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	140.361	140.520	0.159
0	118	142.979	142.900	-0.079
0	122	149.095	149.389	0.294
3	301	137.674	137.535	-0.139
3	306	143.571	144.063	0.492
3	307	155.135	154.942	-0.193
3	308	141.383	142.482	1.099
3	309	148.278	148.880	0.602
5	310	151.304	150.981	-0.323
5	311	145.082	144.713	-0.369
5	312	151.319	152.640	1.321
5	314	147.932	149.825	1.893
5	316	141.950	141.724	-0.226
10	317	153.279	154.005	0.726
10	318	148.057	147.774	-0.283
10	319	143.180	144.230	1.050
10	321	144.281	144.356	0.075
10	322	152.017	152.357	0.340
15	323	133.028	131.667	-1.361
15	325	144.198	143.591	-0.607
15	326	147.409	146.228	-1.181
15	328	145.749	145.280	-0.469
15	333	151.664	149.607	-2.057
30	334	155.867	155.034	-0.833
30	335	146.053	147.105	1.052
30	337	146.710	146.892	0.182
30	339	147.105	149.847	2.742
30	341	144.796	142.917	-1.879
35	342	142.946	140.949	-1.997
35	345	151.974	153.294	1.320
35	346	146.654	145.869	-0.785
35	347	153.314	155.449	2.135
35	348	139.422	139.774	0.352
50	349	140.730	142.590	1.860
50	350	152.568	154.855	2.287
50	351	155.481	158.243	2.762
50	353	151.005	151.707	0.702
50	354	144.532	143.151	-1.381
55	355	138.703	138.855	0.152
55	357	148.685	150.893	2.208
55	359	149.557	149.667	0.110
55	362	146.815	149.228	2.413
55	363	144.839	145.310	0.471
100	1	143.118	147.189	4.071
100	3	139.264	138.693	-0.571
100	4	153.961	155.624	1.663
100	5	156.736	161.558	4.822
100	6	148.734	150.485	1.751
100	7	140.098	145.208	5.110
100	8	138.608	140.101	1.493
100	9	146.251	149.733	3.482
100	10	152.940	153.789	0.849
100	11	157.457	160.845	3.388
100	12	139.695	138.797	-0.898
100	13	147.382	148.769	1.387
100	14	147.042	148.906	1.864
100	15	150.382	152.084	1.702
100	16	144.310	145.598	1.288
100	17	137.311	143.498	6.187
100	18	136.605	138.924	2.319
100	19	146.677	147.852	1.175
100	20	143.798	148.110	4.312
100	21	145.937	144.640	-1.297
100	22	149.202	161.503	12.301
100	24	144.708	143.059	-1.649
105	25	160.103	155.159	-4.944
105	26	157.216	159.582	2.366
105	27	150.495	155.424	4.929
105	28	148.090	148.707	0.617
105	29	152.103	156.642	4.539
	Max	160.103	161.558	12.301
	Average	146.927	148.026	1.098
	Min	133.028	131.667	-4.944
	Std Dev	5.659	6.272	2.380

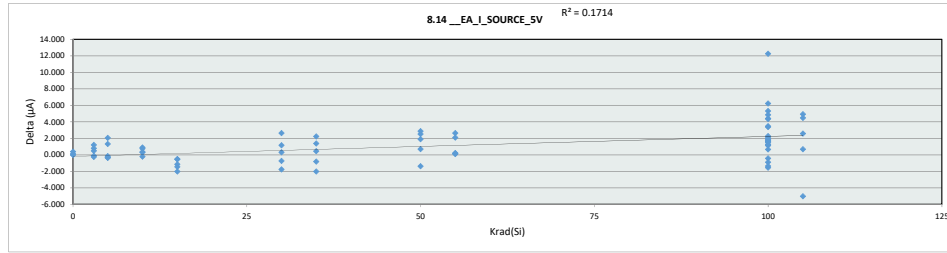


8.13 EA_I_SOURCE_4V											
Test Site											
Tester											
Test Number											
Max Limit	190	µA									
Min Limit	100	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	140.520	137.535	141.724	144.230	131.667	142.917	139.774	142.590	138.855	138.693	148.707
Average	144.270	145.580	147.977	148.544	143.275	148.359	147.067	150.109	146.791	148.408	155.103
Max	149.389	154.942	152.640	154.005	149.607	155.034	155.449	158.243	150.893	161.558	159.582
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

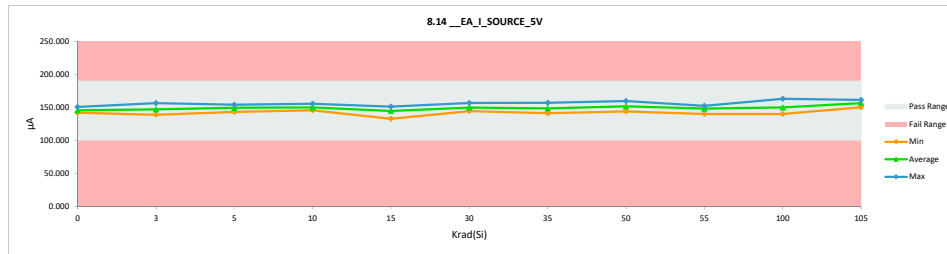


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8.14 EA_I_SOURCE_SV				
Test Site	Tester	Test Number	Unit	
			µA	µA
Max Limit			190	190
Min Limit			100	100
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	142.093	142.175	0.082
0	118	144.513	144.452	-0.061
0	122	150.599	150.945	0.346
3	301	139.294	139.135	-0.159
3	306	145.164	145.608	0.444
3	307	157.025	156.738	-0.287
3	308	142.933	144.098	1.165
3	309	149.845	150.601	0.756
5	310	152.804	152.615	-0.189
5	311	146.821	146.413	-0.408
5	312	152.905	154.185	1.280
5	314	149.490	151.501	2.011
5	316	143.649	143.307	-0.342
10	317	155.004	155.712	0.708
10	318	149.610	149.352	-0.258
10	319	144.979	145.840	0.861
10	321	145.904	146.166	0.262
10	322	153.706	154.012	0.306
15	323	134.533	133.037	-1.496
15	325	145.911	145.345	-0.566
15	326	149.016	147.835	-1.181
15	328	147.505	146.948	-0.557
15	333	153.323	151.268	-2.055
30	334	157.600	156.828	-0.772
30	335	147.670	148.799	1.129
30	337	148.311	148.581	0.270
30	339	148.680	151.288	2.608
30	341	146.434	144.637	-1.797
35	342	144.502	142.448	-2.054
35	345	153.720	155.064	1.344
35	346	148.306	147.442	-0.864
35	347	155.077	157.260	2.183
35	348	140.950	141.357	0.407
50	349	142.290	144.159	1.869
50	350	154.212	156.687	2.475
50	351	157.064	159.902	2.838
50	353	152.725	153.391	0.666
50	354	145.951	144.532	-1.419
55	355	140.275	140.337	0.062
55	357	150.492	152.545	2.053
55	359	151.244	151.304	0.140
55	362	148.453	151.047	2.594
55	363	146.547	146.722	0.175
100	1	144.636	148.970	4.334
100	3	140.985	140.517	-0.468
100	4	155.764	157.406	1.642
100	5	158.348	163.151	4.803
100	6	150.327	152.086	1.759
100	7	141.673	146.946	5.273
100	8	139.984	141.570	1.586
100	9	147.915	151.254	3.339
100	10	154.607	155.232	0.625
100	11	159.078	162.504	3.426
100	12	141.251	140.322	-0.929
100	13	149.197	150.643	1.446
100	14	148.778	150.799	2.021
100	15	151.887	153.777	1.890
100	16	145.935	147.165	1.230
100	17	139.011	145.183	6.182
100	18	138.212	140.444	2.232
100	19	148.344	149.433	1.089
100	20	145.300	149.660	4.360
100	21	147.469	146.080	-1.389
100	22	151.045	163.274	12.229
100	24	146.253	144.667	-1.586
105	25	161.804	156.732	-5.072
105	26	159.032	161.571	2.539
105	27	152.205	157.101	4.896
105	28	149.780	150.423	0.643
105	29	153.834	158.272	4.438
Max		161.804	163.274	12.229
Average		148.568	149.670	1.102
Min		134.533	133.037	-5.072
Std Dev		5.700	6.326	2.394

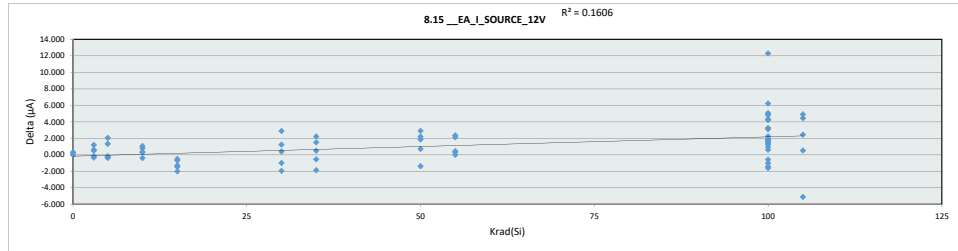


8.14 EA_I_SOURCE_SV											
Test Site	Tester	Test Number	Unit								
			µA	µA							
Max Limit			190	190							
Min Limit			100	100							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	142.175	139.135	143.307	145.840	133.037	144.637	141.357	144.159	140.337	140.322	150.423
Average	145.857	147.236	149.604	150.216	144.887	150.027	148.714	151.734	148.407	150.050	156.820
Max	150.945	156.738	154.185	155.712	151.268	156.828	157.260	159.902	152.545	163.274	161.571
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

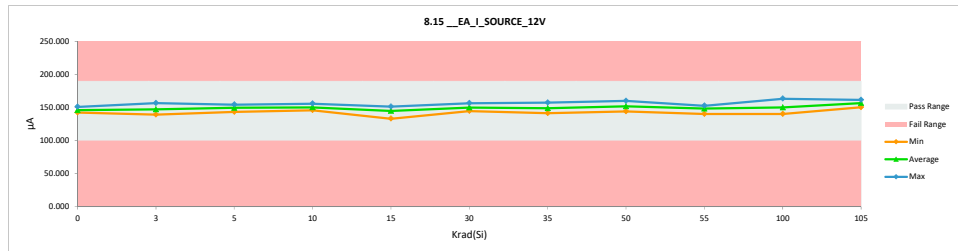


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8.15 EA_I_SOURCE_12V				
Test Site	Tester	Test Number	Unit	Max Limit
			µA	190
			µA	100
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	142.135	142.380	0.245
0	118	144.627	144.599	-0.028
0	122	150.706	151.002	0.296
3	301	139.397	139.222	-0.175
3	306	145.222	145.702	0.480
3	307	157.125	156.766	-0.359
3	308	143.073	144.263	1.190
3	309	149.999	150.606	0.607
5	310	152.896	152.720	-0.176
5	311	146.893	146.513	-0.380
5	312	153.091	154.412	1.321
5	314	149.513	151.544	2.031
5	316	143.736	143.388	-0.348
10	317	155.090	155.872	0.782
10	318	149.716	149.310	-0.406
10	319	144.884	145.932	1.048
10	321	146.015	146.349	0.334
10	322	153.753	154.062	0.309
15	323	134.583	133.101	-1.482
15	325	145.962	145.256	-0.706
15	326	149.228	147.931	-1.297
15	328	147.572	147.056	-0.516
15	333	153.421	151.410	-2.011
30	334	157.632	156.617	-1.015
30	335	147.710	148.930	1.220
30	337	148.323	148.705	0.382
30	339	148.656	151.522	2.866
30	341	146.637	144.682	-1.955
35	342	144.626	142.746	-1.880
35	345	153.797	155.291	1.494
35	346	148.229	147.665	-0.564
35	347	155.155	157.374	2.219
35	348	140.970	141.447	0.477
50	349	142.353	144.194	1.841
50	350	154.413	156.612	2.199
50	351	157.201	160.087	2.886
50	353	152.785	153.490	0.705
50	354	146.049	144.651	-1.398
55	355	140.309	140.283	-0.026
55	357	150.384	152.689	2.305
55	359	151.360	151.665	0.305
55	362	148.692	151.046	2.354
55	363	146.618	147.040	0.422
100	1	144.748	148.958	4.210
100	3	141.090	140.478	-0.612
100	4	155.838	157.577	1.739
100	5	158.555	163.372	4.817
100	6	150.331	152.203	1.872
100	7	141.789	146.837	5.048
100	8	140.122	141.469	1.347
100	9	147.964	151.058	3.094
100	10	154.788	155.388	0.600
100	11	159.298	162.542	3.244
100	12	141.292	140.263	-1.029
100	13	149.244	150.793	1.549
100	14	148.854	150.710	1.856
100	15	151.976	153.782	1.806
100	16	146.011	147.307	1.296
100	17	139.115	145.329	6.214
100	18	138.293	140.525	2.232
100	19	148.509	149.453	0.944
100	20	145.455	149.766	4.311
100	21	147.634	146.204	-1.430
100	22	151.083	163.357	12.274
100	24	146.383	144.779	-1.604
105	25	161.856	156.730	-5.126
105	26	159.174	161.597	2.423
105	27	152.222	157.104	4.882
105	28	149.924	150.430	0.506
105	29	153.898	158.317	4.419
	Max	161.856	163.372	12.274
	Average	148.660	149.749	1.090
	Min	134.583	133.101	-5.126
	Std Dev	5.711	6.329	2.386

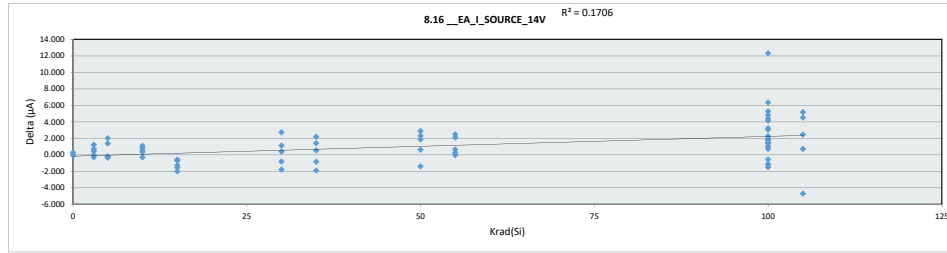


8.15 EA_I_SOURCE_12V															
Test Site	Tester	Test Number	Max Limit	Min Limit	0	3	5	10	15	30	35	50	55	100	105
			190	100	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
			µA	µA	142.380	139.222	143.388	145.932	133.101	144.682	141.447	144.194	140.283	140.263	150.430
					Average	145.994	147.312	149.715	150.305	144.951	150.091	148.905	151.807	148.545	150.098
					Max	151.002	156.766	154.412	155.872	151.410	156.617	157.374	160.087	152.689	163.372
					UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

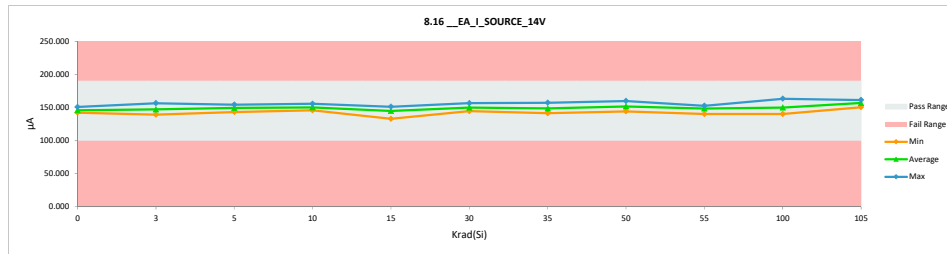


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8.16 EA_I_SOURCE_14V				
Test Site				
Tester				
Test Number				
Unit	µA	µA		
Max Limit	190	190		
Min Limit	100	100		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	142.073	142.329	0.256
0	118	144.581	144.510	-0.071
0	122	150.675	150.922	0.247
3	301	139.388	139.346	-0.042
3	306	145.210	145.630	0.420
3	307	156.983	156.675	-0.308
3	308	142.989	144.183	1.194
3	309	149.918	150.609	0.691
5	310	152.919	152.639	-0.280
5	311	146.755	146.536	-0.219
5	312	153.074	154.433	1.359
5	314	149.431	151.422	1.991
5	316	143.649	143.286	-0.363
10	317	154.993	155.814	0.821
10	318	149.658	149.337	-0.321
10	319	144.784	145.854	1.070
10	321	145.973	146.304	0.331
10	322	153.659	154.154	0.495
15	323	134.582	133.028	-1.554
15	325	145.921	145.201	-0.720
15	326	149.136	147.873	-1.263
15	328	147.503	146.909	-0.594
15	333	153.286	151.267	-2.019
30	334	157.552	156.720	-0.832
30	335	147.703	148.816	1.113
30	337	148.287	148.668	0.381
30	339	148.697	151.408	2.711
30	341	146.457	144.656	-1.801
35	342	144.542	142.618	-1.924
35	345	153.708	155.111	1.403
35	346	148.310	147.459	-0.851
35	347	155.110	157.275	2.165
35	348	140.946	141.463	0.517
50	349	142.335	144.199	1.864
50	350	154.302	156.598	2.296
50	351	157.112	159.988	2.876
50	353	152.723	153.323	0.600
50	354	145.945	144.522	-1.423
55	355	140.347	140.280	-0.067
55	357	150.628	152.706	2.078
55	359	151.420	151.653	0.233
55	362	148.566	151.027	2.461
55	363	146.549	147.187	0.638
100	1	144.659	148.790	4.131
100	3	141.165	140.574	-0.591
100	4	155.681	157.493	1.812
100	5	158.505	163.304	4.799
100	6	150.294	152.186	1.892
100	7	141.747	147.014	5.267
100	8	140.053	141.447	1.394
100	9	147.882	151.081	3.199
100	10	154.520	155.235	0.715
100	11	159.199	162.249	3.050
100	12	141.407	140.270	-1.137
100	13	149.177	150.541	1.364
100	14	148.761	150.683	1.922
100	15	152.074	153.808	1.734
100	16	145.949	147.410	1.461
100	17	139.077	145.411	6.334
100	18	138.219	140.468	2.249
100	19	148.358	149.390	1.032
100	20	145.291	149.674	4.383
100	21	147.464	146.264	-1.200
100	22	151.061	163.353	12.292
100	24	146.309	144.802	-1.507
105	25	161.801	157.082	-4.719
105	26	159.062	161.513	2.451
105	27	152.115	157.266	5.151
105	28	149.811	150.501	0.690
105	29	153.828	158.328	4.500
Max		161.801	163.353	12.292
Average		148.598	149.715	1.118
Min		134.582	133.028	-4.719
Std Dev		5.693	6.320	2.381

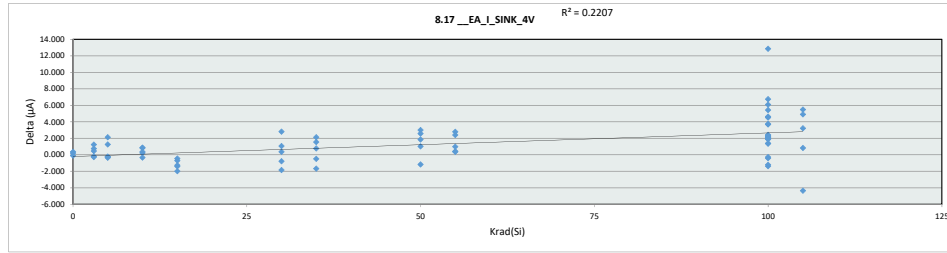


8.16 EA_I_SOURCE_14V											
Test Site											
Tester											
Test Number											
Max Limit	190	µA									
Min Limit	100	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	142.329	139.346	143.286	145.854	133.028	144.656	141.463	144.199	140.280	140.270	150.501
Average	145.920	147.289	149.663	150.293	144.856	150.054	148.785	151.726	148.571	150.066	156.938
Max	150.922	156.675	154.433	155.814	151.267	156.720	157.275	159.988	152.706	163.353	161.513
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

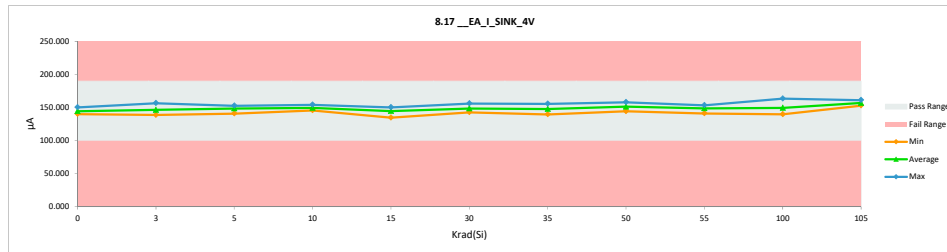


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8.17 EA I SINK 4V				
Test Site				
Tester				
Test Number				
Unit	µA	µA		
Max Limit	190	190		
Min Limit	100	100		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	140.063	140.260	0.197
0	118	143.126	143.006	-0.120
0	122	149.960	150.307	0.347
3	301	139.023	138.834	-0.189
3	306	144.192	144.640	0.448
3	307	156.999	156.699	-0.300
3	308	142.378	143.604	1.226
3	309	148.512	149.242	0.730
5	310	153.018	152.783	-0.235
5	311	144.982	144.612	-0.370
5	312	151.579	152.822	1.243
5	314	150.012	152.125	2.113
5	316	141.261	140.986	-0.275
10	317	153.007	153.816	0.809
10	318	148.352	148.006	-0.346
10	319	144.941	145.797	0.856
10	321	146.110	146.450	0.340
10	322	154.090	154.309	0.219
15	323	136.132	134.723	-1.409
15	325	145.206	144.483	-0.723
15	326	149.032	147.747	-1.285
15	328	146.999	146.530	-0.469
15	333	152.342	150.340	-2.002
30	334	156.915	156.111	-0.804
30	335	146.063	147.115	1.052
30	337	146.819	147.167	0.348
30	339	147.485	150.275	2.790
30	341	144.711	142.840	-1.871
35	342	141.445	139.762	-1.683
35	345	152.526	154.058	1.532
35	346	150.945	150.432	-0.513
35	347	153.475	155.585	2.110
35	348	139.429	140.189	0.760
50	349	142.575	144.420	1.845
50	350	154.158	156.709	2.551
50	351	155.153	158.149	2.996
50	353	151.866	152.868	1.002
50	354	146.130	144.939	-1.191
55	355	140.832	141.241	0.409
55	357	151.079	153.470	2.391
55	359	150.548	150.894	0.346
55	362	148.058	150.831	2.773
55	363	146.870	147.843	0.973
100	1	145.915	150.416	4.501
100	3	142.102	141.667	-0.435
100	4	154.082	156.194	2.112
100	5	155.343	160.761	5.418
100	6	151.402	153.692	2.290
100	7	142.177	148.256	6.079
100	8	139.562	141.582	2.020
100	9	146.249	149.924	3.675
100	10	153.983	155.326	1.343
100	11	157.207	160.925	3.718
100	12	141.211	140.920	-0.291
100	13	144.964	146.820	1.856
100	14	147.055	149.259	2.204
100	15	153.942	155.956	2.014
100	16	147.441	149.424	1.983
100	17	137.928	144.656	6.728
100	18	137.430	139.831	2.401
100	19	146.141	147.512	1.371
100	20	143.052	147.677	4.625
100	21	145.360	144.141	-1.219
100	22	150.630	163.478	12.848
100	24	145.418	144.018	-1.400
105	25	160.446	156.082	-4.364
105	26	157.984	161.188	3.204
105	27	151.979	157.448	5.469
105	28	152.179	152.990	0.811
105	29	152.530	157.413	4.883
	Max	160.446	163.478	12.848
	Average	147.887	149.208	1.321
	Min	136.132	134.723	-4.364
	Std Dev	5.580	6.266	2.493

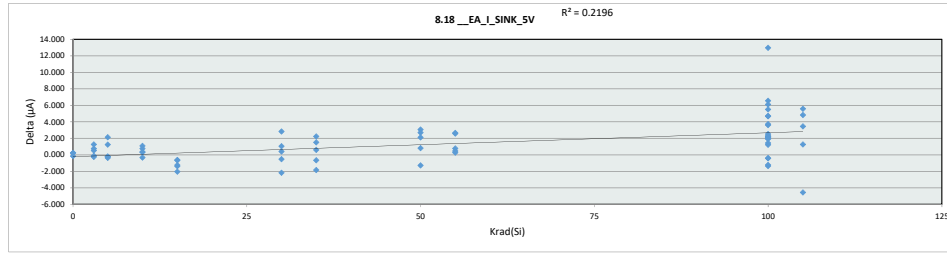


8.17 EA I SINK 4V											
Test Site											
Tester											
Test Number											
Max Limit	190	µA									
Min Limit	100	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	140.260	138.834	140.986	145.797	134.723	142.840	139.762	144.420	141.241	139.831	152.990
Average	144.524	146.604	148.666	149.676	144.765	148.702	148.005	151.417	148.856	149.656	157.024
Max	150.307	156.699	152.822	154.309	150.340	156.111	155.585	158.149	153.470	163.478	161.188
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

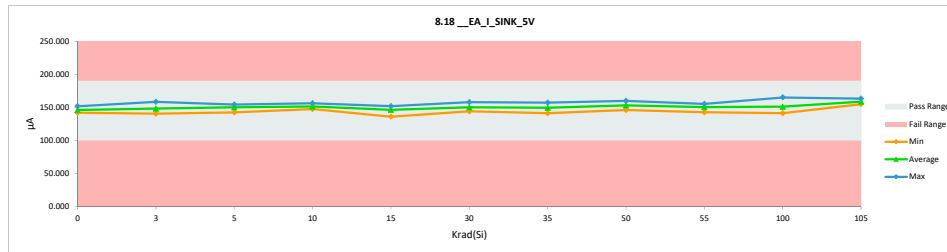


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8.18 EA I SINK 5V				
Test Site	Tester	Test Number	Unit	
			µA	µA
Max Limit	190		190	
Min Limit	100		100	
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	141.911	142.115	0.204
0	118	144.848	144.677	-0.171
0	122	151.767	151.975	0.208
3	301	140.768	140.638	-0.130
3	306	145.814	146.311	0.497
3	307	158.914	158.639	-0.275
3	308	143.995	145.244	1.249
3	309	150.172	150.918	0.746
5	310	154.843	154.603	-0.240
5	311	146.753	146.368	-0.385
5	312	153.347	154.578	1.231
5	314	151.855	153.971	2.116
5	316	142.912	142.685	-0.227
10	317	154.760	155.489	0.729
10	318	150.140	149.795	-0.345
10	319	146.623	147.681	1.058
10	321	147.962	148.263	0.301
10	322	156.004	156.324	0.320
15	323	137.709	136.305	-1.404
15	325	147.036	146.357	-0.679
15	326	150.800	149.545	-1.255
15	328	148.839	148.205	-0.634
15	333	154.179	152.122	-2.057
30	334	158.566	158.027	-0.539
30	335	147.841	148.878	1.037
30	337	148.504	148.897	0.393
30	339	149.065	151.877	2.812
30	341	146.533	144.340	-2.193
35	342	143.241	141.368	-1.873
35	345	154.549	156.045	1.496
35	346	152.710	152.035	-0.675
35	347	155.257	157.466	2.209
35	348	141.218	141.802	0.584
50	349	144.163	146.252	2.089
50	350	155.849	158.531	2.682
50	351	157.056	160.096	3.040
50	353	153.777	154.564	0.787
50	354	147.776	146.476	-1.300
55	355	142.582	142.840	0.258
55	357	152.852	155.403	2.551
55	359	152.428	152.868	0.440
55	362	149.895	152.530	2.635
55	363	148.645	149.393	0.748
100	1	147.608	152.306	4.698
100	3	143.966	143.547	-0.419
100	4	155.897	157.968	2.071
100	5	157.232	162.724	5.492
100	6	153.196	155.457	2.261
100	7	143.974	150.070	6.096
100	8	141.321	143.221	1.900
100	9	148.014	151.599	3.585
100	10	155.839	157.035	1.196
100	11	159.067	162.771	3.704
100	12	142.883	142.449	-0.434
100	13	146.793	148.808	2.015
100	14	148.962	151.162	2.200
100	15	155.734	157.934	2.200
100	16	149.212	151.207	1.995
100	17	139.689	146.218	6.529
100	18	139.016	141.511	2.495
100	19	147.881	149.294	1.413
100	20	144.741	149.394	4.653
100	21	147.079	145.841	-1.238
100	22	152.387	165.350	12.963
100	24	147.196	145.814	-1.382
105	25	162.390	157.814	-4.576
105	26	160.027	163.466	3.439
105	27	153.779	159.344	5.565
105	28	153.858	155.105	1.247
105	29	154.282	159.099	4.817
Max		162.390	165.350	12.963
Average		149.664	150.986	1.322
Min		137.709	136.305	-4.576
Std Dev		5.630	6.347	2.528

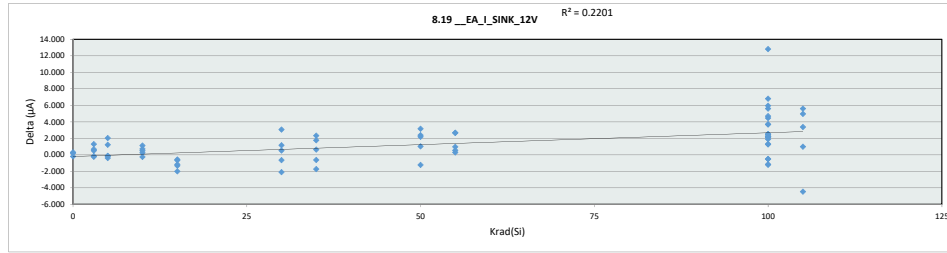


8.18 EA I SINK 5V											
Test Site	Tester	Test Number	Unit								
			190	µA							
Max Limit	190		190	µA							
Min Limit	100		100	µA							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	142.115	140.638	142.685	147.681	136.305	144.340	141.368	146.252	142.840	141.511	155.105
Average	146.256	148.350	150.441	151.510	146.507	150.404	149.743	153.184	150.607	151.440	158.966
Max	151.975	158.639	154.603	156.324	152.122	158.027	157.466	160.096	155.403	165.350	163.466
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

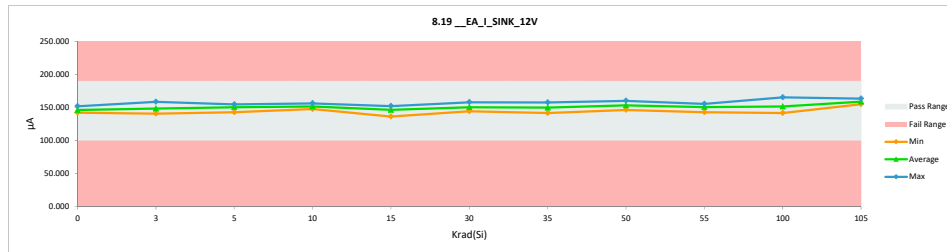


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8.19 EA_I_SINK_12V					
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit
			µA	190	100
			µA	190	100
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta	
0	117	142.008	142.172	0.164	
0	118	145.069	144.841	-0.228	
0	122	151.747	152.024	0.277	
3	301	140.874	140.670	-0.204	
3	306	145.911	146.371	0.460	
3	307	158.971	158.706	-0.265	
3	308	144.182	145.444	1.262	
3	309	150.335	150.971	0.636	
5	310	154.942	154.741	-0.201	
5	311	146.930	146.503	-0.427	
5	312	153.549	154.741	1.192	
5	314	151.926	153.918	1.992	
5	316	143.002	142.850	-0.152	
10	317	154.926	155.596	0.670	
10	318	150.194	149.901	-0.293	
10	319	146.663	147.757	1.094	
10	321	148.114	148.580	0.466	
10	322	156.128	156.350	0.222	
15	323	137.730	136.365	-1.365	
15	325	147.163	146.457	-0.706	
15	326	150.806	149.600	-1.206	
15	328	148.965	148.359	-0.606	
15	333	154.283	152.247	-2.036	
30	334	158.712	158.026	-0.686	
30	335	147.921	149.045	1.124	
30	337	148.546	149.035	0.489	
30	339	149.157	152.178	3.021	
30	341	146.577	144.439	-2.138	
35	342	143.404	141.640	-1.764	
35	345	154.621	156.341	1.720	
35	346	152.808	152.154	-0.654	
35	347	155.358	157.650	2.292	
35	348	141.307	141.913	0.606	
50	349	144.274	146.432	2.158	
50	350	156.152	158.500	2.348	
50	351	157.061	160.185	3.124	
50	353	153.715	154.707	0.992	
50	354	147.947	146.677	-1.270	
55	355	142.657	142.908	0.251	
55	357	152.945	155.565	2.620	
55	359	152.568	153.075	0.507	
55	362	150.036	152.679	2.643	
55	363	148.726	149.713	0.937	
100	1	147.775	152.195	4.420	
100	3	144.100	143.565	-0.535	
100	4	155.957	158.115	2.158	
100	5	157.337	162.904	5.567	
100	6	153.270	155.737	2.467	
100	7	144.203	150.124	5.921	
100	8	141.267	143.138	1.871	
100	9	148.003	151.652	3.649	
100	10	155.917	157.129	1.212	
100	11	159.167	162.837	3.670	
100	12	142.951	142.406	-0.545	
100	13	146.977	149.043	2.066	
100	14	149.048	151.285	2.237	
100	15	155.797	157.965	2.168	
100	16	149.254	151.443	2.189	
100	17	139.731	146.501	6.770	
100	18	139.149	141.626	2.477	
100	19	148.062	149.338	1.276	
100	20	144.856	149.517	4.661	
100	21	147.252	146.061	-1.191	
100	22	152.612	165.403	12.791	
100	24	147.398	146.149	-1.249	
105	25	162.475	157.981	-4.494	
105	26	160.129	163.461	3.332	
105	27	153.836	159.413	5.577	
105	28	154.150	155.107	0.957	
105	29	154.307	159.235	4.928	
	Max	162.475	165.403	12.791	
	Average	149.771	151.105	1.335	
	Min	137.730	136.365	-4.494	
	Std Dev	5.630	6.338	2.514	

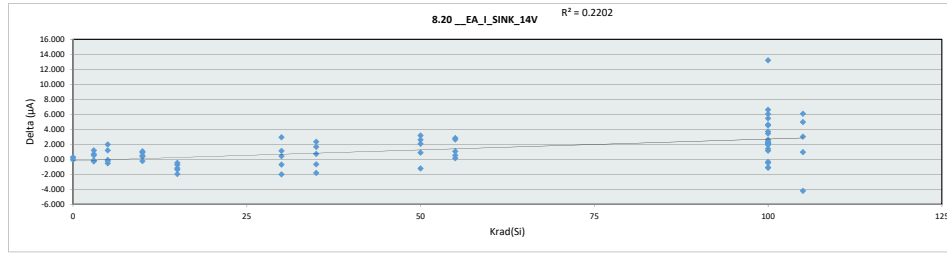


8.19 EA_I_SINK_12V											
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit						
			µA	190	100						
			µA	190	100						
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	142.172	140.670	142.850	147.757	136.365	144.439	141.640	146.432	142.908	141.626	155.107
Average	146.346	148.432	150.551	151.637	146.606	150.545	149.940	153.300	150.788	151.552	159.039
Max	152.024	158.706	154.741	156.350	152.247	158.026	157.650	160.185	155.565	165.403	163.461
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

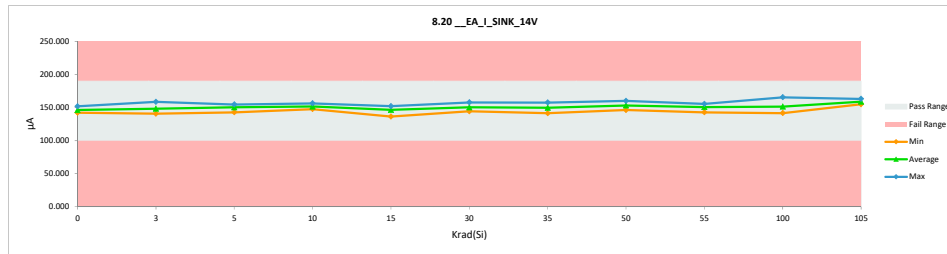


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		8.20 EA_I_SINK_14V		
Test Site				
Tester				
Test Number				
Unit		µA	µA	
Max Limit		190	190	
Min Limit		100	100	
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	141.912	142.155	0.243
0	118	144.842	144.766	-0.076
0	122	151.692	151.933	0.241
3	301	140.882	140.741	-0.141
3	306	145.883	146.387	0.504
3	307	158.918	158.629	-0.289
3	308	144.156	145.330	1.174
3	309	150.227	150.916	0.689
5	310	154.871	154.631	-0.240
5	311	146.950	146.388	-0.562
5	312	153.494	154.660	1.166
5	314	151.769	153.742	1.973
5	316	142.858	142.763	-0.095
10	317	154.816	155.724	0.908
10	318	150.179	149.934	-0.245
10	319	146.604	147.657	1.053
10	321	148.142	148.511	0.369
10	322	155.902	156.363	0.461
15	323	137.742	136.357	-1.385
15	325	147.061	146.323	-0.738
15	326	150.751	149.566	-1.185
15	328	148.790	148.293	-0.497
15	333	154.159	152.181	-1.978
30	334	158.590	157.860	-0.730
30	335	147.838	148.957	1.119
30	337	148.481	148.878	0.397
30	339	149.101	152.032	2.931
30	341	146.358	144.341	-2.017
35	342	143.328	141.497	-1.831
35	345	154.530	156.183	1.653
35	346	152.726	152.039	-0.687
35	347	155.207	157.540	2.333
35	348	141.274	141.983	0.709
50	349	144.251	146.307	2.056
50	350	156.001	158.595	2.594
50	351	156.960	160.126	3.166
50	353	153.652	154.526	0.874
50	354	147.817	146.570	-1.247
55	355	142.662	142.799	0.137
55	357	152.926	155.557	2.631
55	359	152.473	152.995	0.522
55	362	149.972	152.781	2.809
55	363	148.806	149.844	1.038
100	1	147.620	152.131	4.511
100	3	144.022	143.528	-0.494
100	4	155.924	158.047	2.123
100	5	157.327	162.770	5.443
100	6	153.281	155.552	2.271
100	7	144.110	150.136	6.026
100	8	141.235	143.141	1.906
100	9	148.057	151.463	3.406
100	10	155.879	157.005	1.126
100	11	159.044	162.737	3.693
100	12	142.951	142.590	-0.361
100	13	146.756	148.862	2.106
100	14	148.910	151.226	2.316
100	15	155.785	157.912	2.127
100	16	149.210	151.326	2.116
100	17	139.814	146.415	6.601
100	18	139.066	141.641	2.575
100	19	147.835	149.219	1.384
100	20	144.766	149.381	4.615
100	21	147.126	146.017	-1.109
100	22	152.394	165.589	13.195
100	24	147.159	146.024	-1.135
105	25	162.361	158.143	-4.218
105	26	160.099	163.102	3.003
105	27	153.686	159.756	6.070
105	28	154.084	155.022	0.938
105	29	154.275	159.233	4.958
Max		162.361	165.589	13.195
Average		149.690	151.048	1.357
Min		137.742	136.357	-4.218
Std Dev		5.618	6.336	2.526

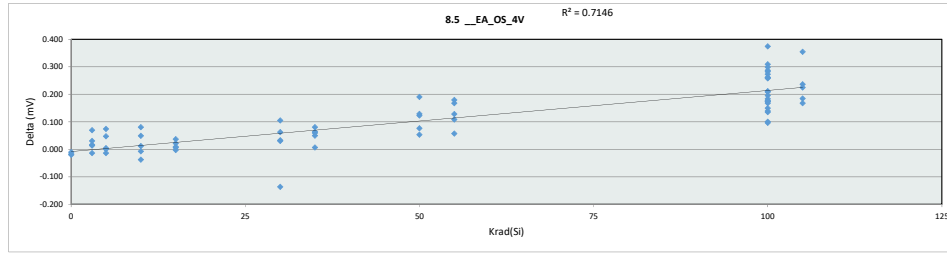


		8.20 EA_I_SINK_14V									
Test Site											
Tester											
Test Number											
Unit		µA	µA								
Max Limit		190	190								
Min Limit		100	100								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	142.155	140.741	142.763	147.657	136.357	144.341	141.497	146.307	142.799	141.641	155.022
Average	146.285	148.401	150.437	151.638	146.544	150.414	149.848	153.225	150.795	151.487	159.051
Max	151.933	158.629	154.660	156.363	152.181	157.860	157.540	160.126	155.557	165.589	163.102
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

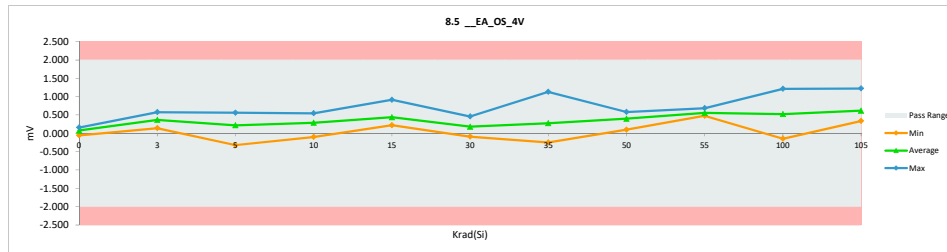


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8.5 EA_OS_4V				
Test Site				
Tester				
Test Number				
Unit	mV		mV	
Max Limit	2		2	
Min Limit	-2		-2	
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	-0.036	-0.054	-0.018
0	118	0.137	0.126	-0.011
0	122	0.178	0.157	-0.021
3	301	0.566	0.578	0.013
3	306	0.369	0.384	0.016
3	307	0.389	0.458	0.069
3	308	0.111	0.141	0.029
3	309	0.286	0.271	-0.015
5	310	0.558	0.562	0.003
5	311	0.083	0.082	-0.001
5	312	0.201	0.248	0.046
5	314	0.541	0.526	-0.015
5	316	-0.394	-0.321	0.073
10	317	-0.061	-0.100	-0.039
10	318	-0.009	0.040	0.048
10	319	0.431	0.510	0.079
10	321	0.535	0.546	0.011
10	322	0.458	0.450	-0.008
15	323	0.894	0.915	0.021
15	325	0.312	0.317	0.005
15	326	0.233	0.269	0.036
15	328	0.470	0.478	0.008
15	333	0.225	0.221	-0.004
30	334	0.326	0.358	0.031
30	335	0.164	0.194	0.029
30	337	0.095	0.127	0.032
30	339	0.116	0.220	0.104
30	341	0.048	-0.090	-0.138
35	342	-0.298	-0.250	0.049
35	345	0.200	0.280	0.080
35	346	1.072	1.131	0.059
35	347	0.018	0.081	0.063
35	348	0.126	0.132	0.006
50	349	0.365	0.554	0.189
50	350	0.475	0.550	0.075
50	351	-0.029	0.099	0.128
50	353	0.166	0.218	0.052
50	354	0.458	0.580	0.122
55	355	0.479	0.606	0.127
55	357	0.345	0.524	0.179
55	359	0.386	0.493	0.107
55	362	0.421	0.478	0.056
55	363	0.518	0.685	0.167
100	1	0.719	0.819	0.099
100	3	0.813	0.909	0.095
100	4	0.087	0.270	0.182
100	5	-0.179	0.082	0.261
100	6	0.793	0.988	0.195
100	7	0.548	0.833	0.286
100	8	0.353	0.610	0.257
100	9	0.162	0.336	0.174
100	10	0.308	0.581	0.272
100	11	0.129	0.336	0.207
100	12	0.607	0.867	0.260
100	13	-0.456	-0.148	0.308
100	14	0.235	0.402	0.167
100	15	0.773	0.922	0.149
100	16	0.840	1.213	0.373
100	17	0.257	0.428	0.172
100	18	0.127	0.337	0.211
100	19	-0.105	0.193	0.298
100	20	-0.024	0.152	0.176
100	21	-0.046	0.088	0.134
100	22	0.522	0.803	0.281
100	24	0.379	0.517	0.138
105	25	0.219	0.403	0.184
105	26	0.193	0.547	0.353
105	27	0.342	0.577	0.236
105	28	1.055	1.222	0.167
105	29	0.112	0.335	0.224
	Max	1.072	1.222	0.373
	Average	0.297	0.407	0.110
	Min	-0.456	-0.321	-0.138
	Std Dev	0.308	0.330	0.108

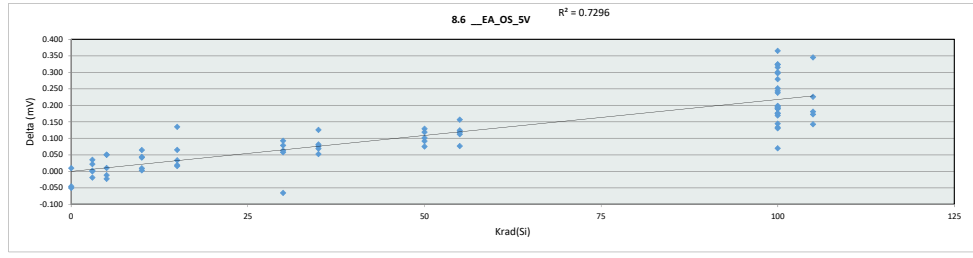


8.5 EA_OS_4V											
Test Site											
Tester											
Test Number											
Max Limit	2										
Min Limit	-2										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	-0.055	0.141	-0.321	-0.100	0.221	-0.090	-0.250	0.099	0.478	-0.148	0.335
Average	0.076	0.367	0.219	0.289	0.440	0.182	0.275	0.400	0.557	0.524	0.617
Max	0.157	0.578	0.562	0.546	0.915	0.458	1.131	0.580	0.685	1.213	1.222
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000

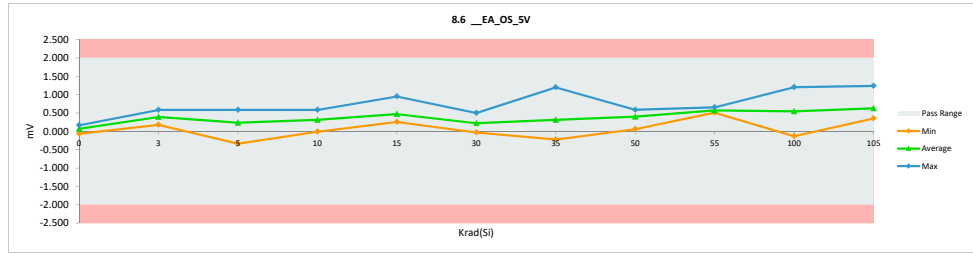


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8.6 EA_OS_5V				
Test Site	Serial #	PRE_HDR	POST_HDR	Delta
0	117	-0.023	-0.069	-0.046
0	118	0.109	0.119	0.009
0	122	0.214	0.163	-0.051
3	301	0.600	0.580	-0.019
3	306	0.423	0.424	0.001
3	307	0.440	0.461	0.021
3	308	0.140	0.174	0.035
3	309	0.312	0.312	-0.001
5	310	0.531	0.581	0.049
5	311	0.120	0.130	0.010
5	312	0.252	0.228	-0.024
5	314	0.521	0.571	0.050
5	316	-0.324	-0.336	-0.012
10	317	-0.018	-0.009	0.009
10	318	-0.020	0.023	0.043
10	319	0.490	0.492	0.002
10	321	0.542	0.583	0.041
10	322	0.413	0.477	0.064
15	323	0.934	0.950	0.016
15	325	0.303	0.336	0.033
15	326	0.192	0.326	0.135
15	328	0.455	0.474	0.018
15	333	0.188	0.252	0.065
30	334	0.419	0.497	0.078
30	335	0.167	0.225	0.057
30	337	0.095	0.157	0.062
30	339	0.150	0.243	0.092
30	341	0.034	-0.032	-0.066
35	342	-0.349	-0.223	0.125
35	345	0.197	0.272	0.075
35	346	1.114	1.195	0.081
35	347	0.056	0.124	0.068
35	348	0.139	0.191	0.052
50	349	0.427	0.528	0.101
50	350	0.479	0.554	0.075
50	351	-0.072	0.057	0.129
50	353	0.181	0.272	0.091
50	354	0.465	0.584	0.118
55	355	0.523	0.635	0.112
55	357	0.436	0.512	0.076
55	359	0.384	0.508	0.124
55	362	0.385	0.542	0.156
55	363	0.535	0.653	0.118
100	1	0.755	0.824	0.069
100	3	0.811	0.955	0.144
100	4	0.135	0.324	0.189
100	5	-0.184	0.060	0.244
100	6	0.757	1.054	0.297
100	7	0.535	0.900	0.365
100	8	0.304	0.628	0.324
100	9	0.171	0.346	0.175
100	10	0.299	0.537	0.238
100	11	0.140	0.337	0.197
100	12	0.632	0.932	0.300
100	13	-0.412	-0.133	0.279
100	14	0.234	0.423	0.189
100	15	0.772	0.948	0.176
100	16	0.880	1.203	0.323
100	17	0.219	0.470	0.251
100	18	0.152	0.345	0.193
100	19	-0.091	0.224	0.315
100	20	-0.001	0.168	0.169
100	21	-0.055	0.078	0.133
100	22	0.592	0.790	0.198
100	24	0.374	0.504	0.130
105	25	0.206	0.387	0.180
105	26	0.229	0.574	0.345
105	27	0.356	0.581	0.225
105	28	1.094	1.236	0.142
105	29	0.178	0.350	0.172
Max		1.114	1.236	0.365
Average		0.309	0.425	0.116
Min		-0.412	-0.336	-0.066
Std Dev		0.311	0.333	0.104

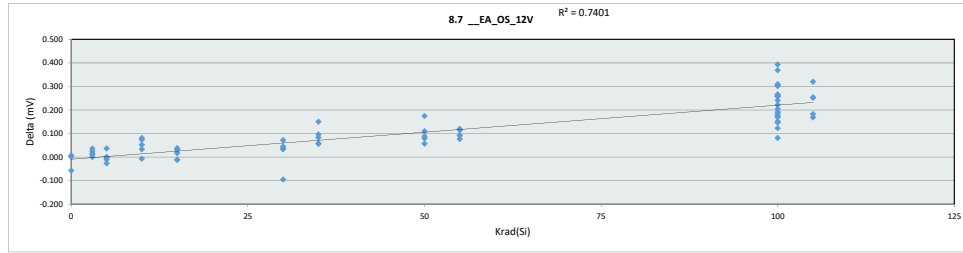


8.6 EA_OS_5V											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	-0.069	0.174	-0.336	-0.010	0.252	-0.032	-0.224	0.057	0.508	-0.133	0.350
Average	0.071	0.390	0.235	0.313	0.468	0.218	0.312	0.399	0.570	0.542	0.626
Max	0.163	0.580	0.581	0.583	0.950	0.497	1.195	0.584	1.203	1.236	
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000

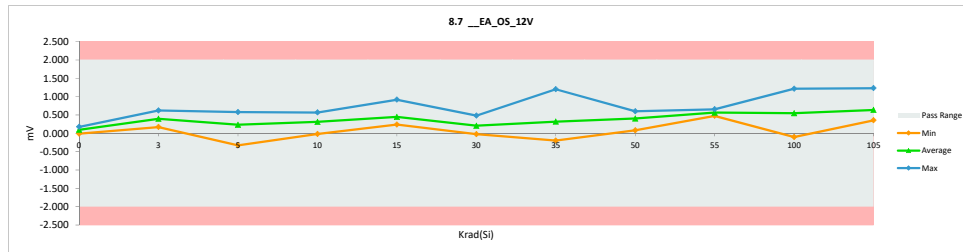


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8.7 EA_OS_12V				
Test Site				
Tester				
Test Number				
Unit	mV		mV	
Max Limit	2		2	
Min Limit	-2		-2	
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	-0.011	-0.009	0.001
0	118	0.115	0.121	0.006
0	122	0.232	0.175	-0.058
3	301	0.584	0.620	0.036
3	306	0.418	0.418	0.000
3	307	0.445	0.456	0.011
3	308	0.154	0.170	0.016
3	309	0.294	0.319	0.025
5	310	0.512	0.549	0.036
5	311	0.128	0.117	-0.011
5	312	0.245	0.246	0.001
5	314	0.578	0.576	-0.002
5	316	-0.301	-0.327	-0.027
10	317	-0.012	-0.020	-0.007
10	318	-0.040	0.040	0.080
10	319	0.430	0.504	0.073
10	321	0.534	0.567	0.033
10	322	0.408	0.459	0.052
15	323	0.927	0.915	-0.012
15	325	0.303	0.292	-0.011
15	326	0.278	0.309	0.030
15	328	0.471	0.488	0.017
15	333	0.199	0.237	0.038
30	334	0.446	0.483	0.037
30	335	0.150	0.222	0.072
30	337	0.112	0.145	0.033
30	339	0.162	0.208	0.045
30	341	0.070	-0.025	-0.095
35	342	-0.347	-0.198	0.150
35	345	0.190	0.273	0.083
35	346	1.107	1.203	0.096
35	347	0.065	0.121	0.056
35	348	0.133	0.191	0.058
50	349	0.420	0.477	0.057
50	350	0.426	0.600	0.174
50	351	-0.031	0.079	0.110
50	353	0.210	0.289	0.079
50	354	0.485	0.573	0.088
55	355	0.537	0.629	0.092
55	357	0.429	0.505	0.076
55	359	0.362	0.476	0.114
55	362	0.435	0.554	0.119
55	363	0.562	0.654	0.092
100	1	0.749	0.872	0.123
100	3	0.779	0.949	0.170
100	4	0.155	0.334	0.178
100	5	-0.162	0.100	0.262
100	6	0.744	1.045	0.301
100	7	0.527	0.896	0.369
100	8	0.373	0.639	0.266
100	9	0.186	0.335	0.148
100	10	0.333	0.575	0.241
100	11	0.152	0.356	0.204
100	12	0.651	0.908	0.257
100	13	-0.409	-0.104	0.305
100	14	0.233	0.380	0.147
100	15	0.723	0.944	0.222
100	16	0.823	1.216	0.393
100	17	0.249	0.436	0.187
100	18	0.156	0.347	0.191
100	19	-0.080	0.229	0.310
100	20	0.018	0.169	0.151
100	21	-0.031	0.050	0.081
100	22	0.562	0.820	0.258
100	24	0.351	0.520	0.169
105	25	0.187	0.439	0.252
105	26	0.227	0.547	0.320
105	27	0.368	0.621	0.253
105	28	1.048	1.230	0.182
105	29	0.185	0.353	0.168
	Max	1.107	1.230	0.393
	Average	0.313	0.427	0.114
	Min	-0.409	-0.327	-0.095
	Std Dev	0.300	0.330	0.109

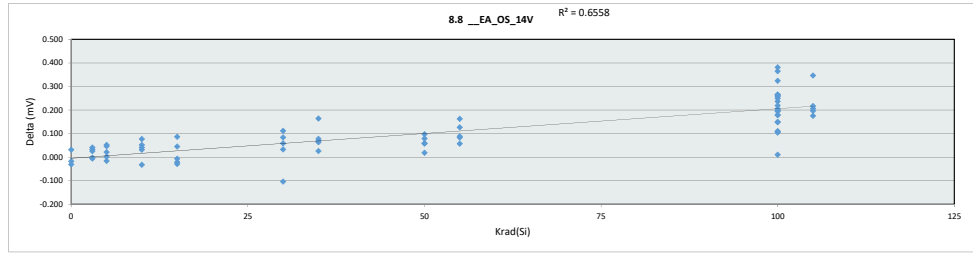


8.7 EA_OS_12V											
Test Site											
Tester											
Test Number											
Max Limit	2										
Min Limit	-2										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	-0.009	0.170	-0.327	-0.020	0.237	-0.025	-0.198	0.079	0.476	-0.104	0.353
Average	0.096	0.397	0.232	0.310	0.448	0.206	0.318	0.404	0.564	0.546	0.638
Max	0.175	0.620	0.576	0.567	0.915	0.483	1.203	0.600	0.654	1.216	1.230
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000

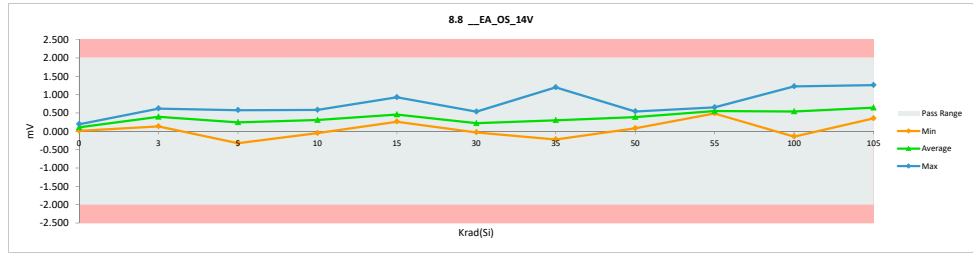


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8.8 EA_OS_14V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	2	2		
Min Limit	-2	-2		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	-0.018	0.013	0.031
0	118	0.140	0.123	-0.018
0	122	0.224	0.192	-0.032
3	301	0.594	0.619	0.025
3	306	0.425	0.419	-0.006
3	307	0.448	0.480	0.033
3	308	0.137	0.134	-0.003
3	309	0.294	0.335	0.041
5	310	0.575	0.576	0.001
5	311	0.075	0.122	0.046
5	312	0.242	0.293	0.052
5	314	0.540	0.561	0.021
5	316	-0.308	-0.325	-0.017
10	317	-0.017	-0.050	-0.033
10	318	-0.061	0.016	0.076
10	319	0.459	0.490	0.031
10	321	0.531	0.583	0.052
10	322	0.457	0.497	0.040
15	323	0.935	0.927	-0.007
15	325	0.320	0.299	-0.021
15	326	0.264	0.309	0.044
15	328	0.525	0.496	-0.029
15	333	0.175	0.262	0.086
30	334	0.423	0.534	0.111
30	335	0.176	0.235	0.058
30	337	0.090	0.173	0.083
30	339	0.156	0.188	0.033
30	341	0.073	-0.031	-0.104
35	342	-0.385	-0.221	0.163
35	345	0.192	0.255	0.063
35	346	1.129	1.197	0.068
35	347	0.041	0.118	0.077
35	348	0.137	0.163	0.025
50	349	0.442	0.521	0.079
50	350	0.509	0.527	0.018
50	351	-0.015	0.082	0.097
50	353	0.210	0.269	0.058
50	354	0.484	0.542	0.057
55	355	0.525	0.613	0.088
55	357	0.430	0.486	0.057
55	359	0.407	0.490	0.083
55	362	0.395	0.521	0.126
55	363	0.489	0.651	0.162
100	1	0.768	0.778	0.010
100	3	0.837	0.941	0.104
100	4	0.142	0.347	0.204
100	5	-0.156	0.108	0.264
100	6	0.723	1.047	0.324
100	7	0.510	0.891	0.381
100	8	0.354	0.615	0.261
100	9	0.183	0.376	0.194
100	10	0.314	0.571	0.257
100	11	0.165	0.364	0.199
100	12	0.679	0.929	0.249
100	13	-0.408	-0.143	0.265
100	14	0.229	0.408	0.179
100	15	0.799	0.948	0.149
100	16	0.859	1.223	0.365
100	17	0.233	0.431	0.197
100	18	0.170	0.348	0.178
100	19	-0.012	0.224	0.236
100	20	0.001	0.149	0.148
100	21	-0.051	0.059	0.110
100	22	0.573	0.792	0.219
100	24	0.388	0.497	0.109
105	25	0.254	0.450	0.196
105	26	0.232	0.578	0.346
105	27	0.373	0.589	0.216
105	28	1.053	1.258	0.206
105	29	0.177	0.352	0.175
Max	1.129	1.258	0.381	
Average	0.318	0.426	0.108	
Min	-0.408	-0.325	-0.104	
Std Dev	0.309	0.331	0.106	

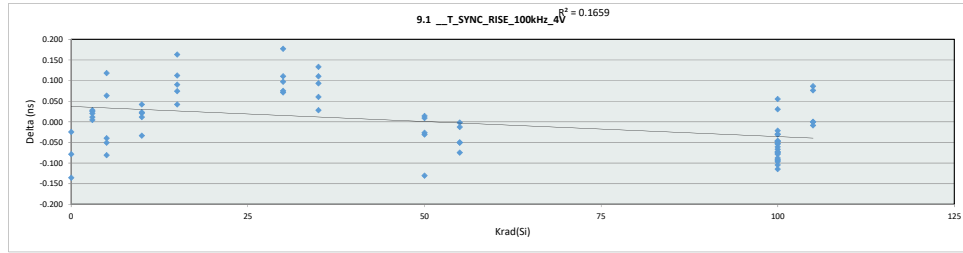


8.8 EA_OS_14V											
Test Site											
Tester											
Test Number											
Max Limit	2	mV									
Min Limit	-2	mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	0.013	0.134	-0.325	-0.050	0.262	-0.031	-0.222	0.082	0.487	-0.143	0.352
Average	0.109	0.398	0.246	0.307	0.458	0.220	0.302	0.388	0.552	0.541	0.645
Max	0.192	0.619	0.576	0.583	0.927	0.534	1.197	0.542	0.651	1.223	1.258
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000

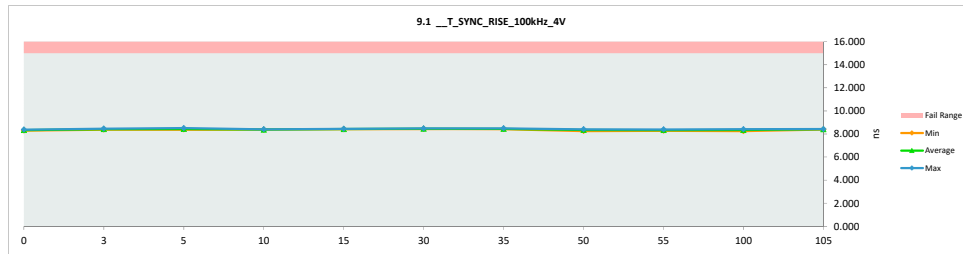


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9.1 T_SYNC_RISE_100kHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.428	8.349	-0.079
0	118	8.435	8.299	-0.136
0	122	8.398	8.373	-0.025
3	301	8.397	8.422	0.025
3	306	8.366	8.394	0.028
3	307	8.375	8.379	0.004
3	308	8.395	8.415	0.020
3	309	8.453	8.464	0.011
5	310	8.337	8.455	0.118
5	311	8.444	8.507	0.063
5	312	8.432	8.392	-0.040
5	314	8.448	8.397	-0.051
5	316	8.486	8.405	-0.081
10	317	8.380	8.401	0.021
10	318	8.358	8.369	0.011
10	319	8.363	8.386	0.023
10	321	8.405	8.371	-0.034
10	322	8.364	8.406	0.042
15	323	8.325	8.415	0.090
15	325	8.361	8.435	0.074
15	326	8.326	8.438	0.112
15	328	8.282	8.445	0.163
15	333	8.362	8.404	0.042
30	334	8.284	8.461	0.177
30	335	8.412	8.483	0.071
30	337	8.338	8.448	0.110
30	339	8.392	8.467	0.075
30	341	8.340	8.437	0.097
35	342	8.292	8.425	0.133
35	345	8.391	8.419	0.028
35	346	8.370	8.463	0.093
35	347	8.433	8.493	0.060
35	348	8.330	8.440	0.110
50	349	8.384	8.398	0.014
50	350	8.393	8.402	0.009
50	351	8.350	8.323	-0.027
50	353	8.388	8.357	-0.031
50	354	8.381	8.250	-0.131
55	355	8.442	8.392	-0.050
55	357	8.359	8.346	-0.013
55	359	8.382	8.307	-0.075
55	362	8.390	8.339	-0.051
55	363	8.283	8.281	-0.002
100	1	8.407	8.353	-0.054
100	3	8.424	8.327	-0.097
100	4	8.422	8.361	-0.061
100	5	8.438	8.359	-0.079
100	6	8.313	8.291	-0.022
100	7	8.399	8.352	-0.047
100	8	8.380	8.287	-0.093
100	9	8.421	8.368	-0.053
100	10	8.445	8.414	-0.031
100	11	8.356	8.411	0.055
100	12	8.385	8.280	-0.105
100	13	8.441	8.348	-0.093
100	14	8.448	8.374	-0.074
100	15	8.407	8.331	-0.076
100	16	8.366	8.293	-0.073
100	17	8.393	8.346	-0.047
100	18	8.437	8.370	-0.067
100	19	8.328	8.358	0.030
100	20	8.377	8.347	-0.030
100	21	8.432	8.344	-0.088
100	22	8.396	8.349	-0.047
100	24	8.362	8.247	-0.115
105	25	8.410	8.410	0.000
105	26	8.310	8.396	0.086
105	27	8.408	8.407	-0.001
105	28	8.424	8.415	-0.009
105	29	8.351	8.427	0.076
Max		8.486	8.507	0.177
Average		8.385	8.383	-0.002
Min		8.282	8.247	-0.136
Std Dev		0.045	0.057	0.073

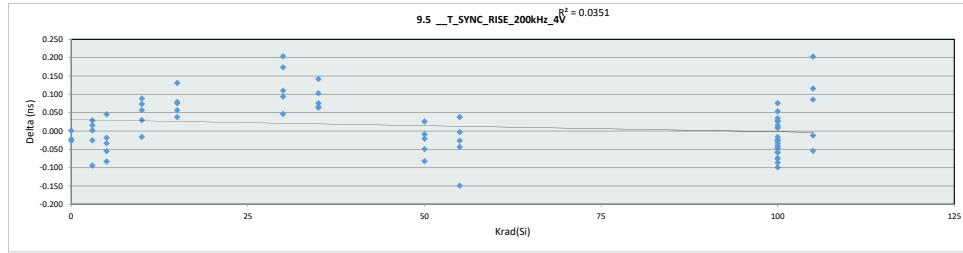


9.1 T_SYNC_RISE_100kHz											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.299	8.379	8.392	8.369	8.404	8.437	8.419	8.250	8.281	8.247	8.396
Average	8.340	8.415	8.431	8.387	8.427	8.459	8.448	8.346	8.333	8.341	8.411
Max	8.373	8.464	8.507	8.406	8.445	8.483	8.493	8.402	8.392	8.414	8.427
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

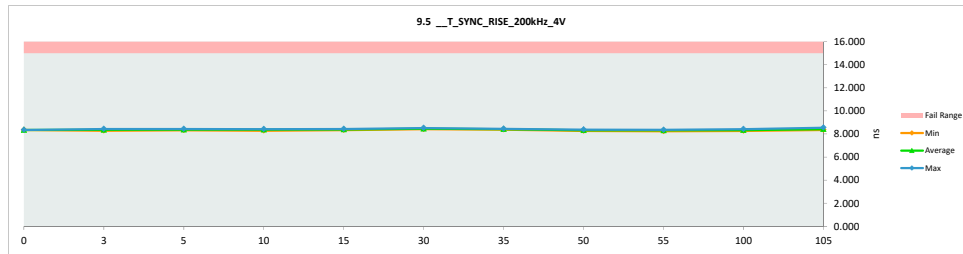


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9.5 T SYNC RISE 200kHz 4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.386	8.358	-0.028
0	118	8.347	8.347	0.000
0	122	8.368	8.345	-0.023
3	301	8.402	8.430	0.028
3	306	8.349	8.364	0.015
3	307	8.374	8.279	-0.095
3	308	8.363	8.337	-0.026
3	309	8.425	8.426	0.001
5	310	8.364	8.345	-0.019
5	311	8.380	8.324	-0.056
5	312	8.352	8.397	0.045
5	314	8.522	8.438	-0.084
5	316	8.418	8.384	-0.034
10	317	8.346	8.402	0.056
10	318	8.299	8.282	-0.017
10	319	8.338	8.426	0.088
10	321	8.369	8.398	0.029
10	322	8.332	8.405	0.073
15	323	8.310	8.440	0.130
15	325	8.339	8.395	0.056
15	326	8.333	8.411	0.078
15	328	8.302	8.339	0.037
15	333	8.319	8.394	0.075
30	334	8.314	8.517	0.203
30	335	8.384	8.430	0.046
30	337	8.327	8.436	0.109
30	339	8.320	8.413	0.093
30	341	8.317	8.490	0.173
35	342	8.281	8.383	0.102
35	345	8.327	8.402	0.075
35	346	8.362	8.427	0.065
35	347	8.372	8.435	0.063
35	348	8.311	8.452	0.141
50	349	8.357	8.347	-0.010
50	350	8.358	8.383	0.025
50	351	8.346	8.263	-0.083
50	353	8.361	8.311	-0.050
50	354	8.354	8.332	-0.022
55	355	8.375	8.348	-0.027
55	357	8.307	8.344	0.037
55	359	8.358	8.354	-0.004
55	362	8.339	8.295	-0.044
55	363	8.382	8.232	-0.150
100	1	8.384	8.325	-0.059
100	3	8.362	8.336	-0.026
100	4	8.427	8.352	-0.075
100	5	8.347	8.330	-0.017
100	6	8.351	8.275	-0.076
100	7	8.332	8.340	0.008
100	8	8.381	8.334	-0.047
100	9	8.393	8.418	0.025
100	10	8.435	8.348	-0.087
100	11	8.340	8.313	-0.027
100	12	8.309	8.268	-0.041
100	13	8.422	8.363	-0.059
100	14	8.392	8.292	-0.100
100	15	8.332	8.297	-0.035
100	16	8.377	8.352	-0.025
100	17	8.352	8.360	0.008
100	18	8.371	8.399	0.028
100	19	8.327	8.279	-0.048
100	20	8.306	8.359	0.053
100	21	8.387	8.402	0.015
100	22	8.338	8.372	0.034
100	24	8.283	8.358	0.075
105	25	8.370	8.357	-0.013
105	26	8.298	8.413	0.115
105	27	8.336	8.421	0.085
105	28	8.342	8.544	0.202
105	29	8.405	8.350	-0.055
	Max	8.522	8.544	0.203
	Average	8.356	8.369	0.013
	Min	8.281	8.232	-0.150
	Std Dev	0.040	0.059	0.072

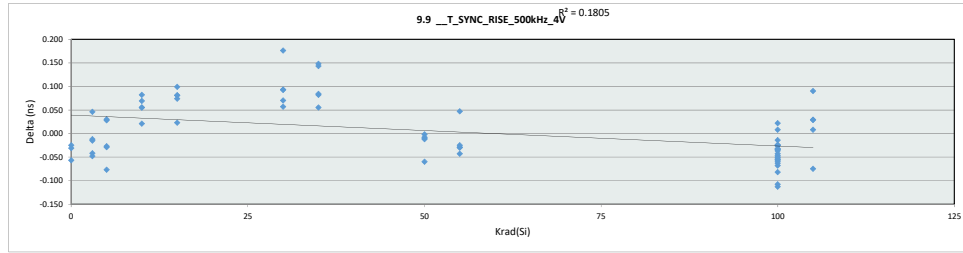


9.5 T_SYNC_RISE_200kHz											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.345	8.279	8.324	8.282	8.339	8.413	8.383	8.263	8.232	8.268	8.350
Average	8.350	8.367	8.378	8.383	8.396	8.457	8.420	8.327	8.315	8.340	8.417
Max	8.358	8.430	8.438	8.426	8.440	8.517	8.452	8.383	8.354	8.418	8.544
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

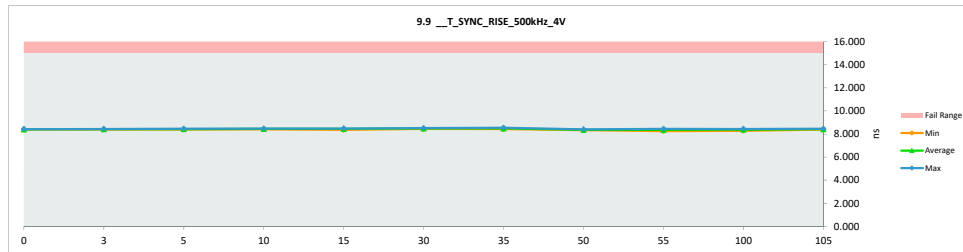


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9.9 T SYNC RISE 500kHz 4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.473	8.416	-0.057
0	118	8.402	8.377	-0.025
0	122	8.407	8.376	-0.031
3	301	8.445	8.403	-0.042
3	306	8.429	8.381	-0.048
3	307	8.362	8.408	0.046
3	308	8.428	8.416	-0.012
3	309	8.445	8.430	-0.015
5	310	8.412	8.385	-0.027
5	311	8.418	8.448	0.030
5	312	8.415	8.443	0.028
5	314	8.466	8.389	-0.077
5	316	8.437	8.408	-0.029
10	317	8.407	8.462	0.055
10	318	8.321	8.390	0.069
10	319	8.378	8.460	0.082
10	321	8.393	8.414	0.021
10	322	8.410	8.465	0.055
15	323	8.373	8.472	0.099
15	325	8.381	8.462	0.081
15	326	8.376	8.450	0.074
15	328	8.326	8.349	0.023
15	333	8.358	8.439	0.081
30	334	8.266	8.442	0.176
30	335	8.423	8.493	0.070
30	337	8.393	8.450	0.057
30	339	8.382	8.475	0.093
30	341	8.409	8.502	0.093
35	342	8.340	8.488	0.148
35	345	8.395	8.538	0.143
35	346	8.372	8.427	0.055
35	347	8.418	8.500	0.082
35	348	8.345	8.429	0.084
50	349	8.363	8.351	-0.012
50	350	8.405	8.395	-0.010
50	351	8.369	8.367	-0.002
50	353	8.368	8.360	-0.008
50	354	8.381	8.321	-0.060
55	355	8.411	8.382	-0.029
55	357	8.393	8.440	0.047
55	359	8.391	8.361	-0.030
55	362	8.378	8.353	-0.025
55	363	8.294	8.251	-0.043
100	1	8.417	8.380	-0.037
100	3	8.440	8.416	-0.024
100	4	8.455	8.429	-0.026
100	5	8.429	8.366	-0.063
100	6	8.381	8.338	-0.043
100	7	8.419	8.385	-0.034
100	8	8.402	8.346	-0.056
100	9	8.479	8.423	-0.056
100	10	8.462	8.412	-0.050
100	11	8.389	8.364	-0.025
100	12	8.396	8.283	-0.113
100	13	8.437	8.369	-0.068
100	14	8.441	8.408	-0.033
100	15	8.387	8.409	0.022
100	16	8.386	8.354	-0.032
100	17	8.408	8.394	-0.014
100	18	8.445	8.363	-0.082
100	19	8.362	8.310	-0.052
100	20	8.433	8.374	-0.059
100	21	8.472	8.425	-0.047
100	22	8.402	8.410	0.008
100	24	8.386	8.278	-0.108
105	25	8.361	8.390	0.029
105	26	8.337	8.427	0.090
105	27	8.419	8.448	0.029
105	28	8.451	8.376	-0.075
105	29	8.396	8.404	0.008
	Max	8.479	8.538	0.176
	Average	8.399	8.404	0.004
	Min	8.266	8.251	-0.113
	Std Dev	0.041	0.054	0.063

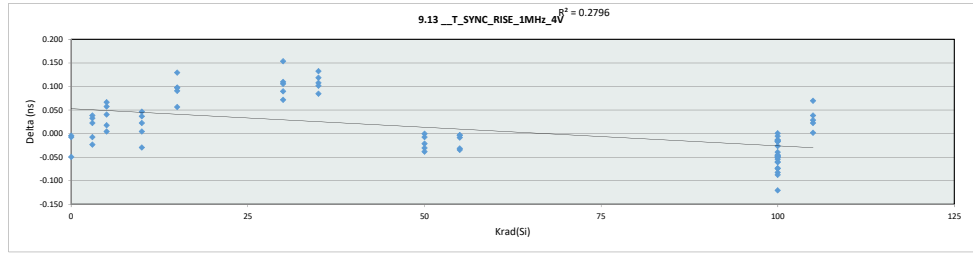


9.9 T_SYNC_RISE_500kHz											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.376	8.381	8.385	8.390	8.349	8.442	8.427	8.321	8.251	8.278	8.376
Average	8.390	8.408	8.415	8.438	8.434	8.472	8.476	8.359	8.357	8.374	8.409
Max	8.416	8.430	8.448	8.465	8.472	8.502	8.538	8.395	8.440	8.429	8.448
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

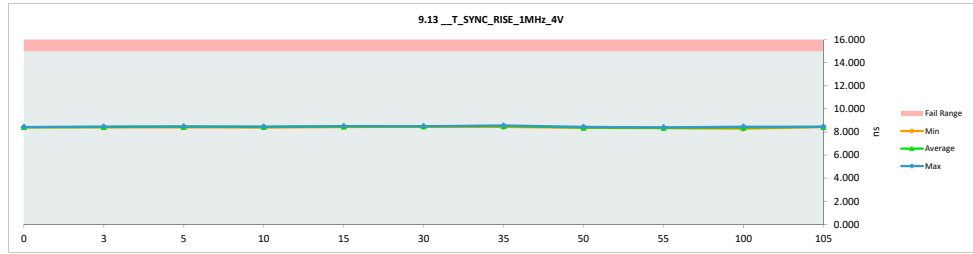


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9.13 T_SYNC_RISE_1MHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.483	8.433	-0.050
0	118	8.409	8.401	-0.008
0	122	8.424	8.419	-0.005
3	301	8.400	8.438	0.038
3	306	8.405	8.397	-0.008
3	307	8.435	8.411	-0.024
3	308	8.417	8.449	0.032
3	309	8.442	8.464	0.022
5	310	8.384	8.401	0.017
5	311	8.399	8.456	0.057
5	312	8.397	8.437	0.040
5	314	8.478	8.482	0.004
5	316	8.406	8.472	0.066
10	317	8.435	8.439	0.004
10	318	8.337	8.383	0.046
10	319	8.421	8.443	0.022
10	321	8.460	8.430	-0.030
10	322	8.426	8.462	0.036
15	323	8.399	8.455	0.056
15	325	8.408	8.498	0.090
15	326	8.381	8.510	0.129
15	328	8.336	8.433	0.097
15	333	8.377	8.474	0.097
30	334	8.340	8.493	0.153
30	335	8.394	8.503	0.109
30	337	8.357	8.462	0.105
30	339	8.384	8.473	0.089
30	341	8.429	8.500	0.071
35	342	8.358	8.442	0.084
35	345	8.447	8.554	0.107
35	346	8.419	8.520	0.101
35	347	8.442	8.560	0.118
35	348	8.334	8.466	0.132
50	349	8.411	8.372	-0.039
50	350	8.440	8.439	-0.001
50	351	8.403	8.372	-0.031
50	353	8.409	8.387	-0.022
50	354	8.368	8.360	-0.008
55	355	8.409	8.405	-0.004
55	357	8.386	8.382	-0.004
55	359	8.401	8.366	-0.035
55	362	8.406	8.374	-0.032
55	363	8.336	8.327	-0.009
100	1	8.415	8.398	-0.017
100	3	8.409	8.382	-0.027
100	4	8.474	8.399	-0.075
100	5	8.439	8.392	-0.047
100	6	8.413	8.358	-0.055
100	7	8.455	8.449	-0.006
100	8	8.413	8.364	-0.049
100	9	8.518	8.397	-0.121
100	10	8.447	8.431	-0.016
100	11	8.409	8.363	-0.046
100	12	8.407	8.346	-0.061
100	13	8.454	8.393	-0.061
100	14	8.429	8.378	-0.051
100	15	8.449	8.399	-0.050
100	16	8.460	8.420	-0.040
100	17	8.446	8.363	-0.083
100	18	8.431	8.343	-0.088
100	19	8.397	8.379	-0.018
100	20	8.411	8.411	0.000
100	21	8.470	8.456	-0.014
100	22	8.384	8.370	-0.014
100	24	8.360	8.286	-0.074
105	25	8.396	8.434	0.038
105	26	8.354	8.423	0.069
105	27	8.420	8.442	0.022
105	28	8.456	8.457	0.001
105	29	8.407	8.435	0.028
	Max	8.518	8.560	0.153
	Average	8.412	8.423	0.011
	Min	8.334	8.286	-0.121
	Std Dev	0.038	0.053	0.061

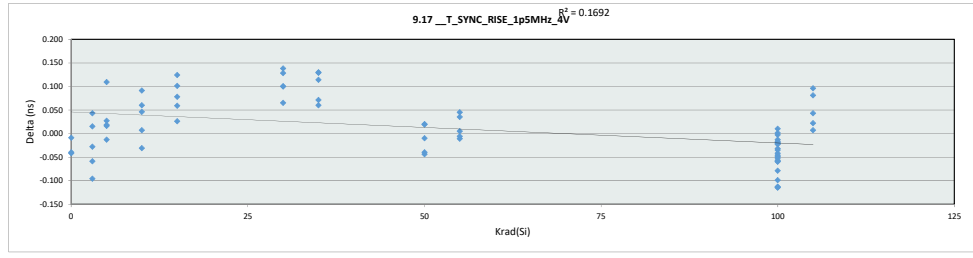


9.13 T_SYNC_RISE_1MHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.401	8.397	8.401	8.383	8.433	8.462	8.442	8.360	8.327	8.286	8.423
Average	8.418	8.432	8.450	8.431	8.474	8.486	8.508	8.386	8.371	8.385	8.438
Max	8.433	8.464	8.482	8.462	8.510	8.503	8.560	8.439	8.405	8.456	8.457
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

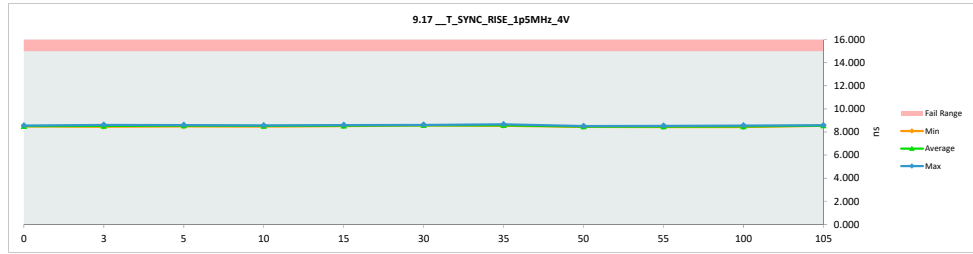


HDR TID Report
TPS7H5001-SP

		9.17 T_SYNC_RISE_1p5MHz_4V		
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.591	8.551	-0.040
0	118	8.502	8.493	-0.009
0	122	8.527	8.485	-0.042
3	301	8.566	8.507	-0.059
3	306	8.515	8.487	-0.028
3	307	8.546	8.589	0.043
3	308	8.548	8.452	-0.096
3	309	8.597	8.612	0.015
5	310	8.462	8.480	0.018
5	311	8.563	8.550	-0.013
5	312	8.475	8.584	0.109
5	314	8.573	8.600	0.027
5	316	8.554	8.570	0.016
10	317	8.509	8.555	0.046
10	318	8.459	8.466	0.007
10	319	8.473	8.564	0.091
10	321	8.487	8.547	0.060
10	322	8.536	8.505	-0.031
15	323	8.451	8.510	0.059
15	325	8.508	8.534	0.026
15	326	8.478	8.602	0.124
15	328	8.436	8.514	0.078
15	333	8.482	8.583	0.101
30	334	8.442	8.570	0.128
30	335	8.519	8.584	0.065
30	337	8.434	8.572	0.138
30	339	8.510	8.610	0.100
30	341	8.488	8.588	0.100
35	342	8.474	8.545	0.071
35	345	8.469	8.583	0.114
35	346	8.513	8.573	0.060
35	347	8.530	8.659	0.129
35	348	8.443	8.573	0.130
50	349	8.496	8.486	-0.010
50	350	8.489	8.509	0.020
50	351	8.477	8.496	0.019
50	353	8.483	8.443	-0.040
50	354	8.500	8.456	-0.044
55	355	8.509	8.498	-0.011
55	357	8.481	8.475	-0.006
55	359	8.494	8.529	0.035
55	362	8.425	8.430	0.005
55	363	8.403	8.448	0.045
100	1	8.526	8.506	-0.020
100	3	8.576	8.462	-0.114
100	4	8.586	8.471	-0.115
100	5	8.553	8.521	-0.032
100	6	8.455	8.456	0.001
100	7	8.518	8.528	0.010
100	8	8.542	8.521	-0.021
100	9	8.583	8.470	-0.113
100	10	8.575	8.558	-0.017
100	11	8.487	8.441	-0.046
100	12	8.490	8.438	-0.052
100	13	8.531	8.471	-0.060
100	14	8.570	8.513	-0.057
100	15	8.524	8.521	-0.003
100	16	8.510	8.461	-0.049
100	17	8.523	8.424	-0.099
100	18	8.518	8.505	-0.013
100	19	8.471	8.436	-0.035
100	20	8.514	8.491	-0.023
100	21	8.584	8.542	-0.042
100	22	8.523	8.464	-0.059
100	24	8.502	8.423	-0.079
105	25	8.470	8.551	0.081
105	26	8.495	8.538	0.043
105	27	8.548	8.555	0.007
105	28	8.554	8.576	0.022
105	29	8.484	8.580	0.096
	Max	8.597	8.659	0.138
	Average	8.509	8.520	0.011
	Min	8.403	8.423	-0.115
	Std Dev	0.044	0.055	0.065

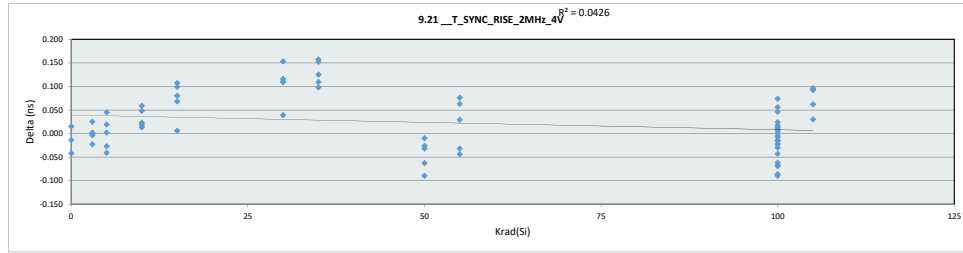


		9.17 T_SYNC_RISE_1p5MHz										
Test Site												
Tester												
Test Number												
Max Limit	15	ns										
Min Limit		ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL												
Min	8.485	8.452	8.480	8.466	8.510	8.570	8.545	8.443	8.430	8.423	8.538	
Average	8.510	8.529	8.557	8.527	8.549	8.585	8.587	8.478	8.476	8.483	8.560	
Max	8.551	8.612	8.600	8.564	8.602	8.610	8.659	8.509	8.529	8.558	8.580	
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	

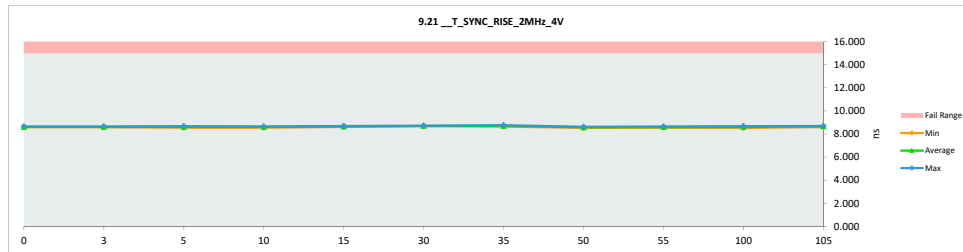


HDR TID Report
TPS7H5001-SP

		9.21 T_SYNC_RISE_2MHz_4V		
Test Site				
Tester				
Test Number				
Unit		ns	ns	
Max Limit		15	15	
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.695	8.653	-0.042
0	118	8.571	8.586	0.015
0	122	8.608	8.594	-0.014
3	301	8.635	8.631	-0.004
3	306	8.378	8.603	0.025
3	307	8.613	8.590	-0.023
3	308	8.617	8.619	0.002
3	309	8.652	8.651	-0.001
5	310	8.588	8.547	-0.041
5	311	8.587	8.632	0.045
5	312	8.600	8.619	0.019
5	314	8.715	8.688	-0.027
5	316	8.620	8.622	0.002
10	317	8.597	8.656	0.059
10	318	8.532	8.550	0.018
10	319	8.592	8.605	0.013
10	321	8.595	8.643	0.048
10	322	8.627	8.650	0.023
15	323	8.572	8.640	0.068
15	325	8.640	8.646	0.006
15	326	8.613	8.693	0.080
15	328	8.500	8.607	0.107
15	333	8.566	8.665	0.099
30	334	8.358	8.574	0.116
30	335	8.618	8.727	0.109
30	337	8.549	8.702	0.153
30	339	8.644	8.683	0.039
30	341	8.603	8.713	0.110
35	342	8.524	8.681	0.157
35	345	8.632	8.757	0.125
35	346	8.603	8.701	0.098
35	347	8.592	8.744	0.152
35	348	8.528	8.637	0.109
50	349	8.709	8.619	-0.090
50	350	8.608	8.582	-0.026
50	351	8.600	8.537	-0.063
50	353	8.611	8.579	-0.032
50	354	8.569	8.559	-0.010
55	355	8.586	8.615	0.029
55	357	8.592	8.655	0.063
55	359	8.618	8.574	-0.044
55	362	8.611	8.579	-0.032
55	363	8.474	8.550	0.076
100	1	8.616	8.623	0.007
100	3	8.700	8.610	-0.090
100	4	8.671	8.680	0.009
100	5	8.659	8.654	-0.005
100	6	8.606	8.607	0.001
100	7	8.661	8.599	-0.062
100	8	8.629	8.606	-0.023
100	9	8.706	8.620	-0.086
100	10	8.694	8.679	-0.015
100	11	8.614	8.599	-0.015
100	12	8.590	8.521	-0.069
100	13	8.570	8.626	0.056
100	14	8.629	8.607	-0.022
100	15	8.612	8.658	0.046
100	16	8.637	8.629	-0.008
100	17	8.613	8.545	-0.068
100	18	8.590	8.614	0.024
100	19	8.585	8.602	0.017
100	20	8.623	8.636	0.013
100	21	8.665	8.622	-0.043
100	22	8.531	8.605	0.074
100	24	8.560	8.530	-0.030
105	25	8.581	8.673	0.092
105	26	8.604	8.699	0.095
105	27	8.580	8.676	0.096
105	28	8.638	8.700	0.062
105	29	8.569	8.599	0.030
	Max	8.715	8.757	0.157
	Average	8.607	8.630	0.023
	Min	8.474	8.521	-0.090
	Std Dev	0.047	0.051	0.062

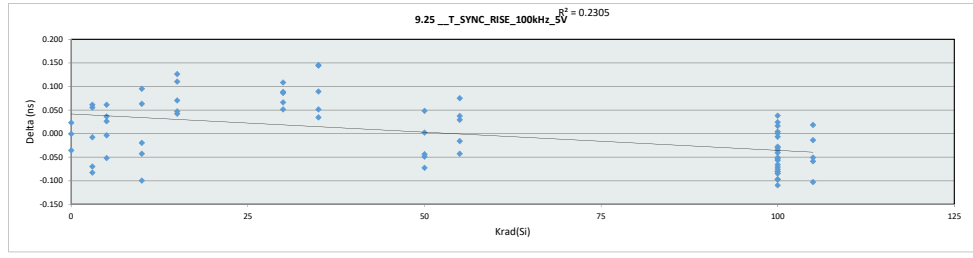


		9.21 T_SYNC_RISE_2MHz_4V											
Test Site													
Tester													
Test Number													
Max Limit		15	ns										
Min Limit			ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105		
LL													
Min	8.586	8.590	8.547	8.550	8.607	8.674	8.637	8.537	8.550	8.521	8.599		
Average	8.611	8.619	8.622	8.621	8.650	8.700	8.704	8.575	8.595	8.612	8.669		
Max	8.653	8.651	8.688	8.656	8.693	8.727	8.757	8.619	8.655	8.680	8.700		
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000		

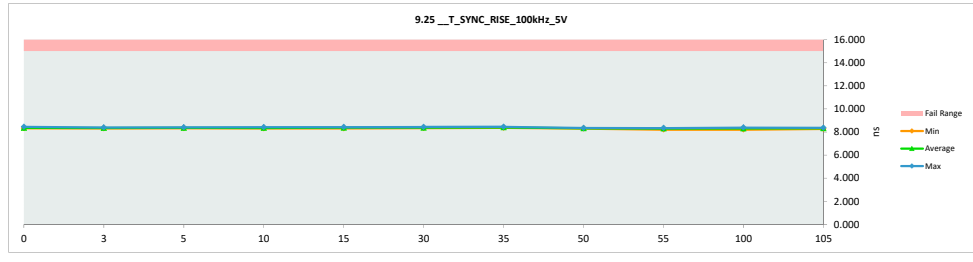


HDR TID Report
TPS7H5001-SP

		9.25 T_SYNC_RISE_100kHz_5V		
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.412	8.435	0.023
0	118	8.321	8.320	-0.001
0	122	8.348	8.312	-0.036
3	301	8.416	8.346	-0.070
3	306	8.301	8.362	0.061
3	307	8.383	8.300	-0.083
3	308	8.320	8.312	-0.008
3	309	8.324	8.379	0.055
5	310	8.285	8.311	0.026
5	311	8.330	8.391	0.061
5	312	8.391	8.339	-0.052
5	314	8.368	8.364	-0.004
5	316	8.303	8.339	0.036
10	317	8.309	8.404	0.095
10	318	8.235	8.298	0.063
10	319	8.386	8.343	-0.043
10	321	8.406	8.306	-0.100
10	322	8.399	8.379	-0.020
15	323	8.287	8.357	0.070
15	325	8.322	8.369	0.047
15	326	8.305	8.415	0.110
15	328	8.260	8.302	0.042
15	333	8.239	8.365	0.126
30	334	8.234	8.322	0.088
30	335	8.312	8.363	0.051
30	337	8.304	8.370	0.066
30	339	8.280	8.366	0.086
30	341	8.310	8.418	0.108
35	342	8.273	8.362	0.089
35	345	8.289	8.434	0.145
35	346	8.302	8.353	0.051
35	347	8.364	8.398	0.034
35	348	8.243	8.387	0.144
50	349	8.329	8.285	-0.044
50	350	8.365	8.292	-0.073
50	351	8.353	8.304	-0.049
50	353	8.281	8.329	0.048
50	354	8.334	8.336	0.002
55	355	8.366	8.323	-0.043
55	357	8.258	8.333	0.075
55	359	8.288	8.317	0.029
55	362	8.245	8.282	0.037
55	363	8.214	8.198	-0.016
100	1	8.274	8.312	0.038
100	3	8.351	8.316	-0.035
100	4	8.365	8.358	-0.007
100	5	8.390	8.314	-0.076
100	6	8.312	8.258	-0.054
100	7	8.358	8.260	-0.098
100	8	8.326	8.245	-0.081
100	9	8.442	8.345	-0.097
100	10	8.379	8.349	-0.030
100	11	8.356	8.285	-0.071
100	12	8.306	8.240	-0.066
100	13	8.318	8.342	0.024
100	14	8.345	8.294	-0.051
100	15	8.298	8.314	0.016
100	16	8.323	8.238	-0.085
100	17	8.312	8.271	-0.041
100	18	8.389	8.332	-0.057
100	19	8.292	8.292	0.000
100	20	8.318	8.322	0.004
100	21	8.401	8.373	-0.028
100	22	8.307	8.226	-0.081
100	24	8.308	8.198	-0.110
105	25	8.344	8.293	-0.051
105	26	8.326	8.312	-0.014
105	27	8.378	8.319	-0.059
105	28	8.329	8.347	0.018
105	29	8.360	8.257	-0.103
	Max	8.442	8.435	0.145
	Average	8.326	8.326	0.000
	Min	8.214	8.198	-0.110
	Std Dev	0.049	0.051	0.066

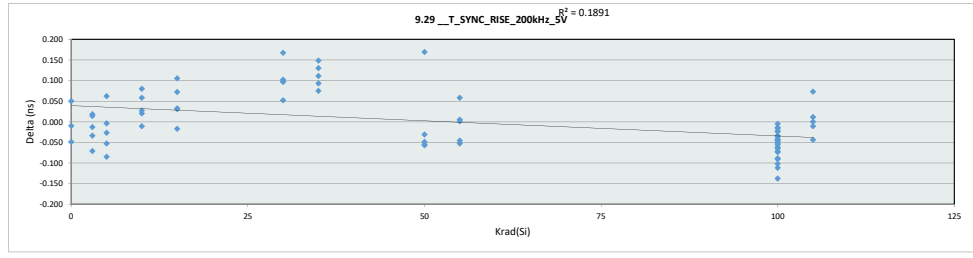


		9.25 T_SYNC_RISE_100kHz												
Test Site														
Tester														
Test Number														
Max Limit	15	ns												
Min Limit		ns												
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105			
LL														
Min	8.312	8.300	8.311	8.298	8.302	8.322	8.353	8.285	8.198	8.198	8.257			
Average	8.356	8.340	8.349	8.346	8.362	8.368	8.387	8.309	8.291	8.295	8.306			
Max	8.435	8.379	8.391	8.404	8.415	8.418	8.434	8.336	8.333	8.373	8.347			
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000			

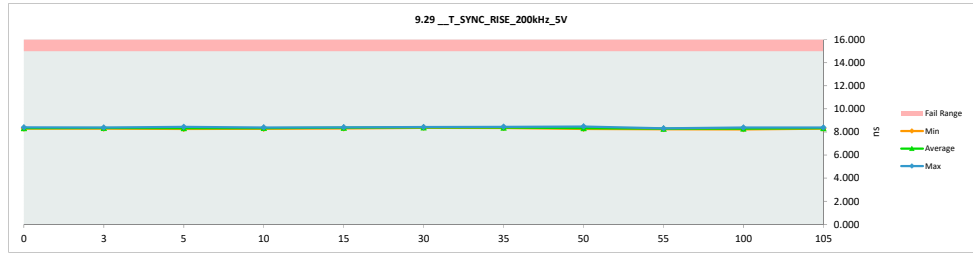


HDR TID Report
TPS7H5001-SP

		9.29 T_SYNC_RISE_200kHz_5V		
Test Site				
Tester				
Test Number				
Unit		ns	ns	
Max Limit		15	15	
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.344	8.394	0.050
0	118	8.355	8.306	-0.049
0	122	8.294	8.284	-0.010
3	301	8.365	8.383	0.018
3	306	8.326	8.323	-0.013
3	307	8.352	8.281	-0.071
3	308	8.350	8.316	-0.034
3	309	8.369	8.383	0.014
5	310	8.284	8.257	-0.027
5	311	8.319	8.266	-0.053
5	312	8.366	8.362	-0.004
5	314	8.373	8.435	0.062
5	316	8.400	8.315	-0.085
10	317	8.308	8.388	0.080
10	318	8.243	8.270	0.027
10	319	8.289	8.347	0.058
10	321	8.320	8.309	-0.011
10	322	8.340	8.360	0.020
15	323	8.305	8.377	0.072
15	325	8.373	8.356	-0.017
15	326	8.296	8.401	0.105
15	328	8.265	8.297	0.032
15	333	8.360	8.391	0.031
30	334	8.253	8.365	0.112
30	335	8.311	8.408	0.097
30	337	8.232	8.399	0.167
30	339	8.303	8.400	0.097
30	341	8.369	8.421	0.052
35	342	8.236	8.366	0.130
35	345	8.348	8.423	0.075
35	346	8.248	8.341	0.093
35	347	8.298	8.446	0.148
35	348	8.245	8.356	0.111
50	349	8.352	8.303	-0.049
50	350	8.346	8.315	-0.031
50	351	8.301	8.470	0.169
50	353	8.333	8.276	-0.057
50	354	8.319	8.264	-0.055
55	355	8.301	8.306	0.005
55	357	8.358	8.305	-0.053
55	359	8.257	8.315	0.058
55	362	8.307	8.261	-0.046
55	363	8.235	8.236	0.001
100	1	8.331	8.297	-0.034
100	3	8.351	8.289	-0.062
100	4	8.387	8.344	-0.043
100	5	8.394	8.338	-0.056
100	6	8.385	8.247	-0.138
100	7	8.361	8.296	-0.065
100	8	8.325	8.251	-0.074
100	9	8.353	8.317	-0.036
100	10	8.395	8.306	-0.089
100	11	8.328	8.226	-0.102
100	12	8.332	8.260	-0.072
100	13	8.333	8.286	-0.047
100	14	8.326	8.302	-0.024
100	15	8.354	8.302	-0.052
100	16	8.360	8.269	-0.091
100	17	8.352	8.336	-0.016
100	18	8.337	8.332	-0.005
100	19	8.293	8.249	-0.044
100	20	8.390	8.278	-0.112
100	21	8.393	8.379	-0.014
100	22	8.291	8.268	-0.023
100	24	8.277	8.233	-0.044
105	25	8.318	8.307	-0.011
105	26	8.260	8.333	0.073
105	27	8.333	8.333	0.000
105	28	8.369	8.380	0.011
105	29	8.329	8.285	-0.044
Max		8.400	8.470	0.169
Average		8.326	8.326	0.000
Min		8.232	8.226	-0.138
Std Dev		0.044	0.057	0.069

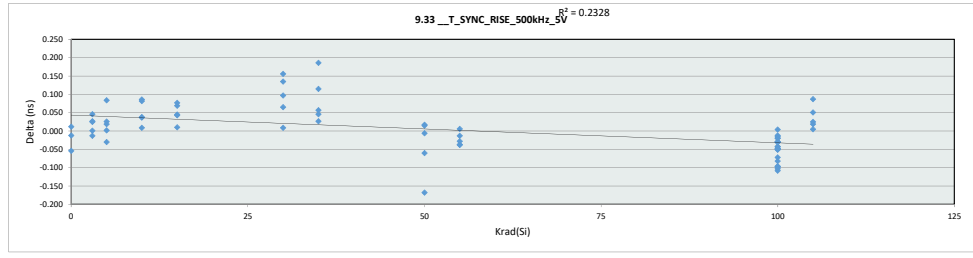


		9.29 T_SYNC_RISE_200kHz												
Test Site														
Tester														
Test Number														
Max Limit		15	ns											
Min Limit			ns											
Krad(Si)		0	3	5	10	15	30	35	50	55	100	105		
LL														
Min		8.284	8.281	8.257	8.270	8.297	8.365	8.341	8.264	8.236	8.226	8.285		
Average		8.328	8.337	8.327	8.335	8.364	8.399	8.386	8.326	8.285	8.291	8.328		
Max		8.394	8.383	8.435	8.388	8.401	8.421	8.446	8.470	8.315	8.379	8.380		
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000		

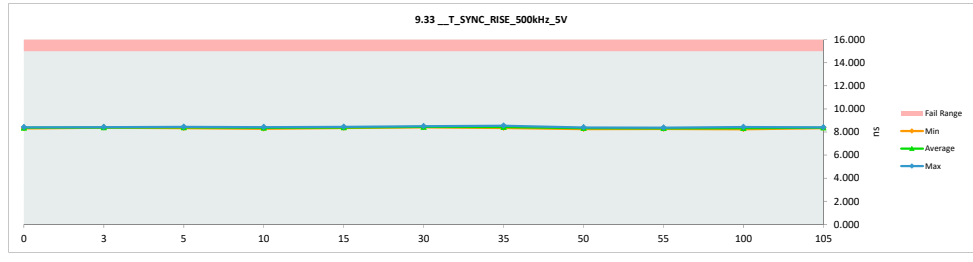


HDR TID Report
TPS7H5001-SP

		9.33 T_SYNC_RISE_500kHz_5V		
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.419	8.406	-0.013
0	118	8.379	8.390	0.011
0	122	8.360	8.305	-0.055
3	301	8.411	8.397	-0.014
3	306	8.349	8.375	0.026
3	307	8.376	8.376	0.000
3	308	8.351	8.396	0.045
3	309	8.392	8.416	0.024
5	310	8.319	8.337	0.018
5	311	8.384	8.409	0.025
5	312	8.323	8.406	0.083
5	314	8.436	8.437	0.001
5	316	8.426	8.395	-0.031
10	317	8.335	8.420	0.085
10	318	8.247	8.283	0.036
10	319	8.325	8.333	0.008
10	321	8.276	8.357	0.081
10	322	8.379	8.416	0.037
15	323	8.325	8.367	0.042
15	325	8.371	8.439	0.068
15	326	8.371	8.380	0.009
15	328	8.303	8.347	0.044
15	333	8.331	8.407	0.076
30	334	8.308	8.404	0.096
30	335	8.396	8.404	0.008
30	337	8.347	8.411	0.064
30	339	8.332	8.466	0.134
30	341	8.340	8.495	0.155
35	342	8.323	8.349	0.026
35	345	8.368	8.424	0.056
35	346	8.391	8.436	0.045
35	347	8.348	8.533	0.185
35	348	8.273	8.387	0.114
50	349	8.356	8.370	0.014
50	350	8.371	8.387	0.016
50	351	8.355	8.348	-0.007
50	353	8.424	8.255	-0.169
50	354	8.377	8.316	-0.061
55	355	8.397	8.368	-0.029
55	357	8.362	8.323	-0.039
55	359	8.325	8.330	0.005
55	362	8.344	8.306	-0.038
55	363	8.277	8.263	-0.014
100	1	8.387	8.344	-0.043
100	3	8.383	8.334	-0.049
100	4	8.396	8.297	-0.099
100	5	8.362	8.341	-0.021
100	6	8.382	8.285	-0.097
100	7	8.414	8.383	-0.031
100	8	8.443	8.339	-0.104
100	9	8.362	8.332	-0.030
100	10	8.423	8.426	0.003
100	11	8.367	8.316	-0.051
100	12	8.303	8.283	-0.020
100	13	8.424	8.315	-0.109
100	14	8.348	8.318	-0.030
100	15	8.380	8.367	-0.013
100	16	8.375	8.331	-0.044
100	17	8.359	8.343	-0.016
100	18	8.376	8.327	-0.049
100	19	8.344	8.271	-0.073
100	20	8.428	8.345	-0.083
100	21	8.439	8.387	-0.052
100	22	8.369	8.337	-0.032
100	24	8.338	8.240	-0.098
105	25	8.250	8.336	0.086
105	26	8.338	8.388	0.050
105	27	8.383	8.387	0.004
105	28	8.379	8.397	0.018
105	29	8.334	8.358	0.024
Max		8.443	8.533	0.185
Average		8.361	8.364	0.003
Min		8.247	8.240	-0.169
Std Dev		0.043	0.055	0.064

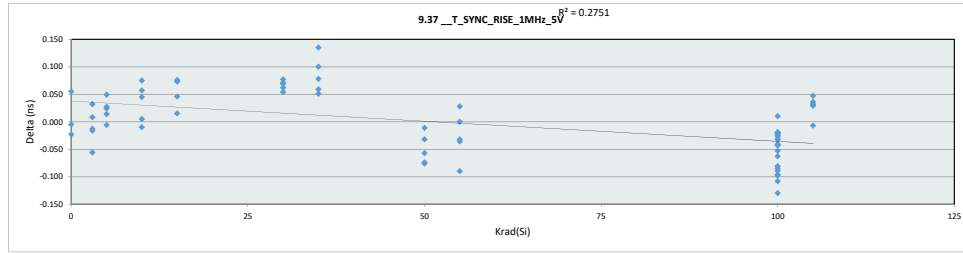


		9.33 T_SYNC_RISE_500kHz									
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.305	8.375	8.337	8.283	8.347	8.404	8.349	8.255	8.263	8.240	8.336
Average	8.367	8.392	8.397	8.362	8.388	8.436	8.426	8.335	8.318	8.330	8.373
Max	8.406	8.416	8.437	8.420	8.439	8.495	8.533	8.387	8.368	8.426	8.397
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

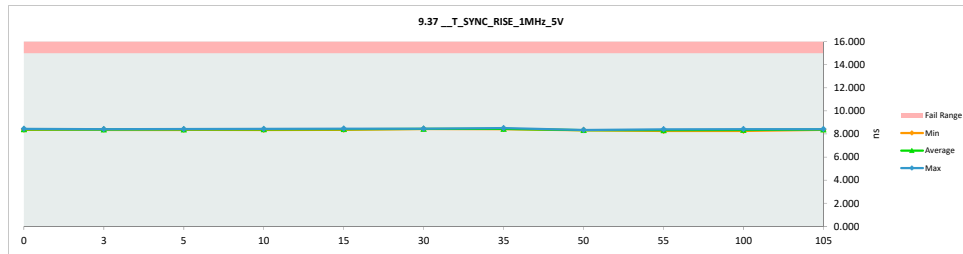


HDR TID Report
TPS7H5001-SP

		9.37 T_SYNC_RISE_1MHz_5V		
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.476	8.453	-0.023
0	118	8.343	8.398	0.055
0	122	8.367	8.362	-0.005
3	301	8.465	8.409	-0.056
3	306	8.391	8.375	-0.016
3	307	8.395	8.382	-0.013
3	308	8.370	8.378	0.008
3	309	8.388	8.420	0.032
5	310	8.311	8.360	0.049
5	311	8.379	8.406	0.027
5	312	8.401	8.415	0.014
5	314	8.402	8.426	0.024
5	316	8.401	8.395	-0.006
10	317	8.409	8.414	0.005
10	318	8.299	8.356	0.057
10	319	8.323	8.398	0.075
10	321	8.359	8.404	0.045
10	322	8.452	8.442	-0.010
15	323	8.415	8.430	0.015
15	325	8.368	8.441	0.073
15	326	8.329	8.403	0.074
15	328	8.323	8.369	0.046
15	333	8.387	8.463	0.076
30	334	8.369	8.440	0.071
30	335	8.374	8.436	0.062
30	337	8.355	8.424	0.069
30	339	8.384	8.461	0.077
30	341	8.413	8.467	0.054
35	342	8.339	8.417	0.078
35	345	8.377	8.436	0.059
35	346	8.392	8.443	0.051
35	347	8.406	8.506	0.100
35	348	8.299	8.434	0.135
50	349	8.380	8.348	-0.032
50	350	8.399	8.323	-0.076
50	351	8.351	8.340	-0.011
50	353	8.413	8.339	-0.074
50	354	8.394	8.337	-0.057
55	355	8.376	8.404	0.028
55	357	8.362	8.362	0.000
55	359	8.389	8.353	-0.036
55	362	8.361	8.271	-0.090
55	363	8.293	8.261	-0.032
100	1	8.426	8.345	-0.081
100	3	8.428	8.339	-0.089
100	4	8.411	8.386	-0.025
100	5	8.423	8.397	-0.026
100	6	8.369	8.337	-0.032
100	7	8.390	8.348	-0.042
100	8	8.371	8.352	-0.019
100	9	8.458	8.395	-0.063
100	10	8.414	8.394	-0.020
100	11	8.408	8.365	-0.043
100	12	8.408	8.300	-0.108
100	13	8.451	8.321	-0.130
100	14	8.382	8.360	-0.022
100	15	8.424	8.339	-0.085
100	16	8.324	8.303	-0.021
100	17	8.449	8.351	-0.098
100	18	8.350	8.360	0.010
100	19	8.343	8.302	-0.041
100	20	8.471	8.375	-0.096
100	21	8.447	8.416	-0.031
100	22	8.388	8.335	-0.053
100	24	8.313	8.272	-0.041
105	25	8.329	8.376	0.047
105	26	8.364	8.357	-0.007
105	27	8.363	8.399	0.036
105	28	8.388	8.417	0.029
105	29	8.359	8.391	0.032
Max		8.476	8.506	0.135
Average		8.383	8.382	-0.001
Min		8.293	8.261	-0.130
Std Dev		0.042	0.050	0.057

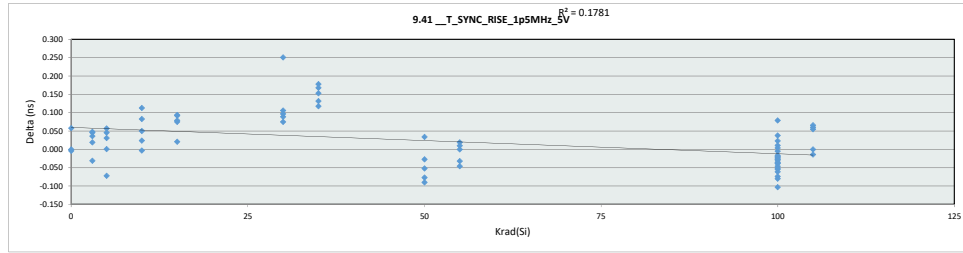


		9.37 T_SYNC_RISE_1MHz_5V									
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.362	8.375	8.360	8.356	8.369	8.424	8.417	8.323	8.261	8.272	8.357
Average	8.404	8.393	8.400	8.403	8.421	8.446	8.447	8.337	8.330	8.350	8.388
Max	8.453	8.420	8.426	8.442	8.463	8.467	8.506	8.348	8.404	8.416	8.417
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

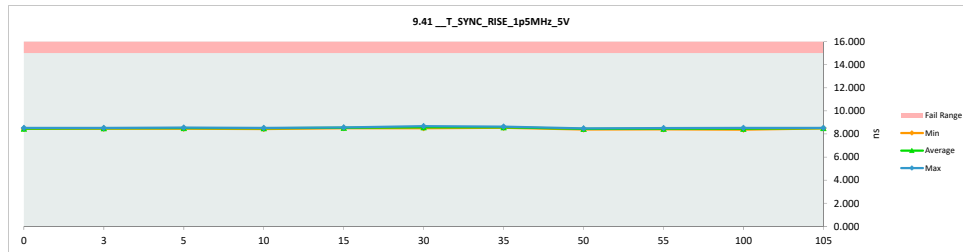


HDR TID Report
TPS7H5001-SP

		9.41 T_SYNC_RISE_1p5MHz_5V		
Test Site				
Tester				
Test Number				
Unit		ns	ns	
Max Limit		15	15	
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.521	8.520	-0.001
0	118	8.454	8.449	-0.005
0	122	8.373	8.430	0.057
3	301	8.496	8.514	0.018
3	306	8.478	8.446	-0.032
3	307	8.455	8.490	0.035
3	308	8.436	8.481	0.045
3	309	8.446	8.493	0.047
5	310	8.417	8.447	0.030
5	311	8.596	8.523	-0.073
5	312	8.464	8.520	0.056
5	314	8.484	8.484	0.000
5	316	8.503	8.548	0.045
10	317	8.495	8.518	0.023
10	318	8.344	8.426	0.082
10	319	8.457	8.506	0.049
10	321	8.387	8.499	0.112
10	322	8.495	8.491	-0.004
15	323	8.463	8.556	0.093
15	325	8.494	8.514	0.020
15	326	8.445	8.536	0.091
15	328	8.413	8.487	0.074
15	333	8.468	8.546	0.078
30	334	8.437	8.533	0.096
30	335	8.471	8.559	0.088
30	337	8.417	8.491	0.074
30	339	8.422	8.672	0.250
30	341	8.496	8.601	0.105
35	342	8.391	8.568	0.177
35	345	8.466	8.583	0.117
35	346	8.414	8.581	0.167
35	347	8.474	8.626	0.152
35	348	8.405	8.536	0.131
50	349	8.512	8.459	-0.053
50	350	8.446	8.479	0.033
50	351	8.501	8.423	-0.078
50	353	8.469	8.378	-0.091
50	354	8.463	8.435	-0.028
55	355	8.507	8.460	-0.047
55	357	8.501	8.510	0.009
55	359	8.438	8.437	-0.001
55	362	8.429	8.396	-0.033
55	363	8.393	8.411	0.018
100	1	8.530	8.455	-0.075
100	3	8.486	8.405	-0.081
100	4	8.483	8.443	-0.040
100	5	8.480	8.459	-0.021
100	6	8.471	8.367	-0.104
100	7	8.458	8.480	0.022
100	8	8.474	8.419	-0.055
100	9	8.522	8.486	-0.036
100	10	8.511	8.514	0.003
100	11	8.487	8.425	-0.062
100	12	8.431	8.406	-0.025
100	13	8.456	8.466	0.010
100	14	8.524	8.477	-0.047
100	15	8.474	8.452	-0.022
100	16	8.488	8.471	-0.017
100	17	8.498	8.468	-0.030
100	18	8.480	8.428	-0.052
100	19	8.393	8.471	0.078
100	20	8.493	8.455	-0.038
100	21	8.454	8.491	0.037
100	22	8.472	8.443	-0.029
100	24	8.374	8.368	-0.006
105	25	8.453	8.512	0.059
105	26	8.444	8.509	0.065
105	27	8.421	8.475	0.054
105	28	8.529	8.514	-0.015
105	29	8.520	8.519	-0.001
	Max	8.596	8.672	0.250
	Average	8.463	8.485	0.021
	Min	8.344	8.367	-0.104
	Std Dev	0.044	0.059	0.070

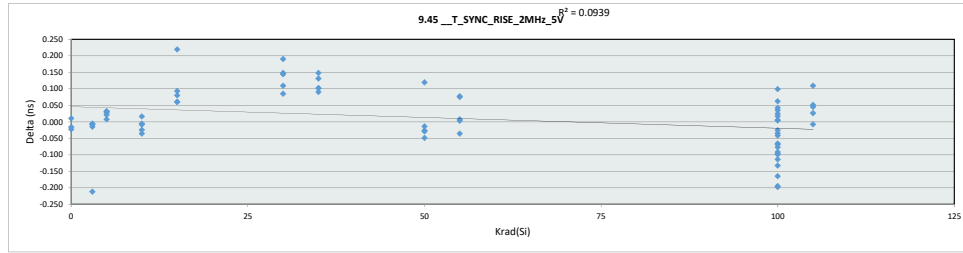


		9.41 T_SYNC_RISE_1p5MHz														
Test Site																
Tester																
Test Number																
Max Limit		15	ns													
Min Limit			ns													
Krad(Si)	LL	Min	Average	Max	UL	0	3	5	10	15	30	35	50	55	100	105
		8.430	8.466	8.520	15.000	8.430	8.466	8.463	8.430	8.430	8.430	8.430	8.430	8.430	8.430	8.430
		8.446	8.485	8.501	15.000	8.446	8.485	8.485	8.446	8.446	8.446	8.446	8.446	8.446	8.446	8.446
		8.447	8.504	8.548	15.000	8.447	8.504	8.504	8.447	8.447	8.447	8.447	8.447	8.447	8.447	8.447
		8.426	8.488	8.518	15.000	8.426	8.488	8.488	8.426	8.426	8.426	8.426	8.426	8.426	8.426	8.426
		8.487	8.528	8.556	15.000	8.487	8.528	8.528	8.487	8.487	8.487	8.487	8.487	8.487	8.487	8.487
		8.491	8.571	8.672	15.000	8.491	8.571	8.571	8.491	8.491	8.491	8.491	8.491	8.491	8.491	8.491
		8.536	8.579	8.626	15.000	8.536	8.579	8.579	8.536	8.536	8.536	8.536	8.536	8.536	8.536	8.536
		8.378	8.435	8.479	15.000	8.378	8.435	8.435	8.378	8.378	8.378	8.378	8.378	8.378	8.378	8.378
		8.396	8.443	8.510	15.000	8.396	8.443	8.443	8.396	8.396	8.396	8.396	8.396	8.396	8.396	8.396
		8.367	8.448	8.514	15.000	8.367	8.448	8.448	8.367	8.367	8.367	8.367	8.367	8.367	8.367	8.367
		8.475	8.506	8.519	15.000	8.475	8.506	8.506	8.475	8.475	8.475	8.475	8.475	8.475	8.475	8.475

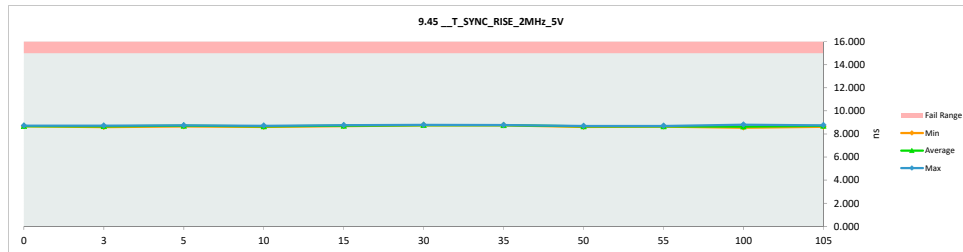


HDR TID Report
TPS7H5001-SP

		9.45 T_SYNC_RISE_2MHz_5V		
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.731	8.715	-0.016
0	118	8.654	8.664	0.010
0	122	8.686	8.663	-0.023
3	301	8.803	8.591	-0.212
3	306	8.672	8.663	-0.009
3	307	8.723	8.716	-0.007
3	308	8.668	8.653	-0.015
3	309	8.709	8.703	-0.006
5	310	8.616	8.647	0.031
5	311	8.689	8.721	0.032
5	312	8.705	8.726	0.021
5	314	8.714	8.741	0.027
5	316	8.738	8.745	0.007
10	317	8.730	8.694	-0.036
10	318	8.589	8.605	0.016
10	319	8.660	8.635	-0.025
10	321	8.666	8.657	-0.009
10	322	8.712	8.706	-0.006
15	323	8.616	8.696	0.080
15	325	8.533	8.752	0.219
15	326	8.655	8.715	0.060
15	328	8.608	8.668	0.060
15	333	8.644	8.737	0.093
30	334	8.643	8.752	0.109
30	335	8.574	8.764	0.190
30	337	8.640	8.788	0.148
30	339	8.680	8.765	0.085
30	341	8.620	8.764	0.144
35	342	8.617	8.748	0.131
35	345	8.669	8.771	0.102
35	346	8.669	8.759	0.090
35	347	8.598	8.746	0.148
35	348	8.642	8.744	0.102
50	349	8.569	8.688	0.119
50	350	8.708	8.659	-0.049
50	351	8.678	8.649	-0.029
50	353	8.712	8.685	-0.027
50	354	8.616	8.602	-0.014
55	355	8.616	8.693	0.077
55	357	8.659	8.666	0.007
55	359	8.606	8.681	0.075
55	362	8.667	8.631	-0.036
55	363	8.627	8.630	0.003
100	1	8.644	8.618	-0.026
100	3	8.658	8.700	0.042
100	4	8.783	8.585	-0.198
100	5	8.689	8.713	0.024
100	6	8.589	8.547	-0.042
100	7	8.664	8.565	-0.099
100	8	8.639	8.542	-0.097
100	9	8.726	8.593	-0.133
100	10	8.781	8.798	0.017
100	11	8.748	8.634	-0.114
100	12	8.572	8.671	0.099
100	13	8.756	8.761	0.005
100	14	8.719	8.642	-0.077
100	15	8.714	8.776	0.062
100	16	8.729	8.637	-0.092
100	17	8.739	8.544	-0.195
100	18	8.750	8.585	-0.165
100	19	8.586	8.621	0.035
100	20	8.729	8.660	-0.069
100	21	8.729	8.663	-0.066
100	22	8.614	8.618	0.004
100	24	8.606	8.571	-0.035
105	25	8.564	8.615	0.051
105	26	8.641	8.750	0.109
105	27	8.701	8.693	-0.008
105	28	8.714	8.740	0.026
105	29	8.688	8.733	0.045
Max		8.803	8.798	0.219
Average		8.669	8.680	0.011
Min		8.533	8.542	-0.212
Std Dev		0.059	0.065	0.087

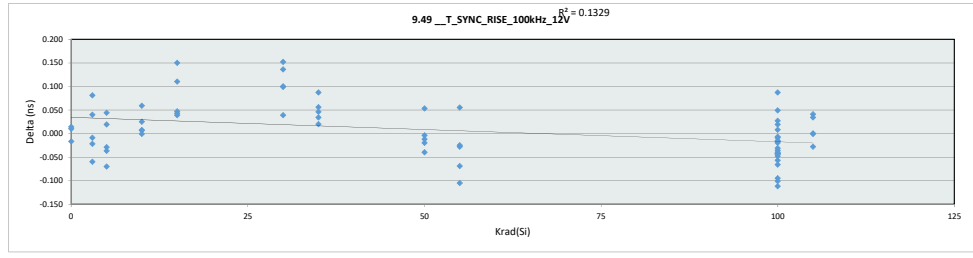


		9.45 T_SYNC_RISE_2MHz_5V												
Test Site														
Tester														
Test Number														
Max Limit	15	ns												
Min Limit		ns												
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105			
LL														
Min	8.663	8.591	8.647	8.605	8.668	8.752	8.744	8.602	8.630	8.542	8.615			
Average	8.681	8.665	8.716	8.659	8.714	8.767	8.754	8.657	8.660	8.638	8.706			
Max	8.715	8.716	8.745	8.706	8.752	8.788	8.771	8.688	8.693	8.798	8.750			
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000			

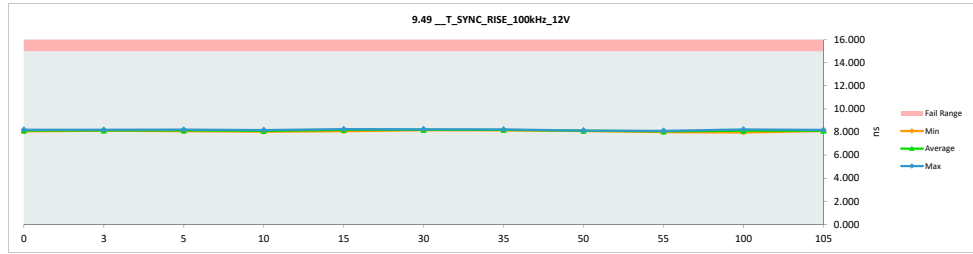


HDR TID Report
TPS7H5001-SP

9.49 T_SYNC_RISE_100kHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.182	8.196	0.014
0	118	8.043	8.053	0.010
0	122	8.156	8.139	-0.017
3	301	8.165	8.105	-0.060
3	306	8.107	8.188	0.081
3	307	8.150	8.128	-0.022
3	308	8.083	8.123	0.040
3	309	8.144	8.135	-0.009
5	310	8.144	8.074	-0.070
5	311	8.085	8.104	0.019
5	312	8.060	8.104	0.044
5	314	8.230	8.201	-0.029
5	316	8.210	8.173	-0.037
10	317	8.097	8.104	0.007
10	318	8.094	8.093	-0.001
10	319	8.140	8.165	0.025
10	321	8.018	8.025	0.007
10	322	8.062	8.121	0.059
15	323	8.136	8.246	0.110
15	325	8.154	8.201	0.047
15	326	8.090	8.240	0.150
15	328	8.023	8.062	0.039
15	333	8.086	8.129	0.043
30	334	8.053	8.153	0.100
30	335	8.117	8.156	0.039
30	337	8.136	8.235	0.099
30	339	8.029	8.181	0.152
30	341	8.088	8.224	0.136
35	342	8.088	8.175	0.087
35	345	8.123	8.143	0.020
35	346	8.169	8.215	0.046
35	347	8.181	8.215	0.034
35	348	8.102	8.158	0.056
50	349	8.107	8.095	-0.012
50	350	8.133	8.129	-0.004
50	351	8.161	8.121	-0.040
50	353	8.100	8.080	-0.020
50	354	8.074	8.127	0.053
55	355	8.195	8.090	-0.105
55	357	8.076	8.051	-0.025
55	359	8.089	8.020	-0.069
55	362	8.044	8.099	0.055
55	363	8.018	7.990	-0.028
100	1	8.141	8.099	-0.042
100	3	8.108	8.195	0.087
100	4	8.157	8.184	0.027
100	5	8.149	8.142	-0.007
100	6	8.138	8.026	-0.112
100	7	8.098	8.062	-0.036
100	8	8.158	8.092	-0.066
100	9	8.220	8.180	-0.040
100	10	8.166	8.215	0.049
100	11	8.068	8.024	-0.044
100	12	8.142	8.111	-0.031
100	13	8.094	8.113	0.019
100	14	8.162	8.114	-0.048
100	15	8.112	8.095	-0.017
100	16	8.214	8.119	-0.095
100	17	8.108	8.088	-0.020
100	18	8.165	8.064	-0.101
100	19	8.037	8.045	0.008
100	20	8.127	8.084	-0.043
100	21	8.170	8.155	-0.015
100	22	8.161	8.152	-0.009
100	24	8.018	7.961	-0.057
105	25	8.110	8.082	-0.028
105	26	8.051	8.085	0.034
105	27	8.176	8.175	-0.001
105	28	8.134	8.134	0.000
105	29	8.065	8.106	0.041
	Max	8.230	8.246	0.152
	Average	8.117	8.124	0.007
	Min	8.018	7.961	-0.112
	Std Dev	0.052	0.061	0.058

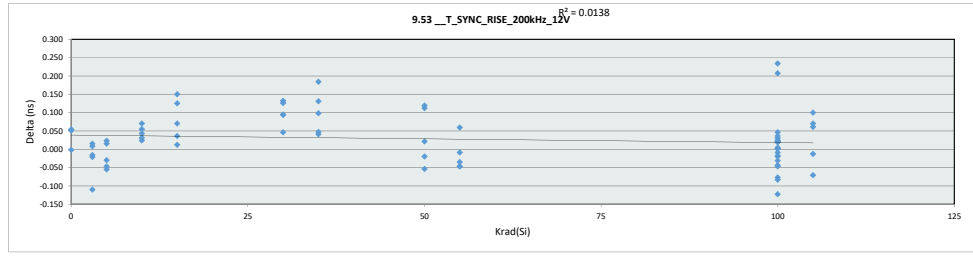


9.49 T_SYNC_RISE_100kHz											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.053	8.105	8.074	8.025	8.062	8.153	8.143	8.080	7.990	7.961	8.082
Average	8.129	8.136	8.131	8.102	8.176	8.190	8.181	8.110	8.050	8.105	8.116
Max	8.196	8.188	8.201	8.165	8.246	8.235	8.215	8.129	8.099	8.215	8.175
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

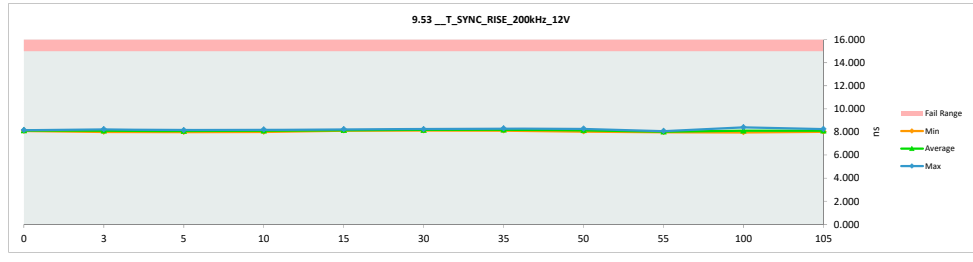


HDR TID Report
TPS7H5001-SP

9.53 T_SYNC_RISE_200kHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.162	8.160	-0.002
0	118	8.037	8.088	0.051
0	122	8.091	8.145	0.054
3	301	8.131	8.115	-0.016
3	306	8.116	8.095	-0.021
3	307	8.116	8.131	0.015
3	308	8.109	7.999	-0.110
3	309	8.218	8.226	0.008
5	310	8.072	8.095	0.023
5	311	8.082	8.097	0.015
5	312	8.049	7.994	-0.055
5	314	8.195	8.148	-0.047
5	316	8.150	8.120	-0.030
10	317	8.114	8.145	0.031
10	318	8.072	8.142	0.070
10	319	8.153	8.196	0.043
10	321	7.981	8.005	0.024
10	322	8.051	8.106	0.055
15	323	8.108	8.233	0.125
15	325	8.122	8.192	0.070
15	326	8.100	8.112	0.012
15	328	7.998	8.148	0.150
15	333	8.076	8.112	0.036
30	334	8.010	8.142	0.132
30	335	8.102	8.228	0.126
30	337	8.102	8.148	0.046
30	339	8.024	8.118	0.094
30	341	8.138	8.232	0.094
35	342	8.069	8.110	0.041
35	345	8.087	8.218	0.131
35	346	8.157	8.204	0.047
35	347	8.110	8.294	0.184
35	348	8.086	8.184	0.098
50	349	8.088	8.034	-0.054
50	350	8.097	8.118	0.021
50	351	8.154	8.266	0.112
50	353	8.132	8.112	-0.020
50	354	8.039	8.158	0.119
55	355	8.115	8.068	-0.047
55	357	7.993	8.052	0.059
55	359	8.066	8.019	-0.047
55	362	8.071	8.036	-0.035
55	363	7.988	7.979	-0.009
100	1	8.109	8.110	0.001
100	3	8.132	8.151	0.019
100	4	8.110	8.131	0.021
100	5	8.073	8.103	0.030
100	6	8.068	8.059	-0.009
100	7	8.030	8.012	-0.018
100	8	8.122	8.145	0.023
100	9	8.164	8.398	0.234
100	10	8.172	8.125	-0.047
100	11	8.016	8.035	0.019
100	12	8.094	8.074	-0.020
100	13	8.025	8.071	0.046
100	14	8.129	8.165	0.036
100	15	8.202	8.079	-0.123
100	16	8.125	8.127	0.002
100	17	8.074	8.030	-0.044
100	18	8.128	8.335	0.207
100	19	8.072	7.989	-0.083
100	20	8.098	8.102	0.004
100	21	8.153	8.176	0.023
100	22	8.114	8.083	-0.031
100	24	8.035	7.958	-0.077
105	25	8.031	8.101	0.070
105	26	8.035	8.096	0.061
105	27	8.148	8.248	0.100
105	28	8.139	8.126	-0.013
105	29	8.093	8.022	-0.071
Max		8.218	8.398	0.234
Average		8.095	8.123	0.028
Min		7.981	7.958	-0.123
Std Dev		0.052	0.083	0.071

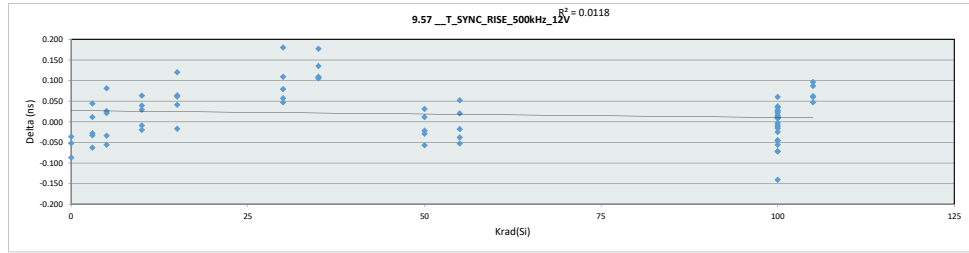


9.53 T_SYNC_RISE_200kHz											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.088	7.999	7.994	8.005	8.112	8.118	8.110	8.034	7.979	7.958	8.022
Average	8.131	8.113	8.091	8.119	8.159	8.174	8.202	8.138	8.031	8.112	8.119
Max	8.160	8.226	8.148	8.196	8.233	8.232	8.294	8.266	8.068	8.398	8.248
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

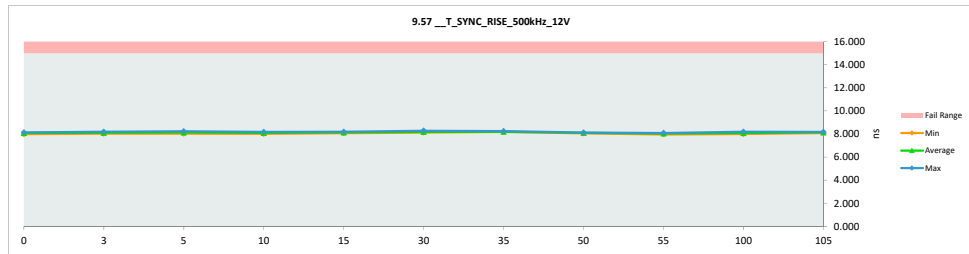


HDR TID Report
TPS7H5001-SP

9.57 T_SYNC_RISE_500kHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.171	8.135	-0.036
0	118	8.064	7.977	-0.087
0	122	8.165	8.113	-0.052
3	301	8.064	8.108	0.044
3	306	8.082	8.019	-0.063
3	307	8.150	8.122	-0.028
3	308	8.105	8.072	-0.033
3	309	8.171	8.182	0.011
5	310	8.099	8.180	0.081
5	311	8.083	8.104	0.021
5	312	8.082	8.026	-0.056
5	314	8.255	8.221	-0.034
5	316	8.110	8.136	0.026
10	317	8.086	8.125	0.039
10	318	8.027	8.056	0.029
10	319	8.107	8.170	0.063
10	321	8.027	8.007	-0.020
10	322	8.083	8.074	-0.009
15	323	8.126	8.190	0.064
15	325	8.179	8.162	-0.017
15	326	8.089	8.150	0.061
15	328	8.022	8.063	0.041
15	333	7.990	8.110	0.120
30	334	8.003	8.112	0.109
30	335	8.113	8.170	0.057
30	337	8.103	8.182	0.079
30	339	8.102	8.149	0.047
30	341	8.097	8.277	0.180
35	342	8.007	8.184	0.177
35	345	8.067	8.172	0.105
35	346	8.083	8.192	0.109
35	347	8.136	8.243	0.107
35	348	8.056	8.191	0.135
50	349	8.073	8.044	-0.029
50	350	8.095	8.106	0.011
50	351	8.132	8.110	-0.022
50	353	8.067	8.098	0.031
50	354	8.114	8.057	-0.057
55	355	8.091	8.073	-0.018
55	357	8.033	8.085	0.052
55	359	8.036	8.056	0.020
55	362	8.077	8.039	-0.038
55	363	8.011	7.958	-0.053
100	1	8.079	8.076	-0.003
100	3	8.163	8.091	-0.072
100	4	8.132	8.154	0.022
100	5	8.079	8.063	-0.016
100	6	8.032	8.041	0.009
100	7	8.029	8.054	0.025
100	8	8.145	8.004	-0.141
100	9	8.181	8.191	0.010
100	10	8.188	8.180	-0.008
100	11	7.980	8.040	0.060
100	12	8.041	8.078	0.037
100	13	8.097	8.051	-0.046
100	14	8.114	8.058	-0.056
100	15	8.049	8.065	0.016
100	16	8.182	8.110	-0.072
100	17	8.046	8.001	-0.045
100	18	8.080	8.068	-0.012
100	19	7.996	8.009	0.013
100	20	8.104	8.112	0.008
100	21	8.137	8.165	0.028
100	22	8.128	8.103	-0.025
100	24	7.953	7.988	0.035
105	25	8.027	8.074	0.047
105	26	8.040	8.136	0.096
105	27	8.107	8.167	0.060
105	28	8.064	8.151	0.087
105	29	8.061	8.123	0.062
Max		8.255	8.277	0.180
Average		8.087	8.105	0.018
Min		7.953	7.958	-0.141
Std Dev		0.057	0.067	0.062

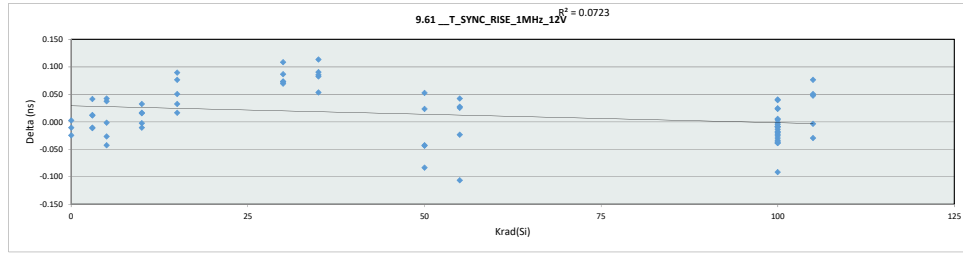


9.57 T_SYNC_RISE_500kHz											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.977	8.019	8.026	8.007	8.063	8.112	8.172	8.044	7.958	7.988	8.074
Average	8.075	8.101	8.133	8.086	8.135	8.178	8.196	8.083	8.042	8.077	8.130
Max	8.135	8.182	8.221	8.170	8.190	8.277	8.243	8.110	8.085	8.191	8.167
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

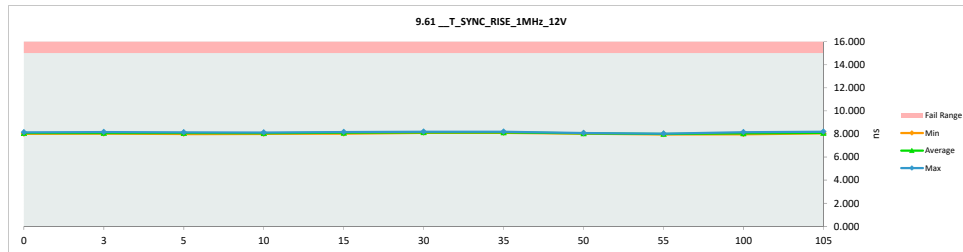


HDR TID Report
TPS7H5001-SP

		9.61 T_SYNC_RISE_1MHz_12V		
Test Site				
Tester				
Test Number				
Unit		ns	ns	
Max Limit		15	15	
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.139	8.128	-0.011
0	118	8.035	8.010	-0.025
0	122	8.093	8.095	0.002
3	301	8.031	8.072	0.041
3	306	8.013	8.024	0.011
3	307	8.083	8.072	-0.011
3	308	8.081	8.093	0.012
3	309	8.170	8.158	-0.012
5	310	8.096	8.069	-0.027
5	311	8.060	8.058	-0.002
5	312	7.962	7.999	0.037
5	314	8.177	8.134	-0.043
5	316	8.079	8.121	0.042
10	317	8.108	8.123	0.015
10	318	8.009	8.025	0.016
10	319	8.113	8.110	-0.003
10	321	7.972	8.004	0.032
10	322	8.055	8.044	-0.011
15	323	8.120	8.152	0.032
15	325	8.080	8.156	0.076
15	326	8.032	8.121	0.089
15	328	7.974	8.024	0.050
15	333	8.038	8.054	0.016
30	334	8.027	8.099	0.072
30	335	8.051	8.120	0.069
30	337	8.094	8.167	0.073
30	339	8.030	8.116	0.086
30	341	8.080	8.188	0.108
35	342	8.008	8.093	0.085
35	345	8.063	8.116	0.053
35	346	8.046	8.136	0.090
35	347	8.069	8.182	0.113
35	348	8.062	8.144	0.082
50	349	8.026	8.049	0.023
50	350	8.075	8.031	-0.044
50	351	8.118	8.075	-0.043
50	353	8.112	8.028	-0.084
50	354	8.006	8.058	0.052
55	355	8.122	8.015	-0.107
55	357	7.983	8.025	0.042
55	359	7.977	8.004	0.027
55	362	7.990	8.015	0.025
55	363	7.986	7.962	-0.024
100	1	8.094	8.055	-0.039
100	3	8.091	8.086	-0.005
100	4	8.088	8.078	-0.010
100	5	8.068	8.038	-0.030
100	6	8.035	8.033	-0.002
100	7	8.036	8.016	-0.020
100	8	8.135	8.096	-0.039
100	9	8.119	8.142	0.023
100	10	8.141	8.144	0.003
100	11	7.964	8.003	0.039
100	12	8.039	8.025	-0.014
100	13	8.009	8.000	-0.009
100	14	8.078	8.044	-0.034
100	15	8.076	8.050	-0.026
100	16	8.132	8.095	-0.037
100	17	8.064	7.972	-0.092
100	18	8.070	8.047	-0.023
100	19	7.966	8.006	0.040
100	20	8.047	8.052	0.005
100	21	8.120	8.102	-0.018
100	22	8.043	8.067	0.024
100	24	7.985	7.976	-0.009
105	25	7.998	8.045	0.047
105	26	8.020	8.070	0.050
105	27	8.116	8.192	0.076
105	28	8.088	8.084	-0.004
105	29	8.101	8.071	-0.030
Max		8.177	8.192	0.113
Average		8.060	8.072	0.013
Min		7.962	7.962	-0.107
Std Dev		0.052	0.055	0.047

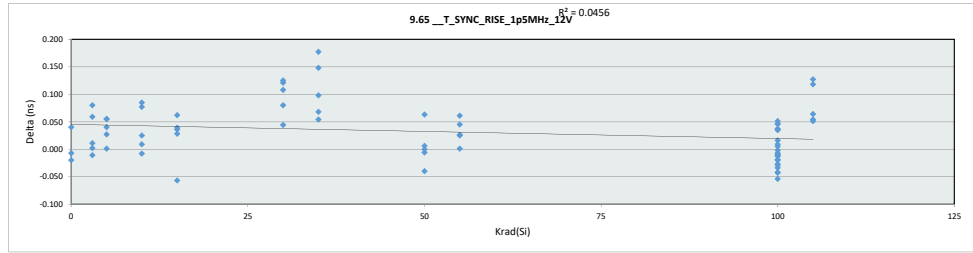


		9.61 T_SYNC_RISE_1MHz_12V														
Test Site																
Tester																
Test Number																
Max Limit		15	ns													
Min Limit			ns													
Krad(Si)	LL	Min	Average	Max	UL	0	3	5	10	15	30	35	50	55	100	105
LL																
Min		8.010	8.024	7.999	8.004	8.024	8.099	8.093	8.028	7.962	7.972	8.045				
Average		8.078	8.084	8.076	8.061	8.101	8.138	8.134	8.048	8.004	8.051	8.092				
Max		8.128	8.158	8.134	8.123	8.156	8.188	8.182	8.075	8.025	8.144	8.192				
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000				

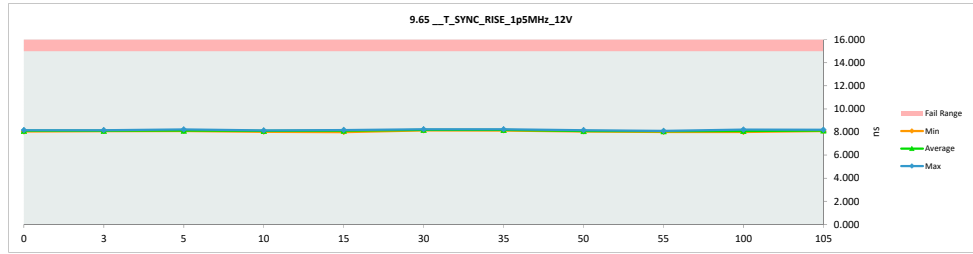


HDR TID Report
TPS7H5001-SP

		9.65 T_SYNC_RISE_1p5MHz_12V		
Test Site				
Tester				
Test Number				
Unit		ns	ns	
Max Limit		15	15	
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.161	8.154	-0.007
0	118	8.058	8.038	-0.020
0	122	8.078	8.118	0.040
3	301	8.098	8.087	-0.011
3	306	8.072	8.083	0.011
3	307	8.058	8.138	0.080
3	308	8.066	8.068	0.002
3	309	8.093	8.152	0.059
5	310	8.050	8.105	0.055
5	311	8.061	8.088	0.027
5	312	8.049	8.089	0.040
5	314	8.159	8.214	0.055
5	316	8.099	8.100	0.001
10	317	8.043	8.128	0.085
10	318	8.023	8.100	0.077
10	319	8.145	8.137	-0.008
10	321	7.991	8.016	0.025
10	322	8.086	8.095	0.009
15	323	8.133	8.169	0.036
15	325	8.087	8.149	0.062
15	326	8.089	8.128	0.039
15	328	8.047	7.990	-0.057
15	333	8.101	8.129	0.028
30	334	8.079	8.204	0.125
30	335	8.082	8.162	0.080
30	337	8.064	8.185	0.121
30	339	8.093	8.137	0.044
30	341	8.123	8.231	0.108
35	342	8.029	8.177	0.148
35	345	8.072	8.126	0.054
35	346	8.054	8.231	0.177
35	347	8.108	8.206	0.098
35	348	8.083	8.151	0.068
50	349	7.986	8.049	0.063
50	350	8.080	8.080	0.000
50	351	8.148	8.154	0.006
50	353	8.054	8.048	-0.006
50	354	8.082	8.042	-0.040
55	355	8.089	8.090	0.001
55	357	8.046	8.072	0.026
55	359	7.995	8.056	0.061
55	362	8.050	8.095	0.045
55	363	7.979	8.004	0.025
100	1	8.050	8.085	0.035
100	3	8.154	8.170	0.016
100	4	8.133	8.120	-0.013
100	5	8.113	8.102	-0.011
100	6	8.029	8.020	-0.009
100	7	8.027	8.020	-0.007
100	8	8.113	8.122	0.009
100	9	8.185	8.165	-0.020
100	10	8.159	8.205	0.046
100	11	8.043	8.024	-0.019
100	12	8.067	8.025	-0.042
100	13	8.067	8.072	0.005
100	14	8.111	8.084	-0.027
100	15	8.048	8.083	0.035
100	16	8.163	8.134	-0.029
100	17	8.102	8.068	-0.034
100	18	8.098	8.044	-0.054
100	19	7.998	7.996	-0.002
100	20	8.069	8.120	0.051
100	21	8.166	8.123	-0.043
100	22	8.084	8.121	0.037
100	24	7.956	8.002	0.046
105	25	8.022	8.076	0.054
105	26	8.047	8.111	0.064
105	27	8.073	8.191	0.118
105	28	8.107	8.158	0.051
105	29	7.984	8.111	0.127
	Max	8.185	8.231	0.177
	Average	8.076	8.108	0.032
	Min	7.956	7.990	-0.057
	Std Dev	0.049	0.059	0.050

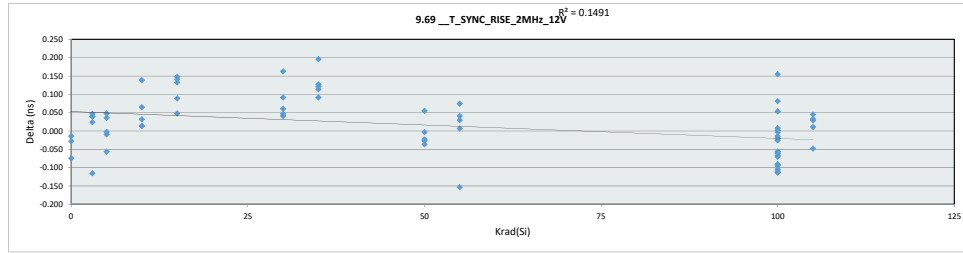


		9.65 T_SYNC_RISE_1p5MHz											
Test Site													
Tester													
Test Number													
Max Limit		15	ns										
Min Limit			ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105		
LL													
Min	8.038	8.068	8.088	8.016	7.990	8.137	8.126	8.042	8.004	7.996	8.076		
Average	8.103	8.106	8.119	8.095	8.113	8.184	8.178	8.075	8.063	8.087	8.129		
Max	8.154	8.152	8.214	8.137	8.169	8.231	8.231	8.154	8.095	8.205	8.191		
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000		

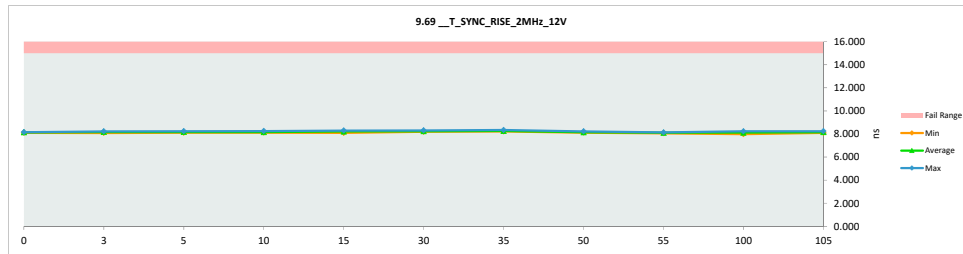


HDR TID Report
TPS7H5001-SP

		9.69 T_SYNC_RISE_2MHz_12V		
Test Site				
Tester				
Test Number				
Unit		ns	ns	
Max Limit		15	15	
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.233	8.158	-0.075
0	118	8.115	8.100	-0.015
0	122	8.185	8.156	-0.029
3	301	8.198	8.082	-0.116
3	306	8.141	8.180	0.039
3	307	8.163	8.209	0.046
3	308	8.139	8.162	0.023
3	309	8.188	8.227	0.039
5	310	8.101	8.148	0.047
5	311	8.139	8.136	-0.003
5	312	8.119	8.109	-0.010
5	314	8.300	8.243	-0.057
5	316	8.167	8.202	0.035
10	317	8.122	8.186	0.064
10	318	8.103	8.116	0.013
10	319	8.114	8.252	0.138
10	321	8.079	8.110	0.031
10	322	8.139	8.152	0.013
15	323	8.147	8.294	0.147
15	325	8.096	8.236	0.140
15	326	8.133	8.265	0.132
15	328	8.046	8.093	0.047
15	333	8.116	8.204	0.088
30	334	8.150	8.190	0.040
30	335	8.157	8.217	0.060
30	337	8.193	8.239	0.046
30	339	8.134	8.225	0.091
30	341	8.142	8.304	0.162
35	342	8.118	8.231	0.113
35	345	8.088	8.214	0.126
35	346	8.145	8.340	0.195
35	347	8.189	8.280	0.091
35	348	8.111	8.231	0.120
50	349	8.141	8.117	-0.024
50	350	8.191	8.164	-0.027
50	351	8.227	8.223	-0.004
50	353	8.148	8.111	-0.037
50	354	8.067	8.121	0.054
55	355	8.257	8.103	-0.154
55	357	8.100	8.129	0.029
55	359	8.060	8.134	0.074
55	362	8.067	8.107	0.040
55	363	8.054	8.060	0.006
100	1	8.155	8.050	-0.105
100	3	8.218	8.219	0.001
100	4	8.203	8.141	-0.062
100	5	8.101	8.154	0.053
100	6	8.140	8.045	-0.095
100	7	8.115	8.094	-0.021
100	8	8.289	8.232	-0.057
100	9	8.264	8.150	-0.114
100	10	8.225	8.232	0.007
100	11	8.143	8.050	-0.093
100	12	8.044	8.198	0.154
100	13	8.101	8.075	-0.026
100	14	8.210	8.119	-0.091
100	15	8.090	8.171	0.081
100	16	8.234	8.163	-0.071
100	17	8.230	8.120	-0.110
100	18	8.118	8.114	-0.004
100	19	8.032	8.017	-0.015
100	20	8.174	8.116	-0.058
100	21	8.201	8.180	-0.021
100	22	8.169	8.055	-0.114
100	24	8.052	7.984	-0.068
105	25	8.088	8.098	0.010
105	26	8.124	8.156	0.032
105	27	8.191	8.235	0.044
105	28	8.176	8.127	-0.049
105	29	8.125	8.153	0.028
	Max	8.300	8.340	0.195
	Average	8.146	8.160	0.014
	Min	8.032	7.984	-0.154
	Std Dev	0.060	0.071	0.078

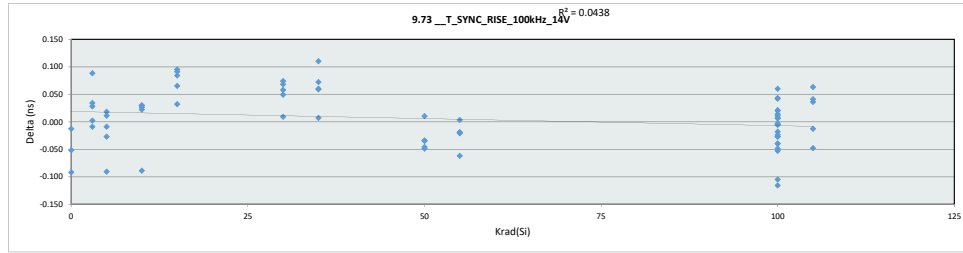


		9.69 T_SYNC_RISE_2MHz_12V											
Test Site													
Tester													
Test Number													
Max Limit		15	ns										
Min Limit			ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105		
LL													
Min	8.100	8.082	8.109	8.110	8.093	8.190	8.214	8.111	8.060	7.984	8.098		
Average	8.138	8.172	8.168	8.163	8.218	8.235	8.259	8.147	8.107	8.122	8.154		
Max	8.158	8.227	8.243	8.252	8.294	8.304	8.340	8.223	8.134	8.232	8.235		
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000		

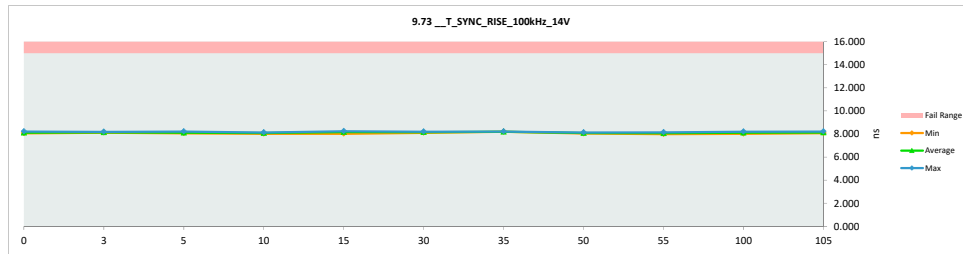


HDR TID Report
TPS7H5001-SP

		9.73 T_SYNC_RISE_100kHz_14V		
Test Site				
Tester				
Test Number				
Unit		ns	ns	
Max Limit		15	15	
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.221	8.208	-0.013
0	118	8.116	8.064	-0.052
0	122	8.165	8.073	-0.092
3	301	8.083	8.111	0.028
3	306	8.100	8.188	0.088
3	307	8.146	8.148	0.002
3	308	8.122	8.156	0.034
3	309	8.140	8.131	-0.009
5	310	8.174	8.083	-0.091
5	311	8.098	8.089	-0.009
5	312	8.083	8.056	-0.027
5	314	8.207	8.218	0.011
5	316	8.131	8.149	0.018
10	317	8.173	8.084	-0.089
10	318	8.035	8.061	0.026
10	319	8.103	8.132	0.029
10	321	7.993	8.023	0.030
10	322	8.095	8.117	0.022
15	323	8.147	8.238	0.091
15	325	8.138	8.233	0.095
15	326	8.110	8.194	0.084
15	328	7.990	8.022	0.032
15	333	8.077	8.142	0.065
30	334	8.065	8.114	0.049
30	335	8.105	8.173	0.068
30	337	8.125	8.199	0.074
30	339	8.088	8.097	0.009
30	341	8.145	8.203	0.058
35	342	8.131	8.203	0.072
35	345	8.185	8.192	0.007
35	346	8.129	8.189	0.060
35	347	8.160	8.219	0.059
35	348	8.082	8.192	0.110
50	349	8.075	8.040	-0.035
50	350	8.151	8.117	-0.034
50	351	8.119	8.129	0.010
50	353	8.124	8.078	-0.046
50	354	8.126	8.077	-0.049
55	355	8.142	8.145	0.003
55	357	8.067	8.048	-0.019
55	359	8.122	8.060	-0.062
55	362	8.114	8.094	-0.020
55	363	8.006	7.985	-0.021
100	1	8.064	8.037	-0.027
100	3	8.146	8.160	0.014
100	4	8.183	8.203	0.020
100	5	8.101	8.074	-0.027
100	6	8.116	8.063	-0.053
100	7	8.091	8.073	-0.018
100	8	8.142	8.148	0.006
100	9	8.285	8.169	-0.116
100	10	8.188	8.185	-0.003
100	11	8.034	8.077	0.043
100	12	8.188	8.083	-0.105
100	13	8.093	8.135	0.042
100	14	8.161	8.121	-0.040
100	15	8.094	8.089	-0.005
100	16	8.156	8.163	0.007
100	17	8.081	8.057	-0.024
100	18	8.142	8.093	-0.049
100	19	8.040	8.061	0.021
100	20	8.089	8.149	0.060
100	21	8.191	8.151	-0.040
100	22	8.098	8.109	0.011
100	24	8.028	8.022	-0.006
105	25	8.077	8.140	0.063
105	26	8.101	8.137	0.036
105	27	8.174	8.215	0.041
105	28	8.147	8.099	-0.048
105	29	8.085	8.072	-0.013
	Max	8.285	8.238	0.110
	Average	8.117	8.122	0.005
	Min	7.990	7.985	-0.116
	Std Dev	0.053	0.060	0.050

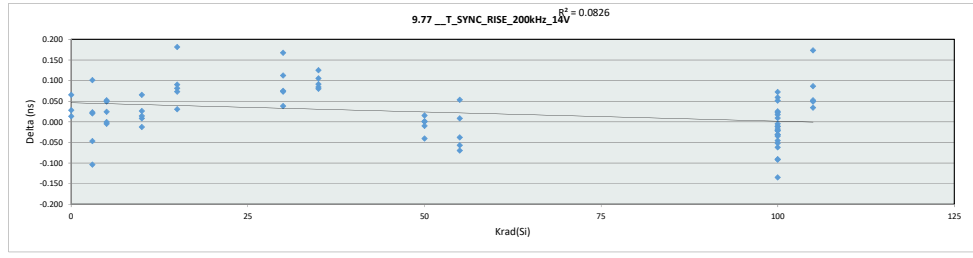


		9.73 T_SYNC_RISE_100kHz												
Test Site														
Tester														
Test Number														
Max Limit		15	ns											
Min Limit			ns											
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105	UL	
Min		8.064	8.111	8.056	8.023	8.022	8.097	8.189	8.040	7.985	8.022	8.072		
Average		8.115	8.147	8.119	8.083	8.166	8.157	8.199	8.088	8.066	8.110	8.133		
Max		8.208	8.188	8.218	8.132	8.238	8.203	8.219	8.129	8.145	8.203	8.215		
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000		

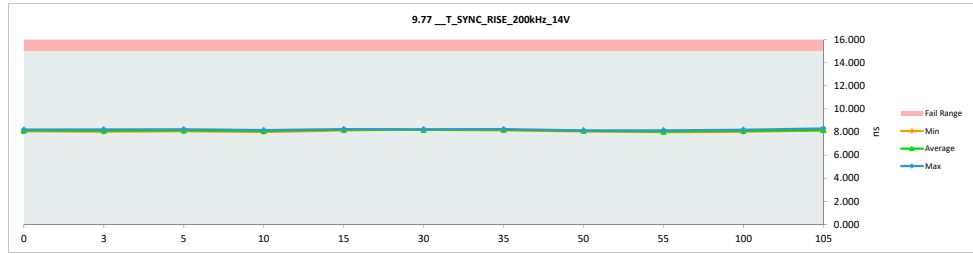


HDR TID Report
TPS7H5001-SP

9.77 T_SYNC_RISE_200kHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.173	8.201	0.028
0	118	7.993	8.058	0.065
0	122	8.080	8.093	0.013
3	301	8.123	8.076	-0.047
3	306	8.096	8.119	0.023
3	307	8.113	8.133	0.020
3	308	8.120	8.016	-0.104
3	309	8.112	8.213	0.101
5	310	8.121	8.116	-0.005
5	311	8.026	8.078	0.052
5	312	8.043	8.067	0.024
5	314	8.185	8.234	0.049
5	316	8.167	8.166	-0.001
10	317	8.078	8.092	0.014
10	318	8.025	8.012	-0.013
10	319	8.136	8.162	0.026
10	321	7.989	7.997	0.008
10	322	8.066	8.131	0.065
15	323	8.166	8.239	0.073
15	325	8.164	8.194	0.030
15	326	8.093	8.183	0.090
15	328	8.004	8.185	0.181
15	333	8.058	8.139	0.081
30	334	7.995	8.162	0.167
30	335	8.084	8.159	0.075
30	337	8.112	8.224	0.112
30	339	8.121	8.159	0.038
30	341	8.134	8.207	0.073
35	342	8.066	8.150	0.084
35	345	8.061	8.166	0.105
35	346	8.152	8.243	0.091
35	347	8.128	8.208	0.080
35	348	8.054	8.179	0.125
50	349	8.094	8.084	-0.010
50	350	8.108	8.108	0.000
50	351	8.182	8.141	-0.041
50	353	8.095	8.096	0.001
50	354	8.042	8.057	0.015
55	355	8.088	8.141	0.053
55	357	8.067	8.029	-0.038
55	359	8.078	8.008	-0.070
55	362	8.026	8.034	0.008
55	363	8.024	7.967	-0.057
100	1	8.098	8.076	-0.022
100	3	8.100	8.159	0.059
100	4	8.135	8.117	-0.018
100	5	8.119	8.027	-0.092
100	6	8.096	8.044	-0.052
100	7	8.046	8.055	0.009
100	8	8.108	8.180	0.072
100	9	8.206	8.160	-0.046
100	10	8.193	8.131	-0.062
100	11	8.042	8.037	-0.005
100	12	8.086	8.075	-0.011
100	13	8.018	8.069	0.051
100	14	8.101	8.089	-0.012
100	15	8.158	8.023	-0.135
100	16	8.145	8.113	-0.032
100	17	8.133	8.042	-0.091
100	18	8.110	8.088	-0.022
100	19	8.059	8.028	-0.031
100	20	8.168	8.133	-0.035
100	21	8.167	8.192	0.025
100	22	8.091	8.108	0.017
100	24	8.011	8.033	0.022
105	25	8.029	8.115	0.086
105	26	8.071	8.120	0.049
105	27	8.136	8.309	0.173
105	28	8.113	8.147	0.034
105	29	8.088	8.140	0.052
Max		8.206	8.309	0.181
Average		8.095	8.118	0.022
Min		7.989	7.967	-0.135
Std Dev		0.053	0.070	0.064

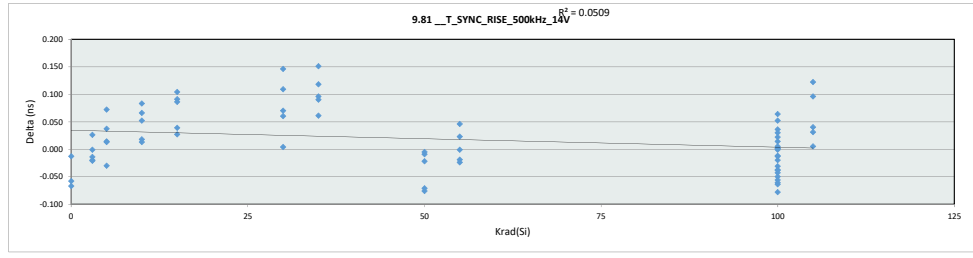


9.77 T_SYNC_RISE_200kHz											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.058	8.016	8.067	7.997	8.139	8.159	8.150	8.057	7.967	8.023	8.115
Average	8.117	8.111	8.132	8.079	8.188	8.182	8.189	8.097	8.036	8.090	8.166
Max	8.201	8.213	8.234	8.162	8.239	8.224	8.243	8.141	8.141	8.192	8.309
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

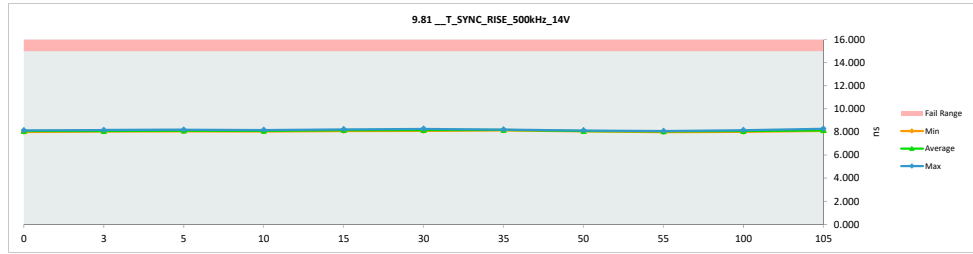


HDR TID Report
TPS7H5001-SP

9.81 T_SYNC_RISE_500kHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.190	8.132	-0.058
0	118	8.071	8.004	-0.067
0	122	8.105	8.092	-0.013
3	301	8.052	8.051	-0.001
3	306	8.056	8.042	-0.014
3	307	8.133	8.112	-0.021
3	308	8.060	8.040	-0.020
3	309	8.137	8.163	0.026
5	310	8.109	8.079	-0.030
5	311	8.056	8.093	0.037
5	312	7.985	8.057	0.072
5	314	8.176	8.190	0.014
5	316	8.125	8.138	0.013
10	317	8.091	8.104	0.013
10	318	7.979	8.062	0.083
10	319	8.103	8.169	0.066
10	321	7.981	8.033	0.052
10	322	8.107	8.125	0.018
15	323	8.121	8.225	0.104
15	325	8.114	8.205	0.091
15	326	8.064	8.150	0.086
15	328	8.045	8.084	0.039
15	333	8.084	8.111	0.027
30	334	8.061	8.121	0.060
30	335	8.035	8.144	0.109
30	337	8.116	8.186	0.070
30	339	8.084	8.088	0.004
30	341	8.121	8.267	0.146
35	342	8.042	8.132	0.090
35	345	8.073	8.169	0.096
35	346	8.052	8.203	0.151
35	347	8.128	8.189	0.061
35	348	8.065	8.183	0.118
50	349	8.070	8.061	-0.009
50	350	8.079	8.074	-0.005
50	351	8.150	8.128	-0.022
50	353	8.150	8.079	-0.071
50	354	8.126	8.050	-0.076
55	355	8.088	8.069	-0.019
55	357	8.031	8.077	0.046
55	359	8.030	8.053	0.023
55	362	8.071	8.070	-0.001
55	363	8.011	7.987	-0.024
100	1	8.091	8.090	-0.001
100	3	8.121	8.122	0.001
100	4	8.173	8.117	-0.056
100	5	8.131	8.070	-0.061
100	6	8.062	8.049	-0.013
100	7	8.064	8.033	-0.031
100	8	8.178	8.140	-0.038
100	9	8.181	8.103	-0.078
100	10	8.102	8.154	0.052
100	11	8.017	8.031	0.014
100	12	8.090	8.047	-0.043
100	13	8.039	8.041	0.002
100	14	8.148	8.084	-0.064
100	15	8.114	8.102	-0.012
100	16	8.186	8.136	-0.050
100	17	8.074	8.036	-0.038
100	18	8.066	8.046	-0.020
100	19	7.981	8.011	0.030
100	20	8.084	8.120	0.036
100	21	8.147	8.152	0.005
100	22	8.060	8.082	0.022
100	24	7.961	8.025	0.064
105	25	7.997	8.093	0.096
105	26	8.060	8.100	0.040
105	27	8.147	8.269	0.122
105	28	8.107	8.138	0.031
105	29	8.094	8.099	0.005
	Max	8.190	8.269	0.151
	Average	8.086	8.104	0.018
	Min	7.961	7.987	-0.078
	Std Dev	0.053	0.060	0.056

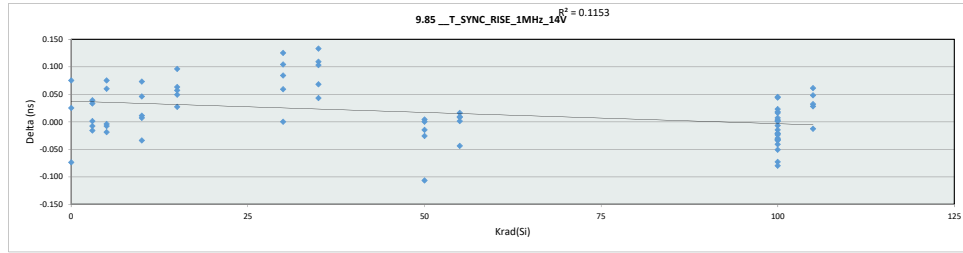


9.81 T_SYNC_RISE_500kHz											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.004	8.040	8.057	8.033	8.084	8.088	8.132	8.050	7.987	8.011	8.093
Average	8.076	8.082	8.111	8.099	8.155	8.161	8.175	8.078	8.051	8.081	8.140
Max	8.132	8.163	8.190	8.169	8.225	8.267	8.203	8.128	8.077	8.154	8.269
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

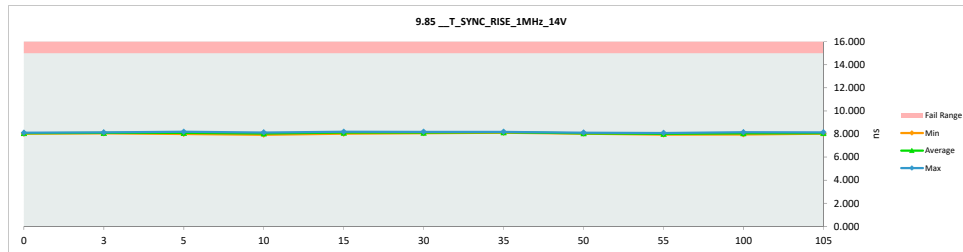


HDR TID Report
TPS7H5001-SP

		9.85 T_SYNC_RISE_1MHz_14V		
Test Site				
Tester				
Test Number				
Unit		ns	ns	
Max Limit		15	15	
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.166	8.092	-0.074
0	118	7.998	8.023	0.025
0	122	8.032	8.107	0.075
3	301	8.080	8.081	0.001
3	306	8.060	8.052	-0.008
3	307	8.076	8.115	0.039
3	308	8.047	8.080	0.033
3	309	8.148	8.132	-0.016
5	310	8.050	8.046	-0.004
5	311	7.994	8.069	0.075
5	312	8.008	8.000	-0.008
5	314	8.205	8.186	-0.019
5	316	8.105	8.165	0.060
10	317	8.017	8.063	0.046
10	318	8.026	8.033	0.007
10	319	8.120	8.131	0.011
10	321	7.964	7.930	-0.034
10	322	8.010	8.083	0.073
15	323	8.137	8.186	0.049
15	325	8.067	8.163	0.096
15	326	8.066	8.123	0.057
15	328	7.996	8.023	0.027
15	333	8.043	8.106	0.063
30	334	8.009	8.134	0.125
30	335	8.027	8.131	0.104
30	337	8.098	8.098	0.000
30	339	8.008	8.067	0.059
30	341	8.100	8.184	0.084
35	342	8.023	8.156	0.133
35	345	8.029	8.132	0.103
35	346	8.071	8.180	0.109
35	347	8.125	8.168	0.043
35	348	8.035	8.103	0.068
50	349	8.069	8.043	-0.026
50	350	8.065	8.069	0.004
50	351	8.115	8.115	0.000
50	353	8.138	8.031	-0.107
50	354	8.046	8.031	-0.015
55	355	8.076	8.085	0.009
55	357	8.019	8.020	0.001
55	359	7.992	8.000	0.008
55	362	8.020	8.036	0.016
55	363	7.993	7.949	-0.044
100	1	8.070	8.073	0.003
100	3	8.077	8.078	0.001
100	4	8.077	8.122	0.045
100	5	8.072	8.021	-0.051
100	6	8.041	8.017	-0.024
100	7	8.010	8.017	0.007
100	8	8.090	8.113	0.023
100	9	8.149	8.128	-0.021
100	10	8.139	8.124	-0.015
100	11	8.035	8.002	-0.033
100	12	8.068	8.038	-0.030
100	13	8.076	8.003	-0.073
100	14	8.092	8.093	0.001
100	15	8.067	8.026	-0.041
100	16	8.097	8.115	0.018
100	17	8.052	7.972	-0.080
100	18	8.064	8.042	-0.022
100	19	8.016	7.984	-0.032
100	20	8.073	8.039	-0.034
100	21	8.135	8.151	0.016
100	22	8.069	8.113	0.044
100	24	7.969	7.962	-0.007
105	25	7.991	8.023	0.032
105	26	7.991	8.052	0.061
105	27	8.115	8.143	0.028
105	28	8.045	8.093	0.048
105	29	8.080	8.067	-0.013
Max		8.205	8.186	0.133
Average		8.060	8.076	0.016
Min		7.964	7.930	-0.107
Std Dev		0.050	0.061	0.049

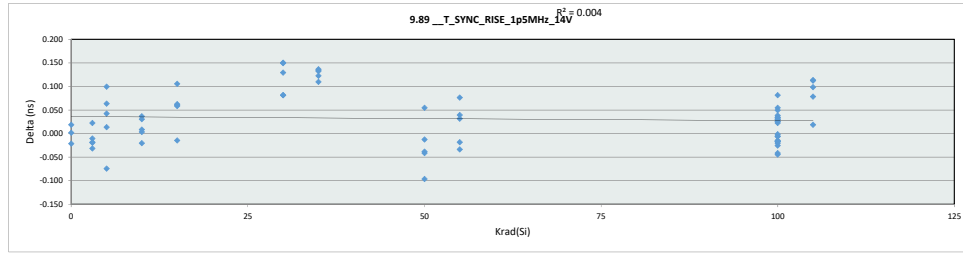


		9.85 T_SYNC_RISE_1MHz_14V										
Test Site												
Tester												
Test Number												
Max Limit		15	ns									
Min Limit			ns									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		8.023	8.052	8.000	7.930	8.023	8.067	8.103	8.031	7.949	7.962	8.023
Average		8.074	8.092	8.093	8.048	8.120	8.123	8.148	8.058	8.018	8.056	8.076
Max		8.107	8.132	8.186	8.131	8.186	8.184	8.180	8.115	8.085	8.151	8.143
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

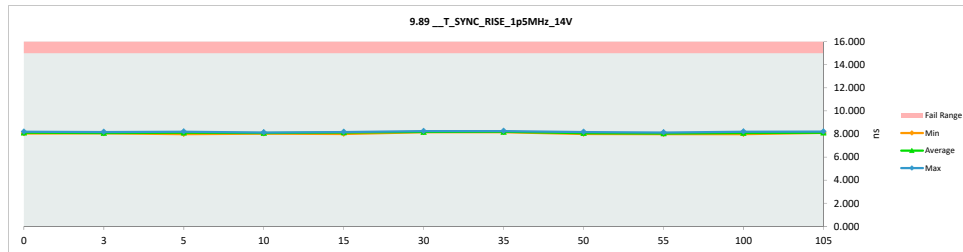


HDR TID Report
TPS7H5001-SP

		9.89 T_SYNC_RISE_1p5MHz_14V		
Test Site				
Tester				
Test Number				
Unit		ns	ns	
Max Limit		15	15	
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.173	8.191	0.018
0	118	8.043	8.044	0.001
0	122	8.134	8.112	-0.022
3	301	8.070	8.092	0.022
3	306	8.085	8.065	-0.020
3	307	8.099	8.088	-0.011
3	308	8.072	8.053	-0.019
3	309	8.198	8.166	-0.032
5	310	8.064	8.077	0.013
5	311	8.024	8.123	0.099
5	312	8.066	7.991	-0.075
5	314	8.152	8.194	0.042
5	316	8.113	8.176	0.063
10	317	8.127	8.106	-0.021
10	318	8.049	8.052	0.003
10	319	8.117	8.125	0.008
10	321	8.001	8.031	0.030
10	322	8.087	8.123	0.036
15	323	8.108	8.168	0.060
15	325	8.068	8.173	0.105
15	326	8.090	8.152	0.062
15	328	8.019	8.004	-0.015
15	333	8.085	8.143	0.058
30	334	8.082	8.163	0.081
30	335	8.071	8.152	0.081
30	337	8.051	8.180	0.129
30	339	8.000	8.150	0.150
30	341	8.098	8.247	0.149
35	342	8.025	8.157	0.132
35	345	8.037	8.159	0.122
35	346	8.114	8.250	0.136
35	347	8.085	8.219	0.134
35	348	8.047	8.156	0.109
50	349	8.146	8.049	-0.097
50	350	8.150	8.111	-0.039
50	351	8.113	8.167	0.054
50	353	8.065	8.052	-0.013
50	354	8.043	8.001	-0.042
55	355	8.056	8.132	0.076
55	357	8.029	7.995	-0.034
55	359	8.001	8.040	0.039
55	362	8.022	8.053	0.031
55	363	8.030	8.011	-0.019
100	1	8.094	8.077	-0.017
100	3	8.133	8.159	0.026
100	4	8.147	8.132	-0.015
100	5	8.112	8.067	-0.045
100	6	8.063	8.037	-0.026
100	7	8.051	8.076	0.025
100	8	8.130	8.112	-0.018
100	9	8.150	8.199	0.049
100	10	8.166	8.200	0.034
100	11	8.016	7.995	-0.021
100	12	8.055	8.038	-0.017
100	13	8.046	8.084	0.038
100	14	8.094	8.087	-0.007
100	15	8.080	8.074	-0.006
100	16	8.131	8.161	0.030
100	17	8.098	8.056	-0.042
100	18	8.060	8.058	-0.002
100	19	8.015	8.037	0.022
100	20	8.050	8.083	0.033
100	21	8.134	8.188	0.054
100	22	8.100	8.130	0.030
100	24	7.928	8.009	0.081
105	25	8.000	8.098	0.098
105	26	7.982	8.094	0.112
105	27	8.128	8.206	0.078
105	28	8.080	8.098	0.018
105	29	7.990	8.103	0.113
	Max	8.198	8.250	0.150
	Average	8.076	8.108	0.032
	Min	7.928	7.991	-0.097
	Std Dev	0.052	0.065	0.057

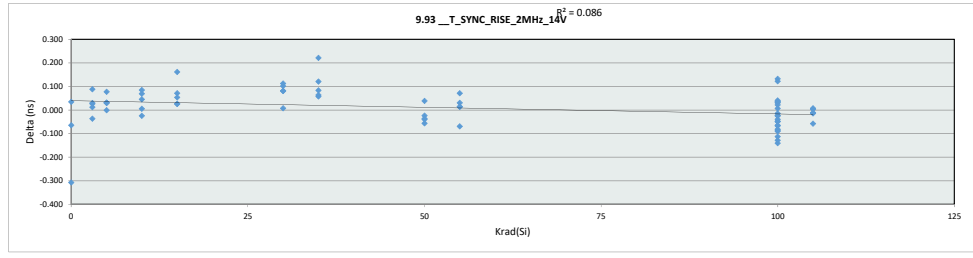


		9.89 T_SYNC_RISE_1p5MHz											
Test Site													
Tester													
Test Number													
Max Limit		15	ns										
Min Limit			ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105		
LL													
Min	8.044	8.053	7.991	8.031	8.004	8.150	8.156	8.001	7.995	7.995	8.094		
Average	8.116	8.093	8.112	8.087	8.128	8.178	8.188	8.076	8.046	8.094	8.120		
Max	8.191	8.166	8.194	8.125	8.173	8.247	8.250	8.167	8.132	8.200	8.206		
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000		

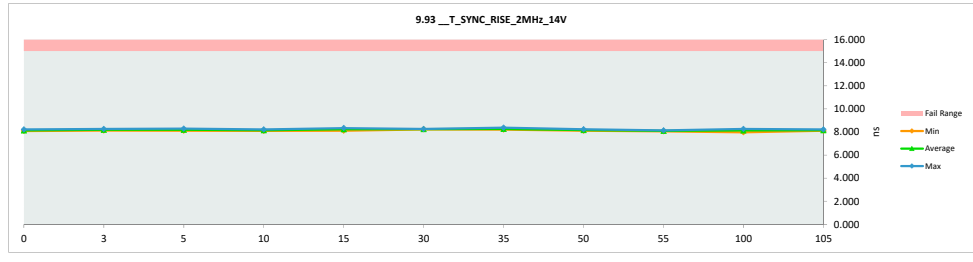


HDR TID Report
TPS7H5001-SP

		9.93 T_SYNC_RISE_2MHz_14V		
Test Site				
Tester				
Test Number				
Unit		ns	ns	
Max Limit		15	15	
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.392	8.084	-0.308
0	118	8.147	8.082	-0.065
0	122	8.177	8.211	0.034
3	301	8.152	8.240	0.088
3	306	8.157	8.120	-0.037
3	307	8.160	8.189	0.029
3	308	8.148	8.160	0.012
3	309	8.225	8.251	0.026
5	310	8.117	8.146	0.029
5	311	8.113	8.144	0.031
5	312	8.032	8.109	0.077
5	314	8.249	8.280	0.031
5	316	8.234	8.233	-0.001
10	317	8.106	8.190	0.084
10	318	8.054	8.123	0.069
10	319	8.207	8.212	0.005
10	321	8.045	8.090	0.045
10	322	8.157	8.132	-0.025
15	323	8.261	8.332	0.071
15	325	8.165	8.326	0.161
15	326	8.177	8.202	0.025
15	328	8.074	8.100	0.026
15	333	8.125	8.178	0.053
30	334	8.150	8.230	0.080
30	335	8.120	8.232	0.112
30	337	8.180	8.261	0.081
30	339	8.123	8.224	0.101
30	341	8.248	8.255	0.007
35	342	8.116	8.199	0.083
35	345	8.167	8.224	0.057
35	346	8.151	8.372	0.221
35	347	8.202	8.265	0.063
35	348	8.128	8.248	0.120
50	349	8.134	8.097	-0.037
50	350	8.196	8.139	-0.057
50	351	8.239	8.199	-0.040
50	353	8.192	8.230	0.038
50	354	8.122	8.098	-0.024
55	355	8.169	8.099	-0.070
55	357	8.104	8.134	0.030
55	359	8.053	8.124	0.071
55	362	8.035	8.049	0.014
55	363	8.057	8.070	0.013
100	1	8.220	8.079	-0.141
100	3	8.223	8.253	0.030
100	4	8.170	8.176	0.006
100	5	8.194	8.103	-0.091
100	6	8.116	8.051	-0.065
100	7	8.107	8.059	-0.048
100	8	8.123	8.245	0.122
100	9	8.253	8.167	-0.086
100	10	8.223	8.208	-0.015
100	11	7.990	8.024	0.034
100	12	8.227	8.248	0.021
100	13	8.134	8.109	-0.025
100	14	8.200	8.071	-0.129
100	15	8.173	8.091	-0.082
100	16	8.218	8.193	-0.025
100	17	8.002	8.134	0.132
100	18	8.150	8.082	-0.068
100	19	8.023	8.057	0.034
100	20	8.128	8.168	0.040
100	21	8.237	8.197	-0.040
100	22	8.147	8.097	-0.050
100	24	8.079	7.966	-0.113
105	25	8.097	8.100	0.003
105	26	8.155	8.097	-0.058
105	27	8.213	8.220	0.007
105	28	8.151	8.139	-0.012
105	29	8.154	8.139	-0.015
Max		8.392	8.372	0.221
Average		8.152	8.162	0.010
Min		7.990	7.966	-0.308
Std Dev		0.070	0.080	0.078

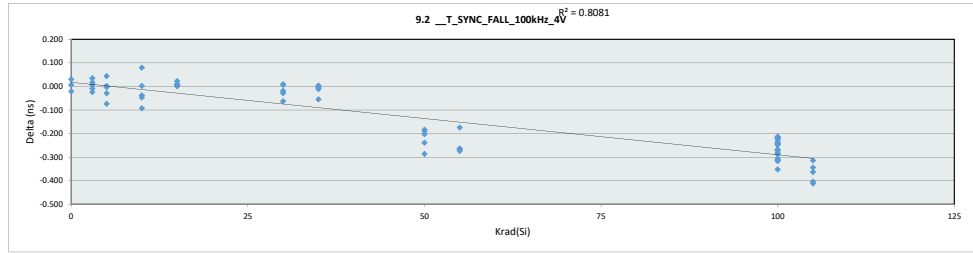


		9.93 T_SYNC_RISE_2MHz_14V												
Test Site														
Tester														
Test Number														
Max Limit		15	ns											
Min Limit			ns											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105			
LL														
Min	8.082	8.120	8.109	8.090	8.100	8.224	8.199	8.097	8.049	7.966	8.097			
Average	8.126	8.192	8.182	8.149	8.228	8.240	8.262	8.153	8.095	8.126	8.139			
Max	8.211	8.251	8.280	8.212	8.332	8.261	8.372	8.230	8.134	8.253	8.220			
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000			

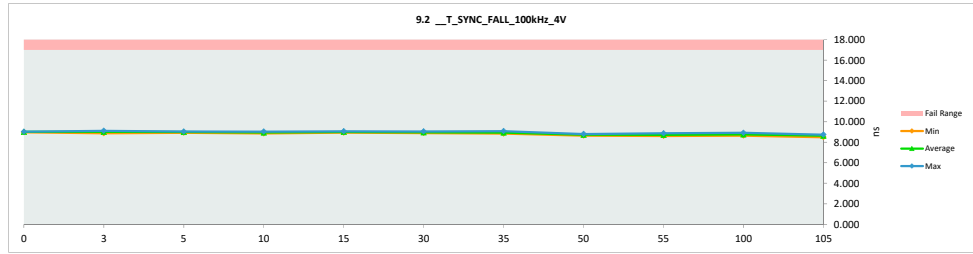


HDR TID Report
TPS7H5001-SP

9.2 T SYNC FALL 100kHz 4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	9.037	9.015	-0.022
0	118	9.021	9.027	0.006
0	122	8.940	8.969	0.029
3	301	9.038	9.028	-0.010
3	306	8.896	8.871	-0.025
3	307	9.014	9.048	0.034
3	308	8.980	8.986	0.006
3	309	9.094	9.110	0.016
5	310	9.029	9.030	0.001
5	311	9.039	9.009	-0.030
5	312	8.993	9.036	0.043
5	314	9.000	8.925	-0.075
5	316	8.995	8.991	-0.004
10	317	8.952	9.031	0.079
10	318	8.992	8.944	-0.048
10	319	8.895	8.856	-0.039
10	321	9.007	8.913	-0.094
10	322	8.989	8.990	0.001
15	323	8.925	8.933	0.008
15	325	9.063	9.063	0.000
15	326	9.040	9.049	0.009
15	328	8.935	8.938	0.003
15	333	9.000	9.022	0.022
30	334	8.937	8.908	-0.029
30	335	8.956	8.892	-0.064
30	337	8.969	8.949	-0.020
30	339	9.044	9.050	0.006
30	341	9.007	9.015	0.008
35	342	9.006	8.994	-0.012
35	345	8.892	8.836	-0.056
35	346	8.924	8.927	0.003
35	347	8.856	8.849	-0.007
35	348	9.082	9.080	-0.002
50	349	8.932	8.645	-0.287
50	350	8.986	8.795	-0.191
50	351	8.888	8.648	-0.240
50	353	8.949	8.764	-0.185
50	354	9.001	8.797	-0.204
55	355	8.956	8.692	-0.264
55	357	8.957	8.690	-0.267
55	359	9.042	8.867	-0.175
55	362	8.961	8.686	-0.275
55	363	8.876	8.609	-0.267
100	1	9.136	8.818	-0.318
100	3	8.962	8.720	-0.242
100	4	9.016	8.705	-0.311
100	5	9.132	8.918	-0.214
100	6	9.058	8.787	-0.271
100	7	9.107	8.839	-0.268
100	8	9.069	8.781	-0.288
100	9	9.182	8.829	-0.353
100	10	9.060	8.786	-0.274
100	11	9.004	8.769	-0.235
100	12	9.009	8.763	-0.246
100	13	9.079	8.794	-0.285
100	14	9.149	8.864	-0.285
100	15	8.871	8.653	-0.218
100	16	8.997	8.754	-0.243
100	17	8.975	8.669	-0.306
100	18	8.959	8.643	-0.316
100	19	9.028	8.779	-0.249
100	20	8.942	8.724	-0.218
100	21	8.963	8.745	-0.218
100	22	8.872	8.648	-0.224
100	24	8.943	8.723	-0.220
105	25	8.851	8.487	-0.364
105	26	9.055	8.740	-0.315
105	27	9.060	8.655	-0.405
105	28	8.985	8.640	-0.345
105	29	9.050	8.637	-0.413
Max		9.182	9.110	0.079
Average		8.994	8.848	-0.147
Min		8.851	8.487	-0.413
Std Dev		0.071	0.147	0.140

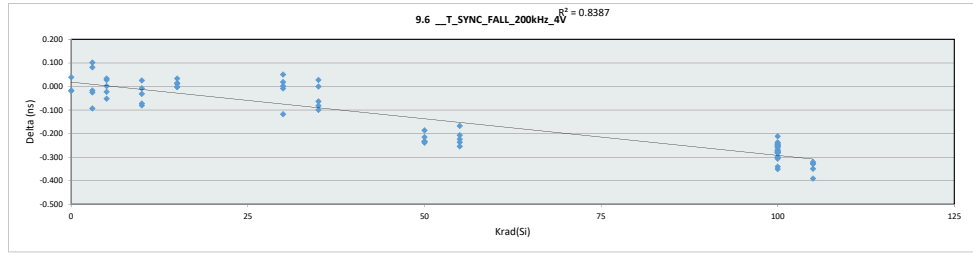


9.2 T_SYNC_FALL_100kHz											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.969	8.871	8.925	8.856	8.933	8.892	8.836	8.645	8.609	8.643	8.487
Average	9.004	9.009	8.998	8.947	9.001	8.963	8.937	8.730	8.709	8.760	8.632
Max	9.027	9.110	9.036	9.031	9.063	9.050	9.080	8.797	8.867	8.918	8.740
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

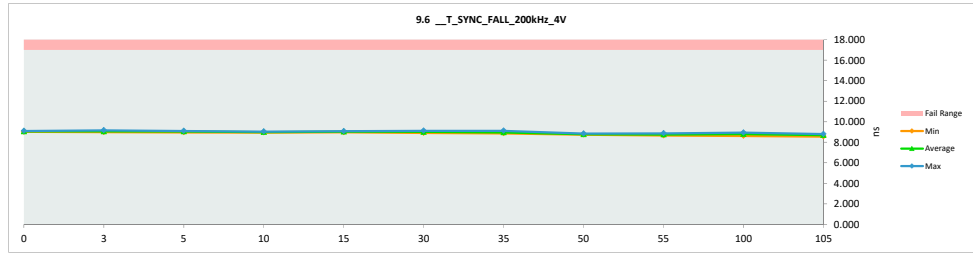


HDR TID Report
TPS7H5001-SP

9.6 T SYNC FALL 200kHz 4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	9.081	9.063	-0.018
0	118	9.056	9.095	0.039
0	122	9.025	9.006	-0.019
3	301	9.101	9.007	-0.094
3	306	8.996	8.978	-0.018
3	307	9.080	9.054	-0.026
3	308	8.990	9.071	0.081
3	309	9.060	9.161	0.101
5	310	9.073	9.074	0.001
5	311	9.149	9.097	-0.052
5	312	9.019	9.046	0.027
5	314	8.983	8.960	-0.023
5	316	9.049	9.082	0.033
10	317	9.045	9.037	-0.008
10	318	9.038	8.965	-0.073
10	319	8.919	8.944	0.025
10	321	8.977	8.945	-0.032
10	322	9.119	9.038	-0.081
15	323	8.966	8.999	0.033
15	325	9.079	9.076	-0.003
15	326	9.067	9.064	-0.003
15	328	8.952	8.963	0.011
15	333	9.042	9.056	0.014
30	334	8.923	8.974	0.051
30	335	8.999	8.990	-0.009
30	337	9.020	8.902	-0.118
30	339	9.072	9.090	0.018
30	341	9.059	9.109	0.050
35	342	9.065	8.965	-0.100
35	345	8.947	8.865	-0.082
35	346	9.002	8.938	-0.064
35	347	8.884	8.911	0.027
35	348	9.117	9.116	-0.001
50	349	8.985	8.746	-0.239
50	350	9.023	8.836	-0.187
50	351	8.971	8.737	-0.234
50	353	8.991	8.757	-0.234
50	354	9.034	8.819	-0.215
55	355	8.967	8.743	-0.224
55	357	8.900	8.732	-0.168
55	359	9.117	8.862	-0.255
55	362	8.973	8.766	-0.207
55	363	8.891	8.654	-0.237
100	1	9.113	8.845	-0.268
100	3	9.016	8.769	-0.247
100	4	9.079	8.796	-0.283
100	5	9.235	8.934	-0.301
100	6	9.055	8.755	-0.300
100	7	9.171	8.897	-0.274
100	8	9.145	8.794	-0.351
100	9	9.227	8.920	-0.307
100	10	9.114	8.865	-0.249
100	11	9.057	8.799	-0.258
100	12	9.031	8.751	-0.280
100	13	9.114	8.836	-0.278
100	14	9.217	8.876	-0.341
100	15	8.919	8.617	-0.302
100	16	9.055	8.814	-0.241
100	17	9.030	8.773	-0.257
100	18	8.977	8.696	-0.281
100	19	9.046	8.792	-0.254
100	20	9.008	8.759	-0.249
100	21	9.006	8.794	-0.212
100	22	8.929	8.691	-0.238
100	24	9.026	8.749	-0.277
105	25	8.878	8.550	-0.328
105	26	9.110	8.789	-0.321
105	27	9.046	8.717	-0.329
105	28	9.047	8.697	-0.350
105	29	9.079	8.688	-0.391
Max		9.235	9.161	0.101
Average		9.037	8.889	-0.148
Min		8.878	8.550	-0.391
Std Dev		0.076	0.145	0.139

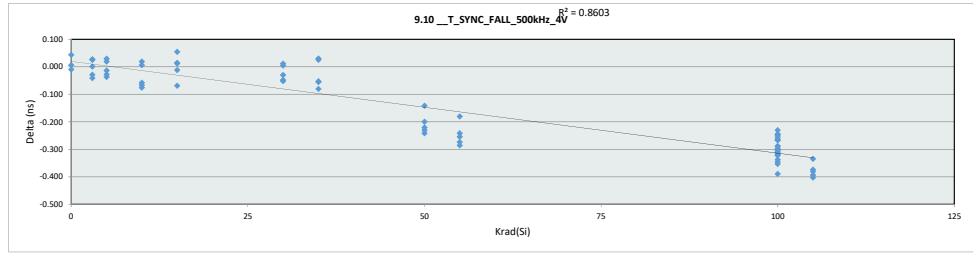


9.6 T_SYNC_FALL_200kHz											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.006	8.978	8.960	8.944	8.963	8.902	8.865	8.737	8.654	8.617	8.550
Average	9.055	9.054	9.052	8.986	9.032	9.013	8.959	8.779	8.751	8.796	8.688
Max	9.095	9.161	9.097	9.038	9.076	9.109	9.116	8.836	8.862	8.934	8.789
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

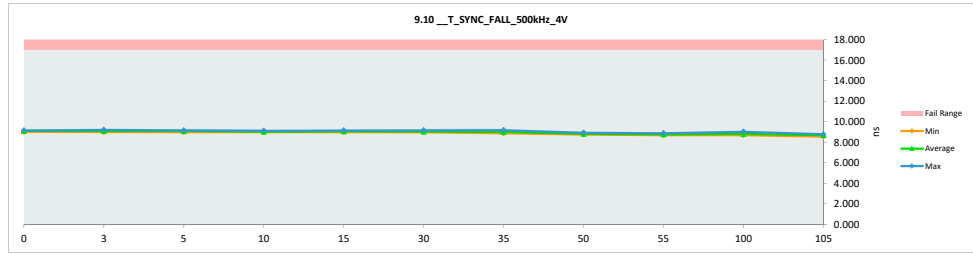


HDR TID Report
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9.10 T_SYNC_FALL_500kHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	9.134	9.140	0.006
0	118	9.094	9.137	0.043
0	122	9.031	9.021	-0.010
3	301	9.112	9.113	0.001
3	306	9.028	8.999	-0.029
3	307	9.137	9.096	-0.041
3	308	9.038	9.063	0.025
3	309	9.180	9.207	0.027
5	310	9.142	9.128	-0.014
5	311	9.123	9.152	0.029
5	312	9.070	9.089	0.019
5	314	9.030	8.993	-0.037
5	316	9.118	9.090	-0.028
10	317	9.104	9.110	0.006
10	318	9.092	9.016	-0.076
10	319	8.967	8.986	0.019
10	321	9.055	8.988	-0.067
10	322	9.128	9.070	-0.058
15	323	9.019	9.033	0.014
15	325	9.047	9.101	0.054
15	326	9.151	9.139	-0.012
15	328	9.052	8.983	-0.069
15	333	9.043	9.055	0.012
30	334	9.016	9.027	0.011
30	335	9.031	9.001	-0.030
30	337	9.019	8.971	-0.048
30	339	9.156	9.160	0.004
30	341	9.121	9.069	-0.052
35	342	9.051	9.081	0.030
35	345	8.935	8.879	-0.056
35	346	9.037	8.956	-0.081
35	347	8.953	8.900	-0.053
35	348	9.151	9.176	0.025
50	349	8.986	8.755	-0.231
50	350	9.069	8.927	-0.142
50	351	8.980	8.780	-0.200
50	353	9.047	8.826	-0.221
50	354	9.098	8.856	-0.242
55	355	9.026	8.784	-0.242
55	357	8.960	8.779	-0.181
55	359	9.149	8.875	-0.274
55	362	9.016	8.730	-0.286
55	363	8.947	8.692	-0.255
100	1	9.210	8.856	-0.354
100	3	9.058	8.758	-0.300
100	4	9.130	8.792	-0.338
100	5	9.263	9.015	-0.248
100	6	9.092	8.825	-0.267
100	7	9.202	8.913	-0.289
100	8	9.115	8.814	-0.301
100	9	9.257	8.934	-0.323
100	10	9.159	8.854	-0.305
100	11	9.131	8.816	-0.315
100	12	9.055	8.810	-0.245
100	13	9.184	8.838	-0.346
100	14	9.241	8.851	-0.390
100	15	8.954	8.723	-0.231
100	16	9.101	8.937	-0.264
100	17	9.070	8.771	-0.299
100	18	9.016	8.706	-0.310
100	19	9.113	8.846	-0.267
100	20	9.034	8.786	-0.248
100	21	9.077	8.821	-0.256
100	22	9.008	8.688	-0.320
100	24	9.065	8.776	-0.289
105	25	8.923	8.549	-0.374
105	26	9.151	8.770	-0.381
105	27	9.115	8.780	-0.335
105	28	9.046	8.643	-0.403
105	29	9.096	8.702	-0.394
	Max	9.263	9.207	0.054
	Average	9.079	8.920	-0.159
	Min	8.923	8.549	-0.403
	Std Dev	0.076	0.155	0.147

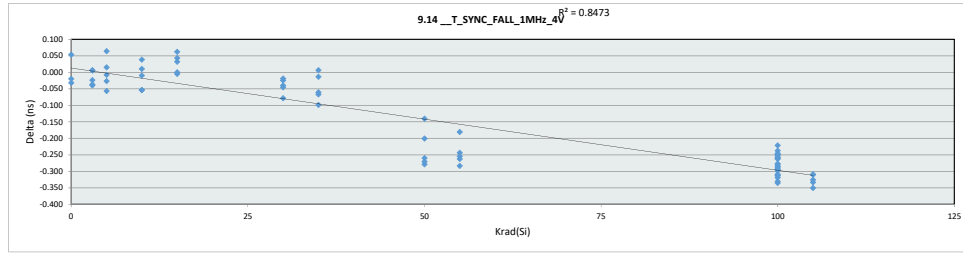


9.10 T_SYNC_FALL_500kHz											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.021	8.999	8.993	8.986	8.983	8.971	8.879	8.755	8.692	8.688	8.549
Average	9.099	9.096	9.090	9.034	9.062	9.046	8.998	8.829	8.772	8.820	8.689
Max	9.140	9.207	9.152	9.110	9.139	9.160	9.176	8.927	8.875	9.015	8.780
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

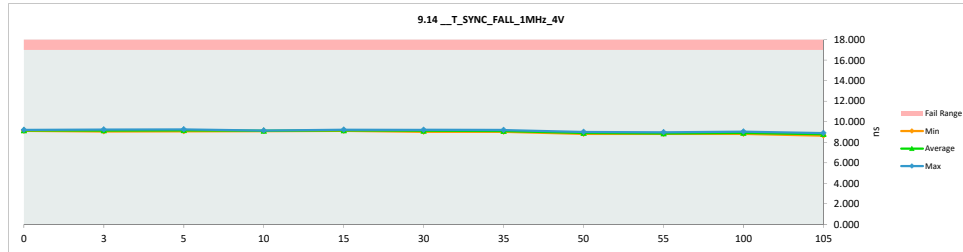


HDR TID Report
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9.14 T_SYNC_FALL_1MHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	9.190	9.158	-0.032
0	118	9.137	9.190	0.053
0	122	9.119	9.099	-0.020
3	301	9.170	9.133	-0.037
3	306	9.034	9.040	0.006
3	307	9.164	9.169	0.005
3	308	9.174	9.134	-0.040
3	309	9.243	9.219	-0.024
5	310	9.233	9.206	-0.027
5	311	9.183	9.247	0.064
5	312	9.130	9.145	0.015
5	314	9.105	9.048	-0.057
5	316	9.162	9.154	-0.008
10	317	9.185	9.131	-0.054
10	318	9.124	9.134	0.010
10	319	9.035	9.073	0.038
10	321	9.115	9.105	-0.010
10	322	9.192	9.138	-0.054
15	323	9.098	9.130	0.032
15	325	9.196	9.191	-0.005
15	326	9.167	9.210	0.043
15	328	9.039	9.101	0.062
15	333	9.124	9.124	0.000
30	334	9.129	9.074	-0.046
30	335	9.090	9.011	-0.079
30	337	9.152	9.112	-0.040
30	339	9.219	9.194	-0.025
30	341	9.199	9.180	-0.019
35	342	9.169	9.175	0.006
35	345	9.064	9.003	-0.061
35	346	9.103	9.036	-0.067
35	347	9.023	9.009	-0.014
35	348	9.277	9.178	-0.099
50	349	9.079	8.808	-0.271
50	350	9.137	8.996	-0.141
50	351	9.129	8.850	-0.279
50	353	9.127	8.867	-0.260
50	354	9.139	8.938	-0.201
55	355	9.097	8.853	-0.244
55	357	9.070	8.815	-0.255
55	359	9.238	8.954	-0.284
55	362	9.075	8.812	-0.263
55	363	8.989	8.808	-0.181
100	1	9.259	9.010	-0.249
100	3	9.155	8.894	-0.261
100	4	9.177	8.883	-0.294
100	5	9.307	9.017	-0.290
100	6	9.165	8.907	-0.258
100	7	9.280	9.003	-0.277
100	8	9.178	8.932	-0.246
100	9	9.316	8.980	-0.336
100	10	9.219	8.899	-0.320
100	11	9.201	8.887	-0.314
100	12	9.151	8.870	-0.281
100	13	9.214	8.920	-0.294
100	14	9.320	9.021	-0.299
100	15	9.035	8.797	-0.238
100	16	9.175	8.866	-0.309
100	17	9.135	8.822	-0.313
100	18	9.113	8.891	-0.222
100	19	9.178	8.892	-0.286
100	20	9.131	8.881	-0.250
100	21	9.167	8.913	-0.254
100	22	9.067	8.804	-0.263
100	24	9.154	8.823	-0.331
105	25	8.963	8.637	-0.326
105	26	9.233	8.882	-0.351
105	27	9.172	8.838	-0.334
105	28	9.096	8.786	-0.310
105	29	9.101	8.791	-0.310
Max		9.320	9.247	0.064
Average		9.150	8.997	-0.153
Min		8.963	8.637	-0.351
Std Dev		0.074	0.145	0.138

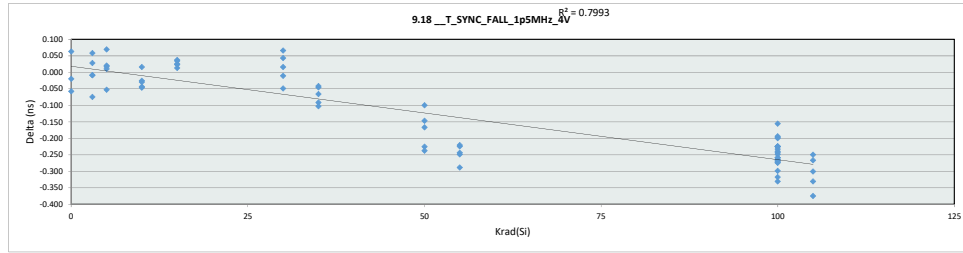


9.14 T_SYNC_FALL_1MHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.099	9.040	9.048	9.073	9.101	9.011	9.003	8.808	8.808	8.797	8.637
Average	9.149	9.139	9.160	9.116	9.151	9.114	9.080	8.892	8.848	8.905	8.787
Max	9.190	9.219	9.247	9.138	9.210	9.194	9.178	8.996	8.954	9.021	8.882
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

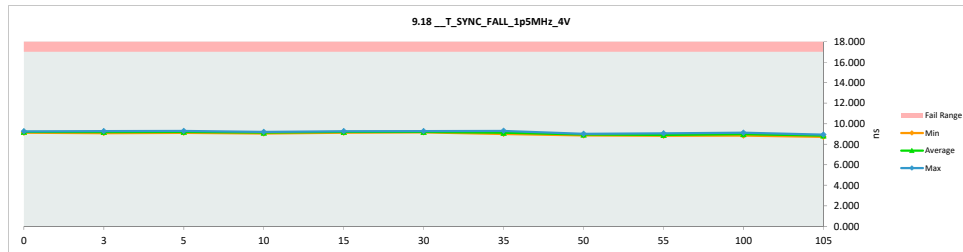


HDR TID Report
TPS7H5001-SP

		9.18 T_SYNC_FALL 1p5MHz 4V		
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	9.268	9.248	-0.020
0	118	9.179	9.242	0.063
0	122	9.172	9.114	-0.058
3	301	9.257	9.182	-0.075
3	306	9.088	9.079	-0.009
3	307	9.144	9.202	0.058
3	308	9.213	9.204	-0.009
3	309	9.248	9.276	0.028
5	310	9.241	9.258	0.017
5	311	9.219	9.288	0.069
5	312	9.180	9.200	0.020
5	314	9.098	9.108	0.010
5	316	9.225	9.172	-0.053
10	317	9.183	9.199	0.016
10	318	9.169	9.143	-0.026
10	319	9.065	9.035	-0.030
10	321	9.167	9.120	-0.047
10	322	9.238	9.194	-0.044
15	323	9.163	9.186	0.023
15	325	9.217	9.230	0.013
15	326	9.231	9.265	0.034
15	328	9.085	9.122	0.037
15	333	9.175	9.200	0.025
30	334	9.129	9.163	0.043
30	335	9.200	9.151	-0.049
30	337	9.118	9.184	0.066
30	339	9.261	9.277	0.016
30	341	9.199	9.188	-0.011
35	342	9.261	9.169	-0.092
35	345	9.068	9.002	-0.066
35	346	9.195	9.092	-0.103
35	347	9.061	9.015	-0.046
35	348	9.338	9.296	-0.042
50	349	9.091	8.853	-0.238
50	350	9.113	9.013	-0.100
50	351	9.066	8.919	-0.147
50	353	9.143	8.976	-0.167
50	354	9.188	8.962	-0.226
55	355	9.167	8.918	-0.249
55	357	9.120	8.831	-0.289
55	359	9.281	9.060	-0.221
55	362	9.091	8.847	-0.244
55	363	9.042	8.817	-0.225
100	1	9.279	9.004	-0.275
100	3	9.187	8.954	-0.233
100	4	9.235	8.936	-0.299
100	5	9.357	9.108	-0.249
100	6	9.207	8.963	-0.244
100	7	9.333	9.069	-0.264
100	8	9.281	8.963	-0.318
100	9	9.343	9.118	-0.225
100	10	9.241	8.982	-0.259
100	11	9.238	8.998	-0.240
100	12	9.177	8.951	-0.226
100	13	9.298	9.029	-0.269
100	14	9.281	9.084	-0.197
100	15	9.043	8.849	-0.194
100	16	9.222	8.997	-0.225
100	17	9.221	8.890	-0.331
100	18	9.107	8.881	-0.226
100	19	9.227	8.968	-0.259
100	20	9.153	8.884	-0.269
100	21	9.172	9.016	-0.156
100	22	9.108	8.835	-0.273
100	24	9.106	8.906	-0.200
105	25	8.962	8.712	-0.250
105	26	9.259	8.928	-0.331
105	27	9.212	8.911	-0.301
105	28	9.192	8.817	-0.375
105	29	9.129	8.862	-0.267
Max		9.357	9.296	0.069
Average		9.185	9.052	-0.133
Min		8.962	8.712	-0.375
Std Dev		0.081	0.145	0.130

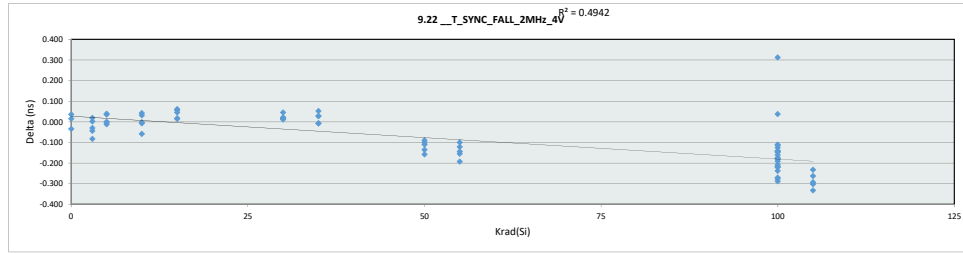


		9.18 T_SYNC_FALL 1p5MHz												
Test Site														
Tester														
Test Number														
Max Limit	17	ns												
Min Limit		ns												
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105			
LL														
Min	9.114	9.079	9.108	9.035	9.122	9.151	9.002	8.853	8.817	8.835	8.712			
Average	9.201	9.189	9.205	9.138	9.201	9.193	9.115	8.945	8.895	8.972	8.846			
Max	9.248	9.276	9.288	9.199	9.265	9.277	9.296	9.013	9.060	9.118	8.928			
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000			

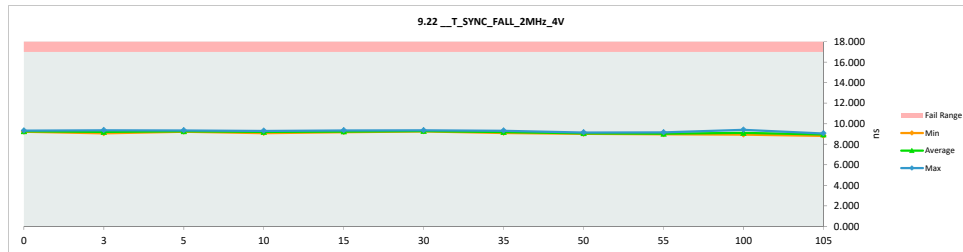


HDR TID Report
TPS7H5001-SP

9.22 T_SYNC_FALL_2MHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	9.274	9.238	-0.036
0	118	9.290	9.325	0.035
0	122	9.201	9.215	0.014
3	301	9.211	9.166	-0.045
3	306	9.139	9.055	-0.084
3	307	9.257	9.226	-0.031
3	308	9.186	9.187	0.001
3	309	9.339	9.357	0.018
5	310	9.307	9.345	0.038
5	311	9.288	9.283	-0.005
5	312	9.249	9.236	-0.013
5	314	9.178	9.212	0.034
5	316	9.242	9.241	-0.001
10	317	9.229	9.270	0.041
10	318	9.207	9.147	-0.060
10	319	9.086	9.077	-0.009
10	321	9.215	9.212	-0.003
10	322	9.266	9.296	0.030
15	323	9.156	9.172	0.016
15	325	9.261	9.274	0.013
15	326	9.299	9.343	0.044
15	328	9.116	9.171	0.055
15	333	9.177	9.237	0.060
30	334	9.249	9.266	0.017
30	335	9.227	9.245	0.018
30	337	9.204	9.248	0.044
30	339	9.340	9.350	0.010
30	341	9.286	9.306	0.020
35	342	9.299	9.290	-0.009
35	345	9.069	9.096	0.027
35	346	9.104	9.095	-0.009
35	347	9.102	9.128	0.026
35	348	9.263	9.314	0.051
50	349	9.137	9.035	-0.102
50	350	9.254	9.143	-0.111
50	351	9.104	9.013	-0.091
50	353	9.201	9.064	-0.137
50	354	9.240	9.081	-0.159
55	355	9.173	8.979	-0.194
55	357	9.174	9.029	-0.145
55	359	9.275	9.152	-0.123
55	362	9.088	8.987	-0.101
55	363	9.120	8.964	-0.156
100	1	9.309	9.182	-0.127
100	3	9.284	9.320	0.036
100	4	9.301	9.011	-0.290
100	5	9.452	9.213	-0.239
100	6	9.240	9.061	-0.179
100	7	9.356	9.213	-0.143
100	8	9.176	9.027	-0.149
100	9	9.444	9.222	-0.222
100	10	9.367	9.089	-0.278
100	11	9.245	9.101	-0.144
100	12	9.088	9.399	0.311
100	13	9.345	9.135	-0.210
100	14	9.395	9.202	-0.193
100	15	9.152	8.934	-0.218
100	16	9.216	9.072	-0.144
100	17	9.280	9.008	-0.272
100	18	9.077	8.962	-0.115
100	19	9.276	9.093	-0.183
100	20	9.175	9.063	-0.112
100	21	9.246	9.070	-0.176
100	22	9.178	8.960	-0.218
100	24	9.150	8.988	-0.162
105	25	9.082	8.818	-0.264
105	26	9.354	9.050	-0.304
105	27	9.331	8.997	-0.334
105	28	9.226	8.932	-0.294
105	29	9.188	8.954	-0.234
Max	9.452	9.399	0.311	
Average	9.229	9.145	-0.084	
Min	9.069	8.818	-0.334	
Std Dev	0.089	0.129	0.121	

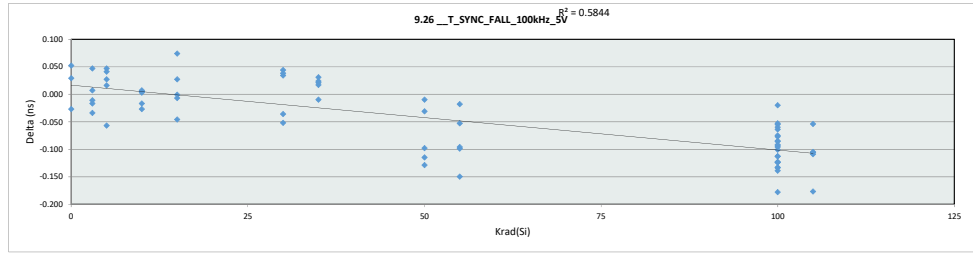


9.22 T_SYNC_FALL_2MHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.215	9.055	9.212	9.077	9.171	9.245	9.095	9.013	8.964	8.934	8.818
Average	9.259	9.198	9.263	9.200	9.239	9.283	9.185	9.067	9.022	9.106	8.950
Max	9.325	9.357	9.345	9.296	9.343	9.350	9.314	9.143	9.152	9.399	9.050
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

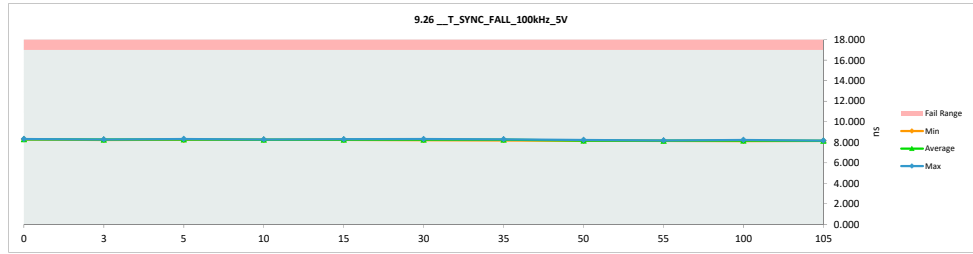


HDR TID Report
TPS7H5001-SP

9.26 T_SYNC_FALL_100kHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.265	8.317	0.052
0	118	8.300	8.273	-0.027
0	122	8.238	8.267	0.029
3	301	8.279	8.268	-0.011
3	306	8.238	8.221	-0.017
3	307	8.230	8.277	0.047
3	308	8.268	8.275	0.007
3	309	8.304	8.270	-0.034
5	310	8.230	8.271	0.041
5	311	8.248	8.275	0.027
5	312	8.266	8.282	0.016
5	314	8.275	8.218	-0.057
5	316	8.272	8.319	0.047
10	317	8.251	8.254	0.003
10	318	8.243	8.248	0.005
10	319	8.243	8.250	0.007
10	321	8.259	8.232	-0.027
10	322	8.293	8.276	-0.017
15	323	8.271	8.270	-0.001
15	325	8.273	8.266	-0.007
15	326	8.222	8.296	0.074
15	328	8.277	8.231	-0.046
15	333	8.211	8.238	0.027
30	334	8.203	8.241	0.038
30	335	8.252	8.216	-0.036
30	337	8.266	8.214	-0.052
30	339	8.256	8.300	0.044
30	341	8.278	8.312	0.034
35	342	8.220	8.241	0.021
35	345	8.195	8.212	0.017
35	346	8.263	8.287	0.024
35	347	8.238	8.228	-0.010
35	348	8.253	8.284	0.031
50	349	8.226	8.195	-0.031
50	350	8.278	8.149	-0.129
50	351	8.233	8.135	-0.098
50	353	8.247	8.132	-0.115
50	354	8.233	8.223	-0.010
55	355	8.247	8.148	-0.099
55	357	8.224	8.171	-0.053
55	359	8.301	8.151	-0.150
55	362	8.200	8.182	-0.018
55	363	8.214	8.118	-0.096
100	1	8.306	8.220	-0.086
100	3	8.259	8.158	-0.101
100	4	8.238	8.146	-0.092
100	5	8.286	8.163	-0.123
100	6	8.294	8.116	-0.178
100	7	8.275	8.179	-0.096
100	8	8.243	8.179	-0.064
100	9	8.294	8.160	-0.134
100	10	8.301	8.188	-0.113
100	11	8.271	8.138	-0.133
100	12	8.238	8.163	-0.075
100	13	8.255	8.200	-0.055
100	14	8.321	8.197	-0.124
100	15	8.209	8.149	-0.060
100	16	8.278	8.139	-0.139
100	17	8.224	8.139	-0.085
100	18	8.240	8.220	-0.020
100	19	8.252	8.139	-0.113
100	20	8.287	8.163	-0.124
100	21	8.240	8.187	-0.053
100	22	8.229	8.135	-0.094
100	24	8.222	8.145	-0.077
105	25	8.184	8.130	-0.054
105	26	8.255	8.146	-0.109
105	27	8.253	8.147	-0.106
105	28	8.310	8.133	-0.177
105	29	8.268	8.163	-0.105
Max	8.321	8.319	0.074	
Average	8.254	8.208	-0.046	
Min	8.184	8.116	-0.178	
Std Dev	0.030	0.058	0.063	

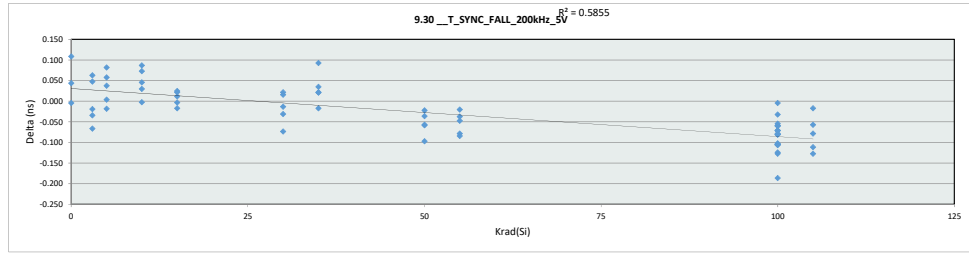


9.26 T_SYNC_FALL_100kHz											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.267	8.221	8.218	8.232	8.231	8.214	8.212	8.132	8.118	8.116	8.130
Average	8.286	8.262	8.273	8.252	8.260	8.257	8.250	8.167	8.154	8.165	8.144
Max	8.317	8.277	8.319	8.276	8.296	8.312	8.287	8.223	8.182	8.220	8.163
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

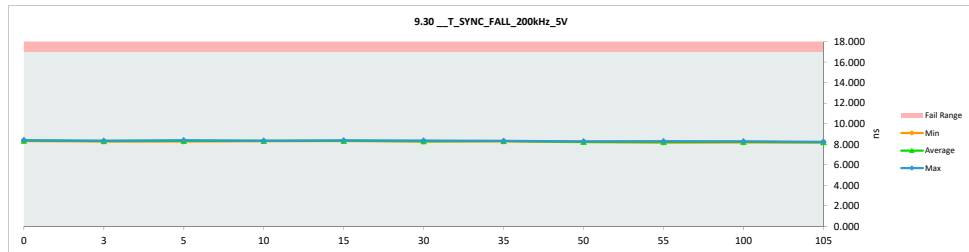


HDR TID Report
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9.30 T_SYNC_FALL_200kHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.317	8.360	0.043
0	118	8.287	8.395	0.108
0	122	8.317	8.312	-0.005
3	301	8.384	8.317	-0.067
3	306	8.275	8.322	0.047
3	307	8.275	8.337	0.062
3	308	8.292	8.257	-0.035
3	309	8.325	8.305	-0.020
5	310	8.320	8.377	0.057
5	311	8.303	8.340	0.037
5	312	8.322	8.325	0.003
5	314	8.278	8.259	-0.019
5	316	8.243	8.324	0.081
10	317	8.294	8.291	-0.003
10	318	8.281	8.326	0.045
10	319	8.251	8.337	0.086
10	321	8.241	8.313	0.072
10	322	8.289	8.318	0.029
15	323	8.327	8.338	0.011
15	325	8.375	8.371	-0.004
15	326	8.342	8.324	-0.018
15	328	8.281	8.305	0.024
15	333	8.280	8.301	0.021
30	334	8.294	8.280	-0.014
30	335	8.248	8.263	0.015
30	337	8.354	8.280	-0.074
30	339	8.323	8.291	-0.032
30	341	8.326	8.347	0.021
35	342	8.244	8.278	0.034
35	345	8.261	8.282	0.021
35	346	8.268	8.288	0.020
35	347	8.233	8.325	0.092
35	348	8.330	8.312	-0.018
50	349	8.238	8.215	-0.023
50	350	8.309	8.251	-0.058
50	351	8.294	8.196	-0.098
50	353	8.261	8.202	-0.059
50	354	8.305	8.268	-0.037
55	355	8.231	8.183	-0.048
55	357	8.252	8.231	-0.021
55	359	8.322	8.284	-0.038
55	362	8.289	8.210	-0.079
55	363	8.245	8.160	-0.085
100	1	8.338	8.233	-0.105
100	3	8.299	8.239	-0.060
100	4	8.310	8.205	-0.105
100	5	8.390	8.203	-0.187
100	6	8.284	8.202	-0.082
100	7	8.350	8.244	-0.106
100	8	8.315	8.243	-0.072
100	9	8.348	8.267	-0.081
100	10	8.322	8.197	-0.125
100	11	8.330	8.224	-0.106
100	12	8.288	8.255	-0.033
100	13	8.328	8.221	-0.107
100	14	8.309	8.237	-0.072
100	15	8.190	8.185	-0.005
100	16	8.347	8.219	-0.128
100	17	8.325	8.246	-0.079
100	18	8.293	8.190	-0.103
100	19	8.267	8.195	-0.072
100	20	8.286	8.225	-0.061
100	21	8.290	8.230	-0.060
100	22	8.221	8.166	-0.055
100	24	8.273	8.192	-0.081
105	25	8.194	8.176	-0.018
105	26	8.297	8.239	-0.058
105	27	8.308	8.180	-0.128
105	28	8.273	8.161	-0.112
105	29	8.273	8.194	-0.079
Max	8.390	8.395	0.108	
Average	8.294	8.262	-0.032	
Min	8.190	8.160	-0.187	
Std Dev	0.041	0.059	0.062	

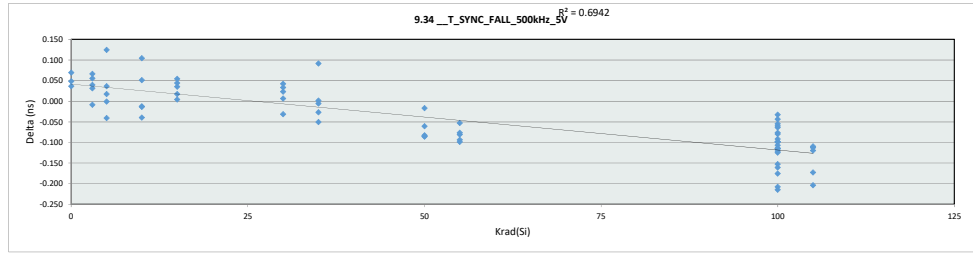


9.30 T_SYNC_FALL_200kHz											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.312	8.257	8.259	8.291	8.301	8.263	8.278	8.196	8.160	8.166	8.161
Average	8.356	8.308	8.325	8.317	8.328	8.292	8.297	8.226	8.214	8.219	8.190
Max	8.395	8.337	8.377	8.337	8.371	8.347	8.325	8.268	8.284	8.267	8.239
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

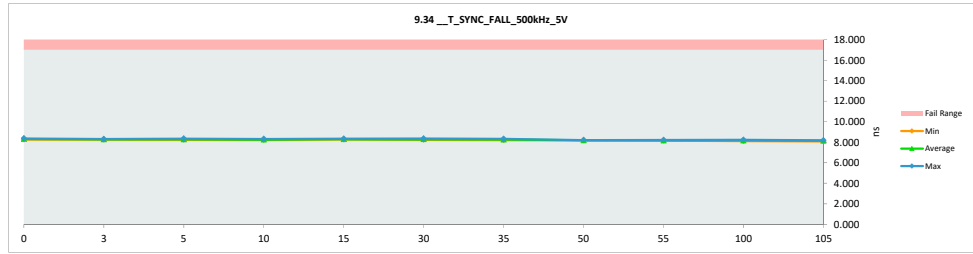


HDR TID Report
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9.34 T_SYNC_FALL_500kHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.312	8.360	0.048
0	118	8.247	8.316	0.069
0	122	8.229	8.265	0.036
3	301	8.252	8.307	0.055
3	306	8.218	8.249	0.031
3	307	8.263	8.302	0.039
3	308	8.262	8.253	-0.009
3	309	8.223	8.289	0.066
5	310	8.269	8.268	-0.001
5	311	8.222	8.346	0.124
5	312	8.266	8.283	0.017
5	314	8.273	8.232	-0.041
5	316	8.238	8.274	0.036
10	317	8.264	8.315	0.051
10	318	8.129	8.233	0.104
10	319	8.226	8.213	-0.013
10	321	8.276	8.236	-0.040
10	322	8.276	8.262	-0.014
15	323	8.240	8.284	0.044
15	325	8.250	8.267	0.017
15	326	8.282	8.336	0.054
15	328	8.228	8.263	0.035
15	333	8.273	8.277	0.004
30	334	8.238	8.271	0.033
30	335	8.259	8.227	-0.032
30	337	8.243	8.266	0.023
30	339	8.297	8.303	0.006
30	341	8.310	8.352	0.042
35	342	8.258	8.207	-0.051
35	345	8.227	8.200	-0.027
35	346	8.245	8.246	0.001
35	347	8.196	8.287	0.091
35	348	8.333	8.327	-0.006
50	349	8.245	8.160	-0.085
50	350	8.248	8.165	-0.083
50	351	8.249	8.163	-0.086
50	353	8.223	8.206	-0.017
50	354	8.249	8.188	-0.061
55	355	8.248	8.154	-0.094
55	357	8.238	8.157	-0.081
55	359	8.290	8.213	-0.077
55	362	8.264	8.165	-0.099
55	363	8.216	8.163	-0.053
100	1	8.308	8.194	-0.114
100	3	8.226	8.149	-0.077
100	4	8.262	8.137	-0.125
100	5	8.282	8.166	-0.116
100	6	8.265	8.104	-0.161
100	7	8.312	8.136	-0.176
100	8	8.272	8.180	-0.092
100	9	8.370	8.162	-0.208
100	10	8.265	8.158	-0.107
100	11	8.273	8.229	-0.044
100	12	8.240	8.180	-0.060
100	13	8.279	8.180	-0.099
100	14	8.282	8.227	-0.055
100	15	8.230	8.131	-0.099
100	16	8.303	8.150	-0.153
100	17	8.259	8.144	-0.115
100	18	8.255	8.156	-0.099
100	19	8.251	8.171	-0.080
100	20	8.265	8.144	-0.121
100	21	8.227	8.163	-0.064
100	22	8.177	8.144	-0.033
100	24	8.349	8.134	-0.215
105	25	8.280	8.076	-0.204
105	26	8.356	8.183	-0.173
105	27	8.276	8.163	-0.113
105	28	8.255	8.145	-0.110
105	29	8.267	8.147	-0.120
	Max	8.370	8.360	0.124
	Average	8.260	8.216	-0.044
	Min	8.129	8.076	-0.215
	Std Dev	0.038	0.068	0.078

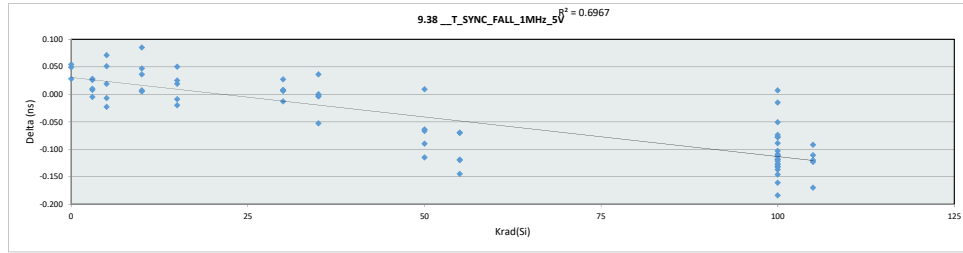


9.34 T_SYNC_FALL_500kHz											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.265	8.249	8.232	8.213	8.263	8.227	8.200	8.160	8.154	8.104	8.076
Average	8.314	8.280	8.281	8.252	8.285	8.284	8.253	8.176	8.170	8.161	8.143
Max	8.360	8.307	8.346	8.315	8.336	8.352	8.327	8.206	8.213	8.229	8.183
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

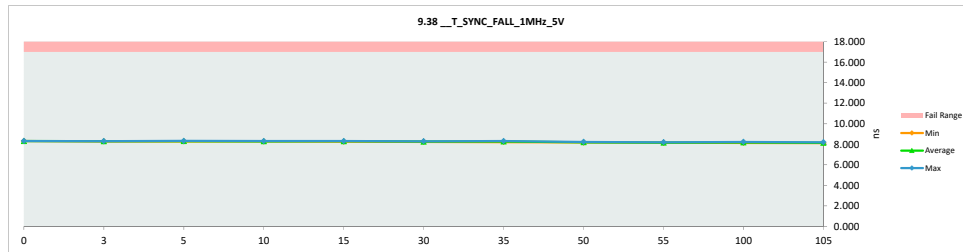


HDR TID Report
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		9.38 T_SYNC_FALL_1MHz_5V		
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.266	8.315	0.049
0	118	8.255	8.309	0.054
0	122	8.280	8.308	0.028
3	301	8.292	8.302	0.010
3	306	8.277	8.272	-0.005
3	307	8.270	8.298	0.028
3	308	8.267	8.275	0.008
3	309	8.270	8.296	0.026
5	310	8.293	8.286	-0.007
5	311	8.256	8.327	0.071
5	312	8.258	8.309	0.051
5	314	8.293	8.270	-0.023
5	316	8.273	8.292	0.019
10	317	8.248	8.295	0.047
10	318	8.255	8.291	0.036
10	319	8.179	8.264	0.085
10	321	8.244	8.251	0.007
10	322	8.306	8.311	0.005
15	323	8.282	8.307	0.025
15	325	8.268	8.287	0.019
15	326	8.317	8.308	-0.009
15	328	8.266	8.246	-0.020
15	333	8.253	8.303	0.050
30	334	8.259	8.266	0.007
30	335	8.217	8.244	0.027
30	337	8.250	8.256	0.006
30	339	8.300	8.287	-0.013
30	341	8.266	8.274	0.008
35	342	8.309	8.309	0.000
35	345	8.277	8.224	-0.053
35	346	8.277	8.273	-0.004
35	347	8.207	8.243	0.036
35	348	8.312	8.309	-0.003
50	349	8.269	8.154	-0.115
50	350	8.310	8.220	-0.090
50	351	8.248	8.184	-0.064
50	353	8.177	8.186	0.009
50	354	8.269	8.202	-0.067
55	355	8.254	8.184	-0.070
55	357	8.287	8.142	-0.145
55	359	8.252	8.133	-0.119
55	362	8.261	8.191	-0.070
55	363	8.260	8.140	-0.120
100	1	8.302	8.175	-0.127
100	3	8.274	8.155	-0.119
100	4	8.253	8.144	-0.109
100	5	8.334	8.216	-0.118
100	6	8.289	8.152	-0.137
100	7	8.369	8.185	-0.184
100	8	8.274	8.185	-0.089
100	9	8.353	8.221	-0.132
100	10	8.290	8.162	-0.128
100	11	8.316	8.155	-0.161
100	12	8.273	8.195	-0.078
100	13	8.311	8.198	-0.113
100	14	8.343	8.221	-0.122
100	15	8.160	8.167	0.007
100	16	8.275	8.224	-0.051
100	17	8.251	8.139	-0.112
100	18	8.268	8.136	-0.132
100	19	8.285	8.182	-0.103
100	20	8.259	8.181	-0.078
100	21	8.202	8.187	-0.015
100	22	8.272	8.126	-0.146
100	24	8.235	8.161	-0.074
105	25	8.232	8.109	-0.123
105	26	8.299	8.188	-0.111
105	27	8.318	8.148	-0.170
105	28	8.266	8.146	-0.120
105	29	8.234	8.142	-0.092
Max		8.369	8.327	0.085
Average		8.271	8.225	-0.046
Min		8.160	8.109	-0.184
Std Dev		0.037	0.064	0.070

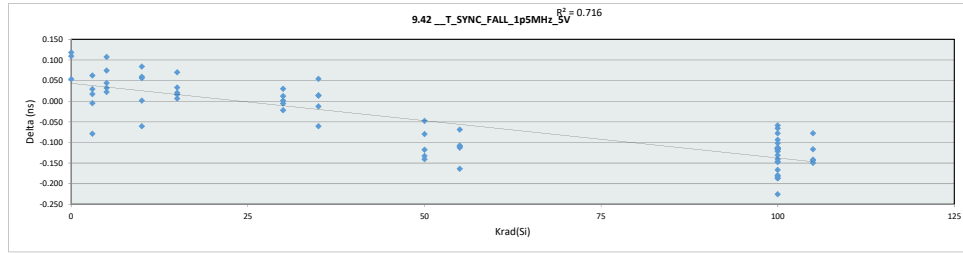


		9.38 T_SYNC_FALL_1MHz_5V									
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.308	8.272	8.270	8.251	8.246	8.244	8.224	8.154	8.133	8.126	8.109
Average	8.311	8.289	8.297	8.282	8.290	8.265	8.272	8.189	8.158	8.176	8.147
Max	8.315	8.302	8.327	8.311	8.308	8.287	8.309	8.220	8.191	8.224	8.188
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

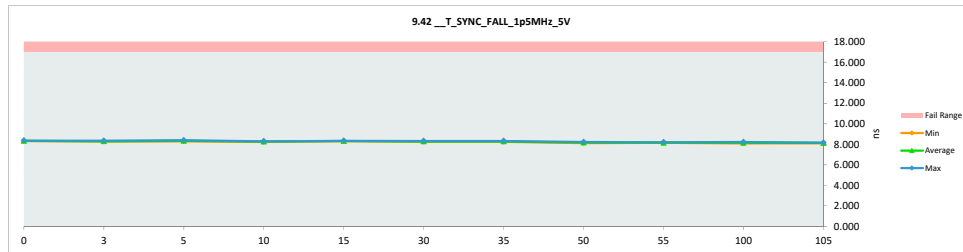


HDR TID Report
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9.42 T_SYNC_FALL_1p5MHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.227	8.345	0.118
0	118	8.266	8.319	0.053
0	122	8.214	8.323	0.109
3	301	8.260	8.289	0.029
3	306	8.251	8.268	0.017
3	307	8.278	8.340	0.062
3	308	8.276	8.271	-0.005
3	309	8.344	8.265	-0.079
5	310	8.307	8.339	0.032
5	311	8.283	8.390	0.107
5	312	8.265	8.287	0.022
5	314	8.273	8.347	0.074
5	316	8.255	8.299	0.044
10	317	8.227	8.283	0.056
10	318	8.175	8.259	0.084
10	319	8.208	8.267	0.059
10	321	8.284	8.223	-0.061
10	322	8.289	8.290	0.001
15	323	8.274	8.294	0.020
15	325	8.260	8.330	0.070
15	326	8.297	8.303	0.006
15	328	8.271	8.304	0.033
15	333	8.301	8.317	0.016
30	334	8.259	8.260	0.001
30	335	8.207	8.237	0.030
30	337	8.269	8.247	-0.022
30	339	8.323	8.317	-0.006
30	341	8.304	8.316	0.012
35	342	8.335	8.274	-0.061
35	345	8.183	8.237	0.054
35	346	8.256	8.269	0.013
35	347	8.219	8.233	0.014
35	348	8.329	8.316	-0.013
50	349	8.304	8.163	-0.141
50	350	8.295	8.215	-0.080
50	351	8.264	8.146	-0.118
50	353	8.240	8.107	-0.133
50	354	8.252	8.204	-0.048
55	355	8.301	8.137	-0.164
55	357	8.278	8.168	-0.110
55	359	8.301	8.188	-0.113
55	362	8.256	8.148	-0.108
55	363	8.206	8.137	-0.069
100	1	8.294	8.172	-0.122
100	3	8.276	8.159	-0.117
100	4	8.264	8.077	-0.187
100	5	8.306	8.191	-0.115
100	6	8.258	8.117	-0.141
100	7	8.315	8.168	-0.147
100	8	8.280	8.098	-0.182
100	9	8.283	8.217	-0.066
100	10	8.260	8.146	-0.114
100	11	8.300	8.120	-0.180
100	12	8.273	8.160	-0.113
100	13	8.339	8.152	-0.187
100	14	8.328	8.161	-0.167
100	15	8.204	8.102	-0.102
100	16	8.314	8.166	-0.148
100	17	8.250	8.156	-0.094
100	18	8.244	8.127	-0.117
100	19	8.270	8.139	-0.131
100	20	8.246	8.168	-0.078
100	21	8.287	8.173	-0.114
100	22	8.191	8.132	-0.059
100	24	8.363	8.137	-0.226
105	25	8.246	8.102	-0.144
105	26	8.260	8.118	-0.142
105	27	8.272	8.155	-0.117
105	28	8.255	8.105	-0.150
105	29	8.183	8.105	-0.078
	Max	8.363	8.390	0.118
	Average	8.269	8.216	-0.053
	Min	8.175	8.077	-0.226
	Std Dev	0.040	0.082	0.087

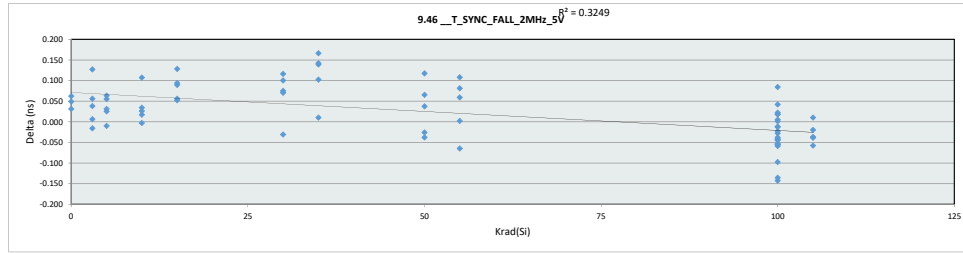


9.42 T_SYNC_FALL_1p5MHz											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit	ns	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.319	8.265	8.287	8.223	8.294	8.237	8.233	8.107	8.137	8.077	8.102
Average	8.329	8.287	8.332	8.264	8.310	8.275	8.266	8.167	8.156	8.147	8.117
Max	8.345	8.340	8.390	8.290	8.330	8.317	8.316	8.215	8.188	8.217	8.155
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

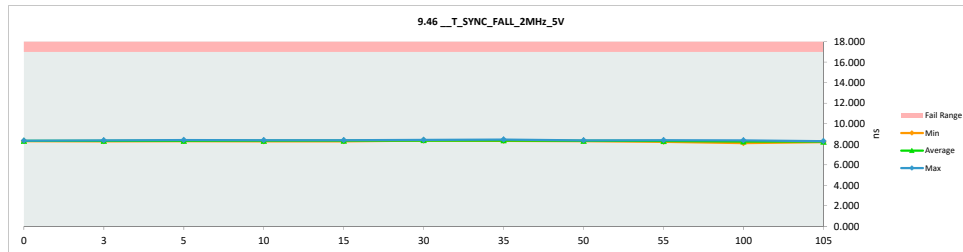


HDR TID Report
TPS7H5001-SP

9.46 T_SYNC_FALL_2MHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.275	8.306	0.031
0	118	8.307	8.356	0.049
0	122	8.283	8.345	0.062
3	301	8.250	8.377	0.127
3	306	8.304	8.360	0.056
3	307	8.335	8.319	-0.016
3	308	8.276	8.282	0.006
3	309	8.282	8.320	0.038
5	310	8.280	8.335	0.055
5	311	8.347	8.410	0.063
5	312	8.314	8.345	0.031
5	314	8.313	8.303	-0.010
5	316	8.299	8.324	0.025
10	317	8.297	8.323	0.026
10	318	8.306	8.340	0.034
10	319	8.243	8.350	0.107
10	321	8.387	8.404	0.017
10	322	8.283	8.280	-0.003
15	323	8.267	8.356	0.089
15	325	8.233	8.289	0.056
15	326	8.300	8.394	0.094
15	328	8.237	8.289	0.052
15	333	8.269	8.397	0.128
30	334	8.383	8.352	-0.031
30	335	8.297	8.397	0.100
30	337	8.298	8.414	0.116
30	339	8.321	8.396	0.075
30	341	8.354	8.424	0.070
35	342	8.329	8.339	0.010
35	345	8.208	8.347	0.139
35	346	8.264	8.366	0.102
35	347	8.233	8.375	0.142
35	348	8.289	8.455	0.166
50	349	8.192	8.309	0.117
50	350	8.403	8.377	-0.026
50	351	8.342	8.304	-0.038
50	353	8.269	8.334	0.065
50	354	8.307	8.344	0.037
55	355	8.280	8.282	0.002
55	357	8.201	8.282	0.081
55	359	8.295	8.403	0.108
55	362	8.301	8.236	-0.065
55	363	8.228	8.287	0.059
100	1	8.330	8.302	-0.028
100	3	8.260	8.247	-0.013
100	4	8.301	8.245	-0.056
100	5	8.386	8.344	-0.042
100	6	8.343	8.284	-0.059
100	7	8.292	8.310	0.018
100	8	8.287	8.244	-0.043
100	9	8.335	8.312	-0.023
100	10	8.352	8.299	-0.053
100	11	8.197	8.239	0.042
100	12	8.283	8.238	-0.045
100	13	8.337	8.325	-0.012
100	14	8.396	8.341	-0.055
100	15	8.267	8.272	0.005
100	16	8.293	8.377	0.084
100	17	8.241	8.105	-0.136
100	18	8.280	8.281	0.001
100	19	8.273	8.295	0.022
100	20	8.379	8.281	-0.098
100	21	8.279	8.297	0.018
100	22	8.351	8.208	-0.143
100	24	8.304	8.266	-0.038
105	25	8.244	8.207	-0.037
105	26	8.326	8.287	-0.039
105	27	8.326	8.268	-0.058
105	28	8.245	8.225	-0.020
105	29	8.293	8.303	0.010
Max	8.403	8.455	0.166	
Average	8.295	8.318	0.022	
Min	8.192	8.105	-0.143	
Std Dev	0.047	0.060	0.066	

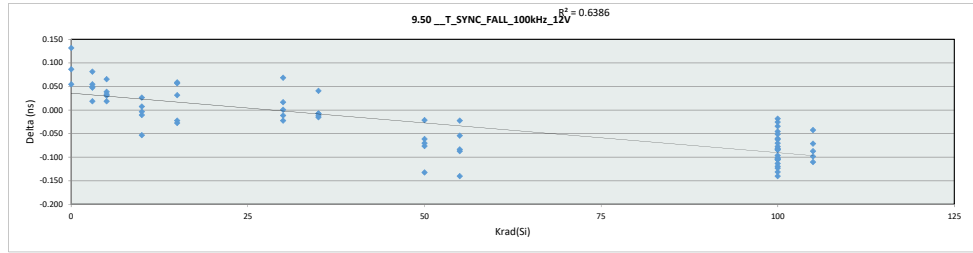


9.46 T_SYNC_FALL_2MHz_5V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.306	8.282	8.303	8.280	8.289	8.352	8.339	8.304	8.236	8.105	8.207
Average	8.336	8.332	8.343	8.339	8.345	8.397	8.376	8.334	8.298	8.278	8.258
Max	8.356	8.377	8.410	8.404	8.397	8.424	8.455	8.377	8.403	8.377	8.303
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

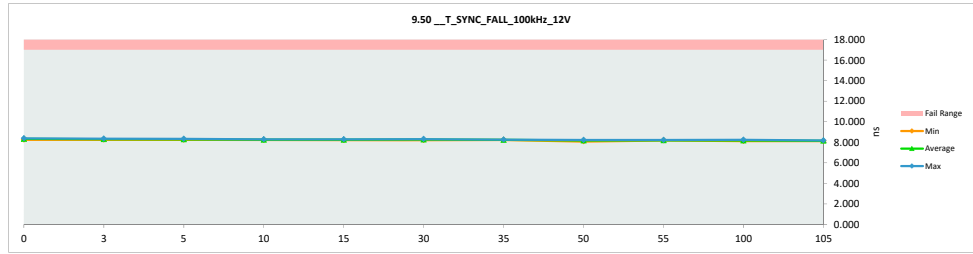


HDR TID Report
TPS7H5001-SP

		9.50 T_SYNC_FALL_100kHz_12V		
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.242	8.373	0.131
0	118	8.264	8.350	0.086
0	122	8.182	8.236	0.054
3	301	8.280	8.334	0.054
3	306	8.217	8.235	0.018
3	307	8.238	8.285	0.047
3	308	8.255	8.304	0.049
3	309	8.189	8.270	0.081
5	310	8.211	8.249	0.038
5	311	8.257	8.287	0.030
5	312	8.258	8.323	0.065
5	314	8.211	8.229	0.018
5	316	8.227	8.260	0.033
10	317	8.252	8.278	0.026
10	318	8.216	8.223	0.007
10	319	8.221	8.210	-0.011
10	321	8.291	8.237	-0.054
10	322	8.276	8.272	-0.004
15	323	8.230	8.202	-0.028
15	325	8.245	8.222	-0.023
15	326	8.240	8.271	0.031
15	328	8.216	8.274	0.058
15	333	8.219	8.275	0.056
30	334	8.251	8.228	-0.023
30	335	8.241	8.257	0.016
30	337	8.198	8.198	0.000
30	339	8.298	8.286	-0.012
30	341	8.230	8.298	0.068
35	342	8.260	8.247	-0.013
35	345	8.246	8.238	-0.008
35	346	8.234	8.226	-0.008
35	347	8.178	8.218	0.040
35	348	8.254	8.238	-0.016
50	349	8.265	8.188	-0.077
50	350	8.265	8.203	-0.062
50	351	8.203	8.070	-0.133
50	353	8.204	8.133	-0.071
50	354	8.235	8.213	-0.022
55	355	8.307	8.166	-0.141
55	357	8.237	8.214	-0.023
55	359	8.287	8.199	-0.088
55	362	8.264	8.180	-0.084
55	363	8.222	8.167	-0.055
100	1	8.282	8.168	-0.114
100	3	8.219	8.158	-0.061
100	4	8.268	8.148	-0.120
100	5	8.335	8.203	-0.132
100	6	8.258	8.180	-0.078
100	7	8.272	8.188	-0.084
100	8	8.239	8.154	-0.085
100	9	8.238	8.141	-0.097
100	10	8.249	8.108	-0.141
100	11	8.266	8.214	-0.052
100	12	8.218	8.172	-0.046
100	13	8.232	8.197	-0.035
100	14	8.280	8.156	-0.124
100	15	8.204	8.098	-0.106
100	16	8.231	8.127	-0.104
100	17	8.265	8.194	-0.071
100	18	8.273	8.171	-0.102
100	19	8.249	8.223	-0.026
100	20	8.205	8.142	-0.063
100	21	8.207	8.188	-0.019
100	22	8.224	8.118	-0.106
100	24	8.252	8.170	-0.082
105	25	8.200	8.157	-0.043
105	26	8.257	8.169	-0.088
105	27	8.226	8.115	-0.111
105	28	8.266	8.167	-0.099
105	29	8.176	8.104	-0.072
	Max	8.335	8.373	0.131
	Average	8.242	8.210	-0.032
	Min	8.176	8.070	-0.141
	Std Dev	0.031	0.063	0.065

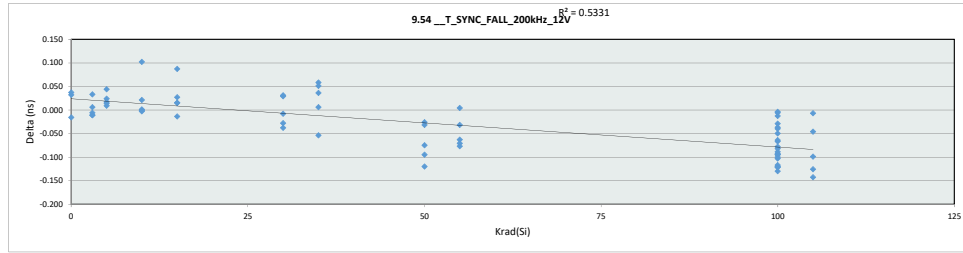


		9.50 T_SYNC_FALL_100kHz										
Test Site												
Tester												
Test Number												
Max Limit	17	ns										
Min Limit		ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL												
Min	8.236	8.235	8.229	8.210	8.202	8.198	8.218	8.070	8.166	8.098	8.104	
Average	8.320	8.286	8.270	8.244	8.249	8.253	8.233	8.161	8.185	8.164	8.142	
Max	8.373	8.334	8.323	8.278	8.275	8.298	8.247	8.213	8.214	8.223	8.169	
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	

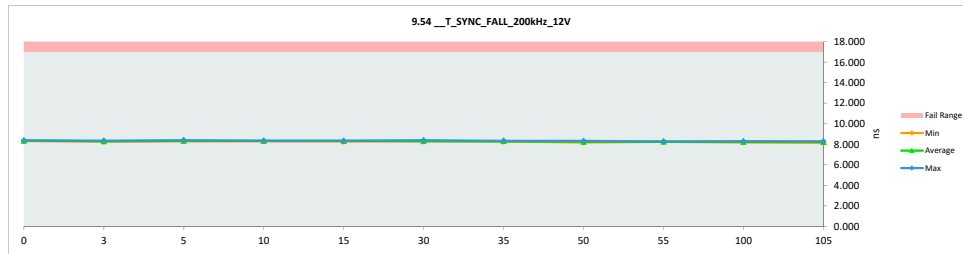


HDR TID Report
TPS7H5001-SP

9.54 T_SYNC_FALL_200kHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.341	8.373	0.032
0	118	8.337	8.374	0.037
0	122	8.326	8.310	-0.016
3	301	8.339	8.345	0.006
3	306	8.264	8.253	-0.011
3	307	8.282	8.276	-0.006
3	308	8.293	8.326	0.033
3	309	8.292	8.281	-0.011
5	310	8.297	8.321	0.024
5	311	8.331	8.375	0.044
5	312	8.382	8.391	0.009
5	314	8.266	8.283	0.017
5	316	8.295	8.309	0.014
10	317	8.314	8.315	0.001
10	318	8.288	8.287	-0.001
10	319	8.234	8.336	0.102
10	321	8.327	8.348	0.021
10	322	8.315	8.312	-0.003
15	323	8.249	8.264	0.015
15	325	8.292	8.307	0.015
15	326	8.311	8.338	0.027
15	328	8.306	8.292	-0.014
15	333	8.256	8.343	0.087
30	334	8.318	8.280	-0.038
30	335	8.307	8.279	-0.028
30	337	8.264	8.295	0.031
30	339	8.353	8.382	0.029
30	341	8.287	8.279	-0.008
35	342	8.273	8.324	0.051
35	345	8.270	8.328	0.058
35	346	8.324	8.270	-0.054
35	347	8.253	8.259	0.006
35	348	8.293	8.329	0.036
50	349	8.260	8.228	-0.032
50	350	8.327	8.252	-0.075
50	351	8.260	8.165	-0.095
50	353	8.335	8.215	-0.120
50	354	8.347	8.321	-0.026
55	355	8.321	8.258	-0.063
55	357	8.294	8.262	-0.032
55	359	8.348	8.271	-0.077
55	362	8.303	8.232	-0.071
55	363	8.232	8.236	0.004
100	1	8.343	8.254	-0.089
100	3	8.297	8.230	-0.067
100	4	8.326	8.206	-0.120
100	5	8.342	8.292	-0.050
100	6	8.291	8.196	-0.095
100	7	8.405	8.283	-0.122
100	8	8.344	8.223	-0.121
100	9	8.240	8.200	-0.040
100	10	8.277	8.174	-0.103
100	11	8.272	8.268	-0.004
100	12	8.277	8.240	-0.037
100	13	8.364	8.264	-0.100
100	14	8.343	8.250	-0.093
100	15	8.273	8.244	-0.029
100	16	8.263	8.199	-0.064
100	17	8.286	8.273	-0.013
100	18	8.260	8.254	-0.006
100	19	8.317	8.222	-0.095
100	20	8.308	8.230	-0.078
100	21	8.320	8.203	-0.117
100	22	8.254	8.172	-0.082
100	24	8.372	8.242	-0.130
105	25	8.286	8.240	-0.046
105	26	8.356	8.230	-0.126
105	27	8.284	8.141	-0.143
105	28	8.334	8.235	-0.099
105	29	8.293	8.286	-0.007
	Max	8.405	8.391	0.102
	Average	8.303	8.272	-0.031
	Min	8.232	8.141	-0.143
	Std Dev	0.037	0.055	0.057

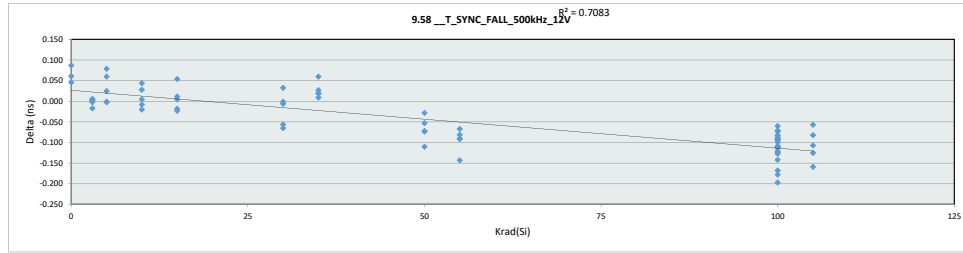


9.54 T_SYNC_FALL_200kHz											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.310	8.253	8.283	8.287	8.264	8.279	8.259	8.165	8.232	8.172	8.141
Average	8.352	8.296	8.336	8.320	8.309	8.303	8.302	8.236	8.252	8.233	8.226
Max	8.374	8.345	8.391	8.348	8.343	8.382	8.329	8.321	8.271	8.292	8.286
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

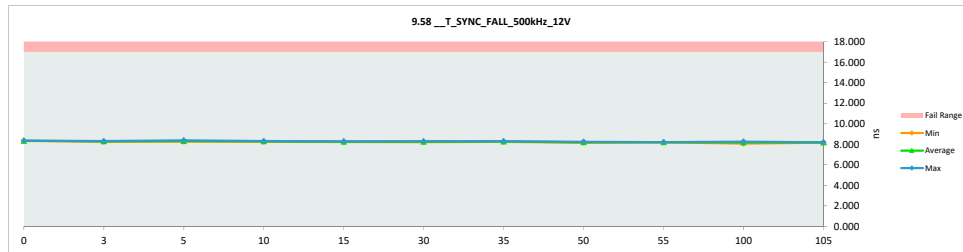


HDR TID Report
TPS7H5001-SP

9.58 T_SYNC_FALL_500kHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.260	8.346	0.086
0	118	8.302	8.347	0.045
0	122	8.263	8.323	0.060
3	301	8.299	8.302	0.003
3	306	8.242	8.239	-0.003
3	307	8.306	8.288	-0.018
3	308	8.277	8.277	0.000
3	309	8.306	8.311	0.005
5	310	8.309	8.307	-0.002
5	311	8.271	8.330	0.059
5	312	8.301	8.379	0.078
5	314	8.255	8.252	-0.003
5	316	8.259	8.283	0.024
10	317	8.264	8.307	0.043
10	318	8.260	8.239	-0.021
10	319	8.232	8.259	0.027
10	321	8.306	8.310	0.004
10	322	8.302	8.293	-0.009
15	323	8.258	8.239	-0.019
15	325	8.242	8.218	-0.024
15	326	8.262	8.273	0.011
15	328	8.279	8.283	0.004
15	333	8.209	8.262	0.053
30	334	8.271	8.205	-0.066
30	335	8.324	8.267	-0.057
30	337	8.239	8.271	0.032
30	339	8.298	8.296	-0.002
30	341	8.249	8.242	-0.007
35	342	8.238	8.257	0.019
35	345	8.258	8.284	0.026
35	346	8.215	8.274	0.059
35	347	8.226	8.234	0.008
35	348	8.282	8.300	0.018
50	349	8.277	8.203	-0.074
50	350	8.226	8.197	-0.029
50	351	8.218	8.145	-0.073
50	353	8.288	8.177	-0.111
50	354	8.295	8.241	-0.054
55	355	8.291	8.199	-0.092
55	357	8.254	8.186	-0.068
55	359	8.296	8.204	-0.092
55	362	8.326	8.182	-0.144
55	363	8.267	8.185	-0.082
100	1	8.315	8.190	-0.125
100	3	8.293	8.181	-0.112
100	4	8.282	8.139	-0.143
100	5	8.342	8.214	-0.128
100	6	8.251	8.155	-0.096
100	7	8.348	8.258	-0.090
100	8	8.262	8.169	-0.093
100	9	8.297	8.197	-0.100
100	10	8.335	8.156	-0.179
100	11	8.284	8.199	-0.085
100	12	8.308	8.197	-0.111
100	13	8.344	8.175	-0.169
100	14	8.298	8.226	-0.072
100	15	8.267	8.153	-0.114
100	16	8.232	8.122	-0.110
100	17	8.283	8.222	-0.061
100	18	8.263	8.180	-0.083
100	19	8.262	8.188	-0.074
100	20	8.272	8.162	-0.110
100	21	8.286	8.194	-0.092
100	22	8.253	8.055	-0.198
100	24	8.312	8.190	-0.122
105	25	8.252	8.194	-0.058
105	26	8.312	8.152	-0.160
105	27	8.255	8.172	-0.083
105	28	8.318	8.192	-0.126
105	29	8.283	8.175	-0.108
	Max	8.348	8.379	0.086
	Average	8.277	8.229	-0.048
	Min	8.209	8.055	-0.198
	Std Dev	0.032	0.062	0.068

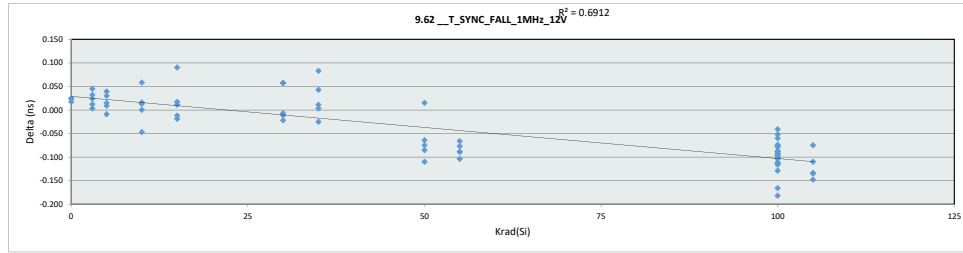


9.58 T_SYNC_FALL_500kHz											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.323	8.239	8.252	8.239	8.218	8.205	8.234	8.145	8.182	8.055	8.152
Average	8.339	8.283	8.310	8.282	8.255	8.256	8.270	8.193	8.191	8.178	8.177
Max	8.347	8.311	8.379	8.310	8.283	8.296	8.300	8.241	8.204	8.258	8.194
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

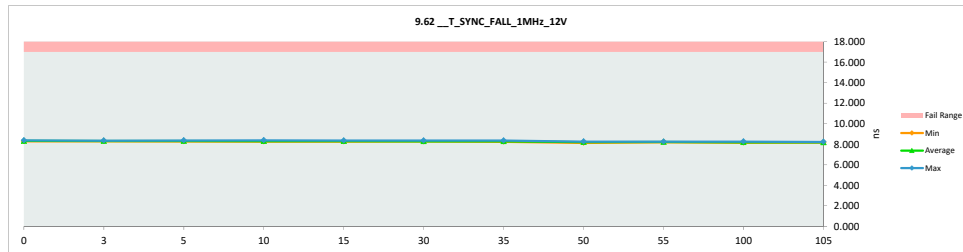


HDR TID Report
TPS7H5001-SP

		9.62 T_SYNC_FALL_1MHz_12V		
Test Site				
Tester				
Test Number				
Unit		ns	ns	
Max Limit		17	17	
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.290	8.314	0.024
0	118	8.348	8.373	0.025
0	122	8.271	8.288	0.017
3	301	8.342	8.354	0.012
3	306	8.282	8.285	0.003
3	307	8.289	8.321	0.032
3	308	8.301	8.325	0.024
3	309	8.271	8.316	0.045
5	310	8.275	8.305	0.030
5	311	8.313	8.328	0.015
5	312	8.372	8.363	-0.009
5	314	8.267	8.276	0.009
5	316	8.256	8.295	0.039
10	317	8.333	8.286	-0.047
10	318	8.274	8.290	0.016
10	319	8.252	8.252	0.000
10	321	8.317	8.375	0.058
10	322	8.298	8.311	0.013
15	323	8.275	8.263	-0.012
15	325	8.304	8.315	0.011
15	326	8.288	8.305	0.017
15	328	8.268	8.358	0.090
15	333	8.307	8.288	-0.019
30	334	8.288	8.266	-0.022
30	335	8.276	8.265	-0.011
30	337	8.230	8.287	0.057
30	339	8.302	8.359	0.057
30	341	8.281	8.274	-0.007
35	342	8.274	8.357	0.083
35	345	8.259	8.262	0.003
35	346	8.260	8.235	-0.025
35	347	8.215	8.258	0.043
35	348	8.317	8.328	0.011
50	349	8.245	8.260	0.015
50	350	8.305	8.230	-0.075
50	351	8.238	8.128	-0.110
50	353	8.246	8.182	-0.064
50	354	8.320	8.235	-0.085
55	355	8.325	8.248	-0.077
55	357	8.297	8.231	-0.066
55	359	8.333	8.229	-0.104
55	362	8.288	8.199	-0.089
55	363	8.281	8.192	-0.089
100	1	8.343	8.161	-0.182
100	3	8.279	8.201	-0.078
100	4	8.277	8.175	-0.102
100	5	8.335	8.248	-0.087
100	6	8.305	8.209	-0.096
100	7	8.336	8.258	-0.078
100	8	8.304	8.193	-0.111
100	9	8.254	8.166	-0.088
100	10	8.259	8.159	-0.100
100	11	8.348	8.232	-0.116
100	12	8.277	8.217	-0.060
100	13	8.305	8.213	-0.092
100	14	8.308	8.194	-0.114
100	15	8.221	8.169	-0.052
100	16	8.275	8.179	-0.096
100	17	8.290	8.216	-0.074
100	18	8.288	8.247	-0.041
100	19	8.293	8.201	-0.092
100	20	8.277	8.174	-0.103
100	21	8.316	8.187	-0.129
100	22	8.312	8.146	-0.166
100	24	8.297	8.221	-0.076
105	25	8.308	8.198	-0.110
105	26	8.295	8.220	-0.075
105	27	8.317	8.169	-0.148
105	28	8.298	8.163	-0.135
105	29	8.308	8.174	-0.134
Max		8.372	8.375	0.090
Average		8.291	8.250	-0.041
Min		8.215	8.128	-0.182
Std Dev		0.031	0.063	0.065

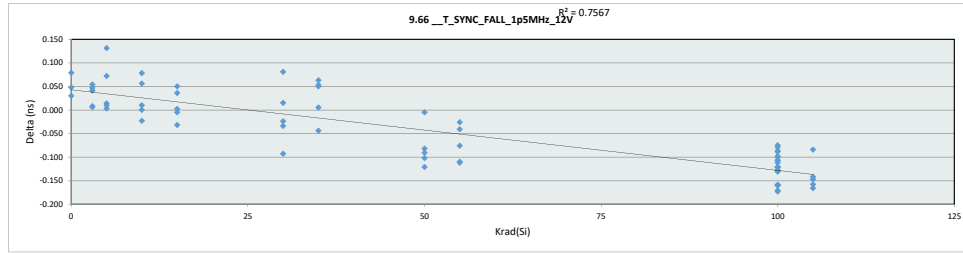


		9.62 T_SYNC_FALL_1MHz_12V										
Test Site												
Tester												
Test Number												
Max Limit		17	ns									
Min Limit			ns									
Krad(Si)		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		8.288	8.285	8.276	8.252	8.263	8.265	8.235	8.128	8.192	8.146	8.163
Average		8.325	8.320	8.313	8.303	8.306	8.290	8.288	8.207	8.220	8.198	8.185
Max		8.373	8.354	8.363	8.375	8.358	8.359	8.357	8.260	8.248	8.258	8.220
UL		17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

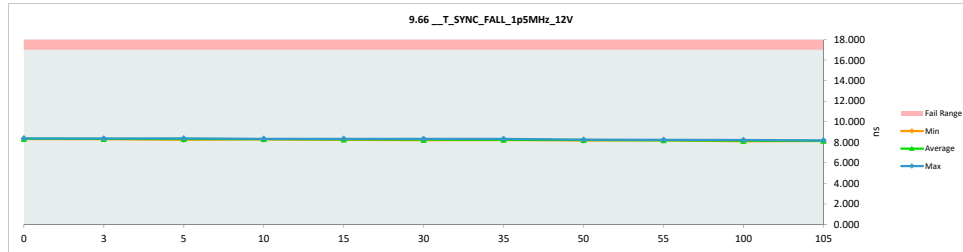


HDR TID Report
TPS7H5001-SP

		9.66 T_SYNC_FALL_1p5MHz_12V		
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.323	8.353	0.030
0	118	8.288	8.367	0.079
0	122	8.257	8.305	0.048
3	301	8.296	8.304	0.008
3	306	8.280	8.286	0.006
3	307	8.304	8.345	0.041
3	308	8.235	8.289	0.054
3	309	8.303	8.350	0.047
5	310	8.218	8.290	0.072
5	311	8.369	8.372	0.003
5	312	8.237	8.368	0.131
5	314	8.231	8.245	0.014
5	316	8.251	8.261	0.010
10	317	8.272	8.328	0.056
10	318	8.246	8.324	0.078
10	319	8.271	8.271	0.000
10	321	8.296	8.306	0.010
10	322	8.310	8.287	-0.023
15	323	8.240	8.235	-0.005
15	325	8.230	8.266	0.036
15	326	8.278	8.328	0.050
15	328	8.283	8.251	-0.032
15	333	8.286	8.288	0.002
30	334	8.300	8.207	-0.093
30	335	8.235	8.201	-0.034
30	337	8.180	8.261	0.081
30	339	8.312	8.327	0.015
30	341	8.261	8.237	-0.024
35	342	8.261	8.311	0.050
35	345	8.210	8.215	0.005
35	346	8.309	8.265	-0.044
35	347	8.155	8.208	0.053
35	348	8.255	8.318	0.063
50	349	8.297	8.206	-0.091
50	350	8.329	8.247	-0.082
50	351	8.168	8.163	-0.005
50	353	8.332	8.211	-0.121
50	354	8.294	8.192	-0.102
55	355	8.301	8.189	-0.112
55	357	8.293	8.217	-0.076
55	359	8.250	8.224	-0.026
55	362	8.223	8.182	-0.041
55	363	8.299	8.189	-0.110
100	1	8.291	8.162	-0.129
100	3	8.257	8.098	-0.159
100	4	8.280	8.120	-0.160
100	5	8.318	8.218	-0.100
100	6	8.298	8.125	-0.173
100	7	8.337	8.216	-0.121
100	8	8.282	8.111	-0.171
100	9	8.280	8.193	-0.087
100	10	8.275	8.144	-0.131
100	11	8.305	8.184	-0.121
100	12	8.253	8.146	-0.107
100	13	8.321	8.199	-0.122
100	14	8.267	8.160	-0.107
100	15	8.261	8.172	-0.089
100	16	8.255	8.143	-0.112
100	17	8.270	8.172	-0.098
100	18	8.252	8.092	-0.160
100	19	8.269	8.109	-0.160
100	20	8.255	8.127	-0.128
100	21	8.224	8.145	-0.079
100	22	8.233	8.104	-0.129
100	24	8.259	8.184	-0.075
105	25	8.300	8.134	-0.166
105	26	8.262	8.178	-0.084
105	27	8.295	8.148	-0.147
105	28	8.274	8.116	-0.158
105	29	8.279	8.137	-0.142
Max		8.369	8.372	0.131
Average		8.272	8.223	-0.048
Min		8.155	8.092	-0.173
Std Dev		0.038	0.077	0.080

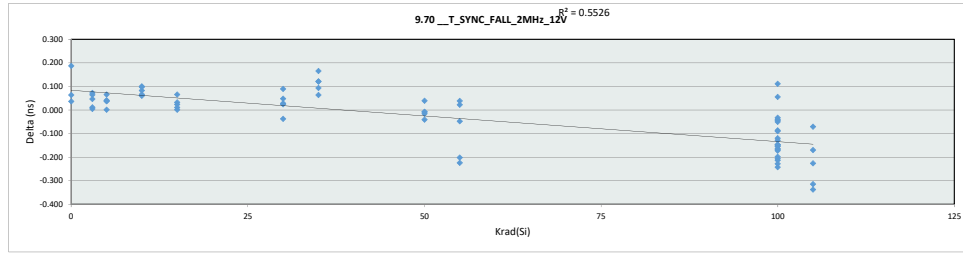


		9.66 T_SYNC_FALL_1p5MHz												
Test Site														
Tester														
Test Number														
Max Limit	17	ns												
Min Limit		ns												
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105			
LL														
Min	8.305	8.286	8.245	8.271	8.235	8.201	8.208	8.163	8.182	8.092	8.116			
Average	8.342	8.315	8.307	8.303	8.274	8.247	8.263	8.204	8.200	8.151	8.143			
Max	8.367	8.350	8.372	8.328	8.328	8.327	8.318	8.247	8.224	8.218	8.178			
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000			

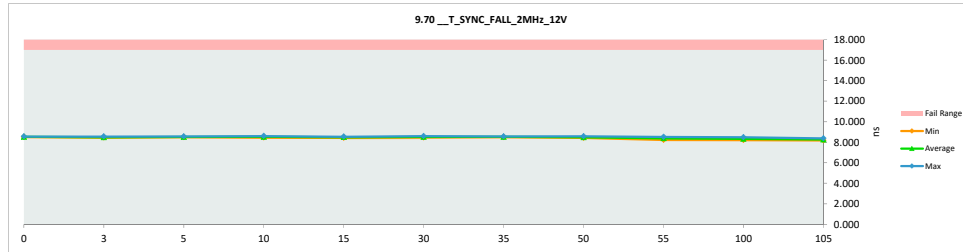


HDR TID Report
TPS7H5001-SP

		9.70 T_SYNC_FALL_2MHz_12V		
Test Site				
Tester				
Test Number				
Unit		ns	ns	
Max Limit		17	17	
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.349	8.536	0.187
0	118	8.512	8.548	0.036
0	122	8.441	8.504	0.063
3	301	8.403	8.475	0.072
3	306	8.415	8.479	0.064
3	307	8.543	8.547	0.004
3	308	8.427	8.438	0.011
3	309	8.423	8.469	0.046
5	310	8.415	8.480	0.065
5	311	8.491	8.532	0.041
5	312	8.555	8.556	0.001
5	314	8.447	8.484	0.037
5	316	8.464	8.502	0.038
10	317	8.461	8.543	0.082
10	318	8.398	8.498	0.100
10	319	8.341	8.438	0.097
10	321	8.536	8.596	0.060
10	322	8.499	8.565	0.066
15	323	8.418	8.483	0.065
15	325	8.420	8.421	0.001
15	326	8.482	8.514	0.032
15	328	8.510	8.533	0.023
15	333	8.472	8.482	0.010
30	334	8.522	8.570	0.048
30	335	8.506	8.468	-0.038
30	337	8.428	8.452	0.024
30	339	8.502	8.591	0.089
30	341	8.480	8.508	0.028
35	342	8.472	8.535	0.063
35	345	8.411	8.532	0.121
35	346	8.363	8.483	0.120
35	347	8.444	8.537	0.093
35	348	8.392	8.557	0.165
50	349	8.483	8.522	0.039
50	350	8.585	8.577	-0.008
50	351	8.441	8.432	-0.009
50	353	8.490	8.474	-0.016
50	354	8.455	8.414	-0.041
55	355	8.442	8.217	-0.225
55	357	8.462	8.484	0.022
55	359	8.472	8.510	0.038
55	362	8.458	8.256	-0.202
55	363	8.473	8.425	-0.048
100	1	8.490	8.247	-0.243
100	3	8.459	8.414	-0.045
100	4	8.405	8.241	-0.164
100	5	8.526	8.475	-0.051
100	6	8.491	8.323	-0.168
100	7	8.493	8.286	-0.207
100	8	8.377	8.248	-0.129
100	9	8.429	8.200	-0.229
100	10	8.481	8.361	-0.120
100	11	8.395	8.305	-0.090
100	12	8.299	8.410	0.111
100	13	8.522	8.482	-0.040
100	14	8.506	8.307	-0.199
100	15	8.426	8.393	-0.033
100	16	8.397	8.452	0.055
100	17	8.518	8.371	-0.147
100	18	8.398	8.311	-0.087
100	19	8.502	8.288	-0.214
100	20	8.481	8.333	-0.148
100	21	8.445	8.290	-0.155
100	22	8.439	8.266	-0.173
100	24	8.421	8.268	-0.153
105	25	8.511	8.196	-0.315
105	26	8.509	8.339	-0.170
105	27	8.506	8.168	-0.338
105	28	8.461	8.234	-0.227
105	29	8.443	8.372	-0.071
Max		8.585	8.596	0.187
Average		8.458	8.425	-0.033
Min		8.299	8.168	-0.338
Std Dev		0.053	0.115	0.120

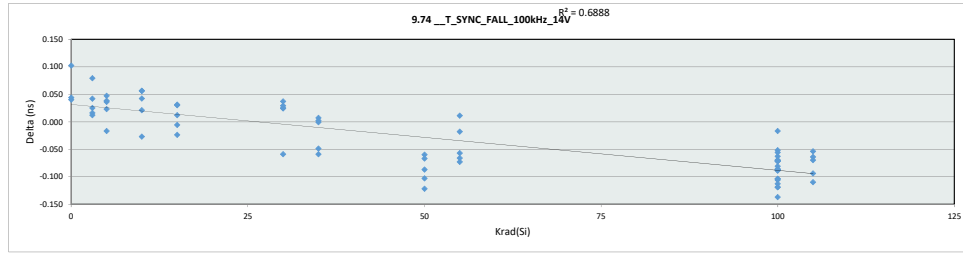


		9.70 T_SYNC_FALL_2MHz_12V										
Test Site												
Tester												
Test Number												
Max Limit		17	ns									
Min Limit			ns									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		8.504	8.438	8.480	8.438	8.421	8.452	8.483	8.414	8.217	8.200	8.168
Average		8.529	8.482	8.511	8.528	8.487	8.518	8.529	8.484	8.378	8.331	8.262
Max		8.548	8.547	8.556	8.596	8.533	8.591	8.557	8.577	8.510	8.482	8.372
UL		17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

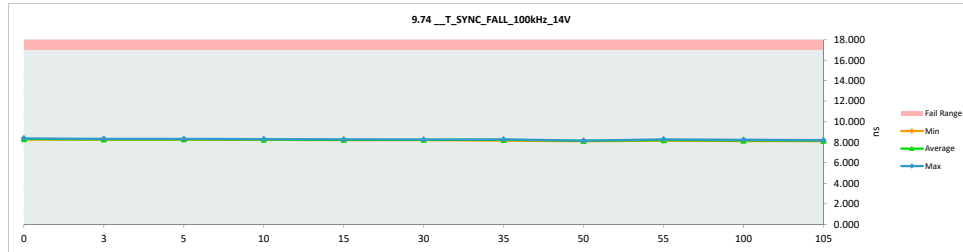


HDR TID Report
TPS7H5001-SP

		9.74 T_SYNC_FALL_100kHz_14V		
Test Site				
Tester				
Test Number				
Unit		ns	ns	
Max Limit		17	17	
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.227	8.267	0.040
0	118	8.278	8.380	0.102
0	122	8.237	8.281	0.044
3	301	8.294	8.306	0.012
3	306	8.211	8.253	0.042
3	307	8.240	8.256	0.016
3	308	8.249	8.328	0.079
3	309	8.231	8.256	0.025
5	310	8.238	8.276	0.038
5	311	8.285	8.321	0.036
5	312	8.330	8.313	-0.017
5	314	8.201	8.248	0.047
5	316	8.230	8.253	0.023
10	317	8.256	8.277	0.021
10	318	8.198	8.254	0.056
10	319	8.177	8.233	0.056
10	321	8.265	8.238	-0.027
10	322	8.261	8.303	0.042
15	323	8.195	8.207	0.012
15	325	8.244	8.238	-0.006
15	326	8.246	8.277	0.031
15	328	8.240	8.270	0.030
15	333	8.230	8.206	-0.024
30	334	8.202	8.226	0.024
30	335	8.266	8.207	-0.059
30	337	8.197	8.226	0.029
30	339	8.231	8.256	0.025
30	341	8.223	8.260	0.037
35	342	8.278	8.277	-0.001
35	345	8.268	8.219	-0.049
35	346	8.254	8.256	0.002
35	347	8.217	8.158	-0.059
35	348	8.239	8.246	0.007
50	349	8.213	8.153	-0.060
50	350	8.216	8.149	-0.067
50	351	8.185	8.098	-0.087
50	353	8.251	8.148	-0.103
50	354	8.271	8.149	-0.122
55	355	8.239	8.166	-0.073
55	357	8.218	8.200	-0.018
55	359	8.273	8.284	0.011
55	362	8.267	8.201	-0.066
55	363	8.207	8.150	-0.057
100	1	8.282	8.226	-0.056
100	3	8.279	8.160	-0.119
100	4	8.251	8.179	-0.072
100	5	8.319	8.200	-0.119
100	6	8.221	8.134	-0.087
100	7	8.322	8.241	-0.081
100	8	8.238	8.166	-0.072
100	9	8.224	8.136	-0.088
100	10	8.278	8.172	-0.106
100	11	8.239	8.150	-0.089
100	12	8.320	8.183	-0.137
100	13	8.262	8.175	-0.087
100	14	8.253	8.182	-0.071
100	15	8.221	8.117	-0.104
100	16	8.218	8.148	-0.070
100	17	8.237	8.174	-0.063
100	18	8.218	8.166	-0.052
100	19	8.260	8.190	-0.070
100	20	8.258	8.154	-0.104
100	21	8.215	8.198	-0.017
100	22	8.218	8.105	-0.113
100	24	8.260	8.174	-0.086
105	25	8.190	8.120	-0.070
105	26	8.253	8.189	-0.064
105	27	8.216	8.106	-0.110
105	28	8.268	8.174	-0.094
105	29	8.230	8.176	-0.054
	Max	8.330	8.380	0.102
	Average	8.243	8.211	-0.032
	Min	8.177	8.098	-0.137
	Std Dev	0.033	0.061	0.059

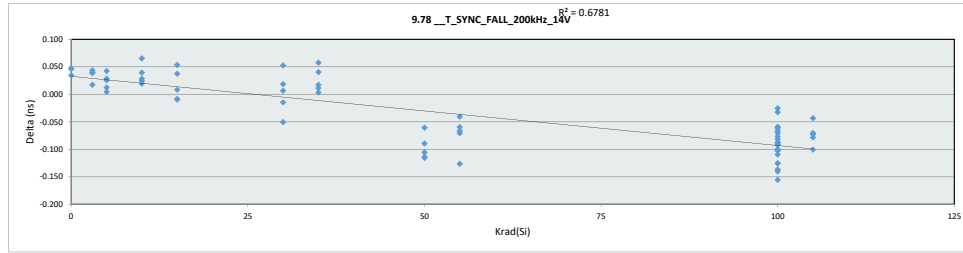


		9.74 T_SYNC_FALL_100kHz										
Test Site												
Tester												
Test Number												
Max Limit		17	ns									
Min Limit			ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL												
Min	8.267	8.253	8.248	8.233	8.206	8.207	8.158	8.098	8.150	8.105	8.106	
Average	8.309	8.280	8.282	8.261	8.240	8.235	8.231	8.139	8.200	8.170	8.153	
Max	8.380	8.328	8.321	8.303	8.277	8.260	8.277	8.153	8.284	8.241	8.189	
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	

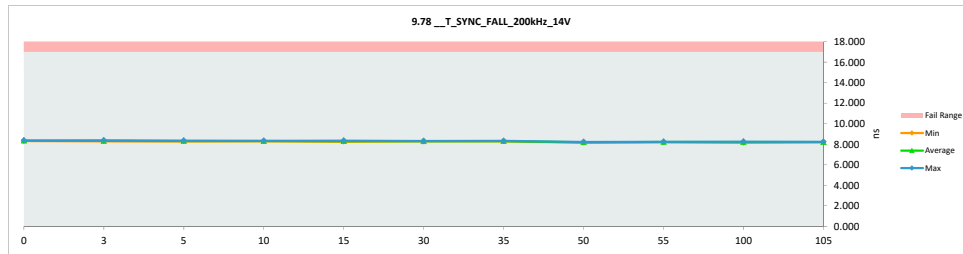


HDR TID Report
TPS7H5001-SP

9.78 T_SYNC_FALL_200kHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.315	8.361	0.046
0	118	8.332	8.366	0.034
0	122	8.263	8.309	0.046
3	301	8.336	8.375	0.039
3	306	8.271	8.310	0.039
3	307	8.242	8.285	0.043
3	308	8.284	8.322	0.038
3	309	8.320	8.337	0.017
5	310	8.282	8.324	0.042
5	311	8.338	8.350	0.012
5	312	8.340	8.344	0.004
5	314	8.249	8.274	0.025
5	316	8.282	8.310	0.028
10	317	8.311	8.335	0.024
10	318	8.250	8.289	0.039
10	319	8.265	8.293	0.028
10	321	8.297	8.316	0.019
10	322	8.260	8.325	0.065
15	323	8.256	8.246	-0.010
15	325	8.318	8.309	-0.009
15	326	8.294	8.302	0.008
15	328	8.299	8.352	0.053
15	333	8.289	8.326	0.037
30	334	8.297	8.282	-0.015
30	335	8.341	8.290	-0.051
30	337	8.229	8.281	0.052
30	339	8.306	8.324	0.018
30	341	8.295	8.301	0.006
35	342	8.277	8.288	0.011
35	345	8.296	8.299	0.003
35	346	8.245	8.302	0.057
35	347	8.260	8.300	0.040
35	348	8.309	8.326	0.017
50	349	8.312	8.206	-0.106
50	350	8.314	8.224	-0.090
50	351	8.256	8.142	-0.114
50	353	8.277	8.216	-0.061
50	354	8.289	8.173	-0.116
55	355	8.260	8.219	-0.041
55	357	8.315	8.244	-0.071
55	359	8.361	8.234	-0.127
55	362	8.302	8.235	-0.067
55	363	8.258	8.198	-0.060
100	1	8.365	8.209	-0.156
100	3	8.268	8.235	-0.033
100	4	8.294	8.168	-0.126
100	5	8.340	8.258	-0.082
100	6	8.304	8.215	-0.089
100	7	8.353	8.243	-0.110
100	8	8.247	8.185	-0.062
100	9	8.327	8.190	-0.137
100	10	8.245	8.178	-0.067
100	11	8.344	8.242	-0.102
100	12	8.246	8.220	-0.026
100	13	8.273	8.202	-0.071
100	14	8.308	8.217	-0.091
100	15	8.232	8.172	-0.060
100	16	8.280	8.139	-0.141
100	17	8.312	8.220	-0.092
100	18	8.301	8.241	-0.060
100	19	8.295	8.192	-0.103
100	20	8.274	8.197	-0.077
100	21	8.306	8.206	-0.100
100	22	8.286	8.160	-0.126
100	24	8.305	8.217	-0.088
105	25	8.305	8.204	-0.101
105	26	8.301	8.228	-0.073
105	27	8.251	8.207	-0.044
105	28	8.313	8.234	-0.079
105	29	8.275	8.204	-0.071
Max		8.365	8.375	0.065
Average		8.292	8.258	-0.034
Min		8.229	8.139	-0.156
Std Dev		0.032	0.061	0.062

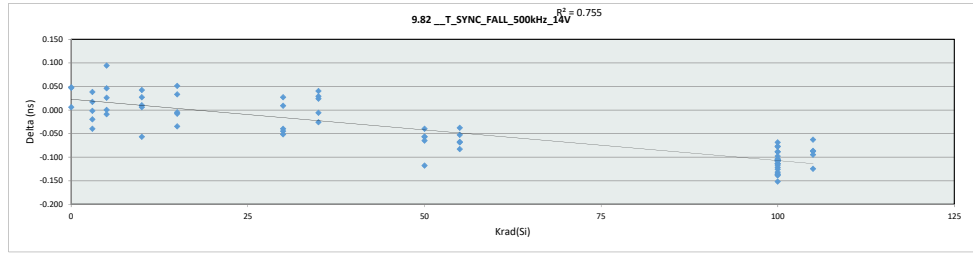


9.78 T_SYNC_FALL_200kHz											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.309	8.285	8.274	8.289	8.246	8.281	8.288	8.142	8.198	8.139	8.204
Average	8.345	8.326	8.320	8.312	8.307	8.296	8.303	8.192	8.226	8.205	8.215
Max	8.366	8.375	8.350	8.335	8.352	8.324	8.326	8.224	8.244	8.258	8.234
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

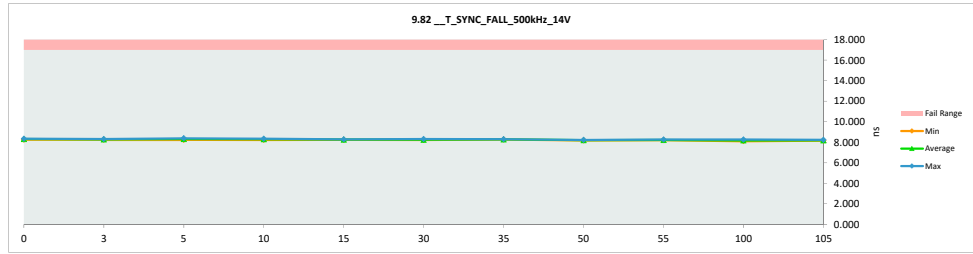


HDR TID Report
TPS7H5001-SP

9.82 T_SYNC_FALL_500kHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.288	8.336	0.048
0	118	8.280	8.327	0.047
0	122	8.237	8.243	0.006
3	301	8.306	8.304	-0.002
3	306	8.214	8.252	0.038
3	307	8.243	8.260	0.017
3	308	8.278	8.238	-0.040
3	309	8.298	8.278	-0.020
5	310	8.281	8.375	0.094
5	311	8.342	8.342	0.000
5	312	8.350	8.341	-0.009
5	314	8.205	8.231	0.026
5	316	8.225	8.271	0.046
10	317	8.257	8.267	0.010
10	318	8.266	8.272	0.006
10	319	8.276	8.219	-0.057
10	321	8.299	8.326	0.027
10	322	8.288	8.330	0.042
15	323	8.239	8.231	-0.008
15	325	8.217	8.268	0.051
15	326	8.302	8.267	-0.035
15	328	8.286	8.281	-0.005
15	333	8.211	8.244	0.033
30	334	8.259	8.214	-0.045
30	335	8.283	8.231	-0.052
30	337	8.234	8.243	0.009
30	339	8.295	8.255	-0.040
30	341	8.274	8.301	0.027
35	342	8.244	8.284	0.040
35	345	8.238	8.262	0.024
35	346	8.284	8.278	-0.006
35	347	8.245	8.274	0.029
35	348	8.304	8.278	-0.026
50	349	8.244	8.204	-0.040
50	350	8.277	8.220	-0.057
50	351	8.223	8.166	-0.057
50	353	8.307	8.189	-0.118
50	354	8.282	8.217	-0.065
55	355	8.258	8.190	-0.068
55	357	8.304	8.221	-0.083
55	359	8.296	8.258	-0.038
55	362	8.280	8.227	-0.053
55	363	8.257	8.188	-0.069
100	1	8.310	8.232	-0.078
100	3	8.275	8.166	-0.109
100	4	8.279	8.127	-0.152
100	5	8.316	8.199	-0.117
100	6	8.302	8.170	-0.132
100	7	8.364	8.258	-0.106
100	8	8.291	8.202	-0.089
100	9	8.254	8.177	-0.077
100	10	8.226	8.137	-0.089
100	11	8.309	8.195	-0.114
100	12	8.281	8.142	-0.139
100	13	8.315	8.201	-0.114
100	14	8.291	8.183	-0.108
100	15	8.235	8.166	-0.069
100	16	8.273	8.151	-0.122
100	17	8.314	8.178	-0.136
100	18	8.269	8.165	-0.104
100	19	8.303	8.166	-0.137
100	20	8.292	8.184	-0.108
100	21	8.298	8.192	-0.106
100	22	8.222	8.096	-0.126
100	24	8.324	8.226	-0.098
105	25	8.247	8.159	-0.088
105	26	8.291	8.228	-0.063
105	27	8.247	8.160	-0.087
105	28	8.305	8.210	-0.095
105	29	8.279	8.154	-0.125
	Max	8.364	8.375	0.094
	Average	8.276	8.229	-0.047
	Min	8.205	8.096	-0.152
	Std Dev	0.034	0.059	0.061

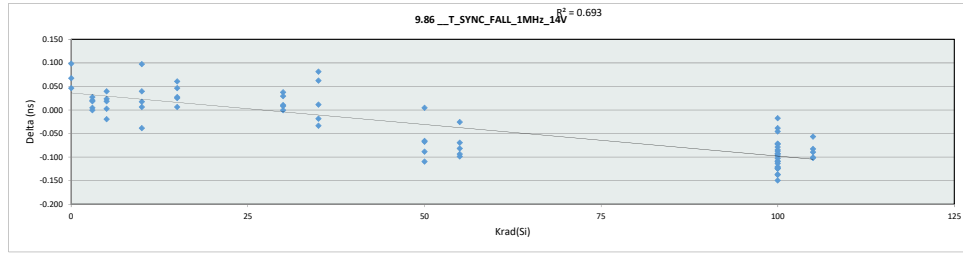


9.82 T_SYNC_FALL_500kHz											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.243	8.238	8.231	8.219	8.231	8.214	8.262	8.166	8.188	8.096	8.154
Average	8.302	8.266	8.312	8.283	8.258	8.249	8.275	8.199	8.217	8.178	8.182
Max	8.336	8.304	8.375	8.330	8.281	8.301	8.284	8.220	8.258	8.258	8.228
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

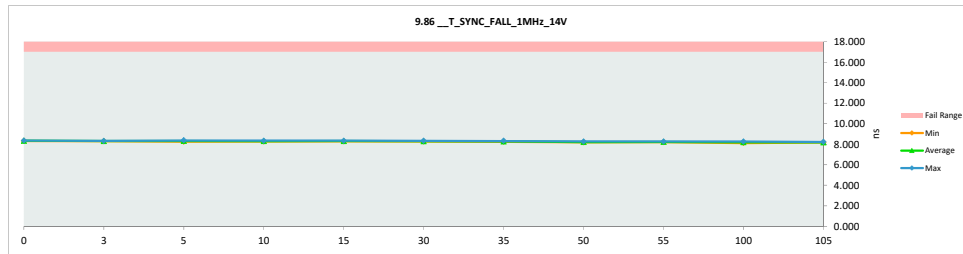


HDR TID Report
TPS7H5001-SP

		9.86 T_SYNC_FALL_1MHz_14V		
Test Site				
Tester				
Test Number				
Unit		ns	ns	
Max Limit		17	17	
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.269	8.336	0.067
0	118	8.313	8.359	0.046
0	122	8.220	8.318	0.098
3	301	8.304	8.322	0.018
3	306	8.264	8.291	0.027
3	307	8.285	8.305	0.020
3	308	8.317	8.316	-0.001
3	309	8.287	8.291	0.004
5	310	8.246	8.285	0.039
5	311	8.349	8.372	0.023
5	312	8.360	8.378	0.018
5	314	8.243	8.245	0.002
5	316	8.303	8.283	-0.020
10	317	8.243	8.340	0.097
10	318	8.260	8.299	0.039
10	319	8.249	8.255	0.006
10	321	8.304	8.265	-0.039
10	322	8.308	8.325	0.017
15	323	8.245	8.270	0.025
15	325	8.289	8.295	0.006
15	326	8.279	8.306	0.027
15	328	8.286	8.332	0.046
15	333	8.247	8.307	0.060
30	334	8.261	8.260	-0.001
30	335	8.270	8.278	0.008
30	337	8.248	8.277	0.029
30	339	8.297	8.334	0.037
30	341	8.291	8.301	0.010
35	342	8.277	8.288	0.011
35	345	8.267	8.248	-0.019
35	346	8.271	8.237	-0.034
35	347	8.176	8.257	0.081
35	348	8.268	8.330	0.062
50	349	8.261	8.265	0.004
50	350	8.288	8.220	-0.068
50	351	8.248	8.182	-0.066
50	353	8.292	8.182	-0.110
50	354	8.273	8.184	-0.089
55	355	8.287	8.217	-0.070
55	357	8.280	8.254	-0.026
55	359	8.333	8.234	-0.099
55	362	8.277	8.195	-0.082
55	363	8.301	8.207	-0.094
100	1	8.308	8.199	-0.109
100	3	8.271	8.173	-0.098
100	4	8.314	8.176	-0.138
100	5	8.316	8.231	-0.085
100	6	8.256	8.210	-0.046
100	7	8.358	8.265	-0.093
100	8	8.251	8.172	-0.079
100	9	8.258	8.144	-0.114
100	10	8.292	8.171	-0.121
100	11	8.316	8.193	-0.123
100	12	8.268	8.196	-0.072
100	13	8.308	8.183	-0.125
100	14	8.309	8.206	-0.103
100	15	8.207	8.189	-0.018
100	16	8.273	8.163	-0.110
100	17	8.317	8.229	-0.088
100	18	8.252	8.213	-0.039
100	19	8.293	8.199	-0.094
100	20	8.286	8.161	-0.125
100	21	8.321	8.171	-0.150
100	22	8.248	8.111	-0.137
100	24	8.270	8.197	-0.073
105	25	8.250	8.167	-0.083
105	26	8.321	8.220	-0.101
105	27	8.285	8.183	-0.102
105	28	8.276	8.186	-0.090
105	29	8.234	8.177	-0.057
	Max	8.360	8.378	0.098
	Average	8.280	8.245	-0.035
	Min	8.176	8.111	-0.150
	Std Dev	0.033	0.063	0.066

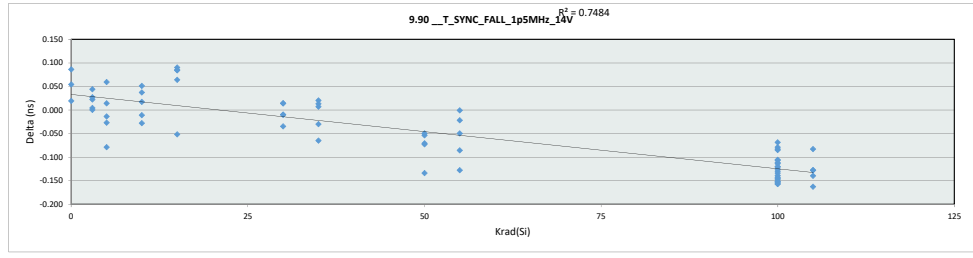


		9.86 T_SYNC_FALL_1MHz_14V										
Test Site												
Tester												
Test Number												
Max Limit		17	ns									
Min Limit			ns									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		8.318	8.291	8.245	8.255	8.270	8.260	8.237	8.182	8.195	8.111	8.167
Average		8.338	8.305	8.313	8.297	8.302	8.290	8.272	8.207	8.221	8.189	8.187
Max		8.359	8.322	8.378	8.340	8.332	8.334	8.330	8.265	8.254	8.265	8.220
UL		17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

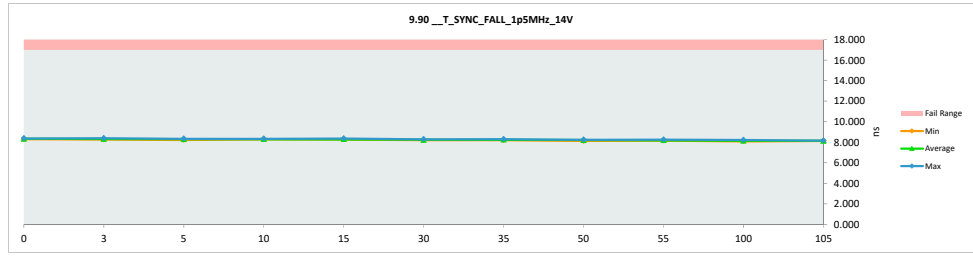


HDR TID Report
TPS7H5001-SP

		9.90 T_SYNC_FALL_1p5MHz_14V		
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.274	8.360	0.086
0	118	8.309	8.363	0.054
0	122	8.276	8.295	0.019
3	301	8.286	8.330	0.044
3	306	8.242	8.264	0.022
3	307	8.379	8.383	0.004
3	308	8.292	8.292	0.000
3	309	8.288	8.315	0.027
5	310	8.249	8.308	0.059
5	311	8.349	8.335	-0.014
5	312	8.407	8.328	-0.079
5	314	8.247	8.220	-0.027
5	316	8.238	8.252	0.014
10	317	8.277	8.328	0.051
10	318	8.317	8.334	0.017
10	319	8.237	8.274	0.037
10	321	8.315	8.304	-0.011
10	322	8.304	8.276	-0.028
15	323	8.193	8.278	0.085
15	325	8.202	8.292	0.090
15	326	8.320	8.268	-0.052
15	328	8.270	8.354	0.084
15	333	8.251	8.315	0.064
30	334	8.233	8.222	-0.010
30	335	8.235	8.200	-0.035
30	337	8.229	8.219	-0.010
30	339	8.269	8.283	0.014
30	341	8.233	8.247	0.014
35	342	8.260	8.267	0.007
35	345	8.236	8.249	0.013
35	346	8.227	8.247	0.020
35	347	8.268	8.203	-0.065
35	348	8.321	8.291	-0.030
50	349	8.249	8.199	-0.050
50	350	8.370	8.236	-0.134
50	351	8.195	8.124	-0.071
50	353	8.269	8.215	-0.054
50	354	8.245	8.172	-0.073
55	355	8.224	8.174	-0.050
55	357	8.244	8.243	-0.001
55	359	8.238	8.216	-0.022
55	362	8.286	8.158	-0.128
55	363	8.283	8.197	-0.086
100	1	8.325	8.173	-0.152
100	3	8.228	8.083	-0.145
100	4	8.235	8.092	-0.143
100	5	8.329	8.182	-0.147
100	6	8.253	8.096	-0.157
100	7	8.357	8.206	-0.151
100	8	8.251	8.117	-0.134
100	9	8.269	8.139	-0.130
100	10	8.236	8.122	-0.114
100	11	8.311	8.190	-0.121
100	12	8.284	8.127	-0.157
100	13	8.305	8.199	-0.106
100	14	8.266	8.154	-0.112
100	15	8.268	8.183	-0.085
100	16	8.311	8.185	-0.126
100	17	8.237	8.158	-0.079
100	18	8.230	8.123	-0.107
100	19	8.253	8.114	-0.139
100	20	8.262	8.142	-0.120
100	21	8.261	8.178	-0.083
100	22	8.246	8.092	-0.154
100	24	8.280	8.211	-0.069
105	25	8.257	8.129	-0.128
105	26	8.309	8.146	-0.163
105	27	8.262	8.122	-0.140
105	28	8.272	8.144	-0.128
105	29	8.235	8.152	-0.083
Max		8.407	8.383	0.090
Average		8.271	8.220	-0.051
Min		8.193	8.083	-0.163
Std Dev		0.043	0.078	0.075

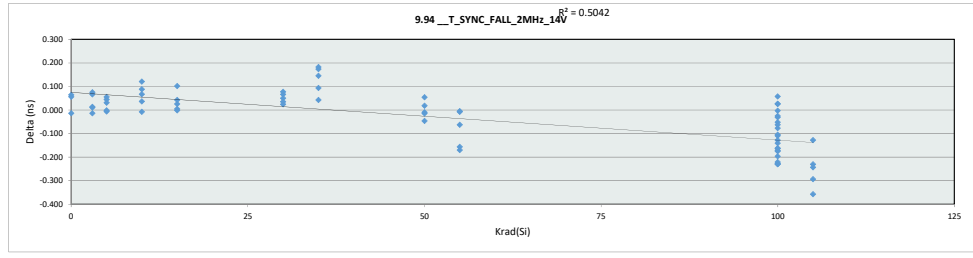


		9.90 T_SYNC_FALL_1p5MHz									
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.295	8.264	8.220	8.274	8.268	8.200	8.203	8.124	8.158	8.083	8.122
Average	8.339	8.317	8.289	8.303	8.301	8.234	8.251	8.189	8.198	8.148	8.139
Max	8.363	8.383	8.335	8.334	8.354	8.283	8.291	8.236	8.243	8.211	8.152
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

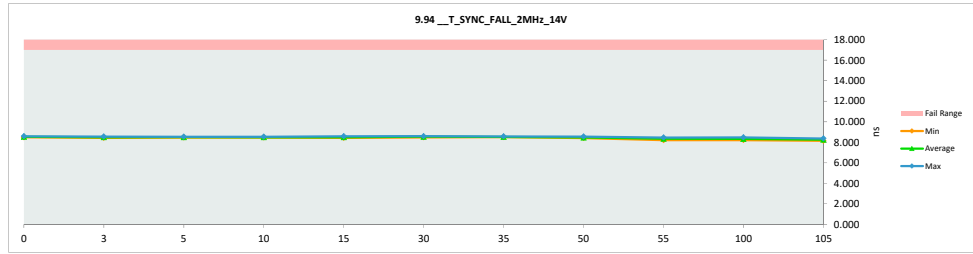


HDR TID Report
TPS7H5001-SP

		9.94 T_SYNC_FALL_2MHz_14V		
Test Site				
Tester				
Test Number				
Unit		ns	ns	
Max Limit		17	17	
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.523	8.579	0.056
0	118	8.485	8.549	0.064
0	122	8.495	8.481	-0.014
3	301	8.476	8.551	0.075
3	306	8.415	8.481	0.066
3	307	8.508	8.521	0.013
3	308	8.445	8.431	-0.014
3	309	8.440	8.450	0.010
5	310	8.446	8.490	0.044
5	311	8.471	8.526	0.055
5	312	8.508	8.539	0.031
5	314	8.463	8.461	-0.002
5	316	8.483	8.476	-0.007
10	317	8.420	8.487	0.067
10	318	8.369	8.457	0.088
10	319	8.333	8.453	0.120
10	321	8.541	8.533	-0.008
10	322	8.495	8.531	0.036
15	323	8.430	8.428	-0.002
15	325	8.424	8.428	0.004
15	326	8.501	8.526	0.025
15	328	8.477	8.578	0.101
15	333	8.465	8.507	0.042
30	334	8.550	8.585	0.035
30	335	8.500	8.524	0.024
30	337	8.404	8.470	0.066
30	339	8.532	8.582	0.050
30	341	8.482	8.559	0.077
35	342	8.471	8.513	0.042
35	345	8.342	8.515	0.173
35	346	8.347	8.492	0.145
35	347	8.448	8.541	0.093
35	348	8.380	8.562	0.182
50	349	8.471	8.489	0.018
50	350	8.596	8.549	-0.047
50	351	8.419	8.409	-0.010
50	353	8.476	8.461	-0.015
50	354	8.369	8.423	0.054
55	355	8.373	8.203	-0.170
55	357	8.459	8.451	-0.008
55	359	8.460	8.455	-0.005
55	362	8.382	8.225	-0.157
55	363	8.471	8.408	-0.063
100	1	8.491	8.261	-0.230
100	3	8.408	8.405	-0.003
100	4	8.416	8.254	-0.162
100	5	8.501	8.475	-0.026
100	6	8.506	8.285	-0.221
100	7	8.481	8.284	-0.197
100	8	8.304	8.329	0.025
100	9	8.407	8.234	-0.173
100	10	8.497	8.434	-0.063
100	11	8.432	8.204	-0.228
100	12	8.350	8.407	0.057
100	13	8.473	8.442	-0.031
100	14	8.471	8.241	-0.230
100	15	8.498	8.388	-0.110
100	16	8.398	8.424	0.026
100	17	8.483	8.355	-0.128
100	18	8.376	8.299	-0.077
100	19	8.422	8.281	-0.141
100	20	8.445	8.393	-0.052
100	21	8.404	8.229	-0.175
100	22	8.425	8.261	-0.164
100	24	8.405	8.300	-0.105
105	25	8.478	8.184	-0.294
105	26	8.576	8.345	-0.231
105	27	8.495	8.137	-0.358
105	28	8.483	8.239	-0.244
105	29	8.489	8.361	-0.128
	Max	8.596	8.585	0.182
	Average	8.452	8.419	-0.033
	Min	8.304	8.137	-0.358
	Std Dev	0.058	0.117	0.117

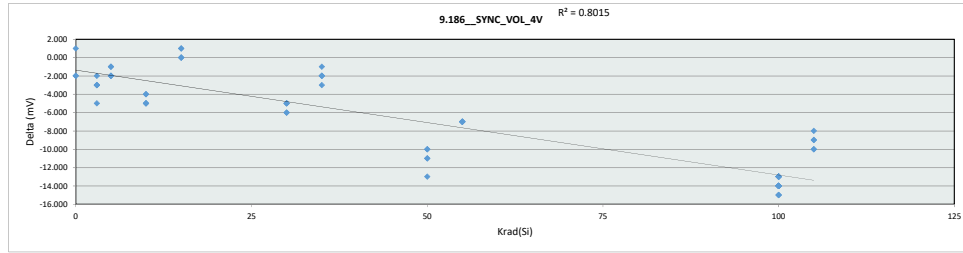


		9.94 T_SYNC_FALL_2MHz_14V										
Test Site												
Tester												
Test Number												
Max Limit		17	ns									
Min Limit			ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL												
Min	8.481	8.431	8.461	8.453	8.428	8.470	8.492	8.409	8.203	8.204	8.137	
Average	8.536	8.487	8.498	8.492	8.493	8.544	8.525	8.466	8.348	8.327	8.253	
Max	8.579	8.551	8.539	8.533	8.578	8.585	8.562	8.549	8.455	8.475	8.361	
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	

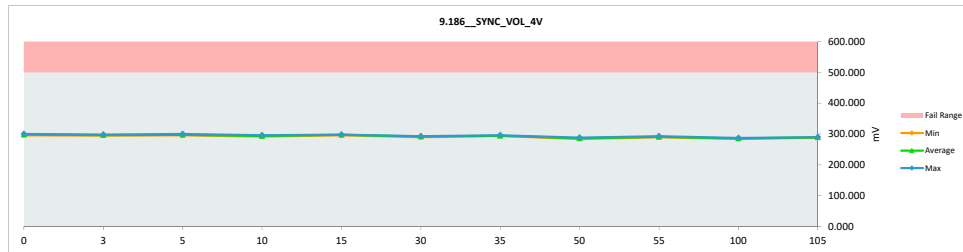


HDR TID Report
TPS7H5001-SP

9.186_SYNC_VOL_4V				
Test Site				
Tester				
Test Number				
Unit	mV		mV	
Max Limit	500		500	
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	301.000	299.000	-2.000
0	118	299.000	300.000	1.000
0	122	298.000	296.000	-2.000
3	301	300.000	295.000	-5.000
3	306	301.000	298.000	-3.000
3	307	300.000	298.000	-2.000
3	308	299.000	296.000	-3.000
3	309	298.000	295.000	-3.000
5	310	301.000	300.000	-1.000
5	311	300.000	299.000	-1.000
5	312	298.000	296.000	-2.000
5	314	298.000	296.000	-2.000
5	316	299.000	297.000	-2.000
10	317	299.000	294.000	-5.000
10	318	298.000	294.000	-4.000
10	319	298.000	293.000	-5.000
10	321	300.000	296.000	-4.000
10	322	297.000	292.000	-5.000
15	323	298.000	298.000	0.000
15	325	295.000	296.000	1.000
15	326	298.000	298.000	0.000
15	328	296.000	297.000	1.000
15	333	298.000	298.000	0.000
30	334	298.000	293.000	-5.000
30	335	296.000	290.000	-6.000
30	337	298.000	292.000	-6.000
30	339	297.000	292.000	-5.000
30	341	297.000	292.000	-5.000
35	342	297.000	296.000	-1.000
35	345	296.000	294.000	-2.000
35	346	297.000	294.000	-3.000
35	347	296.000	294.000	-2.000
35	348	297.000	295.000	-2.000
50	349	297.000	284.000	-13.000
50	350	296.000	286.000	-10.000
50	351	296.000	285.000	-11.000
50	353	298.000	288.000	-10.000
50	354	298.000	287.000	-11.000
55	355	297.000	290.000	-7.000
55	357	298.000	291.000	-7.000
55	359	298.000	291.000	-7.000
55	362	297.000	290.000	-7.000
55	363	300.000	293.000	-7.000
100	1	299.000	285.000	-14.000
100	3	299.000	286.000	-13.000
100	4	299.000	285.000	-14.000
100	5	298.000	285.000	-13.000
100	6	299.000	286.000	-13.000
100	7	299.000	286.000	-13.000
100	8	298.000	284.000	-14.000
100	9	302.000	287.000	-15.000
100	10	300.000	286.000	-14.000
100	11	299.000	285.000	-14.000
100	12	299.000	286.000	-13.000
100	13	301.000	287.000	-14.000
100	14	301.000	287.000	-14.000
100	15	297.000	284.000	-13.000
100	16	302.000	287.000	-15.000
100	17	300.000	286.000	-14.000
100	18	298.000	284.000	-14.000
100	19	298.000	284.000	-14.000
100	20	298.000	285.000	-13.000
100	21	298.000	285.000	-13.000
100	22	300.000	287.000	-13.000
100	24	299.000	284.000	-15.000
105	25	300.000	290.000	-10.000
105	26	297.000	289.000	-8.000
105	27	298.000	289.000	-9.000
105	28	297.000	288.000	-9.000
105	29	300.000	290.000	-10.000
	Max	302.000	300.000	1.000
	Average	298.400	290.929	-7.471
	Min	295.000	284.000	-15.000
	Std Dev	1.536	5.020	5.230

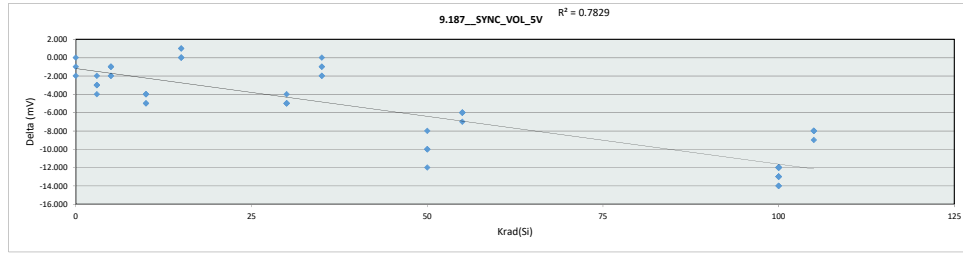


9.186_SYNC_VOL_4V											
Test Site											
Tester											
Test Number											
Max Limit	500										
Min Limit											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	296.000	295.000	296.000	292.000	296.000	290.000	294.000	284.000	290.000	284.000	288.000
Average	298.333	296.400	297.600	293.800	297.400	291.800	294.600	286.000	291.000	285.500	289.200
Max	300.000	298.000	300.000	296.000	298.000	293.000	296.000	298.000	293.000	287.000	290.000
UL	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000

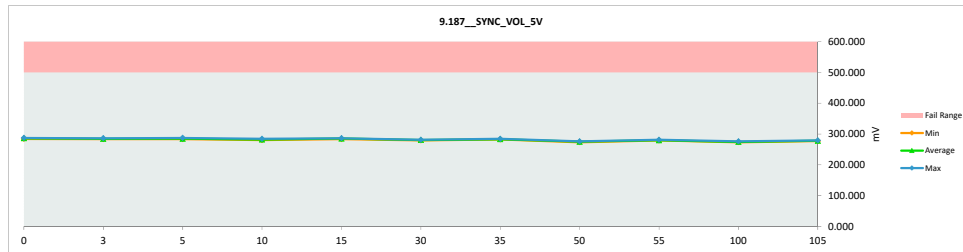


HDR TID Report
TPS7H5001-SP

9.187 SYNC VOL 5V				
Test Site				
Tester				
Test Number				
Unit	mV		mV	
Max Limit	500		500	
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	288.000	287.000	-1.000
0	118	287.000	287.000	0.000
0	122	286.000	284.000	-2.000
3	301	287.000	283.000	-4.000
3	306	289.000	286.000	-3.000
3	307	288.000	285.000	-3.000
3	308	287.000	284.000	-3.000
3	309	285.000	283.000	-2.000
5	310	288.000	287.000	-1.000
5	311	287.000	286.000	-1.000
5	312	285.000	283.000	-2.000
5	314	285.000	284.000	-1.000
5	316	286.000	284.000	-2.000
10	317	287.000	282.000	-5.000
10	318	286.000	282.000	-4.000
10	319	286.000	281.000	-5.000
10	321	288.000	284.000	-4.000
10	322	284.000	280.000	-4.000
15	323	285.000	286.000	1.000
15	325	283.000	283.000	0.000
15	326	286.000	286.000	0.000
15	328	284.000	285.000	1.000
15	333	286.000	286.000	0.000
30	334	286.000	281.000	-5.000
30	335	284.000	279.000	-5.000
30	337	285.000	280.000	-5.000
30	339	285.000	281.000	-4.000
30	341	285.000	280.000	-5.000
35	342	284.000	284.000	0.000
35	345	284.000	283.000	-1.000
35	346	284.000	282.000	-2.000
35	347	284.000	282.000	-2.000
35	348	284.000	283.000	-1.000
50	349	285.000	273.000	-12.000
50	350	283.000	275.000	-8.000
50	351	284.000	274.000	-10.000
50	353	286.000	276.000	-10.000
50	354	285.000	275.000	-10.000
55	355	285.000	279.000	-6.000
55	357	286.000	279.000	-7.000
55	359	285.000	279.000	-6.000
55	362	285.000	279.000	-6.000
55	363	287.000	281.000	-6.000
100	1	286.000	273.000	-13.000
100	3	287.000	275.000	-12.000
100	4	286.000	274.000	-12.000
100	5	285.000	273.000	-12.000
100	6	287.000	275.000	-12.000
100	7	286.000	274.000	-12.000
100	8	285.000	273.000	-12.000
100	9	289.000	275.000	-14.000
100	10	288.000	275.000	-13.000
100	11	286.000	274.000	-12.000
100	12	287.000	275.000	-12.000
100	13	289.000	276.000	-13.000
100	14	289.000	275.000	-14.000
100	15	285.000	273.000	-12.000
100	16	289.000	275.000	-14.000
100	17	287.000	275.000	-12.000
100	18	285.000	273.000	-12.000
100	19	285.000	273.000	-12.000
100	20	286.000	273.000	-13.000
100	21	286.000	274.000	-12.000
100	22	288.000	275.000	-13.000
100	24	286.000	273.000	-13.000
105	25	288.000	279.000	-9.000
105	26	285.000	277.000	-8.000
105	27	286.000	278.000	-8.000
105	28	285.000	277.000	-8.000
105	29	287.000	279.000	-8.000
Max		289.000	287.000	1.000
Average		285.957	279.200	-6.757
Min		283.000	273.000	-14.000
Std Dev		1.508	4.599	4.823

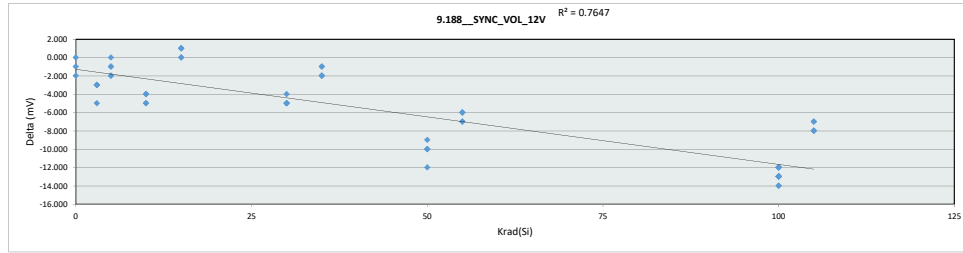


9.187 SYNC VOL 5V											
Test Site											
Tester											
Test Number											
Max Limit	500										
Min Limit											
Unit	mV										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	284.000	283.000	283.000	280.000	283.000	279.000	282.000	273.000	279.000	273.000	277.000
Average	286.000	284.200	284.800	281.800	285.200	280.200	282.800	274.600	279.400	274.136	278.000
Max	287.000	286.000	287.000	284.000	286.000	281.000	284.000	276.000	281.000	276.000	279.000
UL	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000

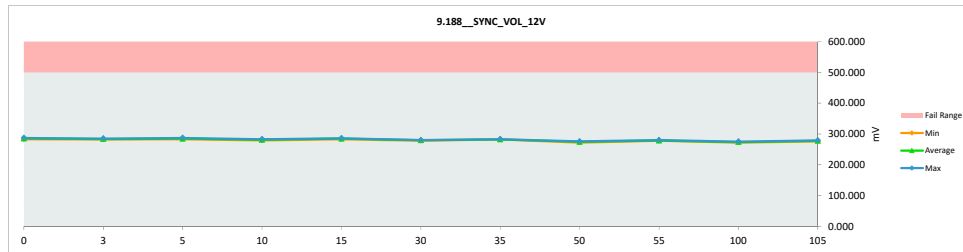


HDR TID Report
TPS7H5001-SP

9.188_SYNC_VOL_12V				
Test Site				
Tester				
Test Number				
Unit	mV		mV	
Max Limit	500		500	
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	288.000	287.000	-1.000
0	118	286.000	286.000	0.000
0	122	285.000	283.000	-2.000
3	301	287.000	282.000	-5.000
3	306	288.000	285.000	-3.000
3	307	288.000	285.000	-3.000
3	308	286.000	283.000	-3.000
3	309	285.000	282.000	-3.000
5	310	287.000	287.000	0.000
5	311	287.000	286.000	-1.000
5	312	284.000	283.000	-1.000
5	314	285.000	283.000	-2.000
5	316	286.000	284.000	-2.000
10	317	286.000	281.000	-5.000
10	318	285.000	281.000	-4.000
10	319	285.000	281.000	-4.000
10	321	287.000	283.000	-4.000
10	322	284.000	279.000	-5.000
15	323	285.000	285.000	0.000
15	325	283.000	283.000	0.000
15	326	285.000	286.000	1.000
15	328	283.000	284.000	1.000
15	333	285.000	286.000	1.000
30	334	285.000	280.000	-5.000
30	335	283.000	278.000	-5.000
30	337	285.000	280.000	-5.000
30	339	284.000	280.000	-4.000
30	341	285.000	280.000	-5.000
35	342	284.000	283.000	-1.000
35	345	284.000	282.000	-2.000
35	346	284.000	282.000	-2.000
35	347	284.000	282.000	-2.000
35	348	283.000	282.000	-1.000
50	349	284.000	272.000	-12.000
50	350	283.000	274.000	-9.000
50	351	284.000	274.000	-10.000
50	353	286.000	276.000	-10.000
50	354	285.000	275.000	-10.000
55	355	284.000	278.000	-6.000
55	357	285.000	279.000	-6.000
55	359	285.000	278.000	-7.000
55	362	284.000	278.000	-6.000
55	363	287.000	280.000	-7.000
100	1	286.000	273.000	-13.000
100	3	287.000	274.000	-13.000
100	4	286.000	273.000	-13.000
100	5	284.000	272.000	-12.000
100	6	286.000	274.000	-12.000
100	7	286.000	274.000	-12.000
100	8	285.000	273.000	-12.000
100	9	289.000	275.000	-14.000
100	10	287.000	274.000	-13.000
100	11	285.000	273.000	-12.000
100	12	287.000	275.000	-12.000
100	13	288.000	275.000	-13.000
100	14	288.000	275.000	-13.000
100	15	285.000	272.000	-13.000
100	16	289.000	275.000	-14.000
100	17	287.000	274.000	-13.000
100	18	285.000	272.000	-13.000
100	19	285.000	273.000	-12.000
100	20	285.000	273.000	-12.000
100	21	285.000	273.000	-12.000
100	22	288.000	275.000	-13.000
100	24	286.000	273.000	-13.000
105	25	287.000	279.000	-8.000
105	26	284.000	277.000	-7.000
105	27	285.000	278.000	-7.000
105	28	284.000	276.000	-8.000
105	29	287.000	279.000	-8.000
Max		289.000	287.000	1.000
Average		285.486	278.671	-6.814
Min		283.000	272.000	-14.000
Std Dev		1.511	4.593	4.852

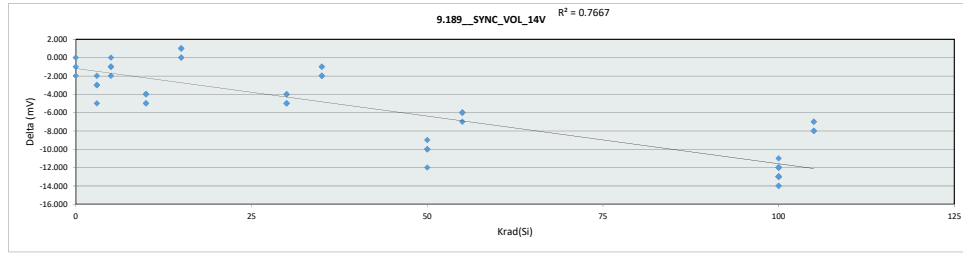


9.188_SYNC_VOL_12V											
Test Site											
Tester											
Test Number											
Max Limit	500										
Min Limit											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	283.000	282.000	283.000	279.000	283.000	278.000	282.000	272.000	278.000	272.000	276.000
Average	285.333	283.400	284.600	281.000	284.800	279.600	282.200	274.200	278.600	273.636	277.800
Max	287.000	285.000	287.000	283.000	286.000	280.000	283.000	276.000	280.000	275.000	279.000
UL	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000

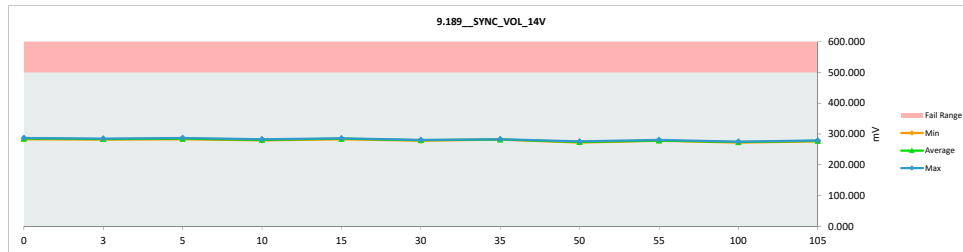


HDR TID Report
TPS7H5001-SP

9.189_SYNC_VOL_14V				
Test Site				
Tester				
Test Number				
Unit	mV		mV	
Max Limit	500		500	
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	288.000	287.000	-1.000
0	118	286.000	286.000	0.000
0	122	285.000	283.000	-2.000
3	301	287.000	282.000	-5.000
3	306	288.000	285.000	-3.000
3	307	287.000	285.000	-2.000
3	308	286.000	283.000	-3.000
3	309	285.000	282.000	-3.000
5	310	287.000	287.000	0.000
5	311	287.000	286.000	-1.000
5	312	284.000	283.000	-1.000
5	314	285.000	284.000	-1.000
5	316	286.000	284.000	-2.000
10	317	286.000	281.000	-5.000
10	318	285.000	281.000	-4.000
10	319	285.000	281.000	-4.000
10	321	287.000	283.000	-4.000
10	322	284.000	279.000	-5.000
15	323	285.000	285.000	0.000
15	325	282.000	283.000	1.000
15	326	285.000	286.000	1.000
15	328	284.000	284.000	0.000
15	333	285.000	286.000	1.000
30	334	285.000	281.000	-4.000
30	335	283.000	278.000	-5.000
30	337	285.000	280.000	-5.000
30	339	284.000	280.000	-4.000
30	341	285.000	280.000	-5.000
35	342	284.000	283.000	-1.000
35	345	284.000	282.000	-2.000
35	346	284.000	282.000	-2.000
35	347	284.000	282.000	-2.000
35	348	283.000	282.000	-1.000
50	349	284.000	272.000	-12.000
50	350	283.000	274.000	-9.000
50	351	284.000	274.000	-10.000
50	353	286.000	276.000	-10.000
50	354	285.000	275.000	-10.000
55	355	284.000	278.000	-6.000
55	357	285.000	279.000	-6.000
55	359	285.000	278.000	-7.000
55	362	284.000	278.000	-6.000
55	363	287.000	281.000	-6.000
100	1	286.000	273.000	-13.000
100	3	287.000	274.000	-13.000
100	4	286.000	274.000	-12.000
100	5	284.000	273.000	-11.000
100	6	286.000	274.000	-12.000
100	7	286.000	274.000	-12.000
100	8	285.000	273.000	-12.000
100	9	289.000	275.000	-14.000
100	10	287.000	274.000	-13.000
100	11	285.000	273.000	-12.000
100	12	287.000	275.000	-12.000
100	13	288.000	275.000	-13.000
100	14	288.000	275.000	-13.000
100	15	285.000	272.000	-13.000
100	16	289.000	275.000	-14.000
100	17	287.000	274.000	-13.000
100	18	285.000	272.000	-13.000
100	19	285.000	273.000	-12.000
100	20	285.000	273.000	-12.000
100	21	285.000	273.000	-12.000
100	22	288.000	275.000	-13.000
100	24	286.000	273.000	-13.000
105	25	287.000	279.000	-8.000
105	26	284.000	277.000	-7.000
105	27	285.000	278.000	-7.000
105	28	284.000	276.000	-8.000
105	29	287.000	279.000	-8.000
Max		289.000	287.000	1.000
Average		285.471	278.743	-6.729
Min		282.000	272.000	-14.000
Std Dev		1.501	4.583	4.857

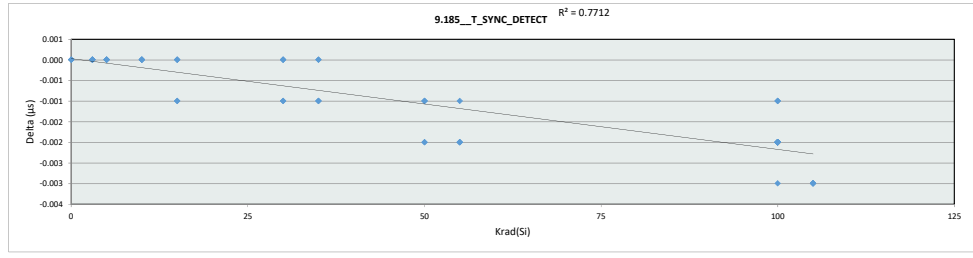


9.189_SYNC_VOL_14V											
Test Site											
Tester											
Test Number											
Max Limit	500										
Min Limit											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	283.000	282.000	283.000	279.000	283.000	278.000	282.000	272.000	278.000	272.000	276.000
Average	285.333	283.400	284.800	281.000	284.800	279.800	282.200	274.200	278.800	273.727	277.800
Max	287.000	285.000	287.000	283.000	286.000	281.000	283.000	276.000	281.000	275.000	279.000
UL	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000

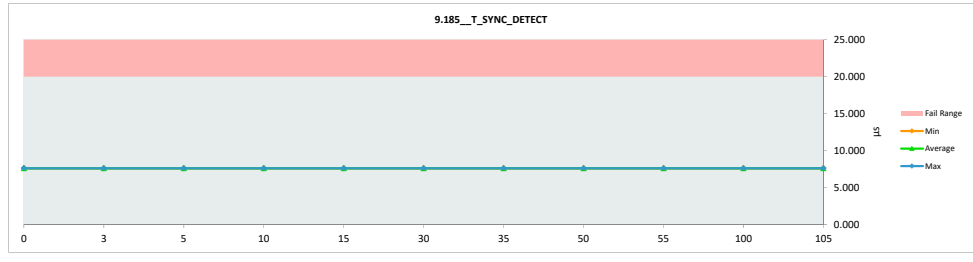


HDR TID Report
TPS7H5001-SP

9.185 T_SYNC_DETECT				
Test Site				
Tester				
Test Number				
Unit	µs	µs		
Max Limit	20	20		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	7.587	7.587	0.000
0	118	7.586	7.586	0.000
0	122	7.587	7.587	0.000
3	301	7.587	7.587	0.000
3	306	7.587	7.587	0.000
3	307	7.588	7.588	0.000
3	308	7.586	7.586	0.000
3	309	7.587	7.587	0.000
5	310	7.587	7.587	0.000
5	311	7.587	7.587	0.000
5	312	7.587	7.587	0.000
5	314	7.587	7.587	0.000
5	316	7.587	7.587	0.000
10	317	7.588	7.588	0.000
10	318	7.587	7.587	0.000
10	319	7.588	7.588	0.000
10	321	7.587	7.587	0.000
10	322	7.587	7.587	0.000
15	323	7.588	7.588	0.000
15	325	7.587	7.586	-0.001
15	326	7.587	7.587	0.000
15	328	7.588	7.587	-0.001
15	333	7.588	7.588	0.000
30	334	7.586	7.585	-0.001
30	335	7.586	7.586	0.000
30	337	7.586	7.586	0.000
30	339	7.586	7.585	-0.001
30	341	7.587	7.587	0.000
35	342	7.586	7.585	-0.001
35	345	7.587	7.587	0.000
35	346	7.587	7.587	0.000
35	347	7.588	7.587	-0.001
35	348	7.588	7.587	-0.001
50	349	7.587	7.586	-0.001
50	350	7.587	7.585	-0.002
50	351	7.586	7.585	-0.001
50	353	7.587	7.586	-0.001
50	354	7.588	7.586	-0.002
55	355	7.587	7.585	-0.002
55	357	7.588	7.586	-0.002
55	359	7.587	7.585	-0.002
55	362	7.587	7.586	-0.001
55	363	7.586	7.584	-0.002
100	1	7.587	7.585	-0.002
100	3	7.586	7.584	-0.002
100	4	7.586	7.584	-0.002
100	5	7.588	7.586	-0.002
100	6	7.587	7.585	-0.002
100	7	7.586	7.584	-0.002
100	8	7.588	7.586	-0.002
100	9	7.586	7.585	-0.001
100	10	7.587	7.585	-0.002
100	11	7.587	7.585	-0.002
100	12	7.588	7.586	-0.002
100	13	7.587	7.586	-0.001
100	14	7.588	7.586	-0.002
100	15	7.588	7.586	-0.002
100	16	7.588	7.586	-0.002
100	17	7.587	7.585	-0.002
100	18	7.588	7.585	-0.003
100	19	7.586	7.584	-0.002
100	20	7.587	7.586	-0.001
100	21	7.586	7.584	-0.002
100	22	7.587	7.585	-0.002
100	24	7.587	7.585	-0.002
105	25	7.587	7.584	-0.003
105	26	7.586	7.583	-0.003
105	27	7.587	7.584	-0.003
105	28	7.587	7.584	-0.003
105	29	7.587	7.584	-0.003
Max		7.588	7.588	0.000
Average		7.587	7.586	-0.001
Min		7.586	7.583	-0.003
Std Dev		0.001	0.001	0.001

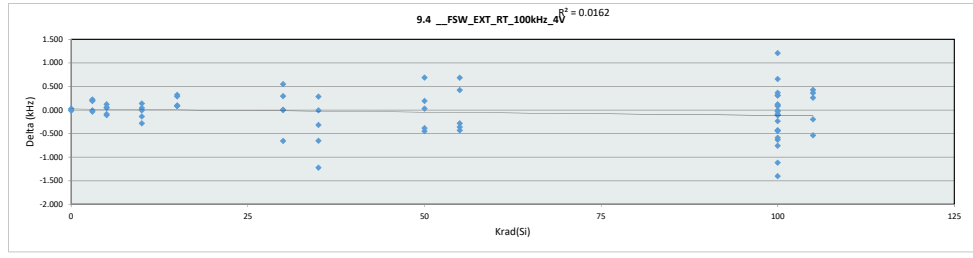


9.185 T_SYNC_DETECT											
Test Site											
Tester											
Test Number											
Max Limit	20	µs									
Min Limit		µs									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.586	7.586	7.587	7.587	7.586	7.585	7.585	7.585	7.584	7.584	7.583
Average	7.587	7.587	7.587	7.587	7.587	7.586	7.587	7.586	7.585	7.585	7.584
Max	7.587	7.588	7.587	7.588	7.588	7.587	7.587	7.586	7.586	7.586	7.584
UL	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000

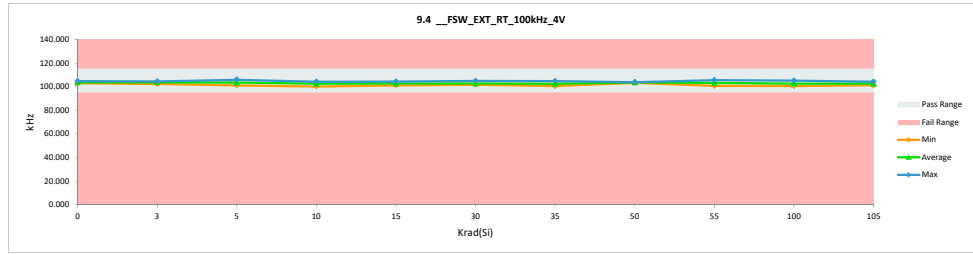


HDR TID Report
TPS7H5001-SP

9.4 FSW_EXT_RT_100kHz_4V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	115	115		
Min Limit	95	95		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	104.618	104.645	0.027
0	118	103.354	103.337	-0.017
0	122	102.782	102.781	-0.001
3	301	104.207	104.428	0.221
3	306	102.076	102.266	0.190
3	307	104.314	104.276	-0.038
3	308	104.159	104.150	-0.009
3	309	103.116	103.312	0.196
5	310	103.143	103.030	-0.113
5	311	105.936	105.847	-0.089
5	312	103.538	103.578	0.040
5	314	103.983	104.038	0.055
5	316	100.956	101.075	0.119
10	317	101.870	101.584	-0.286
10	318	103.848	103.711	-0.137
10	319	103.942	104.079	0.137
10	321	100.177	100.169	-0.008
10	322	103.086	103.126	0.040
15	323	100.930	101.025	0.095
15	325	103.991	104.276	0.285
15	326	101.776	101.857	0.081
15	328	102.723	102.810	0.087
15	333	102.811	103.130	0.319
30	334	102.572	101.913	-0.659
30	335	101.574	101.578	0.004
30	337	104.282	104.831	0.549
30	339	102.619	102.617	-0.002
30	341	101.963	102.254	0.291
35	342	103.885	102.660	-1.225
35	345	100.931	100.921	-0.010
35	346	104.366	104.650	0.284
35	347	100.804	100.485	-0.319
35	348	104.181	103.525	-0.656
50	349	103.196	103.386	0.190
50	350	103.938	103.486	-0.452
50	351	102.603	103.290	0.687
50	353	104.159	103.774	-0.385
50	354	103.815	103.843	0.028
55	355	105.128	105.550	0.422
55	357	100.976	100.614	-0.362
55	359	101.552	102.236	0.684
55	362	104.699	104.266	-0.433
55	363	103.419	103.133	-0.286
100	1	101.908	101.670	-0.238
100	3	103.436	102.315	-1.121
100	4	100.995	101.301	0.306
100	5	101.000	101.364	0.364
100	6	101.549	100.913	-0.636
100	7	103.760	104.417	0.657
100	8	103.944	102.540	-1.404
100	9	104.788	105.096	0.308
100	10	102.494	102.049	-0.445
100	11	103.297	102.857	-0.440
100	12	103.542	103.113	-0.429
100	13	100.393	100.476	0.083
100	14	102.404	101.814	-0.590
100	15	103.745	103.834	0.089
100	16	103.220	103.341	0.121
100	17	102.918	102.885	-0.033
100	18	102.514	102.413	-0.101
100	19	101.778	101.017	-0.761
100	20	103.088	102.997	-0.091
100	21	104.003	103.889	-0.114
100	22	101.077	102.284	1.207
100	24	104.215	104.204	-0.011
105	25	103.722	104.146	0.424
105	26	101.416	101.215	-0.201
105	27	102.363	102.724	0.361
105	28	101.696	101.156	-0.540
105	29	103.244	103.501	0.257
Max		105.936	105.847	1.207
Average		102.922	102.872	-0.049
Min		100.177	100.169	-1.404
Std Dev		1.276	1.318	0.440

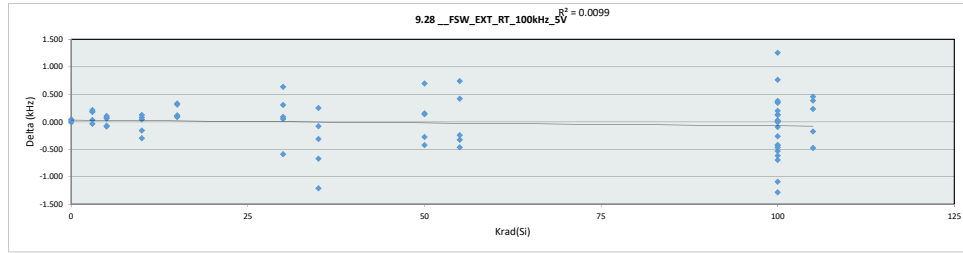


9.4 FSW_EXT_RT_100kHz											
Test Site											
Tester											
Test Number											
Max Limit	115	kHz									
Min Limit	95	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000
Min	102.781	102.266	101.075	100.169	101.025	101.578	100.485	103.290	100.614	100.476	101.156
Average	103.588	103.686	103.514	102.534	102.620	102.639	102.448	103.556	103.160	102.581	102.548
Max	104.645	104.428	105.847	104.079	104.276	104.831	104.650	103.843	105.550	105.096	104.146
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000

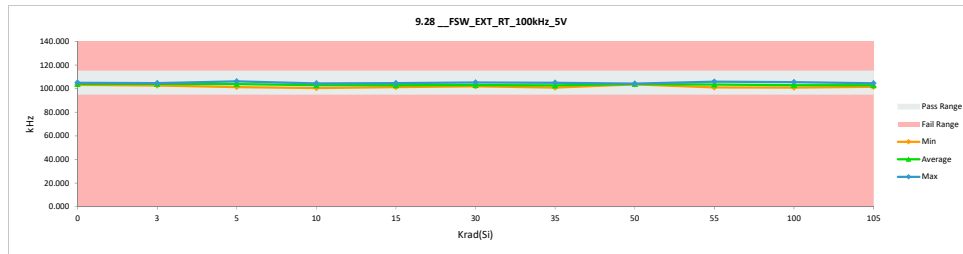


HDR TID Report
TPS7H5001-SP

		9.28 FSW_EXT_RT_100kHz_5V		
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	115	115		
Min Limit	95	95		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	105.007	105.021	0.014
0	118	103.735	103.721	-0.014
0	122	103.163	103.207	0.044
3	301	104.553	104.740	0.187
3	306	102.426	102.638	0.212
3	307	104.679	104.640	-0.039
3	308	104.521	104.551	0.030
3	309	103.509	103.685	0.176
5	310	103.522	103.448	-0.074
5	311	106.281	106.194	-0.087
5	312	103.911	103.969	0.058
5	314	104.359	104.438	0.079
5	316	101.280	101.385	0.105
10	317	102.262	101.962	-0.300
10	318	104.203	104.043	-0.160
10	319	104.389	104.511	0.122
10	321	100.539	100.578	0.039
10	322	103.464	103.540	0.076
15	323	101.253	101.369	0.116
15	325	104.331	104.663	0.332
15	326	102.085	102.186	0.101
15	328	103.067	103.150	0.083
15	333	103.218	103.528	0.310
30	334	102.902	102.312	-0.590
30	335	101.901	101.987	0.086
30	337	104.610	105.245	0.635
30	339	102.967	103.016	0.049
30	341	102.327	102.631	0.304
35	342	104.246	103.034	-1.212
35	345	101.396	101.315	-0.081
35	346	104.774	105.024	0.250
35	347	101.202	100.887	-0.315
35	348	104.586	103.914	-0.672
50	349	103.597	103.749	0.152
50	350	104.284	103.859	-0.425
50	351	102.912	103.607	0.695
50	353	104.469	104.192	-0.277
50	354	104.144	104.279	0.135
55	355	105.502	105.920	0.418
55	357	101.360	101.031	-0.329
55	359	101.809	102.548	0.739
55	362	105.090	104.625	-0.465
55	363	103.766	103.522	-0.244
100	1	102.312	102.048	-0.264
100	3	103.804	102.712	-1.092
100	4	101.340	101.697	0.357
100	5	101.381	101.758	0.377
100	6	101.932	101.313	-0.619
100	7	104.103	104.864	0.761
100	8	104.285	103.002	-1.283
100	9	105.176	105.520	0.344
100	10	102.909	102.479	-0.430
100	11	103.702	103.234	-0.468
100	12	103.927	103.504	-0.423
100	13	100.768	100.889	0.121
100	14	102.771	102.238	-0.533
100	15	104.148	104.275	0.127
100	16	103.542	103.740	0.198
100	17	103.261	103.279	0.018
100	18	102.884	102.787	-0.097
100	19	102.127	101.431	-0.696
100	20	103.457	103.454	-0.003
100	21	104.355	104.352	-0.003
100	22	101.406	102.659	1.253
100	24	104.540	104.565	0.025
105	25	104.098	104.555	0.457
105	26	101.743	101.566	-0.177
105	27	102.731	103.116	0.385
105	28	102.039	101.561	-0.478
105	29	103.631	103.864	0.233
	Max	106.281	106.194	1.253
	Average	103.285	103.262	-0.024
	Min	100.539	100.578	-1.283
	Std Dev	1.279	1.318	0.438

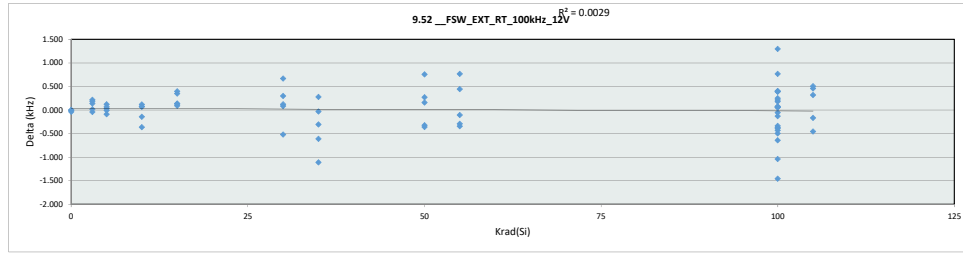


		9.28 FSW_EXT_RT_100kHz									
Test Site											
Tester											
Test Number											
Unit	kHz	kHz									
Max Limit	115	115									
Min Limit	95	95									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000
Min	103.207	102.638	101.385	100.578	101.369	101.987	100.887	103.607	101.031	100.889	101.561
Average	103.983	104.051	103.887	102.927	102.979	103.038	102.835	103.937	103.529	102.991	102.932
Max	105.021	104.740	106.194	104.511	104.663	105.245	105.024	104.279	105.920	105.520	104.555
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000

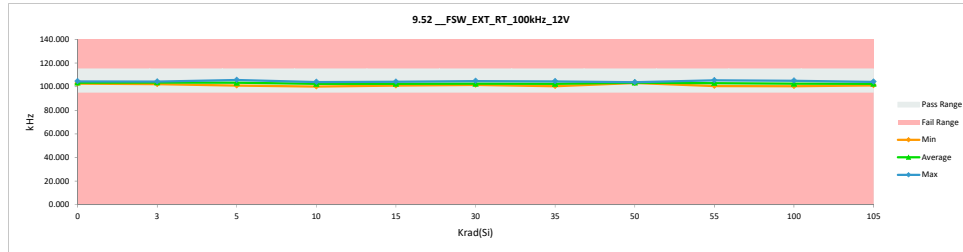


HDR TID Report
TPS7H5001-SP

		9.52	FSW_EXT	RT	100kHz	12V
Test Site						
Tester						
Test Number						
Unit		kHz		kHz		
Max Limit		115		115		
Min Limit		95		95		
Krad(Si)	Serial #	PRE_HDR		POST_HDR		Delta
0	117	104.480		104.480		0.000
0	118	103.214		103.206		-0.008
0	122	102.682		102.643		-0.039
3	301	104.092		104.306		0.214
3	306	101.929		102.113		0.184
3	307	104.131		104.089		-0.042
3	308	104.014		104.028		0.014
3	309	102.974		103.112		0.138
5	310	102.960		102.949		-0.011
5	311	105.748		105.657		-0.091
5	312	103.378		103.408		0.030
5	314	103.834		103.898		0.064
5	316	100.827		100.947		0.120
10	317	101.777		101.410		-0.367
10	318	103.715		103.568		-0.147
10	319	103.852		103.968		0.116
10	321	100.028		100.096		0.068
10	322	102.952		103.023		0.071
15	323	100.796		100.923		0.127
15	325	103.813		104.159		0.346
15	326	101.603		101.691		0.088
15	328	102.475		102.613		0.138
15	333	102.630		103.023		0.393
30	334	102.372		101.851		-0.521
30	335	101.426		101.508		0.082
30	337	104.104		104.771		0.667
30	339	102.436		102.554		0.118
30	341	101.844		102.139		0.295
35	342	103.749		102.636		-1.113
35	345	100.879		100.846		-0.033
35	346	104.233		104.508		0.275
35	347	100.739		100.430		-0.309
35	348	104.063		103.449		-0.614
50	349	103.060		103.328		0.268
50	350	103.779		103.416		-0.363
50	351	102.342		103.095		0.753
50	353	103.970		103.646		-0.324
50	354	103.602		103.760		0.158
55	355	104.934		105.376		0.442
55	357	100.841		100.546		-0.295
55	359	101.273		102.039		0.766
55	362	104.491		104.146		-0.345
55	363	103.148		103.039		-0.109
100	1	101.739		101.608		-0.131
100	3	103.275		102.232		-1.043
100	4	100.823		101.225		0.402
100	5	100.929		101.312		0.383
100	6	101.381		100.881		-0.500
100	7	103.613		104.377		0.764
100	8	103.923		102.464		-1.459
100	9	104.625		105.018		0.393
100	10	102.383		102.043		-0.340
100	11	103.177		102.788		-0.389
100	12	103.378		103.005		-0.373
100	13	100.296		100.472		0.176
100	14	102.245		101.812		-0.433
100	15	103.632		103.844		0.212
100	16	102.988		103.235		0.247
100	17	102.724		102.777		0.053
100	18	102.365		102.307		-0.058
100	19	101.585		100.940		-0.645
100	20	102.926		102.991		0.065
100	21	103.840		103.899		0.059
100	22	100.907		102.200		1.293
100	24	103.988		104.065		0.077
105	25	103.580		104.082		0.502
105	26	101.270		101.101		-0.169
105	27	102.251		102.706		0.455
105	28	101.581		101.123		-0.458
105	29	103.112		103.429		0.317
Max		105.748		105.657		1.293
Average		102.767		102.776		0.009
Min		100.028		100.096		-1.459
Std Dev		1.267		1.302		0.439

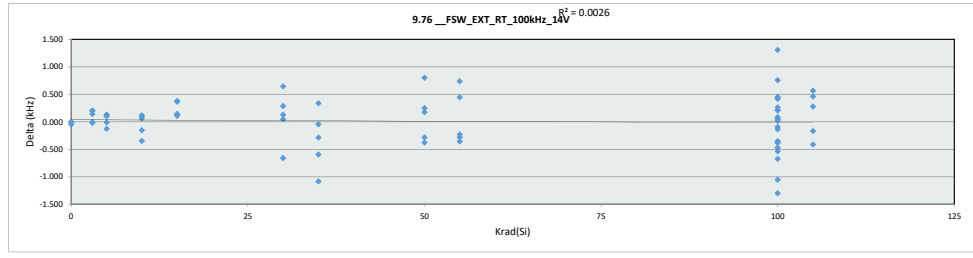


		9.52	FSW_EXT	RT	100kHz						
Test Site											
Tester											
Test Number											
Max Limit		115		kHz							
Min Limit		95		kHz							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000
Min	102.643	102.113	100.947	100.096	100.923	101.508	100.430	103.095	100.546	100.472	101.101
Average	103.443	103.530	103.372	102.413	102.482	102.565	102.374	103.449	103.029	102.523	102.488
Max	104.480	104.306	105.657	103.968	104.159	104.771	104.508	103.760	105.376	105.018	104.082
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000

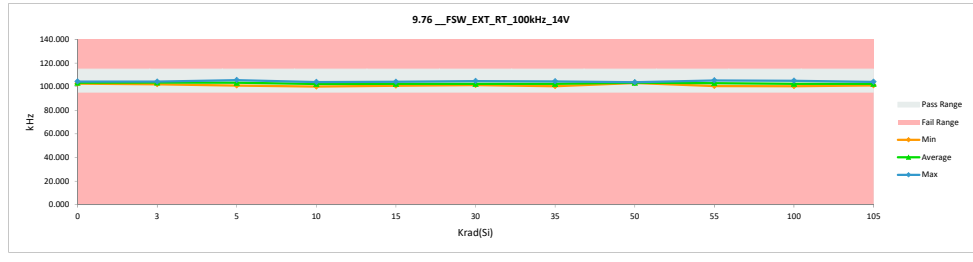


HDR TID Report
TPS7H5001-SP

9.76 FSW_EXT_RT_100kHz_14V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	115	115		
Min Limit	95	95		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	104.473	104.424	-0.049
0	118	103.182	103.178	-0.004
0	122	102.667	102.651	-0.016
3	301	104.117	104.317	0.200
3	306	101.862	102.061	0.199
3	307	104.146	104.126	-0.020
3	308	104.028	104.022	-0.006
3	309	102.963	103.101	0.138
5	310	102.958	102.946	-0.012
5	311	105.746	105.616	-0.130
5	312	103.323	103.421	0.098
5	314	103.766	103.888	0.122
5	316	100.833	100.961	0.128
10	317	101.773	101.423	-0.350
10	318	103.707	103.553	-0.154
10	319	103.858	103.973	0.115
10	321	100.019	100.077	0.058
10	322	102.920	103.021	0.101
15	323	100.758	100.896	0.138
15	325	103.819	104.183	0.364
15	326	101.586	101.694	0.108
15	328	102.485	102.622	0.137
15	333	102.644	103.022	0.378
30	334	102.473	101.811	-0.662
30	335	101.406	101.453	0.047
30	337	104.118	104.762	0.644
30	339	102.425	102.552	0.127
30	341	101.851	102.136	0.285
35	342	103.734	102.649	-1.085
35	345	100.864	100.816	-0.048
35	346	104.226	104.559	0.333
35	347	100.707	100.417	-0.290
35	348	104.025	103.429	-0.596
50	349	103.063	103.311	0.248
50	350	103.792	103.414	-0.378
50	351	102.317	103.118	0.801
50	353	103.973	103.686	-0.287
50	354	103.608	103.783	0.175
55	355	104.931	105.376	0.445
55	357	100.848	100.564	-0.284
55	359	101.280	102.015	0.735
55	362	104.474	104.115	-0.359
55	363	103.236	103.006	-0.230
100	1	101.748	101.610	-0.138
100	3	103.270	102.216	-1.054
100	4	100.822	101.250	0.428
100	5	100.904	101.320	0.416
100	6	101.381	100.841	-0.540
100	7	103.599	104.354	0.755
100	8	103.755	102.452	-1.303
100	9	104.590	105.041	0.451
100	10	102.370	102.015	-0.355
100	11	103.162	102.773	-0.389
100	12	103.367	103.009	-0.358
100	13	100.264	100.474	0.210
100	14	102.243	101.770	-0.473
100	15	103.600	103.808	0.208
100	16	102.996	103.256	0.260
100	17	102.728	102.771	0.043
100	18	102.373	102.278	-0.095
100	19	101.624	100.948	-0.676
100	20	102.938	103.022	0.084
100	21	103.811	103.827	0.016
100	22	100.890	102.195	1.305
100	24	104.003	104.039	0.036
105	25	103.558	104.122	0.564
105	26	101.262	101.092	-0.170
105	27	102.215	102.673	0.458
105	28	101.546	101.132	-0.414
105	29	103.141	103.416	0.275
	Max	105.746	105.616	1.305
	Average	102.759	102.769	0.010
	Min	100.019	100.077	-1.303
	Std Dev	1.267	1.304	0.439

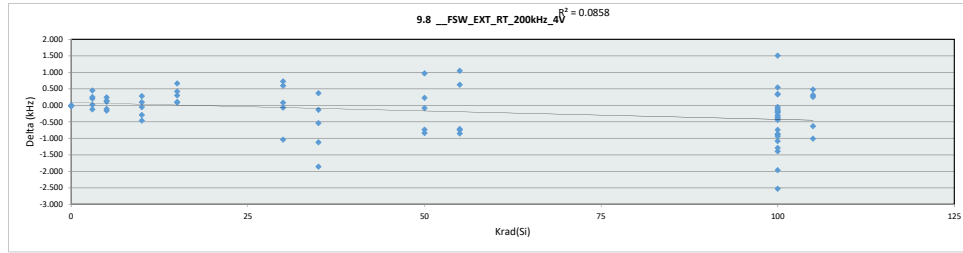


9.76 FSW_EXT_RT_100kHz											
Test Site											
Tester											
Test Number											
Unit	kHz	kHz									
Max Limit	115	115									
Min Limit	95	95									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000
Min	102.651	102.061	100.961	100.077	100.896	101.453	100.417	103.118	100.564	100.474	101.092
Average	103.418	103.525	103.366	102.409	102.483	102.543	102.374	103.462	103.015	102.512	102.487
Max	104.424	104.317	105.616	103.973	104.183	104.762	104.559	103.783	105.376	105.041	104.122
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000

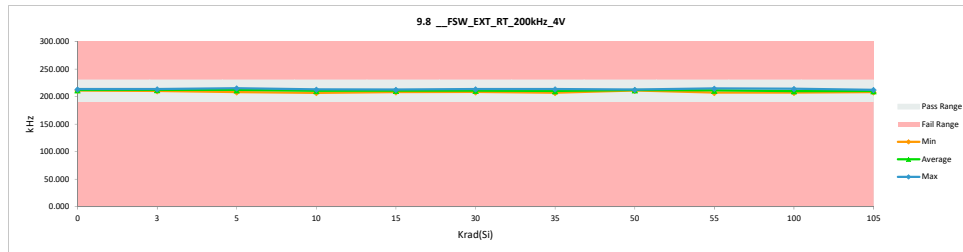


HDR TID Report
TPS7H5001-SP

9.8 FSW_EXT_RT_200kHz_4V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	230	230		
Min Limit	190	190		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	213.413	213.429	0.016
0	118	211.388	211.378	-0.010
0	122	210.882	210.868	-0.014
3	301	212.862	213.321	0.459
3	306	209.896	210.158	0.262
3	307	213.604	213.493	-0.111
3	308	212.332	212.355	0.023
3	309	211.646	211.858	0.212
5	310	211.302	211.206	-0.096
5	311	215.411	215.258	-0.153
5	312	212.521	212.670	0.149
5	314	212.633	212.752	0.119
5	316	207.719	207.970	0.251
10	317	209.693	209.241	-0.452
10	318	212.748	212.465	-0.283
10	319	212.656	212.949	0.293
10	321	206.841	206.798	-0.043
10	322	210.815	210.919	0.104
15	323	207.860	207.969	0.109
15	325	212.325	212.754	0.429
15	326	209.268	209.367	0.099
15	328	211.370	211.679	0.309
15	333	210.774	211.448	0.674
30	334	211.006	209.976	-1.030
30	335	208.267	208.207	-0.060
30	337	212.896	213.631	0.735
30	339	210.888	210.980	0.092
30	341	209.815	210.423	0.608
35	342	212.641	210.793	-1.848
35	345	207.808	207.683	-0.125
35	346	213.103	213.480	0.377
35	347	207.389	206.861	-0.528
35	348	213.284	212.174	-1.110
50	349	210.702	210.940	0.238
50	350	212.748	211.916	-0.832
50	351	210.604	211.585	0.981
50	353	213.515	212.789	-0.726
50	354	212.017	211.944	-0.073
55	355	214.063	214.696	0.633
55	357	207.777	207.041	-0.736
55	359	208.715	209.772	1.057
55	362	213.532	212.689	-0.843
55	363	212.050	211.341	-0.709
100	1	209.510	208.653	-0.857
100	3	212.026	210.073	-1.953
100	4	207.962	207.760	-0.202
100	5	207.639	207.983	0.344
100	6	208.449	207.167	-1.282
100	7	212.020	212.573	0.553
100	8	212.598	210.078	-2.520
100	9	213.789	214.144	0.355
100	10	209.677	208.800	-0.877
100	11	211.216	210.288	-0.928
100	12	211.783	211.052	-0.731
100	13	207.524	207.378	-0.146
100	14	209.989	208.917	-1.072
100	15	212.019	211.731	-0.288
100	16	211.334	211.237	-0.097
100	17	210.930	210.500	-0.430
100	18	209.654	209.467	-0.187
100	19	209.283	207.901	-1.382
100	20	211.069	210.722	-0.347
100	21	212.659	212.285	-0.374
100	22	207.822	209.341	1.519
100	24	213.217	213.176	-0.041
105	25	212.047	212.313	0.266
105	26	208.864	208.242	-0.622
105	27	210.169	210.657	0.488
105	28	208.960	207.961	-0.999
105	29	211.401	211.720	0.319
	Max	215.411	215.258	1.519
	Average	210.948	210.762	-0.186
	Min	206.841	206.798	-2.520
	Std Dev	2.004	2.090	0.710

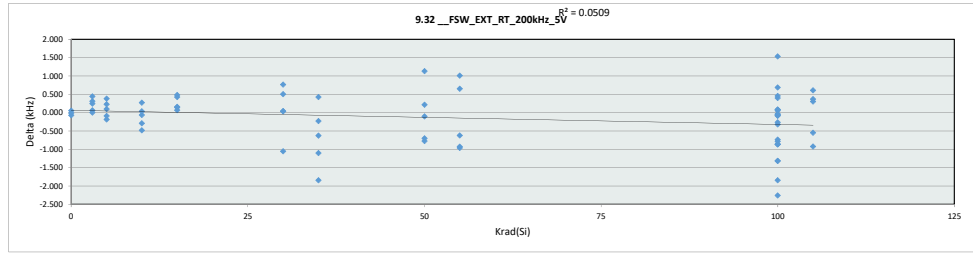


9.8 FSW_EXT_RT_200kHz											
Test Site											
Tester											
Test Number											
Max Limit	230	kHz									
Min Limit	190	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min	210.868	210.158	207.970	206.798	207.969	208.207	206.861	210.940	207.041	207.167	207.961
Average	211.892	212.237	211.971	210.474	210.643	210.643	210.198	211.835	211.108	210.056	210.179
Max	213.429	213.493	215.258	212.949	212.754	213.631	213.480	212.789	214.696	214.144	212.313
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000

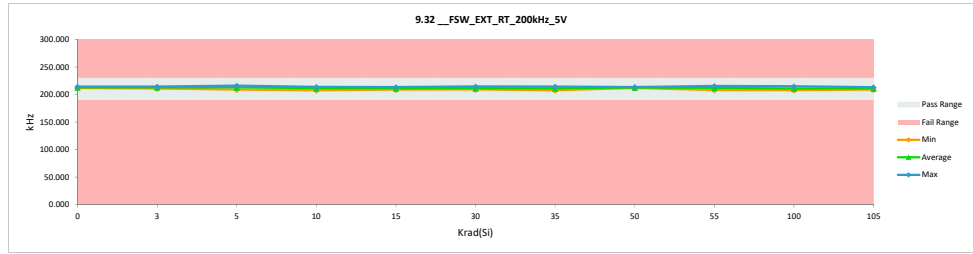


HDR TID Report
TPS7H5001-SP

9.32 FSW_EXT_RT_200kHz_5V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	230	230		
Min Limit	190	190		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	214.448	214.446	-0.002
0	118	212.414	212.350	-0.064
0	122	211.870	211.933	0.063
3	301	213.844	214.299	0.455
3	306	210.774	211.031	0.257
3	307	214.575	214.585	0.010
3	308	213.330	213.401	0.071
3	309	212.551	212.873	0.322
5	310	212.320	212.242	-0.078
5	311	216.397	216.221	-0.176
5	312	213.585	213.691	0.106
5	314	213.669	214.062	0.393
5	316	208.632	208.869	0.237
10	317	210.682	210.210	-0.472
10	318	213.740	213.459	-0.281
10	319	213.753	214.037	0.284
10	321	207.900	207.848	-0.052
10	322	211.888	211.936	0.048
15	323	208.775	208.945	0.170
15	325	213.301	213.737	0.436
15	326	210.088	210.238	0.150
15	328	212.396	212.475	0.079
15	333	211.830	212.324	0.494
30	334	211.979	210.934	-1.045
30	335	209.222	209.272	0.050
30	337	213.935	214.710	0.775
30	339	212.129	212.176	0.047
30	341	210.806	211.318	0.512
35	342	213.620	211.783	-1.837
35	345	208.822	208.603	-0.219
35	346	214.075	214.509	0.434
35	347	208.378	207.761	-0.617
35	348	214.263	213.170	-1.093
50	349	211.774	212.001	0.227
50	350	213.770	213.006	-0.764
50	351	211.548	212.691	1.143
50	353	214.468	213.775	-0.693
50	354	213.045	212.953	-0.092
55	355	215.037	215.700	0.663
55	357	209.158	208.203	-0.955
55	359	209.581	210.602	1.021
55	362	214.644	213.728	-0.916
55	363	212.997	212.384	-0.613
100	1	210.412	209.638	-0.774
100	3	212.998	211.163	-1.835
100	4	208.733	208.827	0.094
100	5	208.593	209.058	0.465
100	6	209.546	208.239	-1.307
100	7	212.948	213.644	0.696
100	8	213.370	211.119	-2.251
100	9	214.800	215.208	0.408
100	10	210.764	209.908	-0.856
100	11	212.260	211.407	-0.853
100	12	212.804	212.079	-0.725
100	13	208.564	208.496	-0.068
100	14	210.916	210.068	-0.848
100	15	212.974	212.937	-0.037
100	16	212.291	212.355	0.064
100	17	211.871	211.561	-0.310
100	18	210.665	210.756	0.091
100	19	210.276	208.970	-1.306
100	20	211.956	211.929	-0.027
100	21	213.696	213.440	-0.256
100	22	208.791	210.334	1.543
100	24	214.126	214.052	-0.074
105	25	213.033	213.413	0.380
105	26	209.804	209.268	-0.536
105	27	211.190	211.810	0.620
105	28	209.912	208.997	-0.915
105	29	212.477	212.790	0.313
	Max	216.397	216.221	1.543
	Average	211.940	211.799	-0.140
	Min	207.900	207.761	-2.251
	Std Dev	2.009	2.093	0.699

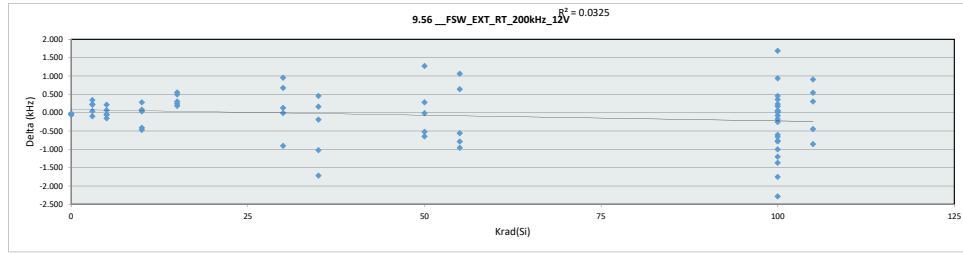


9.32 FSW_EXT_RT_200kHz											
Test Site											
Tester											
Test Number											
Max Limit	230	kHz									
Min Limit	190	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min	211.933	211.031	208.869	207.848	208.945	209.272	207.761	212.001	208.203	208.239	208.997
Average	212.910	213.238	213.017	211.498	211.544	211.682	211.165	212.885	212.123	211.145	211.256
Max	214.446	214.585	216.221	214.037	213.737	214.710	214.509	213.775	215.700	215.208	213.413
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000

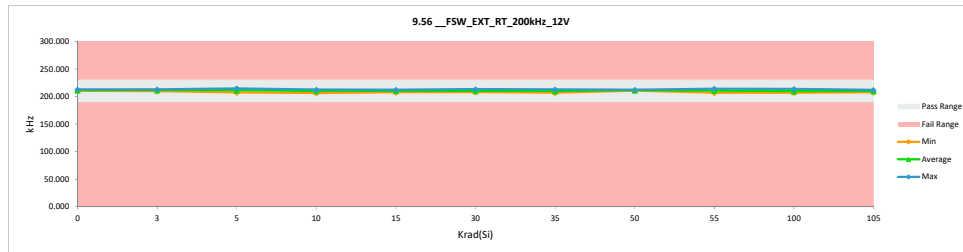


HDR TID Report
TPS7H5001-SP

9.56 FSW_EXT_RT_200kHz_12V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	230	230		
Min Limit	190	190		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	213.154	213.099	-0.055
0	118	211.098	211.080	-0.018
0	122	210.586	210.567	-0.019
3	301	212.639	212.990	0.351
3	306	209.552	209.780	0.228
3	307	213.299	213.211	-0.088
3	308	212.105	212.156	0.051
3	309	211.363	211.600	0.237
5	310	211.001	210.959	-0.042
5	311	215.075	214.929	-0.146
5	312	212.263	212.342	0.079
5	314	212.588	212.537	-0.051
5	316	207.423	207.647	0.224
10	317	209.455	208.997	-0.458
10	318	212.541	212.140	-0.401
10	319	212.462	212.753	0.291
10	321	206.528	206.569	0.041
10	322	210.635	210.724	0.089
15	323	207.562	207.812	0.250
15	325	211.932	212.492	0.560
15	326	208.801	208.996	0.195
15	328	211.091	211.406	0.315
15	333	210.638	211.143	0.505
30	334	210.744	208.847	-0.897
30	335	207.993	208.133	0.140
30	337	212.638	213.601	0.963
30	339	210.983	210.984	0.001
30	341	209.422	210.155	0.683
35	342	212.378	210.668	-1.710
35	345	207.545	207.720	0.175
35	346	212.795	213.262	0.467
35	347	207.128	206.947	-0.181
35	348	213.039	212.026	-1.013
50	349	210.514	210.805	0.291
50	350	212.499	211.860	-0.639
50	351	210.217	211.499	1.282
50	353	213.079	212.565	-0.514
50	354	211.659	211.653	-0.006
55	355	213.756	214.406	0.650
55	357	207.732	206.954	-0.778
55	359	208.310	209.384	1.074
55	362	213.380	212.432	-0.948
55	363	211.695	211.142	-0.553
100	1	209.300	208.526	-0.774
100	3	211.684	209.943	-1.741
100	4	207.525	207.707	0.182
100	5	207.423	207.888	0.465
100	6	208.488	207.129	-1.359
100	7	211.613	212.561	0.948
100	8	212.305	210.030	-2.275
100	9	213.541	213.912	0.371
100	10	209.526	208.874	-0.652
100	11	211.100	210.108	-0.992
100	12	211.460	210.866	-0.594
100	13	207.319	207.379	0.060
100	14	209.647	208.887	-0.760
100	15	211.647	211.706	0.059
100	16	211.012	211.257	0.245
100	17	210.578	210.331	-0.247
100	18	209.443	209.471	0.028
100	19	208.979	207.788	-1.191
100	20	210.828	210.759	-0.069
100	21	212.468	212.306	-0.162
100	22	207.538	209.235	1.697
100	24	212.786	212.807	0.021
105	25	211.699	212.251	0.552
105	26	208.577	208.140	-0.437
105	27	209.929	210.844	0.915
105	28	208.704	207.855	-0.849
105	29	211.319	211.634	0.315
Max	215.075	214.929	1.697	
Average	210.683	210.602	-0.080	
Min	206.528	206.569	-2.275	
Std Dev	1.998	2.041	0.704	

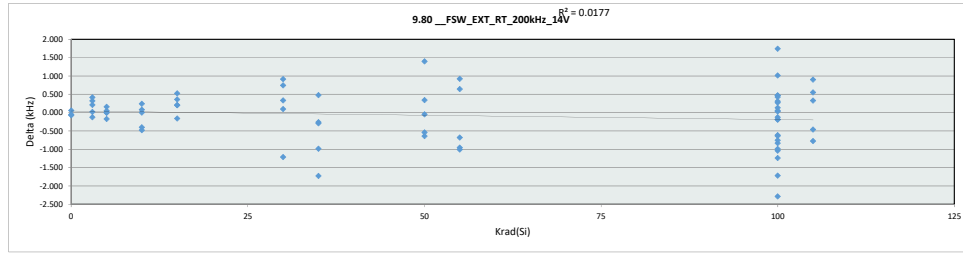


9.56 FSW_EXT_RT_200kHz											
Test Site											
Tester											
Test Number											
Max Limit	230	kHz									
Min Limit	190	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min	210.567	209.780	207.647	206.569	207.812	208.133	206.947	210.805	206.954	207.129	207.855
Average	211.582	211.947	211.683	210.237	210.370	210.544	210.125	211.676	210.864	209.976	210.145
Max	213.099	213.211	214.929	212.753	212.492	213.601	213.262	212.565	214.406	213.912	212.251
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000

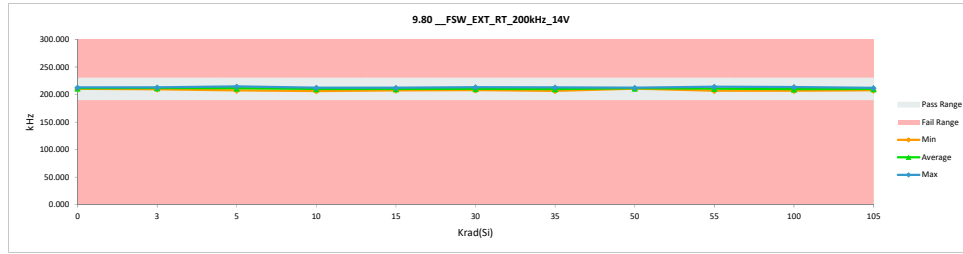


HDR TID Report
TPS7H5001-SP

9.80 FSW_EXT_RT_200kHz_14V				
Test Site				
Tester				
Test Number				
Unit	kHz		kHz	
Max Limit	230		230	
Min Limit	190		190	
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	213.163	213.102	-0.061
0	118	211.052	211.120	0.068
0	122	210.593	210.553	-0.040
3	301	212.641	213.068	0.427
3	306	209.534	209.756	0.222
3	307	213.319	213.203	-0.116
3	308	212.119	212.148	0.029
3	309	211.039	211.369	0.330
5	310	210.935	210.946	0.011
5	311	215.058	214.896	-0.162
5	312	212.248	212.310	0.062
5	314	212.505	212.511	0.006
5	316	207.453	207.624	0.171
10	317	209.456	208.986	-0.470
10	318	212.527	212.137	-0.390
10	319	212.472	212.722	0.250
10	321	206.535	206.546	0.011
10	322	210.608	210.698	0.090
15	323	207.564	207.778	0.214
15	325	211.978	212.513	0.535
15	326	208.796	209.013	0.217
15	328	211.066	210.916	-0.150
15	333	210.650	211.022	0.372
30	334	210.750	209.549	-1.201
30	335	208.000	208.110	0.110
30	337	212.637	213.560	0.923
30	339	210.527	210.869	0.342
30	341	209.476	210.232	0.756
35	342	212.362	210.645	-1.717
35	345	207.518	207.267	-0.251
35	346	212.760	213.247	0.487
35	347	207.119	206.838	-0.281
35	348	212.962	211.988	-0.974
50	349	210.472	210.825	0.353
50	350	212.491	211.862	-0.629
50	351	210.213	211.620	1.407
50	353	213.096	212.565	-0.531
50	354	211.652	211.616	-0.036
55	355	213.701	214.355	0.654
55	357	207.940	206.994	-0.946
55	359	208.310	209.243	0.933
55	362	213.404	212.408	-0.996
55	363	211.715	211.047	-0.668
100	1	209.328	208.505	-0.823
100	3	211.676	209.967	-1.709
100	4	207.523	207.668	0.145
100	5	207.415	207.896	0.481
100	6	208.122	207.093	-1.029
100	7	211.584	212.609	1.025
100	8	212.269	209.994	-2.275
100	9	213.464	213.900	0.436
100	10	209.435	208.832	-0.603
100	11	211.054	210.077	-0.977
100	12	211.462	210.837	-0.625
100	13	207.256	207.321	0.065
100	14	209.612	208.867	-0.745
100	15	211.241	211.721	0.480
100	16	210.999	211.281	0.282
100	17	210.542	210.363	-0.179
100	18	209.424	209.748	0.324
100	19	208.957	207.731	-1.226
100	20	210.881	210.768	-0.113
100	21	212.424	212.249	-0.175
100	22	207.494	209.246	1.752
100	24	212.800	212.842	0.042
105	25	211.702	212.262	0.560
105	26	208.618	208.166	-0.452
105	27	209.930	210.841	0.911
105	28	208.667	207.901	-0.766
105	29	211.306	211.647	0.341
Max		215.058	214.896	1.752
Average		210.651	210.573	-0.078
Min		206.535	206.546	-2.275
Std Dev		1.994	2.053	0.716

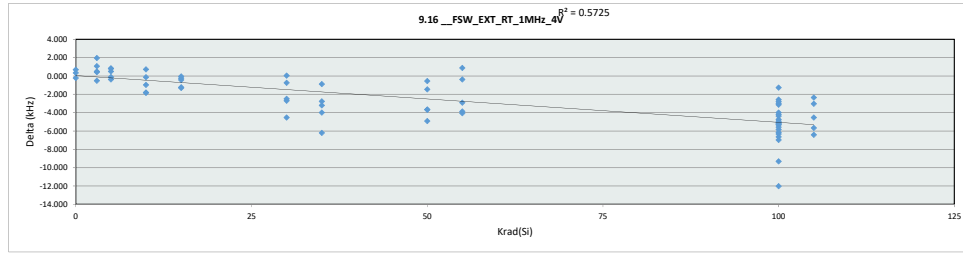


9.80 FSW_EXT_RT_200kHz											
Test Site											
Tester											
Test Number											
Max Limit	230		kHz								
Min Limit	190		kHz								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min	210.553	209.756	207.624	206.546	207.778	208.110	206.838	210.825	206.994	207.093	207.901
Average	211.592	211.909	211.657	210.218	210.248	210.464	209.997	211.698	210.809	209.978	210.163
Max	213.102	213.203	214.896	212.722	212.513	213.560	213.247	212.565	214.355	213.900	212.262
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000

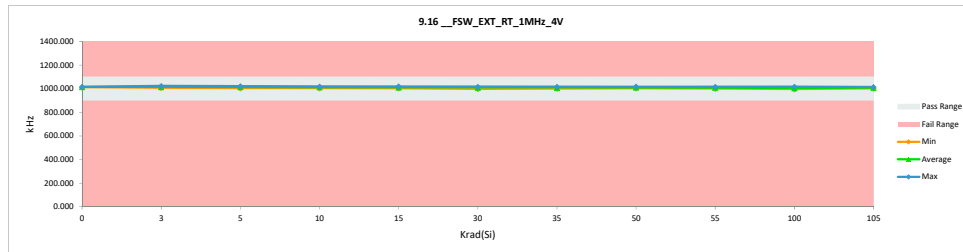


HDR TID Report
TPS7H5001-SP

9.16 FSW_EXT_RT_1MHz_4V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	1100	1100		
Min Limit	900	900		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1016.245	1016.622	0.377
0	118	1013.556	1013.383	-0.173
0	122	1015.491	1016.211	0.720
3	301	1014.555	1016.553	1.998
3	306	1012.874	1013.993	1.119
3	307	1024.504	1024.034	-0.470
3	308	1007.976	1008.444	0.468
3	309	1017.988	1018.531	0.543
5	310	1014.488	1014.415	-0.073
5	311	1022.015	1021.671	-0.344
5	312	1022.342	1023.195	0.853
5	314	1015.498	1016.366	0.868
5	316	1005.069	1005.599	0.530
10	317	1016.275	1014.486	-1.789
10	318	1021.619	1019.854	-1.765
10	319	1018.014	1018.777	0.763
10	321	1006.616	1005.681	-0.935
10	322	1010.488	1010.398	-0.090
15	323	1005.801	1004.536	-1.265
15	325	1012.214	1012.206	-0.008
15	326	1011.758	1010.562	-1.196
15	328	1020.947	1020.750	-0.197
15	333	1015.494	1015.105	-0.389
30	334	1020.001	1015.513	-4.488
30	335	1001.998	999.334	-2.664
30	337	1014.948	1014.243	-0.705
30	339	1019.163	1016.705	-2.458
30	341	1013.367	1013.441	0.074
35	342	1019.553	1013.382	-6.171
35	345	1005.133	1001.968	-3.165
35	346	1015.200	1014.360	-0.840
35	347	1004.479	1001.754	-2.725
35	348	1021.008	1017.053	-3.955
50	349	1006.616	1005.198	-1.418
50	350	1020.537	1016.901	-3.636
50	351	1012.730	1012.212	-0.518
50	353	1024.427	1019.548	-4.879
50	354	1012.661	1009.047	-3.614
55	355	1016.554	1016.208	-0.346
55	357	1005.573	1001.745	-3.828
55	359	1009.879	1010.798	0.919
55	362	1015.256	1011.231	-4.025
55	363	1019.633	1016.757	-2.876
100	1	1009.539	1002.593	-6.946
100	3	1014.215	1004.944	-9.271
100	4	1003.058	997.255	-5.803
100	5	1005.419	1002.318	-3.101
100	6	1004.041	997.433	-6.608
100	7	1014.692	1010.536	-4.156
100	8	1014.064	1002.084	-11.980
100	9	1018.477	1015.727	-2.750
100	10	1007.562	1002.343	-5.219
100	11	1009.961	1004.424	-5.537
100	12	1011.281	1006.582	-4.699
100	13	1010.500	1005.372	-5.128
100	14	1009.957	1004.657	-5.300
100	15	1014.747	1010.411	-4.336
100	16	1014.854	1009.963	-4.891
100	17	1012.609	1006.491	-6.118
100	18	1003.194	1000.653	-2.541
100	19	1010.005	1003.706	-6.299
100	20	1010.903	1006.948	-3.955
100	21	1018.579	1013.520	-5.059
100	22	1003.508	1002.286	-1.222
100	24	1021.103	1018.155	-2.948
105	25	1017.272	1010.893	-6.379
105	26	1011.873	1007.372	-4.501
105	27	1014.889	1012.585	-2.304
105	28	1009.286	1003.661	-5.625
105	29	1018.304	1015.320	-2.984
	Max	1024.504	1024.034	1.998
	Average	1013.349	1010.671	-2.678
	Min	1001.998	997.255	-11.980
	Std Dev	5.693	6.598	2.756

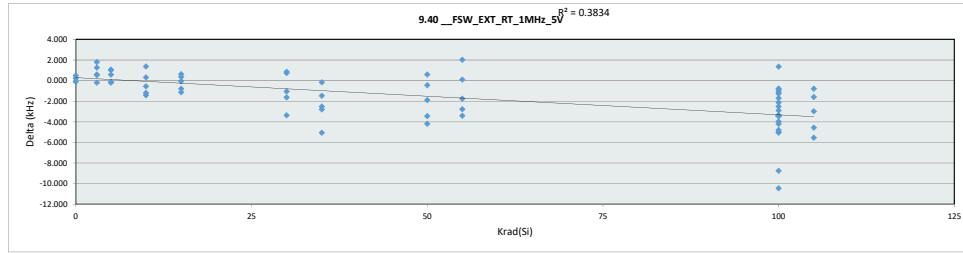


9.16 FSW_EXT_RT_1MHz_4											
Test Site											
Tester											
Test Number											
Max Limit	1100	kHz									
Min Limit	900	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1013.383	1008.444	1005.599	1005.681	1004.536	999.334	1001.754	1005.198	1001.745	997.255	1003.661
Average	1015.405	1016.311	1016.249	1013.839	1012.632	1011.847	1009.703	1012.581	1011.348	1005.836	1009.966
Max	1016.622	1024.034	1023.195	1019.854	1020.750	1016.705	1017.053	1019.548	1016.757	1018.155	1015.320
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000

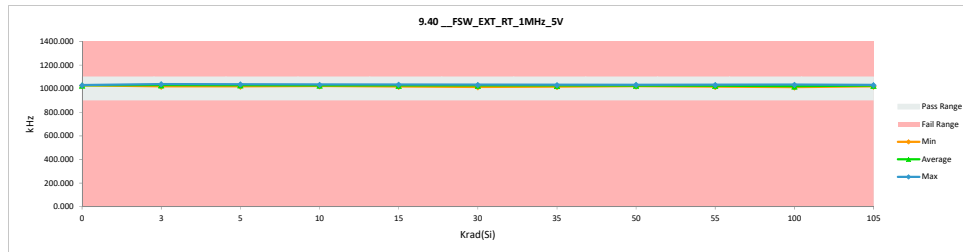


HDR TID Report
TPS7H5001-SP

9.40 FSW_EXT_RT_1MHz_5V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	1100	1100		
Min Limit	900	900		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1029.587	1029.843	0.256
0	118	1028.525	1028.437	-0.088
0	122	1030.559	1031.082	0.523
3	301	1027.024	1028.863	1.839
3	306	1026.905	1028.196	1.291
3	307	1039.762	1039.591	-0.171
3	308	1019.652	1020.271	0.619
3	309	1032.973	1033.559	0.586
5	310	1028.745	1028.584	-0.161
5	311	1035.916	1035.818	-0.098
5	312	1037.916	1038.980	1.064
5	314	1031.018	1032.103	1.085
5	316	1018.864	1019.479	0.615
10	317	1031.474	1030.082	-1.392
10	318	1036.206	1035.045	-1.161
10	319	1032.514	1033.910	1.396
10	321	1022.607	1022.088	-0.519
10	322	1025.326	1025.676	0.350
15	323	1019.337	1018.241	-1.096
15	325	1024.673	1025.337	0.664
15	326	1026.054	1025.300	-0.754
15	328	1036.134	1036.529	0.395
15	333	1029.883	1029.863	-0.020
30	334	1036.056	1032.724	-3.332
30	335	1015.988	1014.399	-1.589
30	337	1028.560	1029.455	0.895
30	339	1034.187	1033.162	-1.025
30	341	1027.591	1028.354	0.763
35	342	1034.751	1029.731	-5.020
35	345	1019.599	1017.115	-2.484
35	346	1027.494	1027.359	-0.135
35	347	1019.362	1017.920	-1.442
35	348	1034.393	1031.632	-2.761
50	349	1021.872	1021.465	-0.407
50	350	1036.663	1034.812	-1.851
50	351	1027.126	1027.755	0.629
50	353	1039.258	1035.108	-4.150
50	354	1026.548	1023.134	-3.414
55	355	1029.979	1030.108	0.129
55	357	1020.464	1017.714	-2.750
55	359	1023.554	1025.615	2.061
55	362	1028.942	1025.567	-3.375
55	363	1035.182	1033.460	-1.722
100	1	1023.208	1018.200	-5.008
100	3	1027.729	1019.003	-8.726
100	4	1016.964	1013.019	-3.945
100	5	1019.920	1019.068	-0.852
100	6	1018.036	1013.080	-4.956
100	7	1028.254	1025.769	-2.485
100	8	1027.203	1016.793	-10.410
100	9	1032.346	1031.107	-1.239
100	10	1022.969	1019.617	-3.352
100	11	1023.581	1020.179	-3.402
100	12	1023.818	1020.955	-2.863
100	13	1025.454	1022.053	-3.401
100	14	1023.845	1020.465	-3.380
100	15	1030.168	1028.083	-2.085
100	16	1029.555	1026.293	-3.262
100	17	1026.187	1021.427	-4.760
100	18	1016.989	1016.255	-0.734
100	19	1023.822	1019.655	-4.167
100	20	1025.507	1023.834	-1.673
100	21	1033.215	1029.836	-3.379
100	22	1017.660	1019.047	1.387
100	24	1035.233	1034.167	-1.066
105	25	1033.450	1027.943	-5.507
105	26	1026.726	1023.778	-2.948
105	27	1030.084	1029.321	-0.763
105	28	1024.292	1019.769	-4.523
105	29	1033.683	1032.125	-1.558
Max		1039.762	1039.591	2.061
Average		1027.703	1026.062	-1.642
Min		1015.988	1013.019	-10.410
Std Dev		5.997	6.555	2.385

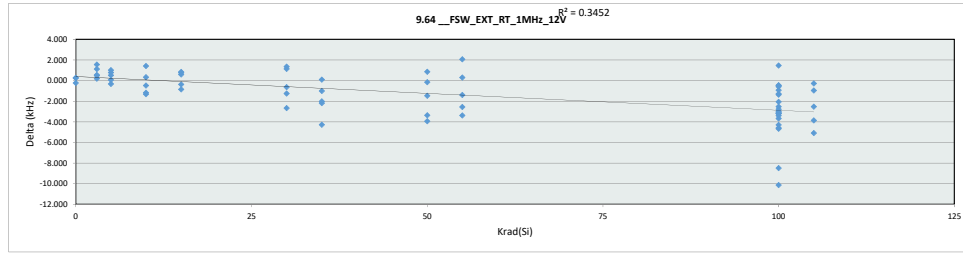


9.40 FSW_EXT_RT_1MHz_5											
Test Site											
Tester											
Test Number											
Max Limit	1100	kHz									
Min Limit	900	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1028.437	1020.271	1019.479	1022.088	1018.241	1014.399	1017.115	1021.465	1017.714	1013.019	1019.769
Average	1029.787	1030.096	1030.993	1029.360	1027.054	1027.619	1024.751	1028.455	1026.493	1021.723	1026.587
Max	1031.082	1039.591	1038.980	1035.045	1036.529	1033.162	1031.632	1035.108	1033.460	1034.167	1032.125
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000

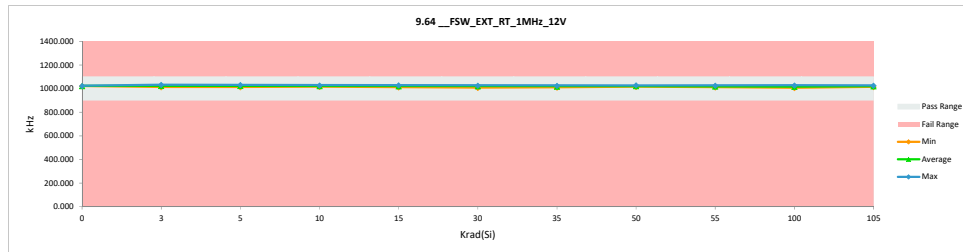


HDR TID Report
TPS7H5001-SP

9.64 FSW_EXT_RT_1MHz_12V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	1100	1100		
Min Limit	900	900		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1024.067	1024.373	0.306
0	118	1023.586	1023.391	-0.195
0	122	1025.359	1025.642	0.283
3	301	1021.889	1023.474	1.585
3	306	1021.838	1022.989	1.151
3	307	1034.128	1034.344	0.216
3	308	1014.580	1015.103	0.523
3	309	1027.821	1028.398	0.577
5	310	1023.244	1023.336	0.092
5	311	1030.689	1030.393	-0.296
5	312	1032.875	1033.683	0.808
5	314	1025.875	1026.923	1.048
5	316	1013.685	1014.230	0.545
10	317	1026.424	1025.137	-1.287
10	318	1030.886	1029.749	-1.137
10	319	1027.009	1028.463	1.454
10	321	1017.709	1017.259	-0.450
10	322	1020.329	1020.684	0.355
15	323	1014.162	1013.351	-0.811
15	325	1019.280	1020.159	0.879
15	326	1020.773	1020.444	-0.329
15	328	1030.564	1031.362	0.798
15	333	1024.339	1024.966	0.627
30	334	1030.352	1028.122	-2.230
30	335	1010.918	1009.697	-1.221
30	337	1023.375	1024.769	1.394
30	339	1029.175	1028.577	-0.598
30	341	1022.224	1023.397	1.173
35	342	1029.385	1025.133	-4.252
35	345	1014.439	1012.460	-1.979
35	346	1021.938	1022.062	0.124
35	347	1014.159	1013.181	-0.978
35	348	1028.873	1026.714	-2.159
50	349	1017.053	1016.929	-0.124
50	350	1031.775	1030.333	-1.442
50	351	1021.668	1022.565	0.897
50	353	1033.982	1030.081	-3.901
50	354	1021.080	1017.753	-3.327
55	355	1024.538	1024.868	0.330
55	357	1015.661	1013.138	-2.523
55	359	1018.365	1020.461	2.096
55	362	1023.809	1020.454	-3.355
55	363	1030.037	1028.669	-1.368
100	1	1018.096	1013.532	-4.564
100	3	1022.357	1013.900	-8.457
100	4	1011.915	1008.556	-3.359
100	5	1014.961	1014.553	-0.408
100	6	1012.916	1008.280	-4.636
100	7	1022.945	1020.917	-2.028
100	8	1022.092	1011.999	-10.093
100	9	1027.074	1026.183	-0.891
100	10	1018.033	1015.008	-3.025
100	11	1018.661	1015.738	-2.923
100	12	1018.053	1015.565	-2.488
100	13	1020.432	1017.253	-3.179
100	14	1018.521	1015.779	-2.742
100	15	1024.816	1023.482	-1.334
100	16	1024.304	1021.188	-3.116
100	17	1020.796	1016.533	-4.263
100	18	1012.000	1011.540	-0.460
100	19	1018.455	1014.814	-3.641
100	20	1020.369	1019.115	-1.254
100	21	1028.078	1024.938	-3.140
100	22	1012.719	1014.216	1.497
100	24	1029.777	1029.229	-0.548
105	25	1028.508	1023.451	-5.057
105	26	1021.625	1019.133	-2.492
105	27	1024.816	1024.569	-0.247
105	28	1019.343	1015.510	-3.833
105	29	1028.643	1027.716	-0.927
	Max	1034.128	1034.344	2.096
	Average	1022.495	1021.142	-1.353
	Min	1010.918	1008.280	-10.093
	Std Dev	5.927	6.442	2.277

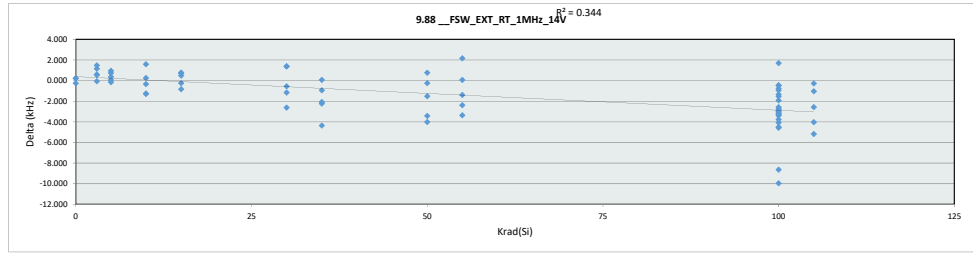


9.64 FSW_EXT_RT_1MHz_1											
Test Site											
Tester											
Test Number											
Max Limit	1100	kHz									
Min Limit	900	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1023.391	1015.103	1014.230	1017.259	1013.351	1009.697	1012.460	1016.929	1013.138	1008.280	1015.510
Average	1024.469	1024.862	1025.713	1024.258	1022.056	1022.912	1019.910	1023.532	1021.518	1016.924	1022.076
Max	1025.642	1034.344	1033.683	1029.749	1031.362	1028.577	1026.714	1030.333	1028.669	1029.229	1027.716
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000

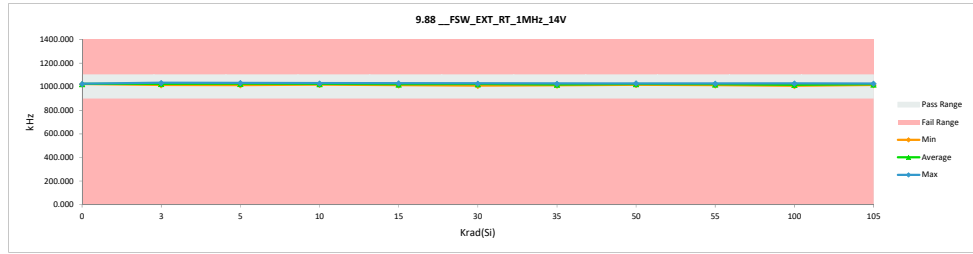


HDR TID Report
TPS7H5001-SP

9.88 FSW_EXT_RT_1MHz_14V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	1100	1100		
Min Limit	900	900		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1023.963	1024.223	0.260
0	118	1023.441	1023.209	-0.232
0	122	1025.311	1025.564	0.253
3	301	1021.807	1023.320	1.513
3	306	1021.690	1022.879	1.189
3	307	1034.101	1034.074	-0.027
3	308	1014.459	1015.059	0.600
3	309	1027.648	1028.271	0.623
5	310	1023.148	1023.233	0.085
5	311	1030.482	1030.369	-0.113
5	312	1032.836	1033.600	0.764
5	314	1025.764	1026.746	0.982
5	316	1013.827	1014.223	0.396
10	317	1026.280	1025.051	-1.229
10	318	1030.820	1029.573	-1.247
10	319	1026.827	1028.441	1.614
10	321	1017.578	1017.269	-0.309
10	322	1020.173	1020.463	0.290
15	323	1013.952	1013.144	-0.808
15	325	1019.218	1020.037	0.819
15	326	1020.632	1020.422	-0.210
15	328	1030.540	1031.278	0.738
15	333	1024.224	1024.740	0.516
30	334	1030.530	1027.945	-2.585
30	335	1010.747	1009.617	-1.130
30	337	1023.264	1024.689	1.425
30	339	1028.996	1028.473	-0.523
30	341	1022.005	1023.421	1.416
35	342	1029.419	1025.096	-4.323
35	345	1014.400	1012.352	-2.048
35	346	1021.859	1021.957	0.098
35	347	1014.081	1013.172	-0.909
35	348	1028.813	1026.610	-2.203
50	349	1016.908	1016.693	-0.215
50	350	1031.652	1030.174	-1.478
50	351	1021.547	1022.345	0.798
50	353	1033.803	1029.818	-3.985
50	354	1021.010	1017.612	-3.398
55	355	1024.592	1024.686	0.094
55	357	1015.502	1013.146	-2.356
55	359	1018.139	1020.328	2.189
55	362	1023.726	1020.400	-3.326
55	363	1029.897	1028.538	-1.359
100	1	1018.086	1013.543	-4.543
100	3	1022.296	1013.689	-8.607
100	4	1011.798	1008.433	-3.365
100	5	1014.916	1014.506	-0.410
100	6	1012.815	1008.351	-4.464
100	7	1022.859	1020.968	-1.891
100	8	1021.860	1011.929	-9.931
100	9	1026.938	1026.024	-0.914
100	10	1017.824	1014.897	-2.927
100	11	1018.535	1015.728	-2.807
100	12	1018.072	1015.528	-2.544
100	13	1020.276	1017.257	-3.019
100	14	1018.386	1015.673	-2.713
100	15	1024.797	1023.295	-1.502
100	16	1024.262	1020.990	-3.272
100	17	1020.600	1016.554	-4.046
100	18	1011.968	1011.520	-0.448
100	19	1018.293	1014.560	-3.733
100	20	1020.246	1018.931	-1.315
100	21	1028.021	1024.879	-3.142
100	22	1012.526	1014.252	1.726
100	24	1029.863	1029.127	-0.736
105	25	1028.465	1023.317	-5.148
105	26	1021.525	1018.981	-2.544
105	27	1024.657	1024.415	-0.242
105	28	1019.355	1015.349	-4.006
105	29	1028.591	1027.588	-1.003
	Max	1034.101	1034.074	2.189
	Average	1022.392	1021.036	-1.356
	Min	1010.747	1008.351	-9.931
	Std Dev	5.932	6.415	2.276

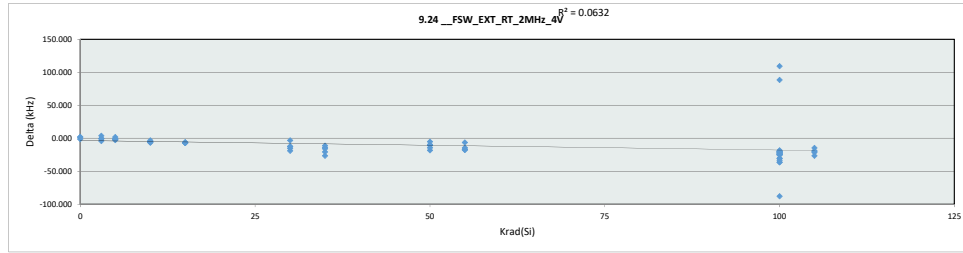


9.88 FSW_EXT_RT_1MHz_1											
Test Site											
Tester											
Test Number											
Max Limit	1100	kHz									
Min Limit	900	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1023.209	1015.059	1014.223	1017.269	1013.144	1009.617	1012.352	1016.693	1013.146	1008.351	1015.349
Average	1024.332	1024.721	1025.634	1024.159	1021.924	1022.829	1019.837	1023.328	1021.420	1016.847	1021.930
Max	1025.564	1034.074	1033.600	1029.573	1031.278	1028.473	1026.610	1030.174	1028.538	1029.127	1027.588
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000

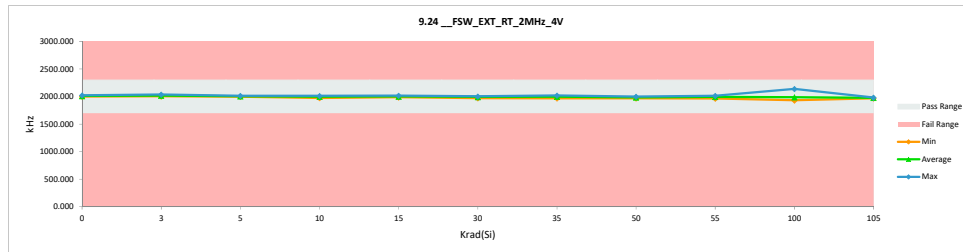


HDR TID Report
TPS7H5001-SP

9.24 FSW_EXT_RT_2MHz_4V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	2300	2300		
Min Limit	1700	1700		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	2022.664	2024.922	2.258
0	118	2002.129	2001.305	-0.824
0	122	2007.121	2009.875	2.754
3	301	2035.923	2040.317	4.394
3	306	2005.968	2005.632	-0.336
3	307	2017.106	2013.541	-3.565
3	308	2021.539	2021.440	-0.099
3	309	2012.688	2012.995	0.307
5	310	2010.173	2009.103	-1.070
5	311	2018.029	2015.828	-2.201
5	312	2010.103	2009.997	-0.106
5	314	2000.614	1999.783	-0.831
5	316	1997.356	1999.862	2.506
10	317	2010.638	2004.655	-5.983
10	318	2020.907	2015.461	-5.446
10	319	2018.404	2013.827	-4.577
10	321	1981.208	1976.397	-4.811
10	322	1993.034	1990.269	-2.765
15	323	2005.042	1999.690	-5.352
15	325	2025.969	2019.502	-6.467
15	326	1995.472	1988.856	-6.616
15	328	2012.214	2005.805	-6.409
15	333	2008.816	2002.696	-6.120
30	334	2007.398	1992.681	-4.717
30	335	1984.508	1972.920	-11.588
30	337	2015.670	1997.101	-18.469
30	339	2016.350	2004.312	-12.038
30	341	2009.453	2006.807	-2.646
35	342	2016.867	1996.726	-20.141
35	345	1998.013	1984.418	-13.595
35	346	2028.485	2017.902	-10.583
35	347	1986.370	1971.026	-15.344
35	348	2047.411	2021.508	-25.903
50	349	1979.569	1969.507	-10.062
50	350	1994.870	1977.202	-17.668
50	351	2000.085	1991.152	-8.933
50	353	2016.070	2002.218	-13.852
50	354	2003.931	1999.269	-4.662
55	355	2022.876	2016.982	-5.894
55	357	1984.251	1967.092	-17.159
55	359	2012.210	1998.389	-13.821
55	362	2018.353	2002.090	-16.263
55	363	2008.422	1992.253	-16.169
100	1	1999.417	1969.680	-29.737
100	3	2019.733	2129.591	109.858
100	4	1994.518	1964.748	-29.770
100	5	1981.622	1958.225	-23.397
100	6	1985.172	1963.492	-21.680
100	7	2017.207	1994.805	-22.402
100	8	2021.414	1934.166	-87.248
100	9	2023.068	2003.052	-20.016
100	10	1980.787	1958.915	-21.872
100	11	2011.777	1976.253	-35.524
100	12	2051.874	2140.784	88.910
100	13	1998.498	1979.285	-19.213
100	14	2004.842	1980.089	-24.753
100	15	1998.434	1973.954	-24.480
100	16	2005.922	1988.274	-17.648
100	17	2014.329	1978.556	-35.773
100	18	1992.058	1971.318	-20.740
100	19	2020.227	1987.542	-32.685
100	20	1992.917	1972.983	-19.934
100	21	2020.904	1996.602	-24.302
100	22	1988.915	1965.355	-23.560
100	24	2034.579	2014.877	-19.702
105	25	1991.558	1972.864	-18.694
105	26	2010.229	1984.287	-25.942
105	27	1995.750	1981.734	-14.016
105	28	1991.128	1970.178	-20.950
105	29	2000.882	1981.930	-18.952
	Max	2051.874	2140.784	109.858
	Average	2007.599	1996.526	-11.073
	Min	1979.569	1934.166	-87.248
	Std Dev	15.371	31.018	23.352

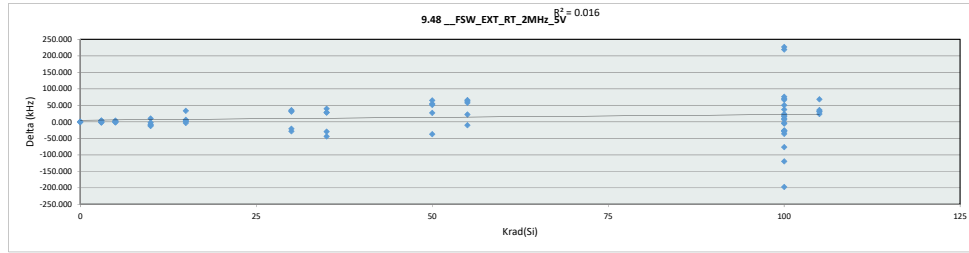


9.24 FSW_EXT_RT_2MHz_4											
Test Site											
Tester											
Test Number											
Max Limit	2300	kHz									
Min Limit	1700	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min	2001.305	2005.632	1999.783	1976.397	1988.856	1972.920	1971.026	1969.507	1967.092	1934.166	1970.178
Average	2012.034	2018.785	2006.915	2000.122	2003.310	1994.764	1998.316	1987.870	1995.361	1991.025	1978.199
Max	2024.922	2040.317	2015.828	2015.461	2019.502	2006.807	2021.508	2002.218	2016.982	2140.784	1984.287
UL	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000

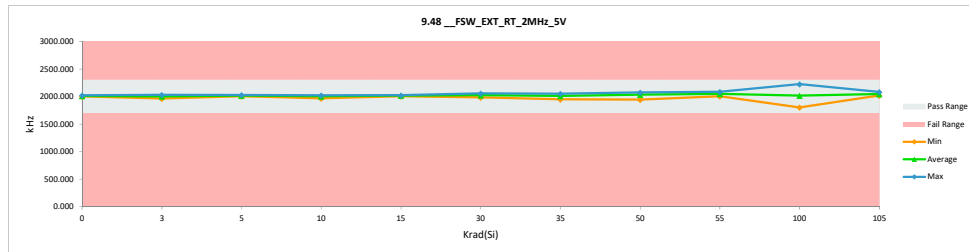


HDR TID Report
TPS7H5001-SP

9.48 FSW_EXT_RT_2MHz_5V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	2300	2300		
Min Limit	1700	1700		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	2000.982	2001.863	0.881
0	118	2007.382	2007.061	-0.321
0	122	2021.414	2023.346	1.932
3	301	1988.192	1993.162	4.970
3	306	2008.080	2008.817	0.737
3	307	2033.063	2032.210	-0.853
3	308	1961.181	1965.828	4.647
3	309	2023.965	2022.887	-1.078
5	310	2011.228	2010.371	-0.857
5	311	2020.946	2021.545	0.599
5	312	2026.332	2028.257	1.925
5	314	2012.301	2013.387	1.086
5	316	1999.751	2004.592	4.841
10	317	2030.733	2023.567	-7.166
10	318	2020.650	2019.002	-1.648
10	319	2010.614	2021.860	11.246
10	321	1978.714	1967.274	-11.440
10	322	2005.473	1996.244	-9.229
15	323	2002.894	2006.266	3.372
15	325	1990.058	2024.293	34.235
15	326	2006.051	2013.394	7.343
15	328	2025.006	2022.374	-2.632
15	333	2012.251	2015.624	3.373
30	334	2013.041	1985.146	-27.895
30	335	1989.833	2026.203	36.370
30	337	2022.756	2056.753	33.997
30	339	2030.153	2010.235	-19.918
30	341	2017.576	2049.460	31.884
35	342	2028.777	2000.163	-28.614
35	345	2005.634	2035.425	29.791
35	346	1981.878	2022.414	40.536
35	347	1992.495	1949.255	-43.240
35	348	2021.681	2050.583	28.902
50	349	1990.836	2056.784	65.948
50	350	1982.186	1945.572	-36.614
50	351	2008.896	2064.869	55.973
50	353	2023.860	2076.227	52.367
50	354	1999.422	2027.639	28.217
55	355	2003.589	2027.042	23.453
55	357	1993.505	2057.508	64.003
55	359	2009.997	2068.721	58.724
55	362	2014.666	2005.571	-9.095
55	363	2019.550	2086.559	67.009
100	1	2000.592	1974.171	-26.421
100	3	1995.370	2215.436	220.066
100	4	1987.381	1985.027	-2.354
100	5	1984.253	2061.306	77.053
100	6	1984.006	2008.801	24.795
100	7	2014.014	1988.587	-25.427
100	8	2000.714	1804.116	-196.598
100	9	2024.744	1949.228	-75.516
100	10	1993.039	2064.350	71.311
100	11	1992.954	1967.381	-25.573
100	12	1999.383	2227.376	227.993
100	13	2015.151	2066.486	51.335
100	14	2004.882	2022.407	17.525
100	15	2011.541	2079.442	67.901
100	16	2023.464	2061.796	38.332
100	17	1999.971	1881.074	-118.897
100	18	1986.005	1957.290	-28.715
100	19	2009.663	2017.594	7.931
100	20	1993.424	2012.784	19.360
100	21	2024.381	2020.163	-4.218
100	22	2005.520	2016.856	11.336
100	24	2046.528	2011.253	-35.275
105	25	1989.552	2021.051	31.499
105	26	2019.915	2053.364	33.449
105	27	2003.206	2040.253	37.047
105	28	2000.619	2025.379	24.760
105	29	2015.723	2084.970	69.247
	Max	2046.528	2227.376	227.993
	Average	2006.766	2020.904	14.139
	Min	1961.181	1804.116	-196.598
	Std Dev	15.662	56.402	56.028

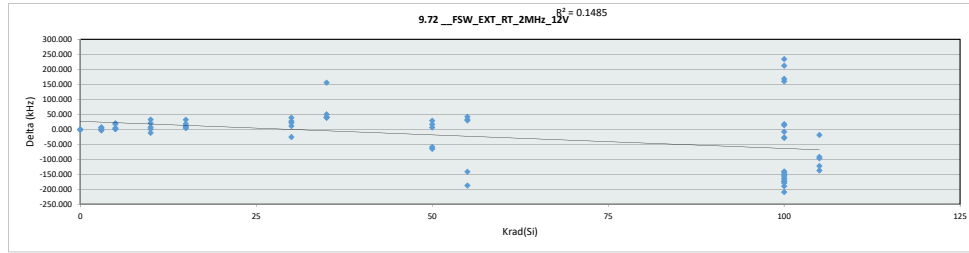


9.48 FSW_EXT_RT_2MHz_5											
Test Site											
Tester											
Test Number											
Max Limit	2300	kHz									
Min Limit	1700	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min	2001.863	1965.828	2004.592	1967.274	2006.266	1985.146	1949.255	1945.572	2005.571	1804.116	2021.051
Average	2010.757	2004.581	2015.630	2005.589	2016.390	2025.559	2011.568	2034.218	2049.080	2017.860	2045.003
Max	2023.346	2032.210	2028.257	2023.567	2024.293	2056.753	2050.583	2076.227	2086.559	2227.376	2084.970
UL	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000

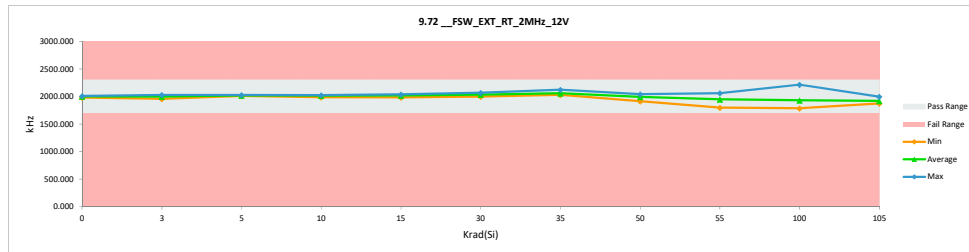


HDR TID Report
TPS7H5001-SP

9.72 FSW_EXT_RT_2MHz_12V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	2300	2300		
Min Limit	1700	1700		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1982.647	1983.180	0.533
0	118	2013.287	2012.593	-0.694
0	122	2009.827	2011.034	1.207
3	301	1973.649	1979.295	5.646
3	306	2016.337	2018.303	1.966
3	307	2033.060	2030.448	-2.612
3	308	1950.516	1958.393	7.877
3	309	2012.822	2012.285	-0.537
5	310	1994.910	2013.529	18.619
5	311	2008.297	2029.579	21.282
5	312	2025.454	2028.164	2.710
5	314	2011.253	2013.031	1.778
5	316	2005.451	2011.156	5.705
10	317	2024.329	2026.448	2.119
10	318	2005.257	2026.829	21.572
10	319	1992.728	2026.367	33.639
10	321	1999.146	1988.487	-10.659
10	322	2012.324	2020.861	8.537
15	323	1975.987	2009.005	33.018
15	325	1973.303	1985.129	11.826
15	326	2022.281	2041.246	18.965
15	328	2030.481	2036.188	5.707
15	333	2022.278	2028.089	5.811
30	334	2025.370	2000.480	-24.890
30	335	1999.367	2011.774	12.407
30	337	2012.687	2040.794	28.107
30	339	2030.722	2070.420	39.698
30	341	2024.507	2047.412	22.905
35	342	2015.957	2057.738	41.781
35	345	1990.335	2032.632	42.297
35	346	1968.166	2124.828	156.662
35	347	1999.435	2038.886	39.451
35	348	2001.699	2052.887	51.188
50	349	1999.018	2028.561	29.543
50	350	2008.039	1950.306	-57.733
50	351	2017.864	2025.015	7.151
50	353	2026.835	2044.336	17.501
50	354	1980.692	1916.635	-64.057
55	355	1985.147	1799.025	-186.122
55	357	1995.602	2028.878	33.276
55	359	1992.757	2023.624	30.867
55	362	1977.765	1837.450	-140.315
55	363	2019.410	2062.442	43.032
100	1	2007.743	1834.494	-173.249
100	3	1977.039	2138.213	161.174
100	4	1970.397	1820.089	-150.308
100	5	1990.357	2005.178	14.821
100	6	1996.050	1854.438	-141.612
100	7	1996.035	1787.818	-208.217
100	8	1981.743	2195.122	213.379
100	9	1997.646	1819.863	-177.783
100	10	1996.318	2015.039	18.721
100	11	1975.413	1811.871	-163.542
100	12	1980.507	2150.168	169.661
100	13	2012.517	1986.154	-26.363
100	14	2011.859	1863.380	-148.479
100	15	2023.628	2016.749	-6.879
100	16	2014.988	1987.735	-27.253
100	17	1981.822	2216.900	235.078
100	18	1961.438	1806.454	-154.984
100	19	1990.346	1851.019	-139.327
100	20	2003.316	1815.121	-188.195
100	21	2007.088	1836.430	-170.658
100	22	2016.361	1865.645	-150.716
100	24	2019.686	1857.021	-162.665
105	25	2006.669	1885.494	-121.175
105	26	2023.831	1928.331	-95.500
105	27	2009.152	1918.620	-90.532
105	28	2012.911	1876.566	-136.345
105	29	2016.193	1998.586	-17.607
Max		2033.060	2216.900	235.078
Average		2002.115	1980.375	-21.740
Min		1950.516	1787.818	-208.217
Std Dev		18.641	96.583	96.023

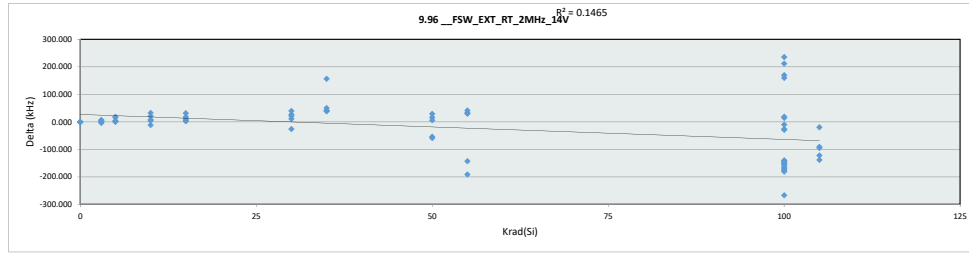


9.72 FSW_EXT_RT_2MHz_1											
Test Site											
Tester											
Test Number											
Max Limit	2300	kHz									
Min Limit	1700	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min	1983.180	1958.393	2011.156	1988.487	1985.129	2000.480	2032.632	1916.635	1799.025	1787.818	1876.566
Average	2002.269	1999.745	2019.092	2017.798	2019.931	2034.176	2061.394	1992.971	1950.284	1933.405	1921.519
Max	2012.593	2030.448	2029.579	2026.829	2041.246	2070.420	2124.828	2044.336	2062.442	2216.900	1998.586
UL	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000

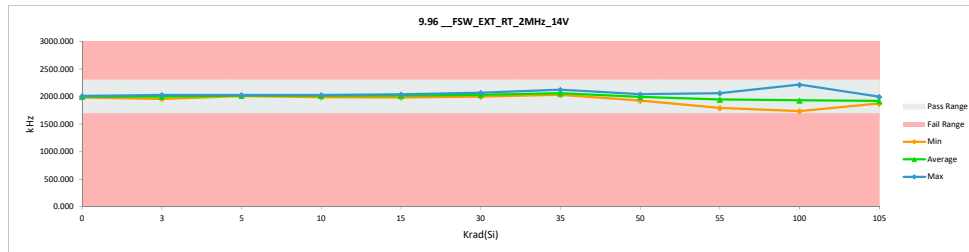


HDR TID Report
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9.96 FSW_EXT_RT_2MHz_14V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	2300	2300		
Min Limit	1700	1700		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1982.232	1982.877	0.645
0	118	2013.081	2012.426	-0.655
0	122	2009.158	2010.646	1.488
3	301	1973.241	1979.221	5.980
3	306	2015.932	2018.150	2.218
3	307	2032.971	2030.160	-2.811
3	308	1950.142	1958.375	8.233
3	309	2012.388	2012.158	-0.230
5	310	1994.196	2013.176	18.980
5	311	2012.277	2029.996	17.719
5	312	2025.356	2028.034	2.678
5	314	2011.026	2012.384	1.358
5	316	2005.679	2010.906	5.227
10	317	2023.779	2029.036	5.257
10	318	2004.500	2026.416	21.916
10	319	1992.275	2026.013	33.738
10	321	1998.742	1988.124	-10.618
10	322	2011.819	2020.703	8.884
15	323	1977.185	2009.970	32.785
15	325	1973.038	1984.821	11.783
15	326	2022.333	2041.285	18.952
15	328	2030.430	2033.785	3.355
15	333	2022.400	2027.834	5.434
30	334	2024.888	1999.805	-25.083
30	335	1999.191	2011.698	12.507
30	337	2012.616	2040.453	27.837
30	339	2030.099	2070.587	40.488
30	341	2024.596	2047.404	22.808
35	342	2015.546	2058.055	42.509
35	345	1990.047	2031.127	41.080
35	346	1968.008	2125.981	157.973
35	347	1999.039	2038.642	39.603
35	348	2001.749	2052.748	50.999
50	349	1998.303	2028.732	30.429
50	350	2007.888	1950.522	-57.366
50	351	2017.710	2024.642	6.932
50	353	2026.126	2043.633	17.507
50	354	1980.596	1927.524	-53.072
55	355	1984.805	1794.659	-190.146
55	357	1995.157	2028.674	33.517
55	359	1992.102	2022.746	30.644
55	362	1977.667	1835.480	-142.187
55	363	2019.368	2062.086	42.718
100	1	2007.475	1833.285	-174.190
100	3	1976.803	2137.912	161.109
100	4	1970.023	1821.613	-148.410
100	5	1989.871	2005.611	15.740
100	6	1995.314	1855.795	-139.519
100	7	1996.022	1844.264	-151.758
100	8	1981.216	2194.200	212.984
100	9	1997.404	1817.674	-179.730
100	10	1995.596	2015.162	19.566
100	11	1975.329	1809.048	-166.281
100	12	1980.423	2151.674	171.251
100	13	2012.872	1988.049	-24.823
100	14	2012.246	1867.035	-145.211
100	15	2022.888	2014.306	-8.582
100	16	2014.626	1986.601	-28.025
100	17	1981.169	2218.028	236.859
100	18	1961.054	1807.791	-153.263
100	19	1990.020	1850.085	-139.935
100	20	2003.043	1736.817	-266.226
100	21	2007.133	1836.765	-170.368
100	22	2016.061	1866.167	-149.894
100	24	2019.408	1859.482	-159.926
105	25	2006.142	1884.952	-121.190
105	26	2024.076	1930.235	-93.841
105	27	2008.514	1918.637	-89.877
105	28	2012.489	1874.828	-137.661
105	29	2016.219	1997.435	-18.784
Max		2032.971	2218.028	236.859
Average		2001.902	1980.071	-21.831
Min		1950.142	1736.817	-266.226
Std Dev		18.679	97.626	97.175



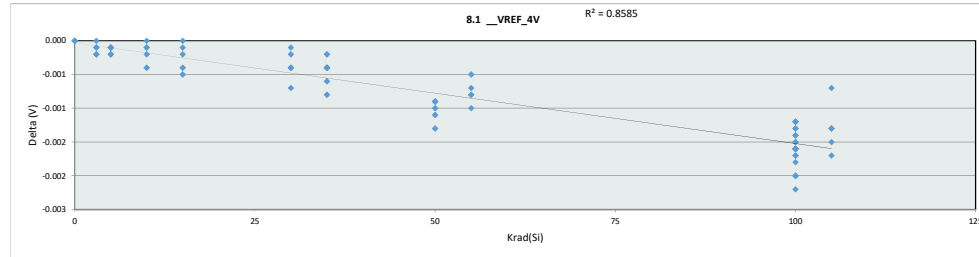
9.96 FSW_EXT_RT_2MHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	2300	kHz									
Min Limit	1700	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min	1982.877	1958.375	2010.906	1988.124	1984.821	1999.605	2031.127	1927.524	1794.659	1736.817	1874.828
Average	2001.983	1999.613	2018.899	2018.058	2019.539	2033.949	2061.311	1995.011	1948.729	1932.607	1921.217
Max	2012.426	2030.160	2029.996	2029.036	2041.285	2070.587	2125.981	2043.633	2062.086	2218.028	1997.435
UL	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000



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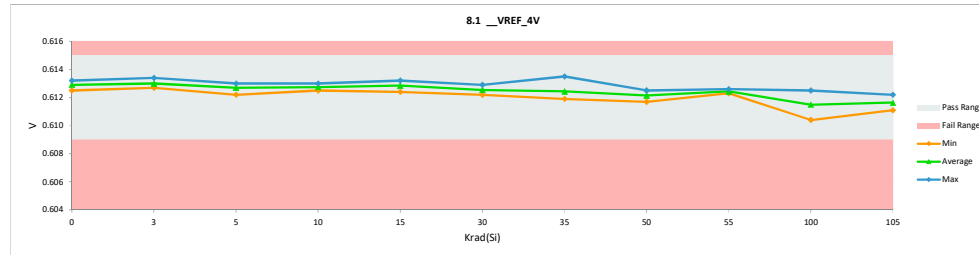
8.1_VREF_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.615
Min Limit	0.609

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	0.613	0.613	0.000
0	118	0.613	0.613	0.000
0	122	0.613	0.613	0.000
3	301	0.613	0.613	0.000
3	306	0.613	0.613	0.000
3	307	0.613	0.613	0.000
3	308	0.613	0.613	0.000
3	309	0.613	0.613	0.000
5	310	0.613	0.613	0.000
5	311	0.613	0.613	0.000
5	312	0.613	0.613	0.000
5	314	0.613	0.613	0.000
5	316	0.612	0.612	0.000
10	317	0.613	0.613	0.000
10	318	0.613	0.613	0.000
10	319	0.613	0.613	0.000
10	321	0.613	0.613	0.000
10	322	0.613	0.613	0.000
15	323	0.614	0.613	0.000
15	325	0.613	0.612	0.000
15	326	0.613	0.613	0.000
15	328	0.613	0.613	0.000
15	323	0.613	0.613	0.000
30	334	0.613	0.613	0.000
30	335	0.613	0.612	-0.001
30	337	0.613	0.613	0.000
30	339	0.613	0.613	0.000
30	341	0.613	0.612	0.000
35	342	0.612	0.612	0.000
35	345	0.613	0.612	0.000
35	346	0.614	0.613	-0.001
35	347	0.613	0.612	0.000
35	348	0.613	0.612	-0.001
50	349	0.613	0.612	-0.001
50	350	0.613	0.612	-0.001
50	351	0.613	0.612	-0.001
50	353	0.613	0.612	-0.001
50	354	0.613	0.613	-0.001
55	355	0.613	0.612	-0.001
55	357	0.613	0.612	-0.001
55	359	0.613	0.612	-0.001
55	362	0.613	0.613	-0.001
55	363	0.613	0.613	0.000
100	1	0.613	0.611	-0.002
100	3	0.614	0.612	-0.002
100	4	0.613	0.612	-0.001
100	5	0.613	0.611	-0.002
100	6	0.614	0.613	-0.001
100	7	0.613	0.612	-0.002
100	8	0.613	0.612	-0.002
100	9	0.613	0.611	-0.001
100	10	0.613	0.611	-0.002
100	11	0.613	0.611	-0.002
100	12	0.613	0.612	-0.001
100	13	0.612	0.610	-0.002
100	14	0.613	0.612	-0.001
100	15	0.614	0.612	-0.002
100	16	0.614	0.612	-0.001
100	17	0.613	0.611	-0.002
100	18	0.613	0.612	-0.001
100	19	0.613	0.612	-0.001
100	20	0.613	0.611	-0.002
100	21	0.613	0.611	-0.002
100	22	0.613	0.612	-0.002
100	24	0.613	0.611	-0.002
105	25	0.613	0.612	-0.001
105	26	0.613	0.612	-0.001
105	27	0.613	0.611	-0.002
105	28	0.613	0.612	-0.001
105	29	0.613	0.611	-0.002
Max		0.614	0.613	0.000
Average		0.613	0.612	-0.001
Min		0.612	0.610	-0.002
Std Dev		0.000	0.001	0.001



8.1_VREF_4V	
Test Site	
Tester	
Test Number	
Max Limit	0.615
Min Limit	0.609

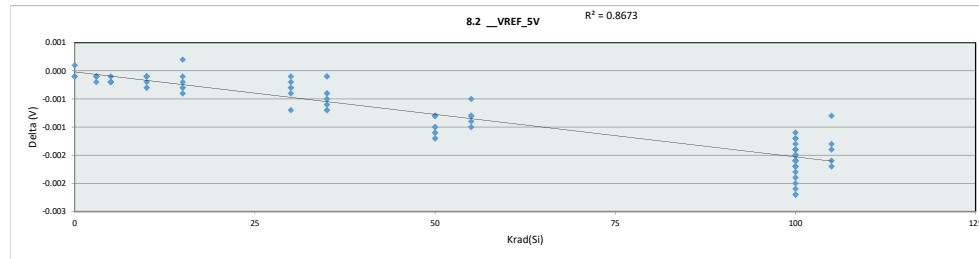
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609
Min	0.613	0.613	0.612	0.613	0.612	0.612	0.612	0.612	0.612	0.610	0.611
Average	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.611	0.612
Max	0.613	0.613	0.613	0.613	0.613	0.613	0.614	0.613	0.613	0.613	0.612
UL	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615



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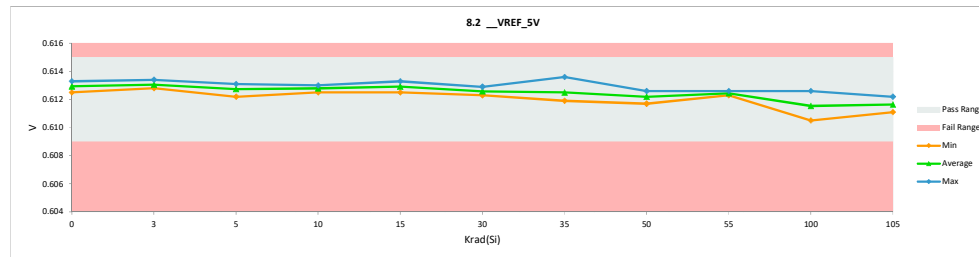
8.2_VREF_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.615
Min Limit	0.609

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	0.613	0.613	0.000
0	118	0.613	0.613	0.000
0	122	0.613	0.613	0.000
3	301	0.614	0.613	0.000
3	306	0.613	0.613	0.000
3	307	0.613	0.613	0.000
3	308	0.613	0.613	0.000
3	309	0.613	0.613	0.000
5	310	0.613	0.613	0.000
5	311	0.613	0.613	0.000
5	312	0.613	0.613	0.000
5	314	0.613	0.613	0.000
5	316	0.612	0.612	0.000
10	317	0.613	0.613	0.000
10	318	0.613	0.613	0.000
10	319	0.613	0.613	0.000
10	321	0.613	0.613	0.000
10	322	0.613	0.613	0.000
15	323	0.614	0.613	0.000
15	325	0.613	0.613	0.000
15	326	0.613	0.613	0.000
15	328	0.613	0.613	0.000
15	333	0.613	0.613	0.000
30	334	0.613	0.613	0.000
30	335	0.613	0.612	-0.001
30	337	0.613	0.613	0.000
30	339	0.613	0.613	0.000
30	341	0.613	0.612	0.000
35	342	0.612	0.612	0.000
35	345	0.613	0.612	-0.001
35	346	0.614	0.614	-0.001
35	347	0.613	0.613	0.000
35	348	0.613	0.612	-0.001
50	349	0.613	0.612	-0.001
50	350	0.613	0.612	-0.001
50	351	0.613	0.612	-0.001
50	353	0.613	0.612	-0.001
50	354	0.613	0.613	-0.001
55	355	0.613	0.612	-0.001
55	357	0.613	0.612	-0.001
55	359	0.613	0.612	-0.001
55	362	0.613	0.613	-0.001
55	363	0.613	0.613	0.000
100	1	0.613	0.611	-0.002
100	3	0.614	0.612	-0.002
100	4	0.613	0.612	-0.001
100	5	0.613	0.611	-0.002
100	6	0.614	0.613	-0.001
100	7	0.613	0.612	-0.001
100	8	0.613	0.612	-0.002
100	9	0.613	0.611	-0.002
100	10	0.613	0.611	-0.002
100	11	0.613	0.612	-0.002
100	12	0.613	0.612	-0.001
100	13	0.613	0.610	-0.002
100	14	0.613	0.612	-0.001
100	15	0.614	0.612	-0.002
100	16	0.614	0.612	-0.001
100	17	0.613	0.611	-0.002
100	18	0.613	0.612	-0.001
100	19	0.613	0.612	-0.001
100	20	0.613	0.611	-0.002
100	21	0.613	0.611	-0.002
100	22	0.613	0.612	-0.002
100	24	0.613	0.611	-0.002
105	25	0.613	0.612	-0.001
105	26	0.613	0.612	-0.001
105	27	0.613	0.611	-0.002
105	28	0.614	0.612	-0.001
105	29	0.613	0.611	-0.002
Max		0.614	0.614	0.000
Average		0.613	0.612	-0.001
Min		0.612	0.610	-0.002
Std Dev		0.000	0.001	0.001



8.2_VREF_5V	
Test Site	
Tester	
Test Number	
Max Limit	0.615
Min Limit	0.609

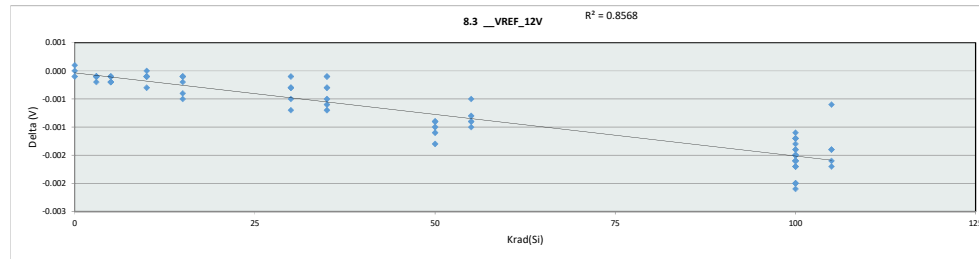
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609
Min	0.613	0.613	0.612	0.613	0.613	0.612	0.612	0.612	0.612	0.611	0.611
Average	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.612
Max	0.613	0.613	0.613	0.613	0.613	0.613	0.614	0.613	0.613	0.613	0.612
UL	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615



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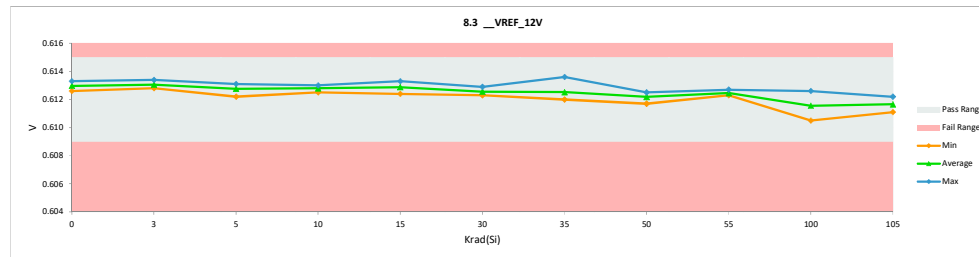
8.3 __VREF_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.615
Min Limit	0.609

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	0.613	0.613	0.000
0	118	0.613	0.613	0.000
0	122	0.613	0.613	0.000
3	301	0.614	0.613	0.000
3	306	0.613	0.613	0.000
3	307	0.613	0.613	0.000
3	308	0.613	0.613	0.000
3	309	0.613	0.613	0.000
5	310	0.613	0.613	0.000
5	311	0.613	0.613	0.000
5	312	0.613	0.613	0.000
5	314	0.613	0.613	0.000
5	316	0.612	0.612	0.000
10	317	0.613	0.613	0.000
10	318	0.613	0.613	0.000
10	319	0.613	0.613	0.000
10	321	0.613	0.613	0.000
10	322	0.613	0.613	0.000
15	323	0.614	0.613	0.000
15	325	0.613	0.612	0.000
15	326	0.613	0.613	0.000
15	328	0.613	0.613	0.000
15	323	0.613	0.613	0.000
30	334	0.613	0.613	-0.001
30	335	0.613	0.612	-0.001
30	337	0.613	0.613	0.000
30	339	0.613	0.613	0.000
30	341	0.613	0.612	0.000
35	342	0.612	0.612	0.000
35	345	0.613	0.612	-0.001
35	346	0.614	0.614	-0.001
35	347	0.613	0.613	0.000
35	348	0.613	0.612	-0.001
50	349	0.613	0.612	-0.001
50	350	0.613	0.612	-0.001
50	351	0.613	0.612	-0.001
50	353	0.613	0.612	-0.001
50	354	0.613	0.613	-0.001
55	355	0.613	0.612	-0.001
55	357	0.613	0.612	-0.001
55	359	0.613	0.612	-0.001
55	362	0.613	0.613	-0.001
55	363	0.613	0.613	0.000
100	1	0.613	0.611	-0.002
100	3	0.614	0.612	-0.002
100	4	0.613	0.612	-0.001
100	5	0.613	0.611	-0.002
100	6	0.614	0.613	-0.001
100	7	0.613	0.612	-0.001
100	8	0.613	0.612	-0.002
100	9	0.613	0.611	-0.002
100	10	0.613	0.611	-0.002
100	11	0.613	0.612	-0.002
100	12	0.613	0.612	-0.001
100	13	0.613	0.610	-0.002
100	14	0.613	0.612	-0.001
100	15	0.614	0.612	-0.002
100	16	0.614	0.612	-0.001
100	17	0.613	0.611	-0.002
100	18	0.613	0.612	-0.001
100	19	0.613	0.612	-0.001
100	20	0.613	0.611	-0.002
100	21	0.613	0.611	-0.002
100	22	0.613	0.612	-0.002
100	24	0.613	0.611	-0.002
105	25	0.613	0.612	-0.001
105	26	0.613	0.612	-0.001
105	27	0.613	0.611	-0.002
105	28	0.614	0.612	-0.001
105	29	0.613	0.611	-0.002
Max		0.614	0.614	0.000
Average		0.613	0.612	-0.001
Min		0.612	0.610	-0.002
Std Dev		0.000	0.001	0.001



8.3 __VREF_12V	
Test Site	
Tester	
Test Number	
Max Limit	0.615
Min Limit	0.609

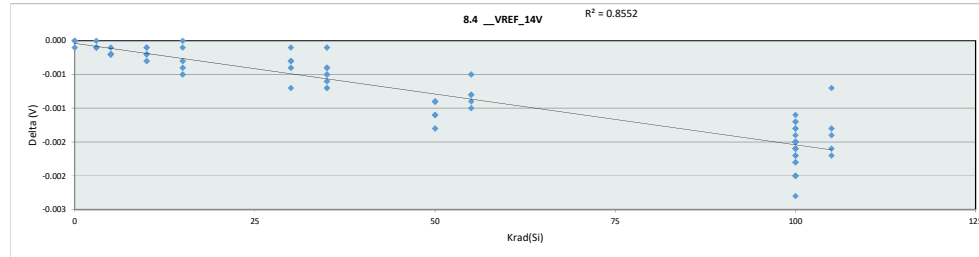
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609
Min	0.613	0.613	0.612	0.613	0.612	0.612	0.612	0.612	0.612	0.611	0.611
Average	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.612
Max	0.613	0.613	0.613	0.613	0.613	0.613	0.614	0.613	0.613	0.613	0.612
UL	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615



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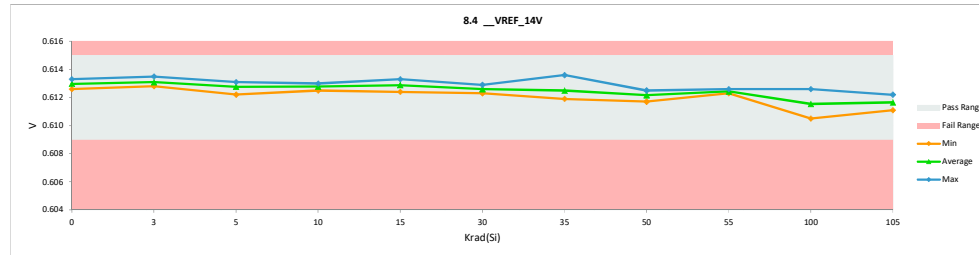
8.4 __VREF_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.615
Min Limit	0.609

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	0.613	0.613	0.000
0	118	0.613	0.613	0.000
0	122	0.613	0.613	0.000
3	301	0.614	0.613	0.000
3	306	0.613	0.613	0.000
3	307	0.613	0.613	0.000
3	308	0.613	0.613	0.000
3	309	0.613	0.613	0.000
5	310	0.613	0.613	0.000
5	311	0.613	0.613	0.000
5	312	0.613	0.613	0.000
5	314	0.613	0.613	0.000
5	316	0.612	0.612	0.000
10	317	0.613	0.613	0.000
10	318	0.613	0.613	0.000
10	319	0.613	0.613	0.000
10	321	0.613	0.613	0.000
10	322	0.613	0.613	0.000
15	323	0.614	0.613	0.000
15	325	0.613	0.612	0.000
15	326	0.613	0.613	0.000
15	328	0.613	0.613	0.000
15	333	0.613	0.613	0.000
30	334	0.613	0.613	0.000
30	335	0.613	0.612	-0.001
30	337	0.613	0.613	0.000
30	339	0.613	0.613	0.000
30	341	0.613	0.612	0.000
35	342	0.612	0.612	0.000
35	345	0.613	0.612	-0.001
35	346	0.614	0.614	-0.001
35	347	0.613	0.613	0.000
35	348	0.613	0.612	-0.001
50	349	0.613	0.612	-0.001
50	350	0.613	0.612	-0.001
50	351	0.613	0.612	-0.001
50	353	0.613	0.612	-0.001
50	354	0.613	0.613	-0.001
55	355	0.613	0.612	-0.001
55	357	0.613	0.612	-0.001
55	359	0.613	0.612	-0.001
55	362	0.613	0.613	-0.001
55	363	0.613	0.613	0.000
100	1	0.613	0.611	-0.002
100	3	0.614	0.612	-0.002
100	4	0.613	0.612	-0.002
100	5	0.613	0.611	-0.002
100	6	0.614	0.613	-0.001
100	7	0.613	0.612	-0.001
100	8	0.613	0.612	-0.002
100	9	0.613	0.611	-0.002
100	10	0.613	0.611	-0.002
100	11	0.613	0.612	-0.002
100	12	0.613	0.612	-0.001
100	13	0.613	0.610	-0.002
100	14	0.613	0.612	-0.001
100	15	0.614	0.612	-0.002
100	16	0.614	0.612	-0.001
100	17	0.613	0.611	-0.002
100	18	0.613	0.612	-0.001
100	19	0.613	0.612	-0.001
100	20	0.613	0.611	-0.002
100	21	0.613	0.611	-0.002
100	22	0.613	0.612	-0.002
100	24	0.613	0.611	-0.002
105	25	0.613	0.612	-0.001
105	26	0.613	0.612	-0.001
105	27	0.613	0.611	-0.002
105	28	0.614	0.612	-0.001
105	29	0.613	0.611	-0.002
Max		0.614	0.614	0.000
Average		0.613	0.612	-0.001
Min		0.612	0.610	-0.002
Std Dev		0.000	0.001	0.001



8.4 VREF_14V	
Test Site	
Tester	
Test Number	
Max Limit	0.615
Min Limit	0.609

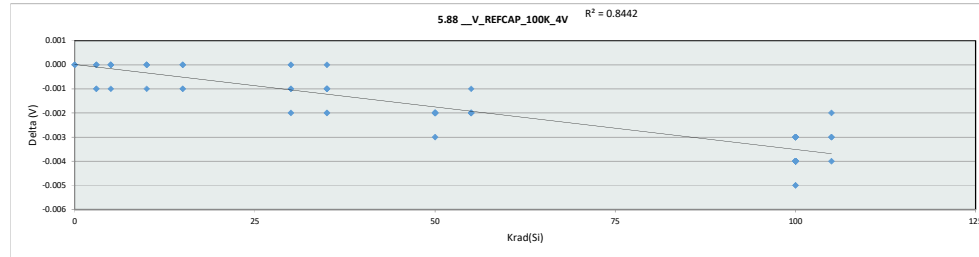
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609
Min	0.613	0.613	0.612	0.613	0.612	0.612	0.612	0.612	0.612	0.611	0.611
Average	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.612
Max	0.613	0.614	0.613	0.613	0.613	0.613	0.614	0.613	0.613	0.613	0.612
UL	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615



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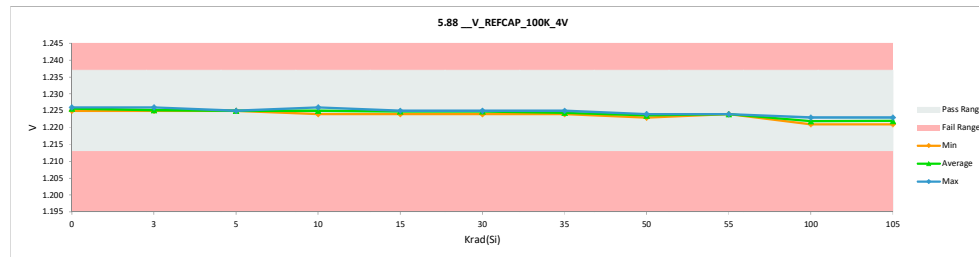
5.88 _V_REFCAP_100K_4V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	1.237 1.237
Min Limit	1.213 1.213

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	301	1.226	1.226	0.000
3	306	1.226	1.225	-0.001
3	307	1.225	1.225	0.000
3	308	1.225	1.225	0.000
3	309	1.225	1.225	-0.001
5	310	1.225	1.225	0.000
5	311	1.226	1.225	-0.001
5	312	1.225	1.225	0.000
5	314	1.225	1.225	0.000
5	316	1.225	1.225	0.000
10	317	1.226	1.226	0.000
10	318	1.225	1.225	0.000
10	319	1.225	1.225	0.000
10	321	1.225	1.225	0.000
10	322	1.225	1.224	-0.001
15	323	1.226	1.225	-0.001
15	325	1.225	1.224	-0.001
15	326	1.225	1.225	0.000
15	328	1.225	1.225	0.000
15	323	1.225	1.225	0.000
30	334	1.226	1.225	-0.001
30	335	1.226	1.224	-0.002
30	337	1.225	1.225	0.000
30	339	1.226	1.225	-0.001
30	341	1.225	1.225	0.000
35	342	1.225	1.224	-0.001
35	345	1.225	1.224	-0.001
35	346	1.226	1.225	-0.001
35	347	1.225	1.225	0.000
35	348	1.226	1.224	-0.002
50	349	1.226	1.224	-0.002
50	350	1.226	1.224	-0.002
50	351	1.226	1.223	-0.003
50	353	1.225	1.223	-0.002
50	354	1.226	1.224	-0.002
55	355	1.226	1.224	-0.002
55	357	1.226	1.224	-0.002
55	359	1.226	1.224	-0.002
55	362	1.226	1.224	-0.002
55	363	1.225	1.224	-0.001
100	1	1.225	1.221	-0.004
100	3	1.226	1.222	-0.004
100	4	1.226	1.223	-0.003
100	5	1.226	1.222	-0.004
100	6	1.226	1.223	-0.003
100	7	1.225	1.222	-0.003
100	8	1.225	1.222	-0.003
100	9	1.225	1.222	-0.003
100	10	1.226	1.221	-0.005
100	11	1.226	1.222	-0.004
100	12	1.226	1.222	-0.004
100	13	1.226	1.221	-0.005
100	14	1.225	1.222	-0.003
100	15	1.226	1.222	-0.004
100	16	1.226	1.222	-0.004
100	17	1.225	1.221	-0.004
100	18	1.225	1.222	-0.003
100	19	1.226	1.223	-0.003
100	20	1.225	1.221	-0.004
100	21	1.226	1.223	-0.003
100	22	1.226	1.222	-0.004
100	24	1.225	1.221	-0.004
105	25	1.225	1.223	-0.002
105	26	1.225	1.222	-0.003
105	27	1.225	1.221	-0.004
105	28	1.225	1.222	-0.003
105	29	1.225	1.222	-0.003
	Max	1.226	1.226	0.000
	Average	1.225	1.224	-0.002
	Min	1.225	1.221	-0.005
	Std Dev	0.001	0.002	0.002



5.88 _V_REFCAP_100K_4V	
Test Site	
Tester	
Test Number	
Max Limit	1.237 V
Min Limit	1.213 V

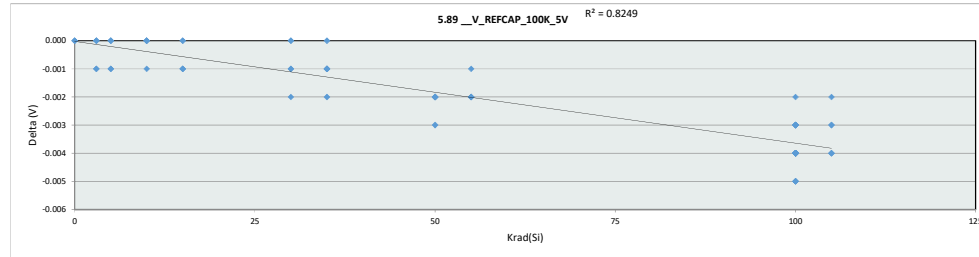
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.224	1.224	1.224	1.224	1.223	1.224	1.221	1.221
Average	1.226	1.225	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.222	1.222
Max	1.226	1.226	1.225	1.226	1.225	1.225	1.225	1.224	1.224	1.223	1.223
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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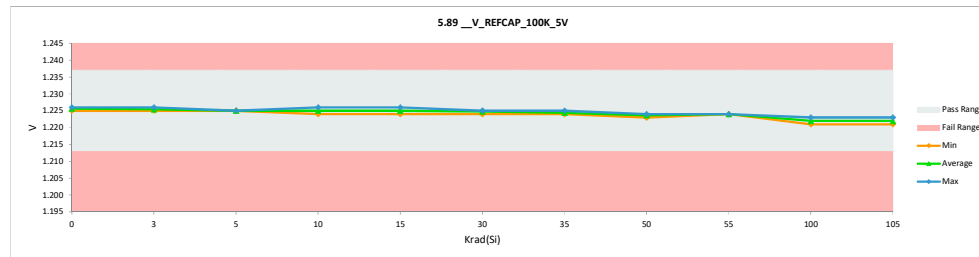
5.89 _V_REFCAP_100K_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	301	1.226	1.226	0.000
3	306	1.226	1.226	0.000
3	307	1.225	1.225	0.000
3	308	1.226	1.225	-0.001
3	309	1.226	1.225	-0.001
5	310	1.225	1.225	0.000
5	311	1.226	1.225	-0.001
5	312	1.225	1.225	0.000
5	314	1.226	1.225	-0.001
5	316	1.225	1.225	0.000
10	317	1.226	1.226	0.000
10	318	1.225	1.225	0.000
10	319	1.225	1.225	0.000
10	321	1.225	1.225	0.000
10	322	1.225	1.224	-0.001
15	323	1.226	1.225	-0.001
15	325	1.225	1.224	-0.001
15	326	1.226	1.226	0.000
15	328	1.226	1.225	-0.001
15	333	1.225	1.225	0.000
30	334	1.226	1.225	-0.001
30	335	1.226	1.224	-0.002
30	337	1.225	1.225	0.000
30	339	1.226	1.225	-0.001
30	341	1.225	1.225	0.000
35	342	1.225	1.224	-0.001
35	345	1.225	1.224	-0.001
35	346	1.226	1.225	-0.001
35	347	1.225	1.225	0.000
35	348	1.226	1.224	-0.002
50	349	1.226	1.224	-0.002
50	350	1.226	1.224	-0.002
50	351	1.226	1.223	-0.003
50	353	1.225	1.223	-0.002
50	354	1.226	1.224	-0.002
55	355	1.226	1.224	-0.002
55	357	1.226	1.224	-0.002
55	359	1.226	1.224	-0.002
55	362	1.226	1.224	-0.002
55	363	1.225	1.224	-0.001
100	1	1.225	1.221	-0.004
100	3	1.226	1.222	-0.004
100	4	1.226	1.223	-0.003
100	5	1.226	1.222	-0.004
100	6	1.226	1.223	-0.003
100	7	1.226	1.222	-0.004
100	8	1.226	1.222	-0.004
100	9	1.225	1.222	-0.003
100	10	1.226	1.221	-0.005
100	11	1.226	1.222	-0.004
100	12	1.226	1.222	-0.004
100	13	1.226	1.221	-0.005
100	14	1.226	1.222	-0.004
100	15	1.226	1.222	-0.004
100	16	1.226	1.222	-0.004
100	17	1.226	1.221	-0.005
100	18	1.225	1.223	-0.002
100	19	1.226	1.223	-0.003
100	20	1.225	1.222	-0.003
100	21	1.226	1.223	-0.003
100	22	1.226	1.222	-0.004
100	24	1.226	1.221	-0.005
105	25	1.225	1.223	-0.002
105	26	1.226	1.222	-0.004
105	27	1.225	1.221	-0.004
105	28	1.225	1.222	-0.003
105	29	1.225	1.222	-0.003
Max		1.226	1.226	0.000
Average		1.226	1.224	-0.002
Min		1.225	1.221	-0.005
Std Dev		0.000	0.002	



5.89 _V_REFCAP_100K_5V	
Test Site	
Tester	
Test Number	
Max Limit	1.237
Min Limit	1.213

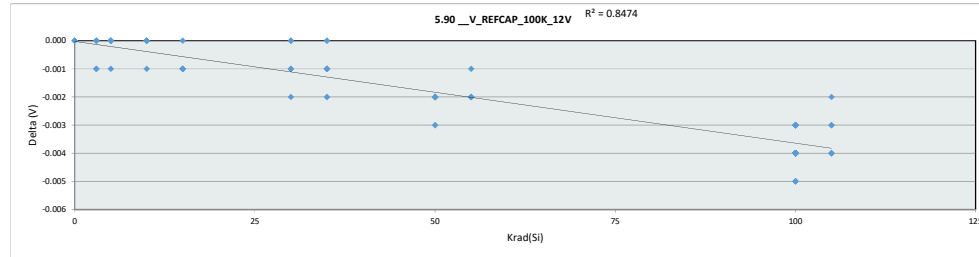
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.224	1.224	1.224	1.224	1.223	1.224	1.221	1.221
Average	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.222	1.222
Max	1.226	1.226	1.225	1.226	1.226	1.225	1.225	1.224	1.224	1.223	1.223
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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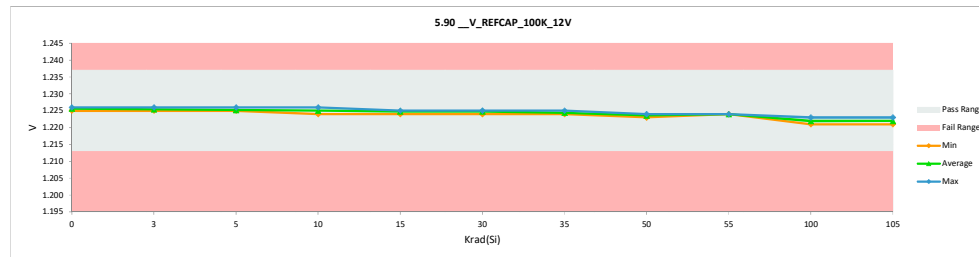
5.90 __V_REFCAP_100K_12V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	1.237 1.237
Min Limit	1.213 1.213

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	301	1.226	1.226	0.000
3	306	1.226	1.226	0.000
3	307	1.225	1.225	0.000
3	308	1.226	1.225	-0.001
3	309	1.226	1.225	-0.001
5	310	1.225	1.225	0.000
5	311	1.226	1.226	0.000
5	312	1.225	1.225	0.000
5	314	1.225	1.225	0.000
5	316	1.226	1.225	-0.001
10	317	1.226	1.226	0.000
10	318	1.225	1.225	0.000
10	319	1.225	1.225	0.000
10	321	1.225	1.225	0.000
10	322	1.225	1.224	-0.001
15	323	1.226	1.225	-0.001
15	325	1.225	1.224	-0.001
15	326	1.226	1.225	-0.001
15	328	1.226	1.225	-0.001
15	323	1.225	1.225	0.000
30	334	1.226	1.225	-0.001
30	335	1.226	1.224	-0.002
30	337	1.225	1.225	0.000
30	339	1.226	1.225	-0.001
30	341	1.225	1.225	0.000
35	342	1.225	1.224	-0.001
35	345	1.225	1.224	-0.001
35	346	1.226	1.225	-0.001
35	347	1.225	1.225	0.000
35	348	1.226	1.224	-0.002
50	349	1.226	1.224	-0.002
50	350	1.226	1.224	-0.002
50	351	1.226	1.223	-0.003
50	353	1.225	1.223	-0.002
50	354	1.226	1.224	-0.002
55	355	1.226	1.224	-0.002
55	357	1.226	1.224	-0.002
55	359	1.226	1.224	-0.002
55	362	1.226	1.224	-0.002
55	363	1.225	1.224	-0.001
100	1	1.225	1.221	-0.004
100	3	1.226	1.222	-0.004
100	4	1.226	1.223	-0.003
100	5	1.226	1.222	-0.004
100	6	1.226	1.223	-0.003
100	7	1.226	1.222	-0.004
100	8	1.226	1.222	-0.004
100	9	1.225	1.222	-0.003
100	10	1.226	1.221	-0.005
100	11	1.226	1.222	-0.004
100	12	1.226	1.222	-0.004
100	13	1.226	1.221	-0.005
100	14	1.225	1.222	-0.003
100	15	1.226	1.222	-0.004
100	16	1.226	1.222	-0.004
100	17	1.225	1.221	-0.004
100	18	1.226	1.222	-0.004
100	19	1.226	1.223	-0.003
100	20	1.225	1.222	-0.003
100	21	1.226	1.223	-0.003
100	22	1.226	1.222	-0.004
100	24	1.226	1.221	-0.005
105	25	1.225	1.223	-0.002
105	26	1.226	1.222	-0.004
105	27	1.225	1.221	-0.004
105	28	1.225	1.222	-0.003
105	29	1.225	1.222	-0.003
	Max	1.226	1.226	0.000
	Average	1.226	1.224	-0.002
	Min	1.225	1.221	-0.005
	Std Dev	0.000	0.002	0.002



5.90 __V_REFCAP_100K_12V	
Test Site	
Tester	
Test Number	
Max Limit	1.237 V
Min Limit	1.213 V

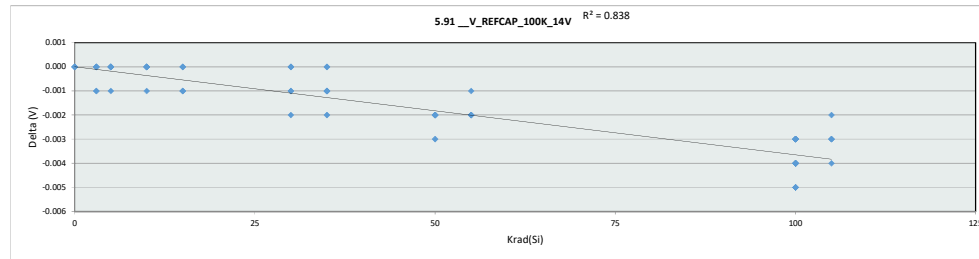
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.224	1.224	1.224	1.224	1.223	1.224	1.221	1.221
Average	1.226	1.225	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.222	1.222
Max	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.223	1.223
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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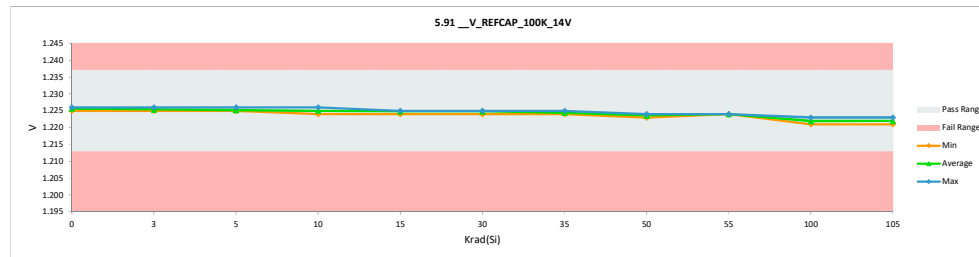
5.91 __V_REFCAP_100K_14V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	1.237 1.237
Min Limit	1.213 1.213

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	301	1.226	1.226	0.000
3	306	1.226	1.226	0.000
3	307	1.225	1.225	0.000
3	308	1.226	1.225	-0.001
3	309	1.226	1.225	-0.001
5	310	1.225	1.225	0.000
5	311	1.226	1.226	0.000
5	312	1.225	1.225	0.000
5	314	1.226	1.225	-0.001
5	316	1.225	1.225	0.000
10	317	1.226	1.226	0.000
10	318	1.225	1.225	0.000
10	319	1.225	1.225	0.000
10	321	1.225	1.225	0.000
10	322	1.225	1.224	-0.001
15	323	1.226	1.225	-0.001
15	325	1.225	1.224	-0.001
15	326	1.226	1.225	-0.001
15	328	1.225	1.225	0.000
15	323	1.225	1.225	0.000
30	334	1.226	1.225	-0.001
30	335	1.226	1.224	-0.002
30	337	1.225	1.225	0.000
30	339	1.226	1.225	-0.001
30	341	1.225	1.225	0.000
35	342	1.225	1.224	-0.001
35	345	1.225	1.224	-0.001
35	346	1.226	1.225	-0.001
35	347	1.225	1.225	0.000
35	348	1.226	1.224	-0.002
50	349	1.226	1.224	-0.002
50	350	1.226	1.224	-0.002
50	351	1.226	1.223	-0.003
50	353	1.225	1.223	-0.002
50	354	1.226	1.224	-0.002
55	355	1.226	1.224	-0.002
55	357	1.226	1.224	-0.002
55	359	1.226	1.224	-0.002
55	362	1.226	1.224	-0.002
55	363	1.225	1.224	-0.001
100	1	1.225	1.221	-0.004
100	3	1.226	1.222	-0.004
100	4	1.226	1.223	-0.003
100	5	1.226	1.222	-0.004
100	6	1.226	1.223	-0.003
100	7	1.226	1.222	-0.004
100	8	1.226	1.222	-0.004
100	9	1.225	1.222	-0.003
100	10	1.226	1.221	-0.005
100	11	1.226	1.222	-0.004
100	12	1.226	1.222	-0.004
100	13	1.226	1.221	-0.005
100	14	1.226	1.222	-0.004
100	15	1.226	1.222	-0.004
100	16	1.226	1.222	-0.004
100	17	1.226	1.221	-0.005
100	18	1.225	1.222	-0.003
100	19	1.226	1.223	-0.003
100	20	1.225	1.222	-0.003
100	21	1.226	1.223	-0.003
100	22	1.226	1.222	-0.004
100	24	1.226	1.221	-0.005
105	25	1.225	1.223	-0.002
105	26	1.225	1.222	-0.003
105	27	1.225	1.221	-0.004
105	28	1.225	1.222	-0.003
105	29	1.225	1.222	-0.003
Max		1.226	1.226	0.000
Average		1.226	1.224	-0.002
Min		1.225	1.221	-0.005
Std Dev		0.000	0.002	



5.91 __V_REFCAP_100K_14V	
Test Site	
Tester	
Test Number	
Max Limit	1.237 V
Min Limit	1.213 V

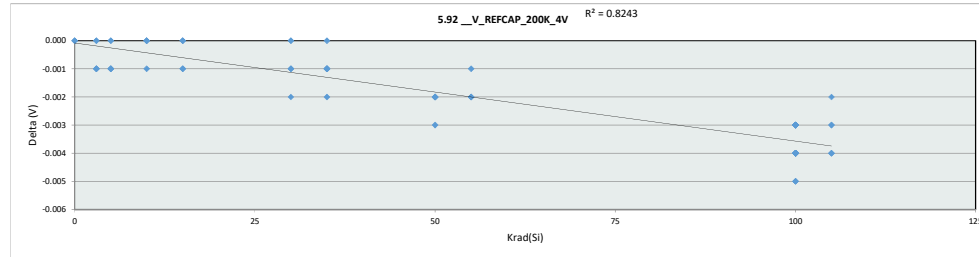
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.224	1.224	1.224	1.224	1.223	1.224	1.221	1.221
Average	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.222	1.222
Max	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.223	1.223
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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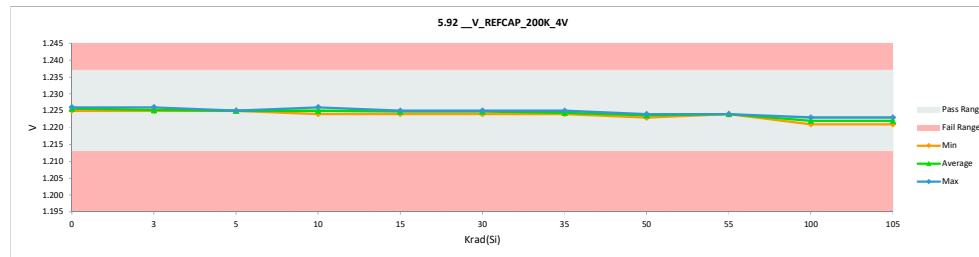
5.92 _V_REFCAP_200K_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	301	1.226	1.226	0.000
3	306	1.226	1.225	-0.001
3	307	1.225	1.225	0.000
3	308	1.226	1.225	-0.001
3	309	1.226	1.225	-0.001
5	310	1.225	1.225	0.000
5	311	1.226	1.225	-0.001
5	312	1.225	1.225	0.000
5	314	1.226	1.225	-0.001
5	316	1.226	1.225	-0.001
10	317	1.226	1.226	0.000
10	318	1.225	1.225	0.000
10	319	1.225	1.225	0.000
10	321	1.225	1.225	0.000
10	322	1.225	1.224	-0.001
15	323	1.226	1.225	-0.001
15	325	1.225	1.224	-0.001
15	326	1.225	1.225	0.000
15	328	1.225	1.225	0.000
15	323	1.225	1.225	0.000
30	334	1.226	1.225	-0.001
30	335	1.226	1.224	-0.002
30	337	1.225	1.225	0.000
30	339	1.226	1.225	-0.001
30	341	1.225	1.225	0.000
35	342	1.225	1.224	-0.001
35	345	1.225	1.224	-0.001
35	346	1.226	1.225	-0.001
35	347	1.225	1.225	0.000
35	348	1.226	1.224	-0.002
50	349	1.226	1.224	-0.002
50	350	1.226	1.224	-0.002
50	351	1.226	1.223	-0.003
50	353	1.225	1.223	-0.002
50	354	1.226	1.224	-0.002
55	355	1.226	1.224	-0.002
55	357	1.226	1.224	-0.002
55	359	1.226	1.224	-0.002
55	362	1.226	1.224	-0.002
55	363	1.225	1.224	-0.001
100	1	1.225	1.221	-0.004
100	3	1.226	1.222	-0.004
100	4	1.226	1.223	-0.003
100	5	1.226	1.222	-0.004
100	6	1.226	1.223	-0.003
100	7	1.225	1.222	-0.003
100	8	1.226	1.222	-0.004
100	9	1.226	1.222	-0.004
100	10	1.226	1.221	-0.005
100	11	1.226	1.222	-0.004
100	12	1.226	1.223	-0.003
100	13	1.226	1.221	-0.005
100	14	1.225	1.222	-0.003
100	15	1.226	1.222	-0.004
100	16	1.226	1.222	-0.004
100	17	1.225	1.221	-0.004
100	18	1.225	1.222	-0.003
100	19	1.226	1.223	-0.003
100	20	1.225	1.222	-0.003
100	21	1.226	1.223	-0.003
100	22	1.226	1.222	-0.004
100	24	1.226	1.221	-0.005
105	25	1.225	1.223	-0.002
105	26	1.226	1.222	-0.004
105	27	1.225	1.221	-0.004
105	28	1.225	1.222	-0.003
105	29	1.225	1.222	-0.003
	Max	1.226	1.226	0.000
	Average	1.226	1.224	-0.002
	Min	1.225	1.221	-0.005
	Std Dev	0.000	0.001	0.002



5.92 _V_REFCAP_200K_4V	
Test Site	
Tester	
Test Number	
Max Limit	1.237
Min Limit	1.213

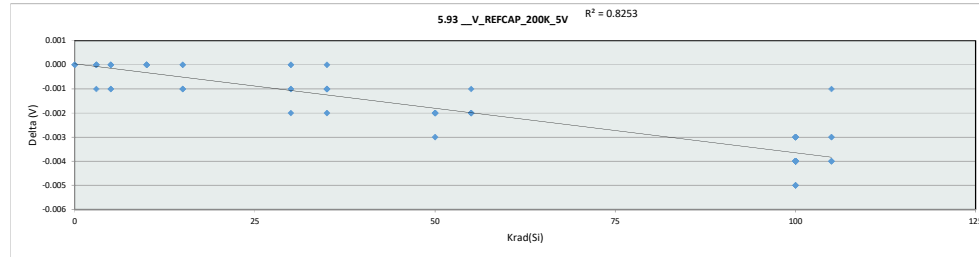
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.224	1.224	1.224	1.224	1.223	1.224	1.221	1.221
Average	1.226	1.225	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.222	1.222
Max	1.226	1.226	1.225	1.226	1.225	1.225	1.225	1.224	1.224	1.223	1.223
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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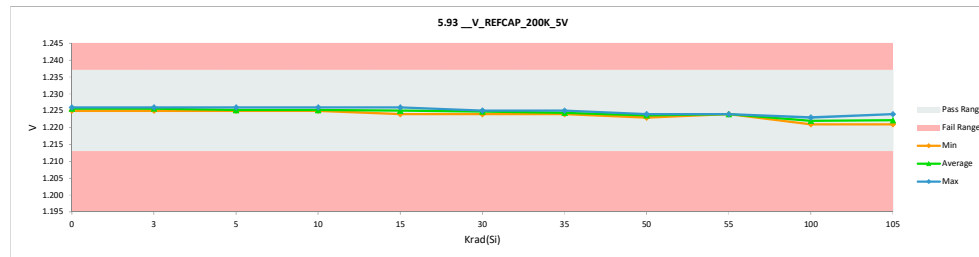
5.93 _V_REFCAP_200K_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	301	1.226	1.226	0.000
3	306	1.226	1.226	0.000
3	307	1.225	1.225	0.000
3	308	1.226	1.225	-0.001
3	309	1.226	1.226	0.000
5	310	1.225	1.225	0.000
5	311	1.226	1.226	0.000
5	312	1.225	1.225	0.000
5	314	1.226	1.225	-0.001
5	316	1.226	1.225	-0.001
10	317	1.226	1.226	0.000
10	318	1.225	1.225	0.000
10	319	1.225	1.225	0.000
10	321	1.225	1.225	0.000
10	322	1.225	1.225	0.000
15	323	1.226	1.225	-0.001
15	325	1.225	1.224	-0.001
15	326	1.226	1.226	0.000
15	328	1.226	1.225	-0.001
15	333	1.225	1.225	0.000
30	334	1.226	1.225	-0.001
30	335	1.226	1.224	-0.002
30	337	1.225	1.225	0.000
30	339	1.226	1.225	-0.001
30	341	1.225	1.225	0.000
35	342	1.225	1.224	-0.001
35	345	1.225	1.224	-0.001
35	346	1.226	1.225	-0.001
35	347	1.225	1.225	0.000
35	348	1.226	1.224	-0.002
50	349	1.226	1.224	-0.002
50	350	1.226	1.224	-0.002
50	351	1.226	1.223	-0.003
50	353	1.225	1.223	-0.002
50	354	1.226	1.224	-0.002
55	355	1.226	1.224	-0.002
55	357	1.226	1.224	-0.002
55	359	1.226	1.224	-0.002
55	362	1.226	1.224	-0.002
55	363	1.225	1.224	-0.001
100	1	1.225	1.221	-0.004
100	3	1.226	1.222	-0.004
100	4	1.226	1.223	-0.003
100	5	1.226	1.222	-0.004
100	6	1.226	1.223	-0.003
100	7	1.226	1.222	-0.004
100	8	1.226	1.222	-0.004
100	9	1.226	1.222	-0.004
100	10	1.226	1.221	-0.005
100	11	1.226	1.222	-0.004
100	12	1.226	1.223	-0.003
100	13	1.226	1.221	-0.005
100	14	1.226	1.222	-0.004
100	15	1.226	1.222	-0.004
100	16	1.226	1.222	-0.004
100	17	1.226	1.221	-0.005
100	18	1.226	1.223	-0.003
100	19	1.226	1.223	-0.003
100	20	1.225	1.222	-0.003
100	21	1.226	1.223	-0.003
100	22	1.226	1.222	-0.004
100	24	1.226	1.221	-0.005
105	25	1.225	1.224	-0.001
105	26	1.226	1.222	-0.004
105	27	1.225	1.221	-0.004
105	28	1.225	1.222	-0.003
105	29	1.225	1.222	-0.003
	Max	1.226	1.226	0.000
	Average	1.226	1.224	-0.002
	Min	1.225	1.221	-0.005
	Std Dev	0.000	0.002	



5.93 _V_REFCAP_200K_5V	
Test Site	
Tester	
Test Number	
Max Limit	1.237
Min Limit	1.213

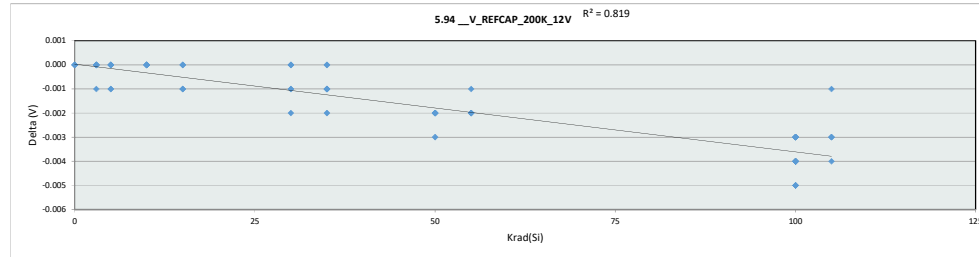
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.224	1.221	1.221
Average	1.226	1.226	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.222	1.222
Max	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.223	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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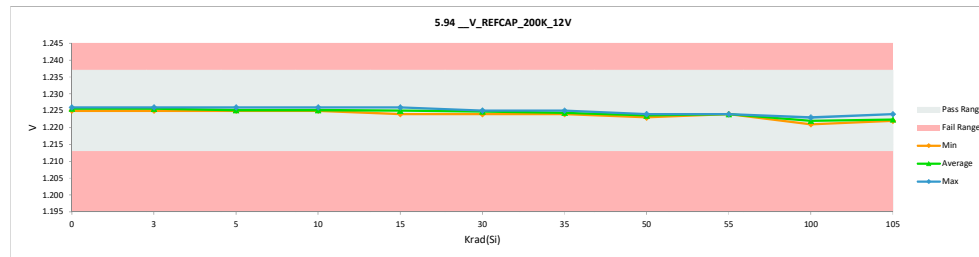
5.94 __V_REFCAP_200K_12V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	1.237 1.237
Min Limit	1.213 1.213

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	301	1.226	1.226	0.000
3	306	1.226	1.226	0.000
3	307	1.225	1.225	0.000
3	308	1.226	1.225	-0.001
3	309	1.226	1.226	0.000
5	310	1.225	1.225	0.000
5	311	1.226	1.226	0.000
5	312	1.225	1.225	0.000
5	314	1.226	1.225	-0.001
5	316	1.226	1.225	-0.001
10	317	1.226	1.226	0.000
10	318	1.225	1.225	0.000
10	319	1.225	1.225	0.000
10	321	1.225	1.225	0.000
10	322	1.225	1.225	0.000
15	323	1.226	1.225	-0.001
15	325	1.225	1.224	-0.001
15	326	1.226	1.226	0.000
15	328	1.226	1.225	-0.001
15	323	1.225	1.225	0.000
30	334	1.226	1.225	-0.001
30	335	1.226	1.224	-0.002
30	337	1.225	1.225	0.000
30	339	1.226	1.225	-0.001
30	341	1.225	1.225	0.000
35	342	1.225	1.224	-0.001
35	345	1.225	1.224	-0.001
35	346	1.226	1.225	-0.001
35	347	1.225	1.225	0.000
35	348	1.226	1.224	-0.002
50	349	1.226	1.224	-0.002
50	350	1.226	1.224	-0.002
50	351	1.226	1.223	-0.003
50	353	1.225	1.223	-0.002
50	354	1.226	1.224	-0.002
55	355	1.226	1.224	-0.002
55	357	1.226	1.224	-0.002
55	359	1.226	1.224	-0.002
55	362	1.226	1.224	-0.002
55	363	1.225	1.224	-0.001
100	1	1.225	1.221	-0.004
100	3	1.226	1.222	-0.004
100	4	1.226	1.223	-0.003
100	5	1.226	1.222	-0.004
100	6	1.226	1.223	-0.003
100	7	1.226	1.222	-0.004
100	8	1.226	1.222	-0.004
100	9	1.226	1.222	-0.004
100	10	1.226	1.221	-0.005
100	11	1.226	1.222	-0.004
100	12	1.226	1.223	-0.003
100	13	1.226	1.221	-0.005
100	14	1.226	1.222	-0.004
100	15	1.226	1.222	-0.004
100	16	1.226	1.222	-0.004
100	17	1.226	1.221	-0.005
100	18	1.226	1.223	-0.003
100	19	1.226	1.223	-0.003
100	20	1.225	1.222	-0.003
100	21	1.226	1.223	-0.003
100	22	1.226	1.222	-0.004
100	24	1.226	1.221	-0.005
105	25	1.225	1.224	-0.001
105	26	1.226	1.222	-0.004
105	27	1.225	1.222	-0.003
105	28	1.225	1.222	-0.003
105	29	1.225	1.222	-0.003
	Max	1.226	1.226	0.000
	Average	1.226	1.224	-0.002
	Min	1.225	1.221	-0.005
	Std Dev	0.000	0.002	0.002



5.94 __V_REFCAP_200K_12V	
Test Site	
Tester	
Test Number	
Max Limit	1.237 V
Min Limit	1.213 V

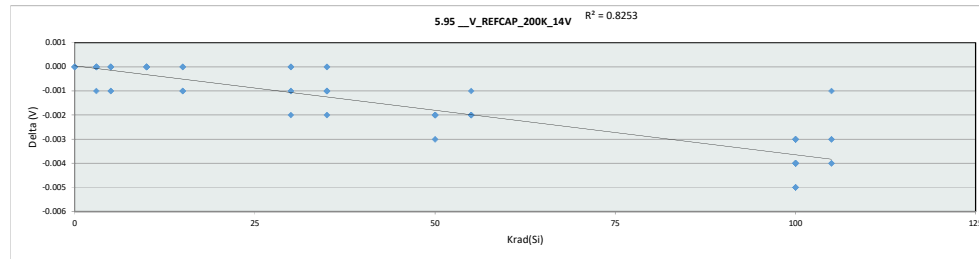
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.223	1.224	1.221	1.222
Average	1.226	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.222	1.222
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.224	1.224	1.223	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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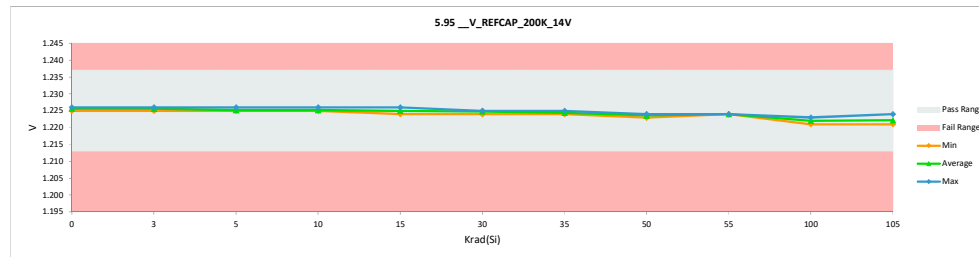
5.95_V_REFCAP_200K_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	301	1.226	1.226	0.000
3	306	1.226	1.226	0.000
3	307	1.225	1.225	0.000
3	308	1.226	1.225	-0.001
3	309	1.226	1.226	0.000
5	310	1.225	1.225	0.000
5	311	1.226	1.226	0.000
5	312	1.225	1.225	0.000
5	314	1.226	1.225	-0.001
5	316	1.226	1.225	-0.001
10	317	1.226	1.226	0.000
10	318	1.225	1.225	0.000
10	319	1.225	1.225	0.000
10	321	1.225	1.225	0.000
10	322	1.225	1.225	0.000
15	323	1.226	1.225	-0.001
15	325	1.225	1.224	-0.001
15	326	1.226	1.226	0.000
15	328	1.226	1.225	-0.001
15	323	1.225	1.225	0.000
30	334	1.226	1.225	-0.001
30	335	1.226	1.224	-0.002
30	337	1.225	1.225	0.000
30	339	1.226	1.225	-0.001
30	341	1.225	1.225	0.000
35	342	1.225	1.224	-0.001
35	345	1.225	1.224	-0.001
35	346	1.226	1.225	-0.001
35	347	1.225	1.225	0.000
35	348	1.226	1.224	-0.002
50	349	1.226	1.224	-0.002
50	350	1.226	1.224	-0.002
50	351	1.226	1.223	-0.003
50	353	1.225	1.223	-0.002
50	354	1.226	1.224	-0.002
55	355	1.226	1.224	-0.002
55	357	1.226	1.224	-0.002
55	359	1.226	1.224	-0.002
55	362	1.226	1.224	-0.002
55	363	1.225	1.224	-0.001
100	1	1.225	1.221	-0.004
100	3	1.226	1.222	-0.004
100	4	1.226	1.223	-0.003
100	5	1.226	1.222	-0.004
100	6	1.226	1.223	-0.003
100	7	1.226	1.222	-0.004
100	8	1.226	1.222	-0.004
100	9	1.226	1.222	-0.004
100	10	1.226	1.221	-0.005
100	11	1.226	1.222	-0.004
100	12	1.226	1.223	-0.003
100	13	1.226	1.221	-0.005
100	14	1.226	1.222	-0.004
100	15	1.226	1.222	-0.004
100	16	1.226	1.222	-0.004
100	17	1.226	1.221	-0.005
100	18	1.226	1.223	-0.003
100	19	1.226	1.223	-0.003
100	20	1.225	1.222	-0.003
100	21	1.226	1.223	-0.003
100	22	1.226	1.222	-0.004
100	24	1.226	1.221	-0.005
105	25	1.225	1.224	-0.001
105	26	1.226	1.222	-0.004
105	27	1.225	1.221	-0.004
105	28	1.225	1.222	-0.003
105	29	1.225	1.222	-0.003
Max		1.226	1.226	0.000
Average		1.226	1.224	-0.002
Min		1.225	1.221	-0.005
Std Dev		0.000	0.002	



5.95_V_REFCAP_200K_14V	
Test Site	
Tester	
Test Number	
Max Limit	1.237
Min Limit	1.213

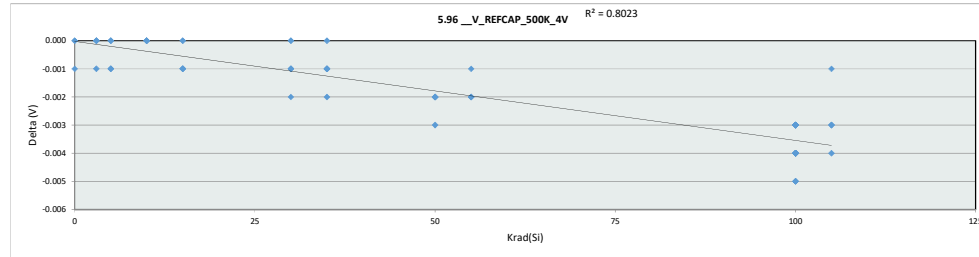
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.224	1.221	1.221	1.221
Average	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.224	1.222	1.222	1.222
Max	1.226	1.226	1.226	1.226	1.226	1.225	1.224	1.224	1.224	1.223	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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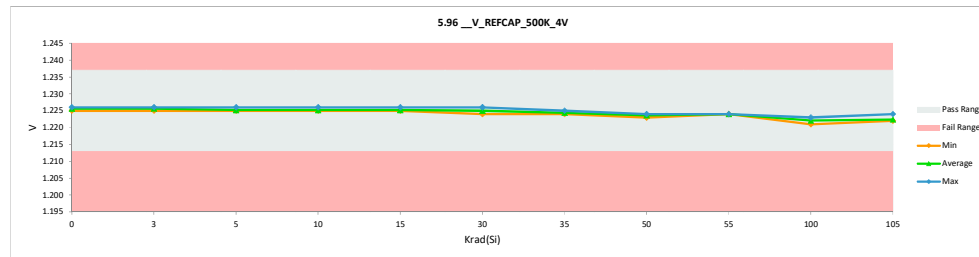
5.96_V_REFCAP_500K_4V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	1.237 1.237
Min Limit	1.213 1.213

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.225	1.225	0.000
0	118	1.227	1.226	-0.001
0	122	1.226	1.226	0.000
3	301	1.226	1.226	0.000
3	306	1.226	1.226	0.000
3	307	1.225	1.225	0.000
3	308	1.226	1.225	-0.001
3	309	1.226	1.226	0.000
5	310	1.225	1.225	0.000
5	311	1.226	1.226	0.000
5	312	1.225	1.225	0.000
5	314	1.226	1.225	-0.001
5	316	1.226	1.225	-0.001
10	317	1.226	1.226	0.000
10	318	1.225	1.225	0.000
10	319	1.225	1.225	0.000
10	321	1.225	1.225	0.000
10	322	1.225	1.225	0.000
15	323	1.226	1.225	-0.001
15	325	1.225	1.225	0.000
15	326	1.226	1.226	0.000
15	328	1.226	1.225	-0.001
15	323	1.226	1.225	-0.001
30	334	1.226	1.225	-0.001
30	335	1.226	1.224	-0.002
30	337	1.226	1.225	-0.001
30	339	1.226	1.226	0.000
30	341	1.225	1.225	0.000
35	342	1.225	1.224	-0.001
35	345	1.225	1.224	-0.001
35	346	1.226	1.225	-0.001
35	347	1.225	1.225	0.000
35	348	1.226	1.224	-0.002
50	349	1.226	1.224	-0.002
50	350	1.226	1.224	-0.002
50	351	1.226	1.223	-0.003
50	353	1.225	1.223	-0.002
50	354	1.226	1.224	-0.002
55	355	1.226	1.224	-0.002
55	357	1.226	1.224	-0.002
55	359	1.226	1.224	-0.002
55	362	1.226	1.224	-0.002
55	363	1.225	1.224	-0.001
100	1	1.225	1.221	-0.004
100	3	1.226	1.222	-0.004
100	4	1.226	1.223	-0.003
100	5	1.226	1.222	-0.004
100	6	1.226	1.223	-0.003
100	7	1.226	1.222	-0.004
100	8	1.226	1.222	-0.004
100	9	1.226	1.222	-0.004
100	10	1.226	1.221	-0.005
100	11	1.226	1.222	-0.004
100	12	1.226	1.223	-0.003
100	13	1.226	1.221	-0.005
100	14	1.226	1.222	-0.004
100	15	1.226	1.223	-0.003
100	16	1.226	1.223	-0.003
100	17	1.226	1.221	-0.005
100	18	1.226	1.223	-0.003
100	19	1.226	1.223	-0.003
100	20	1.225	1.222	-0.003
100	21	1.226	1.223	-0.003
100	22	1.226	1.222	-0.004
100	24	1.226	1.221	-0.005
105	25	1.225	1.224	-0.001
105	26	1.226	1.222	-0.004
105	27	1.225	1.222	-0.003
105	28	1.225	1.221	-0.005
105	29	1.225	1.222	-0.003
	Max	1.227	1.226	0.000
	Average	1.226	1.224	-0.002
	Min	1.225	1.221	-0.005
	Std Dev	0.000	0.002	0.002



5.96_V_REFCAP_500K_4V	
Test Site	
Tester	
Test Number	
Max Limit	1.237 V
Min Limit	1.213 V

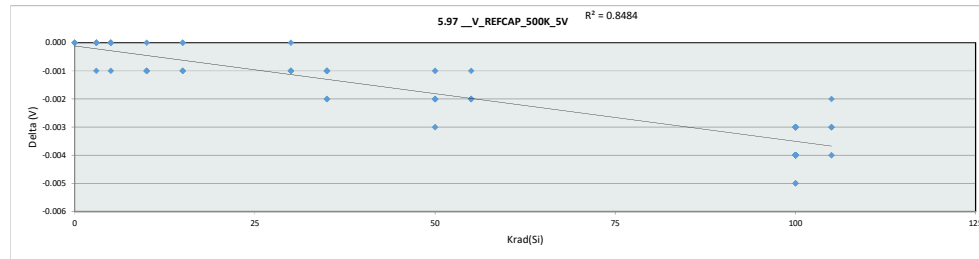
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.224	1.221	1.222
Average	1.226	1.226	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.222	1.222
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.224	1.224	1.223	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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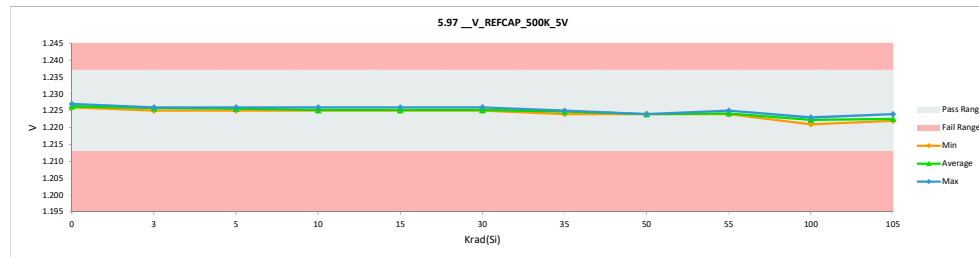
5.97 __V_REFCAP_500K_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.226	1.226	0.000
3	301	1.226	1.226	0.000
3	306	1.226	1.226	0.000
3	307	1.226	1.225	-0.001
3	308	1.226	1.226	0.000
3	309	1.226	1.226	0.000
5	310	1.225	1.225	0.000
5	311	1.226	1.226	0.000
5	312	1.226	1.225	-0.001
5	314	1.226	1.226	0.000
5	316	1.226	1.226	0.000
10	317	1.226	1.226	0.000
10	318	1.226	1.225	-0.001
10	319	1.226	1.225	-0.001
10	321	1.226	1.225	-0.001
10	322	1.226	1.225	-0.001
15	323	1.226	1.225	-0.001
15	325	1.225	1.225	0.000
15	326	1.226	1.226	0.000
15	328	1.226	1.225	-0.001
15	323	1.226	1.225	-0.001
30	334	1.226	1.225	-0.001
30	335	1.226	1.225	-0.001
30	337	1.226	1.225	-0.001
30	339	1.226	1.226	0.000
30	341	1.226	1.225	-0.001
35	342	1.226	1.225	-0.001
35	345	1.226	1.225	-0.001
35	346	1.227	1.225	-0.002
35	347	1.226	1.225	-0.001
35	348	1.226	1.224	-0.002
50	349	1.227	1.224	-0.003
50	350	1.226	1.224	-0.002
50	351	1.226	1.224	-0.002
50	353	1.225	1.224	-0.001
50	354	1.226	1.224	-0.002
55	355	1.226	1.224	-0.002
55	357	1.226	1.224	-0.002
55	359	1.226	1.224	-0.002
55	362	1.227	1.225	-0.002
55	363	1.225	1.224	-0.001
100	1	1.225	1.221	-0.004
100	3	1.226	1.222	-0.004
100	4	1.226	1.223	-0.003
100	5	1.226	1.222	-0.004
100	6	1.226	1.223	-0.003
100	7	1.226	1.222	-0.004
100	8	1.226	1.222	-0.004
100	9	1.226	1.222	-0.004
100	10	1.226	1.222	-0.004
100	11	1.226	1.223	-0.003
100	12	1.226	1.223	-0.003
100	13	1.226	1.222	-0.004
100	14	1.226	1.223	-0.003
100	15	1.226	1.223	-0.003
100	16	1.226	1.223	-0.003
100	17	1.226	1.221	-0.005
100	18	1.226	1.223	-0.003
100	19	1.226	1.223	-0.003
100	20	1.226	1.222	-0.004
100	21	1.227	1.223	-0.004
100	22	1.226	1.222	-0.004
100	24	1.226	1.221	-0.005
105	25	1.226	1.224	-0.002
105	26	1.226	1.223	-0.003
105	27	1.226	1.222	-0.004
105	28	1.225	1.222	-0.003
105	29	1.225	1.222	-0.003
Max		1.227	1.227	0.000
Average		1.226	1.224	-0.002
Min		1.225	1.221	-0.005
Std Dev		0.000	0.002	0.002



5.97 __V_REFCAP_500K_5V	
Test Site	
Tester	
Test Number	
Max Limit	1.237
Min Limit	1.213

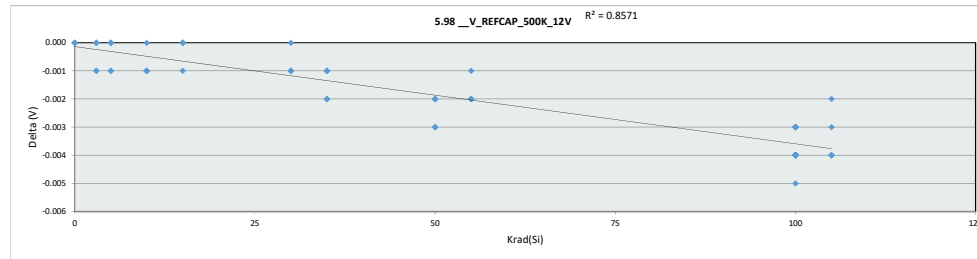
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.221	1.222
Average	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.224	1.224	1.222	1.223
Max	1.227	1.226	1.226	1.226	1.226	1.226	1.225	1.224	1.225	1.223	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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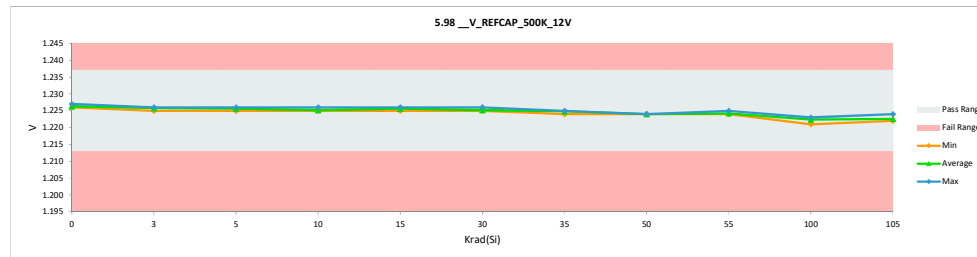
5.98 __V_REFCAP_500K_12V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	1.237 1.237
Min Limit	1.213 1.213

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.226	1.226	0.000
3	301	1.227	1.226	-0.001
3	306	1.226	1.226	0.000
3	307	1.226	1.225	-0.001
3	308	1.226	1.226	0.000
3	309	1.226	1.226	0.000
5	310	1.226	1.225	-0.001
5	311	1.226	1.226	0.000
5	312	1.226	1.225	-0.001
5	314	1.226	1.226	0.000
5	316	1.226	1.226	0.000
10	317	1.226	1.226	0.000
10	318	1.226	1.225	-0.001
10	319	1.226	1.225	-0.001
10	321	1.226	1.225	-0.001
10	322	1.226	1.225	-0.001
15	323	1.226	1.225	-0.001
15	325	1.225	1.225	0.000
15	326	1.226	1.226	0.000
15	328	1.226	1.226	0.000
15	333	1.226	1.226	0.000
30	334	1.226	1.225	-0.001
30	335	1.226	1.225	-0.001
30	337	1.226	1.225	-0.001
30	339	1.226	1.226	0.000
30	341	1.226	1.225	-0.001
35	342	1.226	1.225	-0.001
35	345	1.226	1.225	-0.001
35	346	1.227	1.225	-0.002
35	347	1.226	1.225	-0.001
35	348	1.226	1.224	-0.002
50	349	1.227	1.224	-0.003
50	350	1.226	1.224	-0.002
50	351	1.227	1.224	-0.003
50	353	1.226	1.224	-0.002
50	354	1.226	1.224	-0.002
55	355	1.226	1.224	-0.002
55	357	1.226	1.224	-0.002
55	359	1.226	1.224	-0.002
55	362	1.227	1.225	-0.002
55	363	1.225	1.224	-0.001
100	1	1.225	1.221	-0.004
100	3	1.226	1.222	-0.004
100	4	1.226	1.223	-0.003
100	5	1.226	1.222	-0.004
100	6	1.227	1.223	-0.004
100	7	1.226	1.222	-0.004
100	8	1.226	1.222	-0.004
100	9	1.226	1.223	-0.003
100	10	1.226	1.222	-0.004
100	11	1.226	1.223	-0.003
100	12	1.226	1.223	-0.003
100	13	1.226	1.222	-0.004
100	14	1.226	1.222	-0.004
100	15	1.226	1.223	-0.003
100	16	1.226	1.223	-0.003
100	17	1.226	1.222	-0.004
100	18	1.226	1.223	-0.003
100	19	1.226	1.223	-0.003
100	20	1.226	1.222	-0.004
100	21	1.227	1.223	-0.004
100	22	1.226	1.222	-0.004
100	24	1.226	1.221	-0.005
105	25	1.226	1.224	-0.002
105	26	1.226	1.223	-0.003
105	27	1.226	1.222	-0.004
105	28	1.226	1.222	-0.004
105	29	1.226	1.222	-0.004
	Max	1.227	1.227	0.000
	Average	1.226	1.224	-0.002
	Min	1.225	1.221	-0.005
	Std Dev	0.000	0.002	0.002



5.98 __V_REFCAP_500K_12V	
Test Site	
Tester	
Test Number	
Max Limit	1.237 V
Min Limit	1.213 V

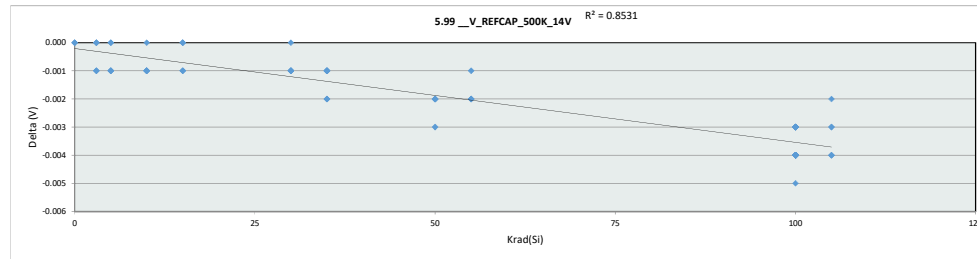
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.221	1.222
Average	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.222	1.223
Max	1.227	1.226	1.226	1.226	1.226	1.226	1.225	1.224	1.225	1.223	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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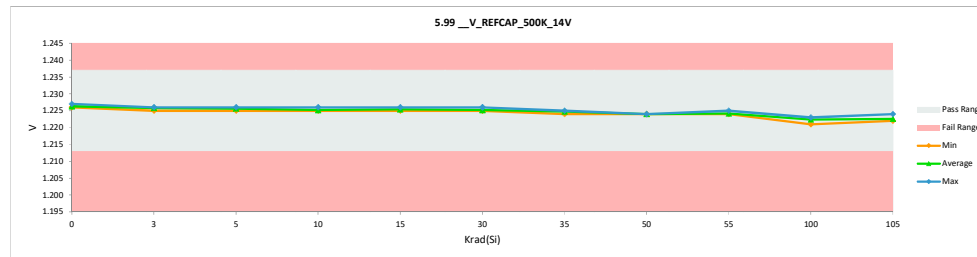
5.99 __V_REFCAP_500K_14V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	1.237 1.237
Min Limit	1.213 1.213

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.226	1.226	0.000
3	301	1.227	1.226	-0.001
3	306	1.226	1.226	0.000
3	307	1.226	1.225	-0.001
3	308	1.226	1.226	0.000
3	309	1.226	1.226	0.000
5	310	1.226	1.225	-0.001
5	311	1.227	1.226	-0.001
5	312	1.226	1.225	-0.001
5	314	1.226	1.226	0.000
5	316	1.226	1.226	0.000
10	317	1.226	1.226	0.000
10	318	1.226	1.225	-0.001
10	319	1.226	1.225	-0.001
10	321	1.226	1.225	-0.001
10	322	1.226	1.225	-0.001
15	323	1.226	1.225	-0.001
15	325	1.225	1.225	0.000
15	326	1.226	1.226	0.000
15	328	1.226	1.225	-0.001
15	333	1.226	1.226	0.000
30	334	1.226	1.225	-0.001
30	335	1.226	1.225	-0.001
30	337	1.226	1.225	-0.001
30	339	1.226	1.226	0.000
30	341	1.226	1.225	-0.001
35	342	1.226	1.225	-0.001
35	345	1.226	1.225	-0.001
35	346	1.227	1.225	-0.002
35	347	1.226	1.225	-0.001
35	348	1.226	1.224	-0.002
50	349	1.227	1.224	-0.003
50	350	1.226	1.224	-0.002
50	351	1.226	1.224	-0.002
50	353	1.226	1.224	-0.002
50	354	1.226	1.224	-0.002
55	355	1.226	1.224	-0.002
55	357	1.226	1.224	-0.002
55	359	1.226	1.224	-0.002
55	362	1.227	1.225	-0.002
55	363	1.225	1.224	-0.001
100	1	1.225	1.221	-0.004
100	3	1.226	1.222	-0.004
100	4	1.226	1.223	-0.003
100	5	1.226	1.222	-0.004
100	6	1.227	1.223	-0.004
100	7	1.226	1.222	-0.004
100	8	1.226	1.222	-0.004
100	9	1.226	1.223	-0.003
100	10	1.226	1.222	-0.004
100	11	1.226	1.223	-0.003
100	12	1.226	1.223	-0.003
100	13	1.226	1.222	-0.004
100	14	1.226	1.222	-0.004
100	15	1.226	1.223	-0.003
100	16	1.226	1.223	-0.003
100	17	1.226	1.222	-0.004
100	18	1.226	1.223	-0.003
100	19	1.226	1.223	-0.003
100	20	1.226	1.222	-0.004
100	21	1.227	1.223	-0.004
100	22	1.226	1.222	-0.004
100	24	1.226	1.221	-0.005
105	25	1.226	1.224	-0.002
105	26	1.226	1.223	-0.003
105	27	1.226	1.222	-0.004
105	28	1.225	1.222	-0.003
105	29	1.226	1.222	-0.004
Max		1.227	1.227	0.000
Average		1.226	1.224	-0.002
Min		1.225	1.221	-0.005
Std Dev		0.000	0.002	0.001



5.99 __V_REFCAP_500K_14V	
Test Site	
Tester	
Test Number	
Max Limit	1.237 V
Min Limit	1.213 V

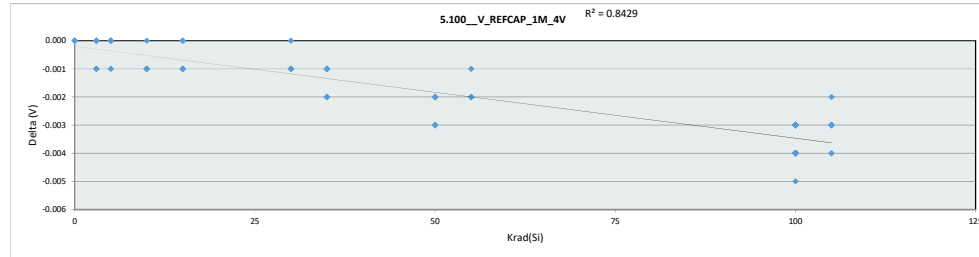
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.221	1.222
Average	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.224	1.224	1.222	1.223
Max	1.227	1.226	1.226	1.226	1.226	1.226	1.225	1.224	1.225	1.223	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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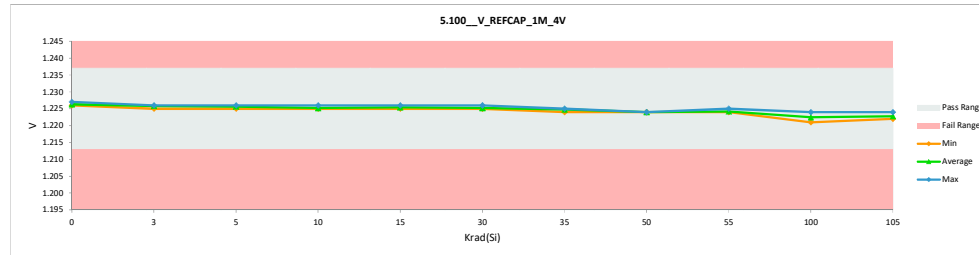
5.100_V_REFCAP_1M_4V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	1.237 1.237
Min Limit	1.213 1.213

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.226	1.226	0.000
3	301	1.227	1.226	-0.001
3	306	1.226	1.226	0.000
3	307	1.226	1.225	-0.001
3	308	1.226	1.226	0.000
3	309	1.226	1.226	0.000
5	310	1.225	1.225	0.000
5	311	1.226	1.226	0.000
5	312	1.226	1.225	-0.001
5	314	1.226	1.226	0.000
5	316	1.226	1.226	0.000
10	317	1.226	1.226	0.000
10	318	1.226	1.225	-0.001
10	319	1.226	1.225	-0.001
10	321	1.226	1.225	-0.001
10	322	1.226	1.225	-0.001
15	323	1.226	1.225	-0.001
15	325	1.226	1.225	-0.001
15	326	1.226	1.226	0.000
15	328	1.226	1.225	-0.001
15	333	1.226	1.226	0.000
30	334	1.226	1.225	-0.001
30	335	1.226	1.225	-0.001
30	337	1.226	1.225	-0.001
30	339	1.226	1.226	0.000
30	341	1.226	1.225	-0.001
35	342	1.226	1.225	-0.001
35	345	1.226	1.225	-0.001
35	346	1.227	1.225	-0.002
35	347	1.226	1.225	-0.001
35	348	1.226	1.224	-0.002
50	349	1.227	1.224	-0.003
50	350	1.226	1.224	-0.002
50	351	1.227	1.224	-0.003
50	353	1.226	1.224	-0.002
50	354	1.226	1.224	-0.002
55	355	1.226	1.224	-0.002
55	357	1.226	1.224	-0.002
55	359	1.226	1.224	-0.002
55	362	1.227	1.225	-0.002
55	363	1.225	1.224	-0.001
100	1	1.225	1.222	-0.003
100	3	1.226	1.222	-0.004
100	4	1.226	1.223	-0.003
100	5	1.226	1.222	-0.004
100	6	1.227	1.224	-0.003
100	7	1.226	1.222	-0.004
100	8	1.226	1.222	-0.004
100	9	1.226	1.223	-0.003
100	10	1.226	1.222	-0.004
100	11	1.226	1.223	-0.003
100	12	1.226	1.223	-0.003
100	13	1.226	1.222	-0.004
100	14	1.226	1.222	-0.004
100	15	1.226	1.223	-0.003
100	16	1.226	1.223	-0.003
100	17	1.226	1.222	-0.004
100	18	1.226	1.223	-0.003
100	19	1.226	1.223	-0.003
100	20	1.226	1.222	-0.004
100	21	1.227	1.223	-0.004
100	22	1.226	1.222	-0.004
100	24	1.226	1.221	-0.005
105	25	1.226	1.224	-0.002
105	26	1.226	1.223	-0.003
105	27	1.226	1.222	-0.004
105	28	1.226	1.223	-0.003
105	29	1.225	1.222	-0.003
	Max	1.227	1.227	0.000
	Average	1.226	1.224	-0.002
	Min	1.225	1.221	-0.005
	Std Dev	0.000	0.001	0.001



5.100_V_REFCAP_1M_4V	
Test Site	
Tester	
Test Number	
Max Limit	1.237 V
Min Limit	1.213 V

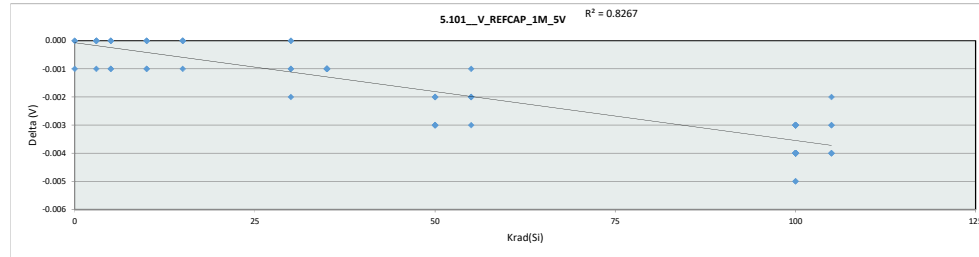
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.221	1.222
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.222	1.223
Max	1.227	1.226	1.226	1.226	1.226	1.226	1.225	1.224	1.225	1.224	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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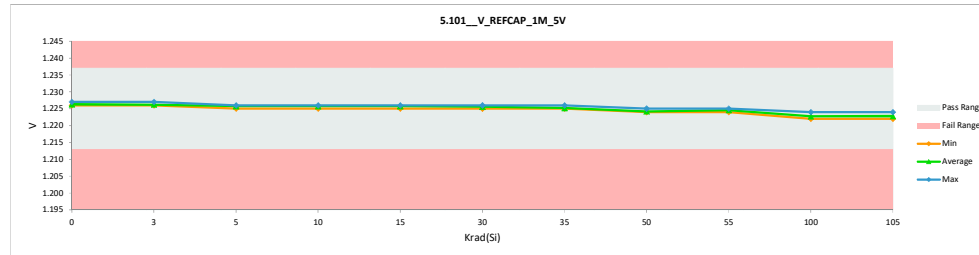
5.101_V_REFCAP_1M_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.227	1.226	-0.001
3	301	1.227	1.227	0.000
3	306	1.227	1.226	-0.001
3	307	1.226	1.226	0.000
3	308	1.226	1.226	0.000
3	309	1.226	1.226	0.000
5	310	1.226	1.225	-0.001
5	311	1.227	1.226	-0.001
5	312	1.226	1.226	0.000
5	314	1.226	1.226	0.000
5	316	1.226	1.226	0.000
10	317	1.227	1.226	-0.001
10	318	1.226	1.226	0.000
10	319	1.226	1.226	0.000
10	321	1.226	1.226	0.000
10	322	1.226	1.225	-0.001
15	323	1.226	1.226	0.000
15	325	1.226	1.225	-0.001
15	326	1.226	1.226	0.000
15	328	1.226	1.226	0.000
15	333	1.226	1.226	0.000
30	334	1.226	1.225	-0.001
30	335	1.227	1.225	-0.002
30	337	1.226	1.226	0.000
30	339	1.227	1.226	-0.001
30	341	1.226	1.226	0.000
35	342	1.226	1.225	-0.001
35	345	1.226	1.225	-0.001
35	346	1.227	1.226	-0.001
35	347	1.226	1.225	-0.001
35	348	1.226	1.225	-0.001
50	349	1.227	1.224	-0.003
50	350	1.227	1.224	-0.003
50	351	1.227	1.224	-0.003
50	353	1.226	1.224	-0.002
50	354	1.227	1.225	-0.002
55	355	1.226	1.224	-0.002
55	357	1.227	1.225	-0.002
55	359	1.227	1.224	-0.003
55	362	1.227	1.225	-0.002
55	363	1.226	1.225	-0.001
100	1	1.226	1.222	-0.004
100	3	1.226	1.223	-0.003
100	4	1.227	1.223	-0.004
100	5	1.227	1.223	-0.004
100	6	1.227	1.224	-0.003
100	7	1.226	1.223	-0.003
100	8	1.226	1.223	-0.003
100	9	1.226	1.223	-0.003
100	10	1.227	1.222	-0.005
100	11	1.227	1.223	-0.004
100	12	1.226	1.223	-0.003
100	13	1.227	1.222	-0.005
100	14	1.226	1.223	-0.003
100	15	1.227	1.223	-0.004
100	16	1.226	1.223	-0.003
100	17	1.226	1.222	-0.004
100	18	1.226	1.223	-0.003
100	19	1.226	1.223	-0.003
100	20	1.226	1.222	-0.004
100	21	1.227	1.223	-0.004
100	22	1.227	1.223	-0.004
100	24	1.226	1.222	-0.004
105	25	1.226	1.224	-0.002
105	26	1.226	1.223	-0.003
105	27	1.226	1.222	-0.004
105	28	1.226	1.223	-0.003
105	29	1.226	1.222	-0.004
Max		1.227	1.227	0.000
Average		1.226	1.224	-0.002
Min		1.226	1.222	-0.005
Std Dev		0.000	0.001	0.002



5.101_V_REFCAP_1M_5V	
Test Site	
Tester	
Test Number	
Max Limit	1.237
Min Limit	1.213

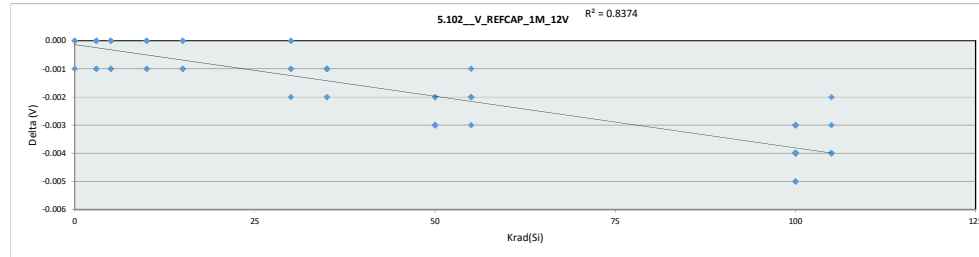
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.222	1.222
Average	1.226	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.224	1.225	1.223
Max	1.227	1.227	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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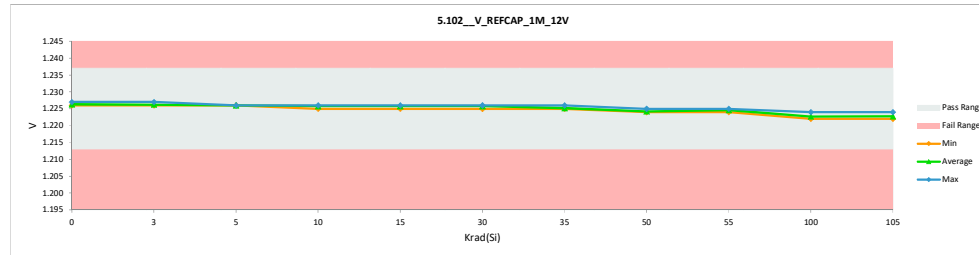
5.102_V_REFCAP_1M_12V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	1.237 1.237
Min Limit	1.213 1.213

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.227	1.226	-0.001
3	301	1.227	1.227	0.000
3	306	1.227	1.226	-0.001
3	307	1.226	1.226	0.000
3	308	1.226	1.226	0.000
3	309	1.227	1.226	-0.001
5	310	1.226	1.226	0.000
5	311	1.227	1.226	-0.001
5	312	1.226	1.226	0.000
5	314	1.226	1.226	0.000
5	316	1.227	1.226	-0.001
10	317	1.227	1.226	-0.001
10	318	1.226	1.226	0.000
10	319	1.226	1.226	0.000
10	321	1.226	1.226	0.000
10	322	1.226	1.225	-0.001
15	323	1.227	1.226	-0.001
15	325	1.226	1.225	-0.001
15	326	1.226	1.226	0.000
15	328	1.226	1.226	0.000
15	333	1.227	1.226	-0.001
30	334	1.227	1.226	-0.001
30	335	1.227	1.225	-0.002
30	337	1.226	1.226	0.000
30	339	1.227	1.226	-0.001
30	341	1.226	1.226	0.000
35	342	1.226	1.225	-0.001
35	345	1.226	1.225	-0.001
35	346	1.227	1.226	-0.001
35	347	1.226	1.225	-0.001
35	348	1.227	1.225	-0.002
50	349	1.227	1.224	-0.003
50	350	1.227	1.224	-0.003
50	351	1.227	1.224	-0.003
50	353	1.226	1.224	-0.002
50	354	1.227	1.225	-0.002
55	355	1.226	1.224	-0.002
55	357	1.227	1.225	-0.002
55	359	1.227	1.224	-0.003
55	362	1.227	1.225	-0.002
55	363	1.226	1.225	-0.001
100	1	1.226	1.222	-0.004
100	3	1.227	1.222	-0.005
100	4	1.227	1.223	-0.004
100	5	1.227	1.223	-0.004
100	6	1.227	1.224	-0.003
100	7	1.226	1.223	-0.003
100	8	1.226	1.222	-0.004
100	9	1.227	1.223	-0.004
100	10	1.227	1.222	-0.005
100	11	1.227	1.223	-0.004
100	12	1.226	1.223	-0.003
100	13	1.227	1.222	-0.005
100	14	1.226	1.223	-0.003
100	15	1.227	1.223	-0.004
100	16	1.227	1.223	-0.004
100	17	1.226	1.222	-0.004
100	18	1.226	1.223	-0.003
100	19	1.227	1.223	-0.004
100	20	1.226	1.222	-0.004
100	21	1.227	1.223	-0.004
100	22	1.227	1.223	-0.004
100	24	1.227	1.222	-0.005
105	25	1.226	1.224	-0.002
105	26	1.227	1.223	-0.004
105	27	1.226	1.222	-0.004
105	28	1.226	1.223	-0.003
105	29	1.226	1.222	-0.004
Max		1.227	1.227	0.000
Average		1.227	1.224	-0.002
Min		1.226	1.222	-0.005
Std Dev		0.001	0.002	0.002



5.102_V_REFCAP_1M_12V	
Test Site	
Tester	
Test Number	
Max Limit	1.237 V
Min Limit	1.213 V

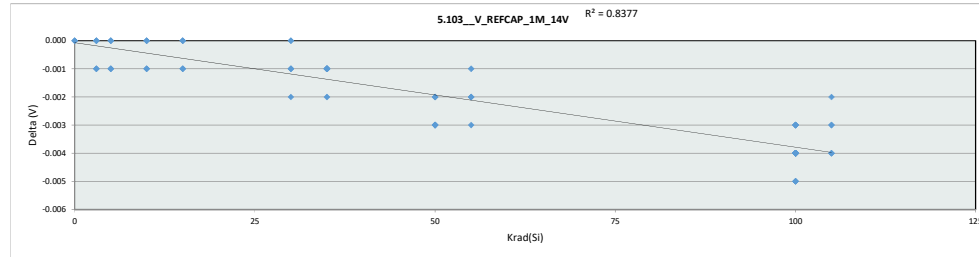
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.224	1.224	1.222	1.222
Average	1.226	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.224	1.225	1.223
Max	1.227	1.227	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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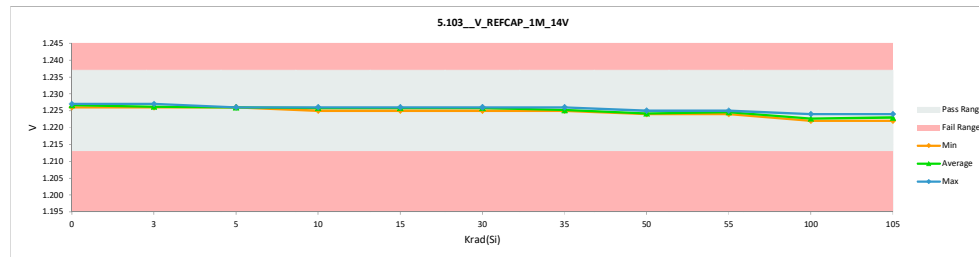
5.103_V_REFCAP_1M_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.227	1.227	0.000
3	301	1.227	1.227	0.000
3	306	1.227	1.226	-0.001
3	307	1.226	1.226	0.000
3	308	1.226	1.226	0.000
3	309	1.227	1.226	-0.001
5	310	1.226	1.226	0.000
5	311	1.227	1.226	-0.001
5	312	1.226	1.226	0.000
5	314	1.226	1.226	0.000
5	316	1.227	1.226	-0.001
10	317	1.227	1.226	-0.001
10	318	1.226	1.226	0.000
10	319	1.226	1.226	0.000
10	321	1.226	1.226	0.000
10	322	1.226	1.225	-0.001
15	323	1.226	1.226	0.000
15	325	1.226	1.225	-0.001
15	326	1.226	1.226	0.000
15	328	1.226	1.226	0.000
15	333	1.227	1.226	-0.001
30	334	1.227	1.226	-0.001
30	335	1.227	1.225	-0.002
30	337	1.226	1.226	0.000
30	339	1.227	1.226	-0.001
30	341	1.226	1.226	0.000
35	342	1.226	1.225	-0.001
35	345	1.226	1.225	-0.001
35	346	1.227	1.226	-0.001
35	347	1.226	1.225	-0.001
35	348	1.227	1.225	-0.002
50	349	1.227	1.224	-0.003
50	350	1.227	1.224	-0.003
50	351	1.227	1.224	-0.003
50	353	1.226	1.224	-0.002
50	354	1.227	1.225	-0.002
55	355	1.226	1.224	-0.002
55	357	1.227	1.225	-0.002
55	359	1.227	1.224	-0.003
55	362	1.227	1.225	-0.002
55	363	1.226	1.225	-0.001
100	1	1.226	1.222	-0.004
100	3	1.227	1.222	-0.005
100	4	1.227	1.223	-0.004
100	5	1.227	1.223	-0.004
100	6	1.227	1.224	-0.003
100	7	1.226	1.223	-0.003
100	8	1.226	1.222	-0.004
100	9	1.227	1.223	-0.004
100	10	1.227	1.222	-0.005
100	11	1.227	1.223	-0.004
100	12	1.226	1.223	-0.003
100	13	1.227	1.222	-0.005
100	14	1.226	1.223	-0.003
100	15	1.227	1.223	-0.004
100	16	1.227	1.223	-0.004
100	17	1.226	1.222	-0.004
100	18	1.226	1.223	-0.003
100	19	1.227	1.223	-0.004
100	20	1.226	1.222	-0.004
100	21	1.227	1.223	-0.004
100	22	1.227	1.223	-0.004
100	24	1.227	1.222	-0.005
105	25	1.226	1.224	-0.002
105	26	1.227	1.223	-0.004
105	27	1.226	1.222	-0.004
105	28	1.226	1.223	-0.003
105	29	1.226	1.223	-0.003
	Max	1.227	1.227	0.000
	Average	1.227	1.224	-0.002
	Min	1.226	1.222	-0.005
	Std Dev	0.001	0.002	0.002



5.103_V_REFCAP_1M_14V	
Test Site	
Tester	
Test Number	
Max Limit	1.237
Min Limit	1.213

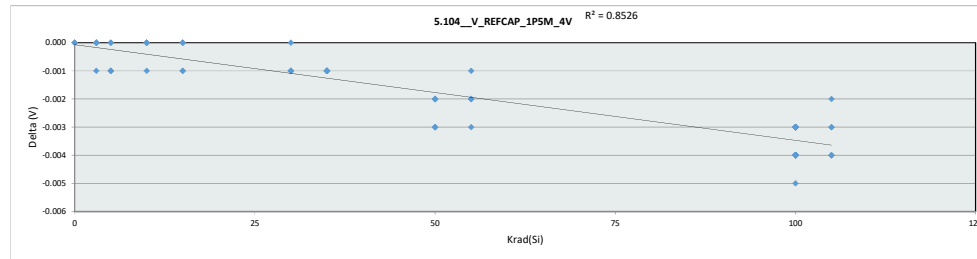
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.224	1.224	1.222	1.222
Average	1.227	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.224	1.225	1.223
Max	1.227	1.227	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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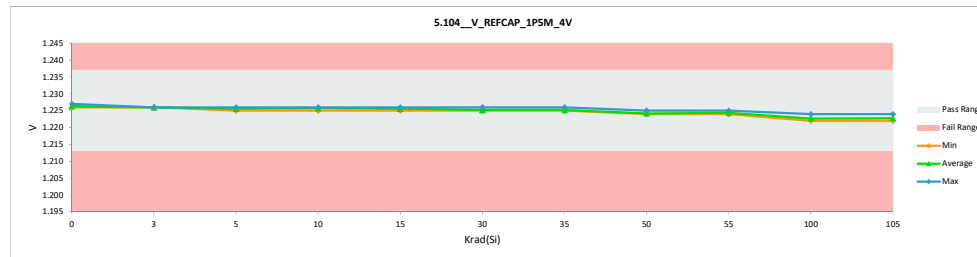
5.104_V_REFCAP_1P5M_4V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	1.237 1.237
Min Limit	1.213 1.213

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.226	1.226	0.000
3	301	1.227	1.226	-0.001
3	306	1.226	1.226	0.000
3	307	1.226	1.226	0.000
3	308	1.226	1.226	0.000
3	309	1.226	1.226	0.000
5	310	1.226	1.225	-0.001
5	311	1.227	1.226	-0.001
5	312	1.226	1.225	-0.001
5	314	1.226	1.226	0.000
5	316	1.226	1.226	0.000
10	317	1.226	1.226	0.000
10	318	1.226	1.226	0.000
10	319	1.226	1.226	0.000
10	321	1.226	1.226	0.000
10	322	1.226	1.225	-0.001
15	323	1.226	1.225	-0.001
15	325	1.226	1.225	-0.001
15	326	1.226	1.226	0.000
15	328	1.226	1.226	0.000
15	333	1.226	1.226	0.000
30	334	1.226	1.225	-0.001
30	335	1.226	1.225	-0.001
30	337	1.226	1.225	-0.001
30	339	1.226	1.226	0.000
30	341	1.226	1.225	-0.001
35	342	1.226	1.225	-0.001
35	345	1.226	1.225	-0.001
35	346	1.227	1.226	-0.001
35	347	1.226	1.225	-0.001
35	348	1.226	1.225	-0.001
50	349	1.227	1.224	-0.003
50	350	1.226	1.224	-0.002
50	351	1.227	1.224	-0.003
50	353	1.226	1.224	-0.002
50	354	1.227	1.225	-0.002
55	355	1.226	1.224	-0.002
55	357	1.226	1.225	-0.001
55	359	1.227	1.224	-0.003
55	362	1.227	1.225	-0.002
55	363	1.226	1.224	-0.002
100	1	1.225	1.222	-0.003
100	3	1.226	1.222	-0.004
100	4	1.227	1.223	-0.004
100	5	1.226	1.223	-0.003
100	6	1.227	1.224	-0.003
100	7	1.226	1.223	-0.003
100	8	1.226	1.222	-0.004
100	9	1.226	1.223	-0.003
100	10	1.226	1.222	-0.004
100	11	1.227	1.223	-0.004
100	12	1.226	1.223	-0.003
100	13	1.227	1.222	-0.005
100	14	1.226	1.223	-0.003
100	15	1.227	1.223	-0.004
100	16	1.226	1.223	-0.003
100	17	1.226	1.222	-0.004
100	18	1.226	1.223	-0.003
100	19	1.226	1.223	-0.003
100	20	1.226	1.222	-0.004
100	21	1.227	1.223	-0.004
100	22	1.226	1.223	-0.003
100	24	1.226	1.222	-0.004
105	25	1.226	1.224	-0.002
105	26	1.226	1.223	-0.003
105	27	1.226	1.222	-0.004
105	28	1.226	1.223	-0.003
105	29	1.226	1.222	-0.004
	Max	1.227	1.227	0.000
	Average	1.226	1.224	-0.002
	Min	1.225	1.222	-0.005
	Std Dev	0.000	0.001	0.002



5.104_V_REFCAP_1P5M_4V	
Test Site	
Tester	
Test Number	
Max Limit	1.237 V
Min Limit	1.213 V

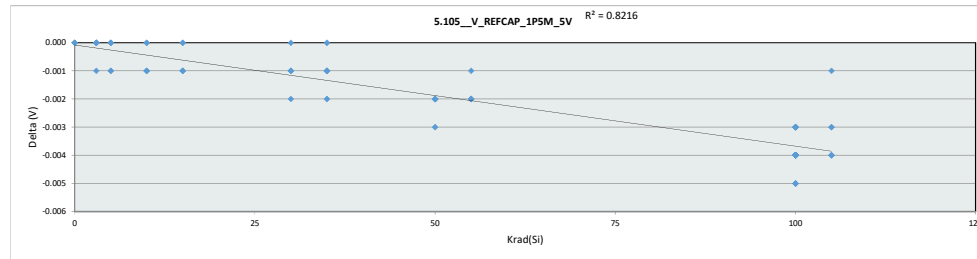
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.222	1.222
Average	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.223	1.223
Max	1.227	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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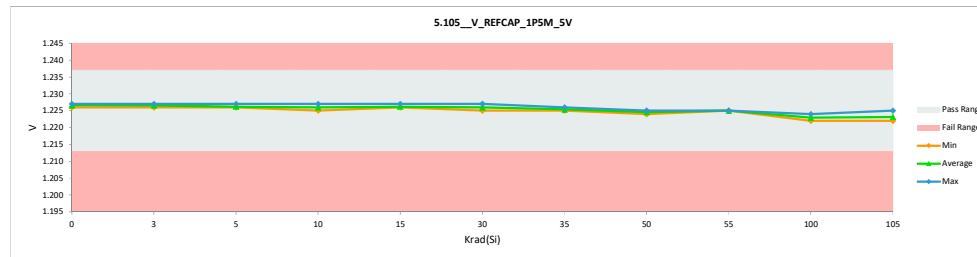
5.105_V_REFCAP_1P5M_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.227	1.227	0.000
3	301	1.227	1.227	0.000
3	306	1.227	1.227	0.000
3	307	1.226	1.226	0.000
3	308	1.227	1.226	-0.001
3	309	1.227	1.227	0.000
5	310	1.226	1.226	0.000
5	311	1.227	1.227	0.000
5	312	1.226	1.226	0.000
5	314	1.227	1.226	-0.001
5	316	1.227	1.226	-0.001
10	317	1.227	1.227	0.000
10	318	1.227	1.226	-0.001
10	319	1.226	1.226	0.000
10	321	1.227	1.226	-0.001
10	322	1.226	1.225	-0.001
15	323	1.227	1.226	-0.001
15	325	1.226	1.226	0.000
15	326	1.227	1.227	0.000
15	328	1.227	1.226	-0.001
15	333	1.227	1.226	-0.001
30	334	1.227	1.226	-0.001
30	335	1.227	1.225	-0.002
30	337	1.227	1.226	-0.001
30	339	1.227	1.227	0.000
30	341	1.227	1.226	-0.001
35	342	1.226	1.225	-0.001
35	345	1.226	1.225	-0.001
35	346	1.227	1.226	-0.001
35	347	1.226	1.226	0.000
35	348	1.227	1.225	-0.002
50	349	1.227	1.225	-0.002
50	350	1.227	1.225	-0.002
50	351	1.227	1.224	-0.003
50	353	1.226	1.224	-0.002
50	354	1.227	1.225	-0.002
55	355	1.227	1.225	-0.002
55	357	1.227	1.225	-0.002
55	359	1.227	1.225	-0.002
55	362	1.227	1.225	-0.002
55	363	1.226	1.225	-0.001
100	1	1.226	1.222	-0.004
100	3	1.227	1.223	-0.004
100	4	1.227	1.224	-0.003
100	5	1.227	1.223	-0.004
100	6	1.227	1.224	-0.003
100	7	1.227	1.223	-0.004
100	8	1.227	1.223	-0.004
100	9	1.227	1.223	-0.004
100	10	1.227	1.222	-0.005
100	11	1.227	1.223	-0.004
100	12	1.227	1.223	-0.004
100	13	1.227	1.222	-0.005
100	14	1.227	1.223	-0.004
100	15	1.227	1.223	-0.004
100	16	1.227	1.223	-0.004
100	17	1.227	1.222	-0.005
100	18	1.226	1.223	-0.003
100	19	1.227	1.224	-0.003
100	20	1.226	1.223	-0.003
100	21	1.227	1.224	-0.003
100	22	1.227	1.223	-0.004
100	24	1.227	1.222	-0.005
105	25	1.226	1.225	-0.001
105	26	1.227	1.223	-0.004
105	27	1.226	1.222	-0.004
105	28	1.226	1.223	-0.003
105	29	1.226	1.223	-0.003
	Max	1.227	1.227	0.000
	Average	1.227	1.225	-0.002
	Min	1.226	1.222	-0.005
	Std Dev	0.000	0.002	0.002



5.105_V_REFCAP_1P5M_5V	
Test Site	
Tester	
Test Number	
Max Limit	1.237 V
Min Limit	1.213 V

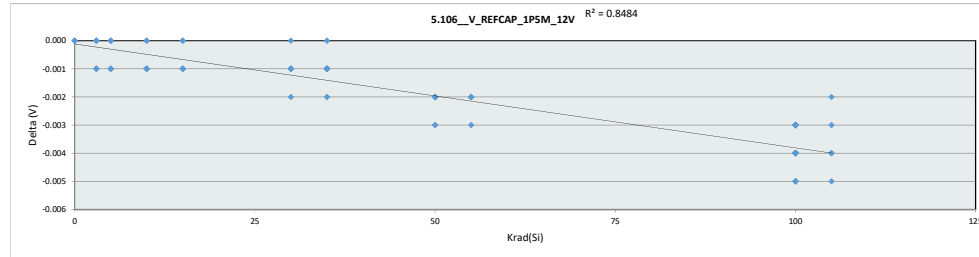
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.225	1.226	1.225	1.225	1.224	1.225	1.222	1.222
Average	1.227	1.227	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.223	1.223
Max	1.227	1.227	1.227	1.227	1.227	1.227	1.226	1.225	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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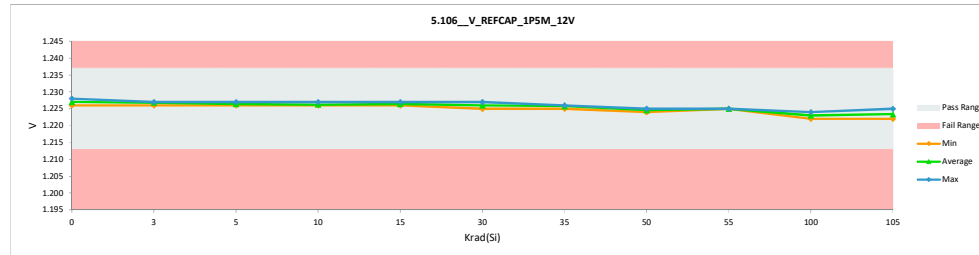
5.106_V_REFCAP_1P5M_12V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	1.237 1.237
Min Limit	1.213 1.213

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.226	1.226	0.000
0	118	1.228	1.228	0.000
0	122	1.227	1.227	0.000
3	301	1.228	1.227	-0.001
3	306	1.227	1.227	0.000
3	307	1.227	1.227	0.000
3	308	1.227	1.226	-0.001
3	309	1.227	1.227	0.000
5	310	1.226	1.226	0.000
5	311	1.227	1.227	0.000
5	312	1.227	1.226	-0.001
5	314	1.227	1.226	-0.001
5	316	1.227	1.227	0.000
10	317	1.227	1.227	0.000
10	318	1.227	1.226	-0.001
10	319	1.227	1.226	-0.001
10	321	1.227	1.226	-0.001
10	322	1.226	1.226	0.000
15	323	1.227	1.226	-0.001
15	325	1.227	1.226	-0.001
15	326	1.227	1.227	0.000
15	328	1.227	1.226	-0.001
15	333	1.227	1.227	0.000
30	334	1.227	1.226	-0.001
30	335	1.227	1.225	-0.002
30	337	1.227	1.226	-0.001
30	339	1.227	1.227	0.000
30	341	1.227	1.226	-0.001
35	342	1.227	1.226	-0.001
35	345	1.227	1.226	-0.001
35	346	1.227	1.226	-0.001
35	347	1.226	1.226	0.000
35	348	1.227	1.225	-0.002
50	349	1.227	1.225	-0.002
50	350	1.227	1.225	-0.002
50	351	1.227	1.224	-0.003
50	353	1.226	1.224	-0.002
50	354	1.227	1.225	-0.002
55	355	1.227	1.225	-0.002
55	357	1.227	1.225	-0.002
55	359	1.227	1.225	-0.002
55	362	1.228	1.225	-0.003
55	363	1.227	1.225	-0.002
100	1	1.226	1.222	-0.004
100	3	1.227	1.223	-0.004
100	4	1.227	1.224	-0.003
100	5	1.227	1.223	-0.004
100	6	1.227	1.224	-0.003
100	7	1.227	1.223	-0.004
100	8	1.227	1.223	-0.004
100	9	1.227	1.224	-0.003
100	10	1.227	1.222	-0.005
100	11	1.227	1.223	-0.004
100	12	1.227	1.223	-0.004
100	13	1.227	1.222	-0.005
100	14	1.227	1.223	-0.004
100	15	1.227	1.223	-0.004
100	16	1.227	1.223	-0.004
100	17	1.227	1.222	-0.005
100	18	1.226	1.223	-0.003
100	19	1.227	1.224	-0.003
100	20	1.227	1.223	-0.004
100	21	1.227	1.224	-0.003
100	22	1.227	1.223	-0.004
100	24	1.227	1.222	-0.005
105	25	1.227	1.225	-0.002
105	26	1.227	1.224	-0.003
105	27	1.227	1.222	-0.005
105	28	1.227	1.223	-0.004
105	29	1.227	1.223	-0.004
	Max	1.228	1.228	0.000
	Average	1.227	1.225	-0.002
	Min	1.226	1.222	-0.005
	Std Dev	0.000	0.002	0.002



5.106_V_REFCAP_1P5M_12V	
Test Site	
Tester	
Test Number	
Max Limit	1.237 V
Min Limit	1.213 V

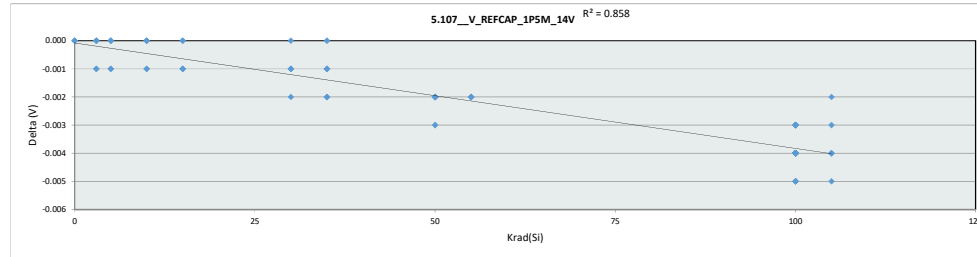
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.225	1.222
Average	1.227	1.227	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.223	1.223
Max	1.228	1.227	1.227	1.227	1.227	1.227	1.226	1.225	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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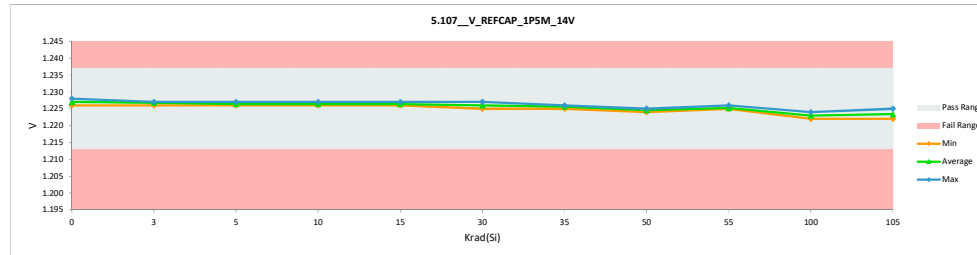
5.107_V_REFCAP_1P5M_14V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	1.237 1.237
Min Limit	1.213 1.213

Krad(Si)	Serial #	PRE HDR	POST HDR	Delta
0	117	1.226	1.226	0.000
0	118	1.228	1.228	0.000
0	122	1.227	1.227	0.000
3	301	1.228	1.227	-0.001
3	306	1.227	1.227	0.000
3	307	1.227	1.227	0.000
3	308	1.227	1.226	-0.001
3	309	1.227	1.227	0.000
5	310	1.226	1.226	0.000
5	311	1.227	1.227	0.000
5	312	1.227	1.226	-0.001
5	314	1.227	1.226	-0.001
5	316	1.227	1.227	0.000
10	317	1.227	1.227	0.000
10	318	1.227	1.227	0.000
10	319	1.227	1.226	-0.001
10	321	1.227	1.226	-0.001
10	322	1.226	1.226	0.000
15	323	1.227	1.226	-0.001
15	325	1.227	1.226	-0.001
15	326	1.227	1.227	0.000
15	328	1.227	1.226	-0.001
15	333	1.227	1.227	0.000
30	334	1.227	1.226	-0.001
30	335	1.227	1.225	-0.002
30	337	1.227	1.226	-0.001
30	339	1.227	1.227	0.000
30	341	1.227	1.226	-0.001
35	342	1.227	1.226	-0.001
35	345	1.227	1.225	-0.002
35	346	1.227	1.226	-0.001
35	347	1.226	1.226	0.000
35	348	1.227	1.225	-0.002
50	349	1.227	1.225	-0.002
50	350	1.227	1.225	-0.002
50	351	1.227	1.224	-0.003
50	353	1.226	1.224	-0.002
50	354	1.227	1.225	-0.002
55	355	1.227	1.225	-0.002
55	357	1.227	1.225	-0.002
55	359	1.227	1.225	-0.002
55	362	1.228	1.226	-0.002
55	363	1.227	1.225	-0.002
100	1	1.226	1.222	-0.004
100	3	1.227	1.223	-0.004
100	4	1.227	1.224	-0.003
100	5	1.227	1.223	-0.004
100	6	1.227	1.224	-0.003
100	7	1.227	1.223	-0.004
100	8	1.227	1.223	-0.004
100	9	1.227	1.223	-0.004
100	10	1.227	1.222	-0.005
100	11	1.227	1.223	-0.004
100	12	1.227	1.223	-0.004
100	13	1.227	1.222	-0.005
100	14	1.227	1.223	-0.004
100	15	1.227	1.223	-0.004
100	16	1.227	1.223	-0.004
100	17	1.227	1.222	-0.005
100	18	1.226	1.223	-0.003
100	19	1.227	1.224	-0.003
100	20	1.227	1.223	-0.004
100	21	1.227	1.224	-0.003
100	22	1.227	1.223	-0.004
100	24	1.227	1.222	-0.005
105	25	1.227	1.225	-0.002
105	26	1.227	1.224	-0.003
105	27	1.227	1.222	-0.005
105	28	1.227	1.223	-0.004
105	29	1.227	1.223	-0.004
	Max	1.228	1.228	0.000
	Average	1.227	1.225	-0.002
	Min	1.226	1.222	-0.005
	Std Dev	0.000	0.002	0.002



5.107_V_REFCAP_1P5M_14V	
Test Site	
Tester	
Test Number	
Max Limit	1.237 V
Min Limit	1.213 V

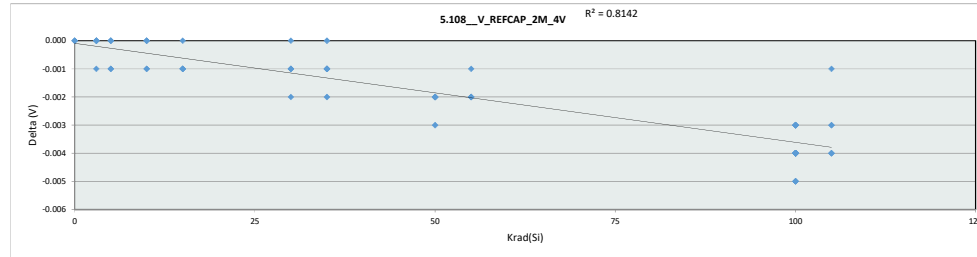
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.225	1.222
Average	1.227	1.227	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.223	1.223
Max	1.228	1.227	1.227	1.227	1.227	1.227	1.226	1.225	1.226	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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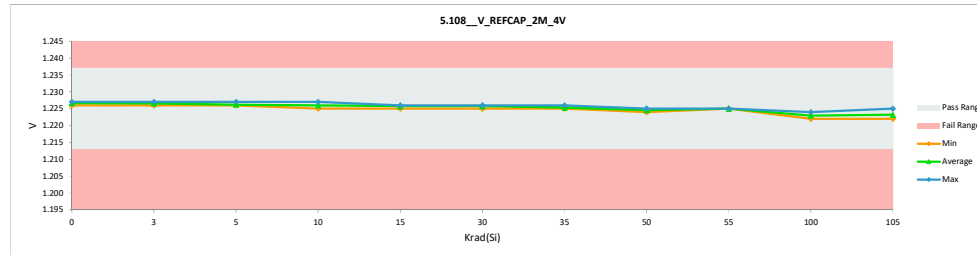
5.108_V_REFCAP_2M_4V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	1.237 1.237
Min Limit	1.213 1.213

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.227	1.227	0.000
3	301	1.227	1.227	0.000
3	306	1.227	1.227	0.000
3	307	1.226	1.226	0.000
3	308	1.227	1.226	-0.001
3	309	1.227	1.227	0.000
5	310	1.226	1.226	0.000
5	311	1.227	1.227	0.000
5	312	1.226	1.226	0.000
5	314	1.227	1.226	-0.001
5	316	1.227	1.226	-0.001
10	317	1.227	1.227	0.000
10	318	1.227	1.226	-0.001
10	319	1.226	1.226	0.000
10	321	1.226	1.226	0.000
10	322	1.226	1.225	-0.001
15	323	1.227	1.226	-0.001
15	325	1.226	1.225	-0.001
15	326	1.226	1.226	0.000
15	328	1.227	1.226	-0.001
15	323	1.227	1.226	-0.001
30	334	1.227	1.226	-0.001
30	335	1.227	1.225	-0.002
30	337	1.226	1.226	0.000
30	339	1.227	1.226	-0.001
30	341	1.227	1.226	-0.001
35	342	1.226	1.225	-0.001
35	345	1.226	1.225	-0.001
35	346	1.227	1.226	-0.001
35	347	1.226	1.226	0.000
35	348	1.227	1.225	-0.002
50	349	1.227	1.225	-0.002
50	350	1.227	1.225	-0.002
50	351	1.227	1.224	-0.003
50	353	1.226	1.224	-0.002
50	354	1.227	1.225	-0.002
55	355	1.227	1.225	-0.002
55	357	1.227	1.225	-0.002
55	359	1.227	1.225	-0.002
55	362	1.227	1.225	-0.002
55	363	1.226	1.225	-0.001
100	1	1.226	1.222	-0.004
100	3	1.227	1.223	-0.004
100	4	1.227	1.224	-0.003
100	5	1.227	1.223	-0.004
100	6	1.227	1.224	-0.003
100	7	1.226	1.223	-0.003
100	8	1.227	1.223	-0.004
100	9	1.227	1.223	-0.004
100	10	1.227	1.222	-0.005
100	11	1.227	1.223	-0.004
100	12	1.227	1.223	-0.004
100	13	1.227	1.222	-0.005
100	14	1.226	1.223	-0.003
100	15	1.227	1.223	-0.004
100	16	1.227	1.223	-0.004
100	17	1.227	1.222	-0.005
100	18	1.226	1.223	-0.003
100	19	1.227	1.224	-0.003
100	20	1.226	1.223	-0.003
100	21	1.227	1.224	-0.003
100	22	1.227	1.223	-0.004
100	24	1.227	1.222	-0.005
105	25	1.226	1.225	-0.001
105	26	1.227	1.223	-0.004
105	27	1.226	1.222	-0.004
105	28	1.226	1.223	-0.003
105	29	1.226	1.223	-0.003
	Max	1.227	1.227	0.000
	Average	1.227	1.225	-0.002
	Min	1.226	1.222	-0.005
	Std Dev	0.000	0.002	0.002



5.108_V_REFCAP_2M_4V	
Test Site	
Tester	
Test Number	
Max Limit	1.237 V
Min Limit	1.213 V

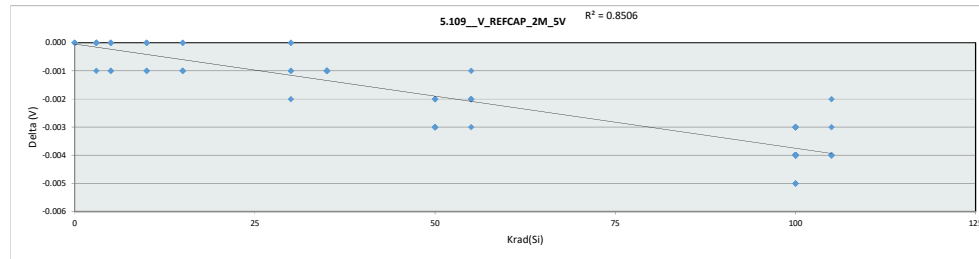
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.224	1.225	1.222	1.222
Average	1.227	1.227	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.223	1.223
Max	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.225	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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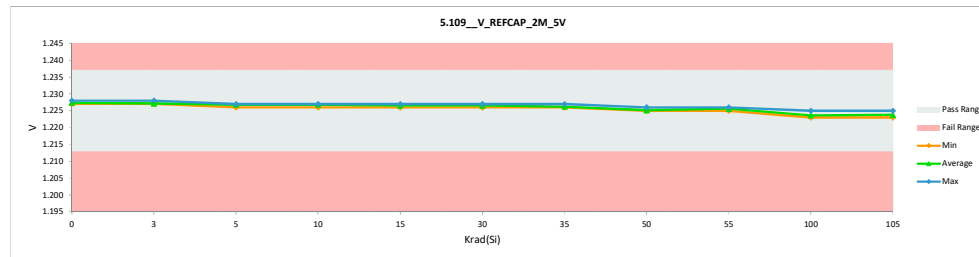
5.109_V_REFCAP_2M_5V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	1.237 1.237
Min Limit	1.213 1.213

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.227	1.227	0.000
0	118	1.228	1.228	0.000
0	122	1.227	1.227	0.000
3	301	1.228	1.228	0.000
3	306	1.228	1.227	-0.001
3	307	1.227	1.227	0.000
3	308	1.227	1.227	0.000
3	309	1.227	1.227	0.000
5	310	1.227	1.226	-0.001
5	311	1.228	1.227	-0.001
5	312	1.227	1.227	0.000
5	314	1.227	1.227	0.000
5	316	1.227	1.227	0.000
10	317	1.228	1.227	-0.001
10	318	1.227	1.227	0.000
10	319	1.227	1.227	0.000
10	321	1.227	1.227	0.000
10	322	1.227	1.226	-0.001
15	323	1.227	1.226	-0.001
15	325	1.227	1.226	-0.001
15	326	1.227	1.227	0.000
15	328	1.227	1.227	0.000
15	333	1.228	1.227	-0.001
30	334	1.227	1.226	-0.001
30	335	1.228	1.226	-0.002
30	337	1.227	1.227	0.000
30	339	1.228	1.227	-0.001
30	341	1.227	1.227	0.000
35	342	1.227	1.226	-0.001
35	345	1.227	1.226	-0.001
35	346	1.228	1.227	-0.001
35	347	1.227	1.226	-0.001
35	348	1.227	1.226	-0.001
50	349	1.228	1.225	-0.003
50	350	1.228	1.225	-0.003
50	351	1.228	1.225	-0.003
50	353	1.227	1.225	-0.002
50	354	1.228	1.226	-0.002
55	355	1.227	1.225	-0.002
55	357	1.228	1.226	-0.002
55	359	1.228	1.225	-0.003
55	362	1.228	1.226	-0.002
55	363	1.227	1.226	-0.001
100	1	1.227	1.223	-0.004
100	3	1.227	1.223	-0.004
100	4	1.228	1.224	-0.004
100	5	1.228	1.224	-0.004
100	6	1.228	1.225	-0.003
100	7	1.227	1.223	-0.004
100	8	1.227	1.223	-0.004
100	9	1.228	1.224	-0.004
100	10	1.228	1.223	-0.005
100	11	1.228	1.224	-0.004
100	12	1.227	1.224	-0.003
100	13	1.228	1.223	-0.005
100	14	1.227	1.224	-0.003
100	15	1.228	1.224	-0.004
100	16	1.227	1.224	-0.003
100	17	1.227	1.223	-0.004
100	18	1.227	1.224	-0.003
100	19	1.227	1.224	-0.003
100	20	1.227	1.223	-0.004
100	21	1.228	1.224	-0.004
100	22	1.228	1.224	-0.004
100	24	1.228	1.223	-0.005
105	25	1.227	1.225	-0.002
105	26	1.228	1.224	-0.004
105	27	1.227	1.223	-0.004
105	28	1.227	1.224	-0.003
105	29	1.227	1.223	-0.004
	Max	1.228	1.228	0.000
	Average	1.227	1.225	-0.002
	Min	1.227	1.223	-0.005
	Std Dev	0.000	0.002	



5.109_V_REFCAP_2M_5V	
Test Site	
Tester	
Test Number	
Max Limit	1.237 V
Min Limit	1.213 V

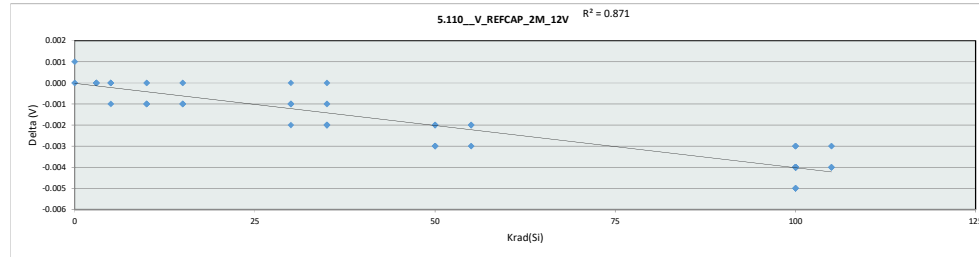
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.227	1.227	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.223	1.223
Average	1.227	1.227	1.227	1.227	1.227	1.227	1.226	1.225	1.226	1.224	1.224
Max	1.228	1.228	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.225	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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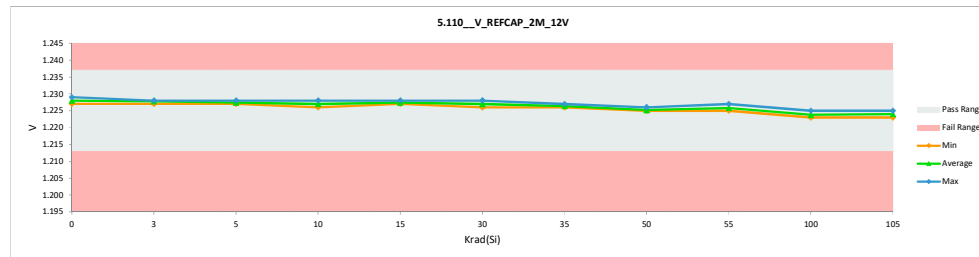
5.110_V_REFCAP_2M_12V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	1.237 1.237
Min Limit	1.213 1.213

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.227	1.227	0.000
0	118	1.228	1.229	0.001
0	122	1.228	1.228	0.000
3	301	1.228	1.228	0.000
3	306	1.228	1.228	0.000
3	307	1.228	1.228	0.000
3	308	1.227	1.227	0.000
3	309	1.228	1.228	0.000
5	310	1.227	1.227	0.000
5	311	1.228	1.228	0.000
5	312	1.227	1.227	0.000
5	314	1.228	1.227	-0.001
5	316	1.228	1.228	0.000
10	317	1.228	1.228	0.000
10	318	1.228	1.227	-0.001
10	319	1.227	1.227	0.000
10	321	1.228	1.227	-0.001
10	322	1.227	1.226	-0.001
15	323	1.228	1.227	-0.001
15	325	1.228	1.227	-0.001
15	326	1.228	1.228	0.000
15	328	1.228	1.227	-0.001
15	323	1.228	1.228	0.000
30	334	1.228	1.227	-0.001
30	335	1.228	1.226	-0.002
30	337	1.228	1.227	-0.001
30	339	1.228	1.228	0.000
30	341	1.228	1.227	-0.001
35	342	1.228	1.226	-0.002
35	345	1.228	1.226	-0.002
35	346	1.228	1.227	-0.001
35	347	1.227	1.227	0.000
35	348	1.228	1.226	-0.002
50	349	1.228	1.225	-0.003
50	350	1.228	1.225	-0.003
50	351	1.228	1.225	-0.003
50	353	1.227	1.225	-0.002
50	354	1.228	1.226	-0.002
55	355	1.228	1.225	-0.003
55	357	1.228	1.226	-0.002
55	359	1.228	1.225	-0.003
55	362	1.229	1.227	-0.002
55	363	1.228	1.226	-0.002
100	1	1.227	1.223	-0.004
100	3	1.228	1.223	-0.005
100	4	1.228	1.224	-0.004
100	5	1.228	1.224	-0.004
100	6	1.228	1.225	-0.003
100	7	1.228	1.224	-0.004
100	8	1.228	1.224	-0.004
100	9	1.228	1.225	-0.003
100	10	1.228	1.223	-0.005
100	11	1.228	1.224	-0.004
100	12	1.228	1.224	-0.004
100	13	1.228	1.223	-0.005
100	14	1.227	1.224	-0.003
100	15	1.228	1.224	-0.004
100	16	1.228	1.224	-0.004
100	17	1.228	1.223	-0.005
100	18	1.227	1.224	-0.003
100	19	1.228	1.224	-0.004
100	20	1.228	1.223	-0.005
100	21	1.228	1.224	-0.004
100	22	1.228	1.224	-0.004
100	24	1.228	1.223	-0.005
105	25	1.228	1.225	-0.003
105	26	1.228	1.224	-0.004
105	27	1.227	1.223	-0.004
105	28	1.227	1.224	-0.003
105	29	1.228	1.224	-0.004
	Max	1.229	1.229	0.001
	Average	1.228	1.226	-0.002
	Min	1.227	1.223	-0.005
	Std Dev	0.000	0.002	0.002



5.110_V_REFCAP_2M_12V	
Test Site	
Tester	
Test Number	
Max Limit	1.237 V
Min Limit	1.213 V

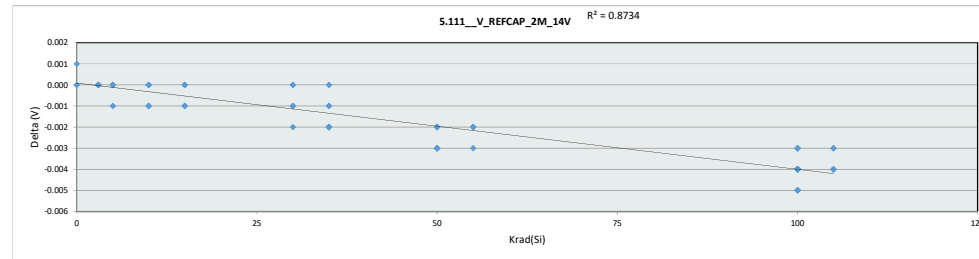
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.227	1.227	1.227	1.226	1.227	1.226	1.226	1.225	1.225	1.223	1.223
Average	1.228	1.228	1.227	1.227	1.227	1.227	1.226	1.225	1.226	1.224	1.224
Max	1.229	1.228	1.228	1.228	1.228	1.228	1.227	1.226	1.227	1.225	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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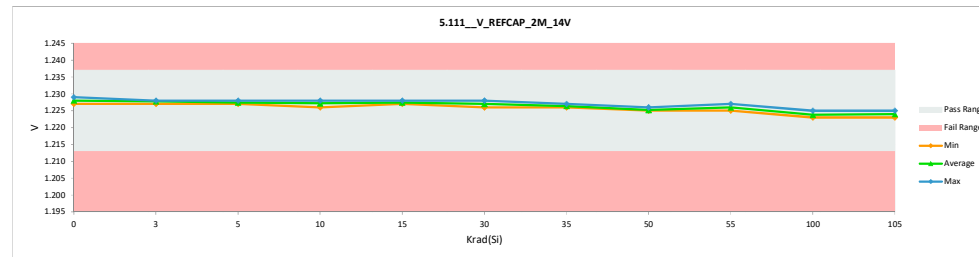
5.111_V_REFCAP_2M_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.227	1.227	0.000
0	118	1.228	1.229	0.001
0	122	1.228	1.228	0.000
3	301	1.228	1.228	0.000
3	306	1.228	1.228	0.000
3	307	1.228	1.228	0.000
3	308	1.227	1.227	0.000
3	309	1.228	1.228	0.000
5	310	1.227	1.227	0.000
5	311	1.228	1.228	0.000
5	312	1.227	1.227	0.000
5	314	1.228	1.227	-0.001
5	316	1.228	1.228	0.000
10	317	1.228	1.228	0.000
10	318	1.228	1.228	0.000
10	319	1.227	1.227	0.000
10	321	1.228	1.227	-0.001
10	322	1.227	1.226	-0.001
15	323	1.228	1.227	-0.001
15	325	1.227	1.227	0.000
15	326	1.228	1.228	0.000
15	328	1.228	1.227	-0.001
15	333	1.228	1.228	0.000
30	334	1.228	1.227	-0.001
30	335	1.228	1.226	-0.002
30	337	1.227	1.227	0.000
30	339	1.228	1.228	0.000
30	341	1.228	1.227	-0.001
35	342	1.228	1.226	-0.002
35	345	1.228	1.226	-0.002
35	346	1.228	1.227	-0.001
35	347	1.227	1.227	0.000
35	348	1.228	1.226	-0.002
50	349	1.228	1.225	-0.003
50	350	1.228	1.225	-0.003
50	351	1.228	1.225	-0.003
50	353	1.227	1.225	-0.002
50	354	1.228	1.226	-0.002
55	355	1.228	1.225	-0.003
55	357	1.228	1.226	-0.002
55	359	1.228	1.226	-0.002
55	362	1.229	1.227	-0.002
55	363	1.228	1.226	-0.002
100	1	1.227	1.223	-0.004
100	3	1.228	1.223	-0.005
100	4	1.228	1.224	-0.004
100	5	1.228	1.224	-0.004
100	6	1.228	1.225	-0.003
100	7	1.228	1.224	-0.004
100	8	1.228	1.224	-0.004
100	9	1.228	1.225	-0.003
100	10	1.228	1.223	-0.005
100	11	1.228	1.224	-0.004
100	12	1.228	1.224	-0.004
100	13	1.228	1.223	-0.005
100	14	1.227	1.224	-0.003
100	15	1.228	1.224	-0.004
100	16	1.228	1.224	-0.004
100	17	1.228	1.223	-0.005
100	18	1.227	1.224	-0.003
100	19	1.228	1.224	-0.004
100	20	1.228	1.223	-0.005
100	21	1.228	1.224	-0.004
100	22	1.228	1.224	-0.004
100	24	1.228	1.223	-0.005
105	25	1.228	1.225	-0.003
105	26	1.228	1.224	-0.004
105	27	1.227	1.223	-0.004
105	28	1.227	1.224	-0.003
105	29	1.228	1.224	-0.004
Max		1.229	1.229	0.001
Average		1.228	1.226	-0.002
Min		1.227	1.223	-0.005
Std Dev		0.000	0.002	0.002



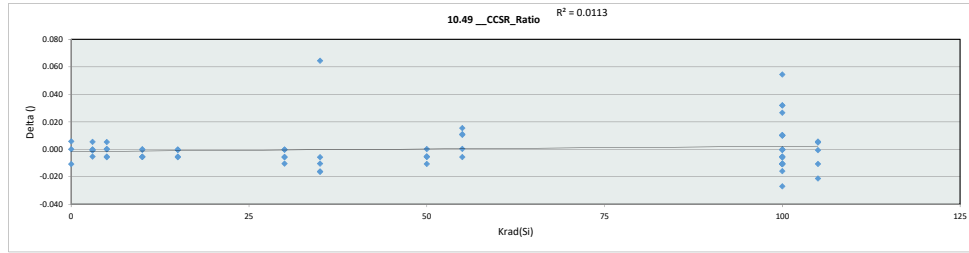
5.111_V_REFCAP_2M_14V	
Test Site	
Tester	
Test Number	
Max Limit	1.237
Min Limit	1.213

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.227	1.227	1.227	1.226	1.227	1.226	1.226	1.225	1.225	1.223	1.223
Average	1.228	1.228	1.227	1.227	1.227	1.227	1.226	1.225	1.226	1.224	1.224
Max	1.229	1.228	1.228	1.228	1.228	1.228	1.227	1.226	1.227	1.225	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

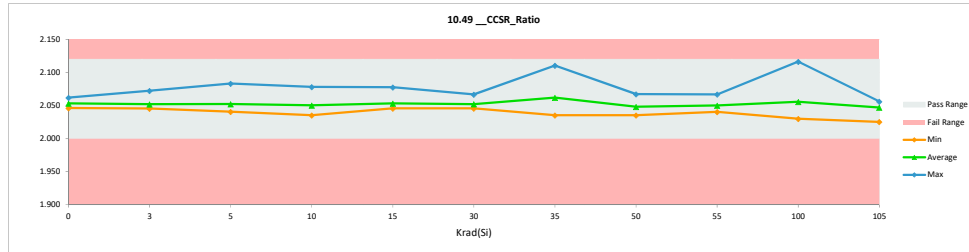


HDR TID Report
TPS7H5001-SP

		10.49 CCSR Ratio		
Test Site				
Tester				
Test Number				
Unit				
Max Limit	2.12		2.12	
Min Limit	2		2	
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	2.040	2.046	0.006
0	118	2.073	2.062	-0.011
0	122	2.051	2.051	0.000
3	301	2.051	2.046	-0.005
3	306	2.046	2.046	-0.001
3	307	2.067	2.072	0.006
3	308	2.051	2.051	0.000
3	309	2.046	2.046	0.000
5	310	2.046	2.051	0.006
5	311	2.046	2.041	-0.005
5	312	2.046	2.041	-0.005
5	314	2.083	2.083	0.000
5	316	2.046	2.046	0.001
10	317	2.041	2.035	-0.005
10	318	2.083	2.078	-0.005
10	319	2.046	2.041	-0.005
10	321	2.051	2.051	0.000
10	322	2.046	2.046	0.000
15	323	2.051	2.046	-0.006
15	325	2.051	2.046	-0.005
15	326	2.051	2.051	0.000
15	328	2.046	2.046	0.000
15	333	2.078	2.078	0.000
30	334	2.067	2.067	0.000
30	335	2.046	2.046	0.000
30	337	2.062	2.056	-0.005
30	339	2.056	2.046	-0.010
30	341	2.051	2.046	-0.006
35	342	2.078	2.062	-0.016
35	345	2.041	2.035	-0.006
35	346	2.046	2.111	0.065
35	347	2.078	2.062	-0.016
35	348	2.051	2.041	-0.010
50	349	2.056	2.051	-0.005
50	350	2.067	2.067	0.000
50	351	2.046	2.041	-0.005
50	353	2.046	2.035	-0.010
50	354	2.051	2.046	-0.005
55	355	2.040	2.052	0.011
55	357	2.046	2.046	0.001
55	359	2.035	2.046	0.011
55	362	2.051	2.067	0.016
55	363	2.046	2.041	-0.005
100	1	2.051	2.051	0.000
100	3	2.051	2.046	-0.005
100	4	2.057	2.051	-0.006
100	5	2.051	2.078	0.027
100	6	2.051	2.051	0.000
100	7	2.062	2.117	0.055
100	8	2.051	2.035	-0.016
100	9	2.041	2.051	0.010
100	10	2.073	2.046	-0.027
100	11	2.051	2.083	0.032
100	12	2.056	2.046	-0.010
100	13	2.041	2.030	-0.011
100	14	2.051	2.051	0.000
100	15	2.051	2.041	-0.010
100	16	2.051	2.041	-0.010
100	17	2.067	2.057	-0.010
100	18	2.051	2.083	0.032
100	19	2.051	2.046	-0.005
100	20	2.062	2.062	0.000
100	21	2.041	2.051	0.010
100	22	2.041	2.051	0.010
100	24	2.046	2.057	0.011
105	25	2.052	2.051	0.000
105	26	2.046	2.051	0.005
105	27	2.046	2.025	-0.021
105	28	2.046	2.052	0.006
105	29	2.067	2.056	-0.010
Max		2.083	2.117	0.065
Average		2.053	2.053	0.000
Min		2.035	2.025	-0.027
Std Dev		0.011	0.016	0.015

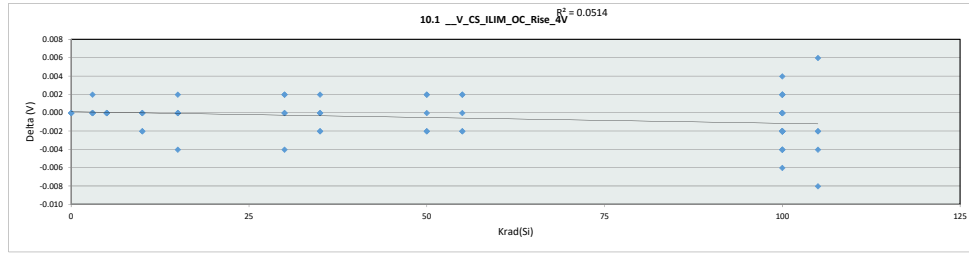


		10.49 CCSR Ratio											
Test Site													
Tester													
Test Number													
Max Limit	2.12												
Min Limit	2												
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105		
LL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000		
Min	2.046	2.046	2.041	2.035	2.046	2.046	2.035	2.035	2.041	2.030	2.025		
Average	2.053	2.052	2.082	2.050	2.053	2.052	2.062	2.048	2.050	2.056	2.047		
Max	2.062	2.072	2.083	2.078	2.078	2.078	2.111	2.067	2.067	2.117	2.056		
UL	2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120		

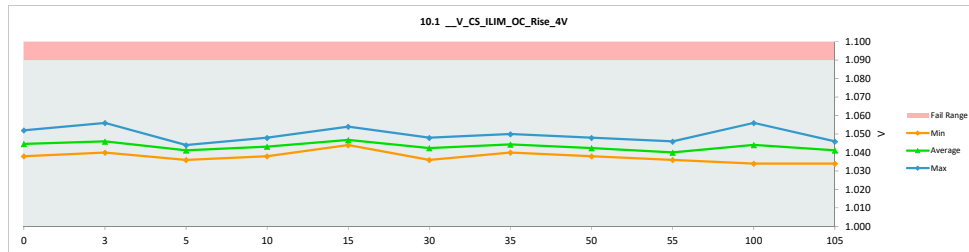


HDR TID Report
TPS7H5001-SP

10.1 V_CS_ILIM_OC_Rise_4V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.09	1.09		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.052	1.052	0.000
0	118	1.038	1.038	0.000
0	122	1.044	1.044	0.000
3	301	1.040	1.040	0.000
3	306	1.046	1.046	0.000
3	307	1.054	1.056	0.002
3	308	1.044	1.044	0.000
3	309	1.044	1.044	0.000
5	310	1.038	1.038	0.000
5	311	1.044	1.044	0.000
5	312	1.044	1.044	0.000
5	314	1.036	1.036	0.000
5	316	1.044	1.044	0.000
10	317	1.044	1.044	0.000
10	318	1.048	1.048	0.000
10	319	1.044	1.044	0.000
10	321	1.040	1.038	-0.002
10	322	1.044	1.042	-0.002
15	323	1.046	1.046	0.000
15	325	1.050	1.046	-0.004
15	326	1.044	1.044	0.000
15	328	1.042	1.044	0.002
15	333	1.054	1.054	0.000
30	334	1.040	1.036	-0.004
30	335	1.046	1.046	0.000
30	337	1.046	1.048	0.002
30	339	1.038	1.038	0.000
30	341	1.042	1.044	0.002
35	342	1.048	1.046	-0.002
35	345	1.038	1.040	0.002
35	346	1.044	1.044	0.000
35	347	1.050	1.050	0.000
35	348	1.044	1.042	-0.002
50	349	1.044	1.042	-0.002
50	350	1.038	1.038	0.000
50	351	1.040	1.038	-0.002
50	353	1.044	1.046	0.002
50	354	1.046	1.048	0.002
55	355	1.040	1.042	0.002
55	357	1.034	1.036	0.002
55	359	1.042	1.040	-0.002
55	362	1.036	1.036	0.000
55	363	1.048	1.046	-0.002
100	1	1.048	1.050	0.002
100	3	1.034	1.034	0.000
100	4	1.040	1.040	0.000
100	5	1.040	1.036	-0.004
100	6	1.042	1.040	-0.002
100	7	1.042	1.044	0.002
100	8	1.050	1.046	-0.004
100	9	1.044	1.042	-0.002
100	10	1.048	1.050	0.002
100	11	1.042	1.044	0.002
100	12	1.044	1.044	0.000
100	13	1.040	1.040	0.000
100	14	1.044	1.042	-0.002
100	15	1.046	1.042	-0.004
100	16	1.048	1.042	-0.006
100	17	1.038	1.036	-0.002
100	18	1.044	1.048	0.004
100	19	1.050	1.048	-0.002
100	20	1.050	1.048	-0.002
100	21	1.054	1.052	-0.002
100	22	1.058	1.056	-0.002
100	24	1.048	1.046	-0.002
105	25	1.048	1.046	-0.002
105	26	1.040	1.046	0.006
105	27	1.046	1.044	-0.002
105	28	1.038	1.034	-0.004
105	29	1.044	1.036	-0.008
Max		1.058	1.056	0.006
Average		1.044	1.043	-0.001
Min		1.034	1.034	-0.008
Std Dev		0.005	0.005	0.002

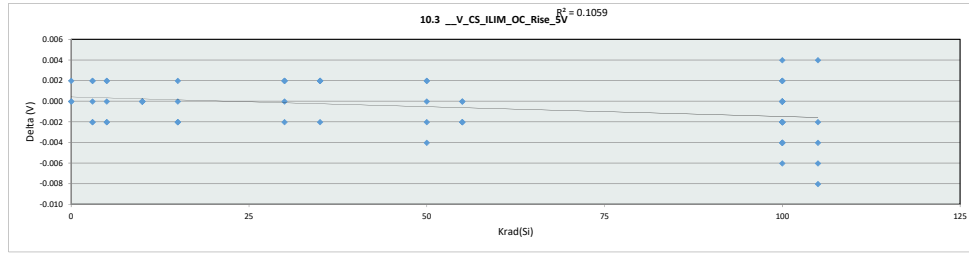


10.1 V_CS_ILIM_OC_Rise											
Test Site											
Tester											
Test Number											
Max Limit	1.09	V									
Min Limit	V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.038	1.040	1.036	1.038	1.044	1.036	1.040	1.038	1.036	1.034	1.034
Average	1.045	1.046	1.041	1.043	1.047	1.042	1.044	1.042	1.040	1.044	1.041
Max	1.052	1.056	1.044	1.048	1.054	1.048	1.050	1.048	1.046	1.056	1.046
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090

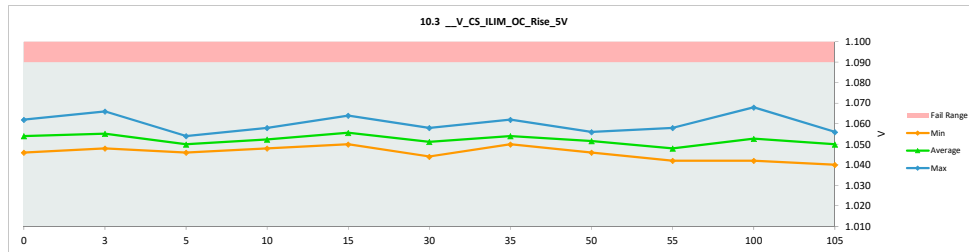


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		10.3 V_CS_ILIM_OC_Rise_5V		
Test Site	Tester			
Test Number	Unit	V	V	
Max Limit	1.09	1.09		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.062	1.062	0.000
0	118	1.046	1.046	0.000
0	122	1.052	1.054	0.002
3	301	1.050	1.048	-0.002
3	306	1.056	1.056	0.000
3	307	1.064	1.066	0.002
3	308	1.052	1.054	0.002
3	309	1.054	1.052	-0.002
5	310	1.046	1.046	0.000
5	311	1.052	1.054	0.002
5	312	1.056	1.054	-0.002
5	314	1.044	1.046	0.002
5	316	1.052	1.050	-0.002
10	317	1.052	1.052	0.000
10	318	1.058	1.058	0.000
10	319	1.054	1.054	0.000
10	321	1.048	1.048	0.000
10	322	1.050	1.050	0.000
15	323	1.056	1.054	-0.002
15	325	1.058	1.056	-0.002
15	326	1.054	1.054	0.000
15	328	1.052	1.050	-0.002
15	333	1.062	1.064	0.002
30	334	1.046	1.044	-0.002
30	335	1.054	1.054	0.000
30	337	1.056	1.058	0.002
30	339	1.046	1.048	0.002
30	341	1.050	1.052	0.002
35	342	1.054	1.052	-0.002
35	345	1.048	1.050	0.002
35	346	1.052	1.054	0.002
35	347	1.060	1.062	0.002
35	348	1.050	1.052	0.002
50	349	1.054	1.050	-0.004
50	350	1.044	1.046	0.002
50	351	1.052	1.050	-0.002
50	353	1.054	1.056	0.002
50	354	1.056	1.056	0.000
55	355	1.048	1.048	0.000
55	357	1.044	1.044	0.000
55	359	1.050	1.048	-0.002
55	362	1.044	1.042	-0.002
55	363	1.060	1.058	-0.002
100	1	1.058	1.060	0.002
100	3	1.044	1.042	-0.002
100	4	1.048	1.050	0.002
100	5	1.048	1.044	-0.004
100	6	1.050	1.050	0.000
100	7	1.050	1.050	0.000
100	8	1.058	1.054	-0.004
100	9	1.052	1.050	-0.002
100	10	1.058	1.058	0.000
100	11	1.050	1.052	0.002
100	12	1.054	1.052	-0.002
100	13	1.048	1.048	0.000
100	14	1.054	1.050	-0.004
100	15	1.056	1.052	-0.004
100	16	1.056	1.050	-0.006
100	17	1.046	1.044	-0.002
100	18	1.050	1.054	0.004
100	19	1.058	1.056	-0.002
100	20	1.058	1.056	-0.002
100	21	1.062	1.062	0.000
100	22	1.070	1.068	-0.002
100	24	1.058	1.058	0.000
105	25	1.060	1.056	-0.004
105	26	1.050	1.054	0.004
105	27	1.056	1.054	-0.002
105	28	1.046	1.040	-0.006
105	29	1.054	1.046	-0.008
	Max	1.070	1.068	0.004
	Average	1.053	1.052	-0.001
	Min	1.044	1.040	-0.008
	Std Dev	0.005	0.006	0.002

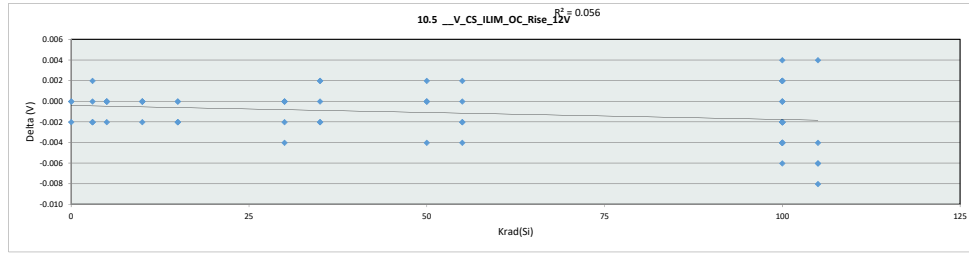


		10.3 V_CS_ILIM_OC_Rise										
Test Site	Tester											
Test Number	Unit	1.09	V									
Max Limit	1.09	V										
Min Limit		V										
Krad(Si)	LL	Min	Average	Max	UL							
0		1.046	1.054	1.062	1.090							
3		1.048	1.055	1.066	1.090							
5		1.046	1.050	1.054	1.090							
10		1.048	1.052	1.058	1.090							
15		1.050	1.051	1.054	1.090							
30		1.044	1.054	1.062	1.090							
35		1.050	1.054	1.058	1.090							
50		1.046	1.052	1.056	1.090							
55		1.042	1.052	1.056	1.090							
100		1.042	1.052	1.058	1.090							
105		1.040	1.053	1.068	1.090							

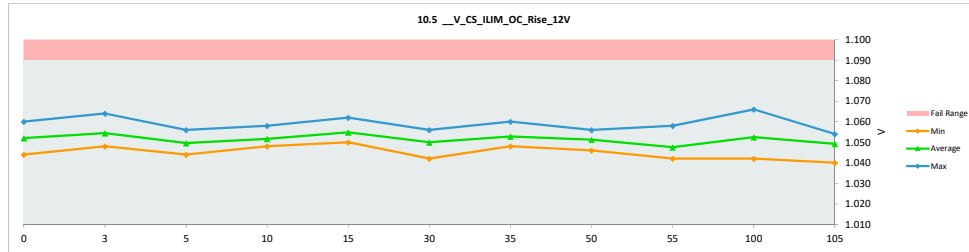


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10.5 V_CS_ILIM_OC_Rise_12V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.09	1.09		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.062	1.060	-0.002
0	118	1.044	1.044	0.000
0	122	1.052	1.052	0.000
3	301	1.050	1.048	-0.002
3	306	1.056	1.054	-0.002
3	307	1.064	1.064	0.000
3	308	1.052	1.054	0.002
3	309	1.054	1.052	-0.002
5	310	1.044	1.044	0.000
5	311	1.054	1.054	0.000
5	312	1.056	1.056	0.000
5	314	1.044	1.044	0.000
5	316	1.052	1.050	-0.002
10	317	1.052	1.050	-0.002
10	318	1.058	1.058	0.000
10	319	1.052	1.052	0.000
10	321	1.048	1.048	0.000
10	322	1.050	1.050	0.000
15	323	1.054	1.054	0.000
15	325	1.056	1.056	0.000
15	326	1.054	1.052	-0.002
15	328	1.052	1.050	-0.002
15	333	1.064	1.062	-0.002
30	334	1.046	1.042	-0.004
30	335	1.056	1.054	-0.002
30	337	1.056	1.056	0.000
30	339	1.048	1.048	0.000
30	341	1.050	1.050	0.000
35	342	1.054	1.052	-0.002
35	345	1.046	1.048	0.002
35	346	1.054	1.054	0.000
35	347	1.058	1.060	0.002
35	348	1.052	1.050	-0.002
50	349	1.054	1.050	-0.004
50	350	1.046	1.046	0.000
50	351	1.050	1.050	0.000
50	353	1.054	1.054	0.000
50	354	1.054	1.056	0.002
55	355	1.048	1.048	0.000
55	357	1.042	1.044	0.002
55	359	1.050	1.046	-0.004
55	362	1.044	1.042	-0.002
55	363	1.060	1.058	-0.002
100	1	1.058	1.060	0.002
100	3	1.044	1.042	-0.002
100	4	1.048	1.050	0.002
100	5	1.048	1.042	-0.006
100	6	1.050	1.050	0.000
100	7	1.050	1.052	0.002
100	8	1.058	1.054	-0.004
100	9	1.052	1.048	-0.004
100	10	1.058	1.058	0.000
100	11	1.050	1.052	0.002
100	12	1.054	1.052	-0.002
100	13	1.048	1.048	0.000
100	14	1.054	1.050	-0.004
100	15	1.056	1.052	-0.004
100	16	1.056	1.052	-0.004
100	17	1.046	1.044	-0.002
100	18	1.050	1.054	0.004
100	19	1.058	1.056	-0.002
100	20	1.058	1.056	-0.002
100	21	1.062	1.060	-0.002
100	22	1.068	1.066	-0.002
100	24	1.058	1.056	-0.002
105	25	1.060	1.054	-0.006
105	26	1.050	1.054	0.004
105	27	1.056	1.052	-0.004
105	28	1.046	1.040	-0.006
105	29	1.054	1.046	-0.008
Max	1.068	1.066	0.004	
Average	1.053	1.052	-0.001	
Min	1.042	1.040	-0.008	
Std Dev	0.005	0.005	0.002	

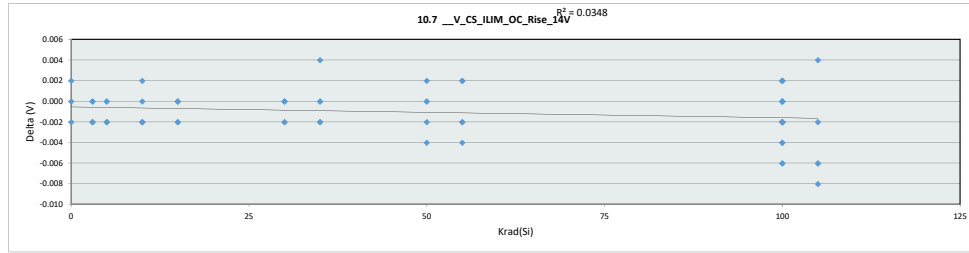


10.5 V_CS_ILIM_OC_Rise											
Test Site											
Tester											
Test Number											
Max Limit	1.09	V									
Min Limit	V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.044	1.048	1.044	1.048	1.050	1.042	1.048	1.046	1.042	1.042	1.040
Average	1.052	1.054	1.050	1.052	1.055	1.050	1.053	1.051	1.048	1.052	1.049
Max	1.060	1.064	1.056	1.058	1.062	1.056	1.060	1.056	1.058	1.066	1.054
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090

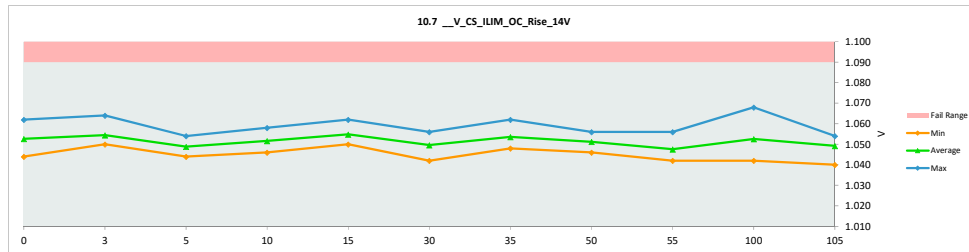


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10.7 V_CS_ILIM_OC_Rise_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.09	1.09		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	1.060	1.062	0.002
0	118	1.046	1.044	-0.002
0	122	1.052	1.052	0.000
3	301	1.050	1.050	0.000
3	306	1.056	1.054	-0.002
3	307	1.064	1.064	0.000
3	308	1.054	1.052	-0.002
3	309	1.054	1.052	-0.002
5	310	1.044	1.044	0.000
5	311	1.054	1.052	-0.002
5	312	1.056	1.054	-0.002
5	314	1.044	1.044	0.000
5	316	1.052	1.050	-0.002
10	317	1.050	1.052	0.002
10	318	1.058	1.058	0.000
10	319	1.054	1.052	-0.002
10	321	1.048	1.046	-0.002
10	322	1.052	1.050	-0.002
15	323	1.054	1.054	0.000
15	325	1.056	1.056	0.000
15	326	1.054	1.052	-0.002
15	328	1.050	1.050	0.000
15	333	1.064	1.062	-0.002
30	334	1.044	1.042	-0.002
30	335	1.056	1.054	-0.002
30	337	1.056	1.056	0.000
30	339	1.046	1.046	0.000
30	341	1.050	1.050	0.000
35	342	1.054	1.052	-0.002
35	345	1.048	1.048	0.000
35	346	1.052	1.056	0.004
35	347	1.062	1.062	0.000
35	348	1.052	1.050	-0.002
50	349	1.054	1.050	-0.004
50	350	1.046	1.046	0.000
50	351	1.052	1.050	-0.002
50	353	1.054	1.054	0.000
50	354	1.054	1.056	0.002
55	355	1.046	1.048	0.002
55	357	1.042	1.044	0.002
55	359	1.050	1.048	-0.002
55	362	1.044	1.042	-0.002
55	363	1.060	1.056	-0.004
100	1	1.058	1.060	0.002
100	3	1.044	1.044	0.000
100	4	1.048	1.050	0.002
100	5	1.048	1.042	-0.006
100	6	1.050	1.050	0.000
100	7	1.050	1.050	0.000
100	8	1.058	1.052	-0.006
100	9	1.052	1.050	-0.002
100	10	1.058	1.058	0.000
100	11	1.050	1.052	0.002
100	12	1.054	1.052	-0.002
100	13	1.048	1.048	0.000
100	14	1.052	1.050	-0.002
100	15	1.056	1.052	-0.004
100	16	1.056	1.052	-0.004
100	17	1.046	1.044	-0.002
100	18	1.052	1.054	0.002
100	19	1.058	1.056	-0.002
100	20	1.058	1.056	-0.002
100	21	1.062	1.060	-0.002
100	22	1.068	1.068	0.000
100	24	1.058	1.056	-0.002
105	25	1.060	1.054	-0.006
105	26	1.050	1.054	0.004
105	27	1.054	1.052	-0.002
105	28	1.046	1.040	-0.006
105	29	1.054	1.046	-0.008
	Max	1.068	1.068	0.004
	Average	1.053	1.052	-0.001
	Min	1.042	1.040	-0.008
	Std Dev	0.005	0.006	0.002



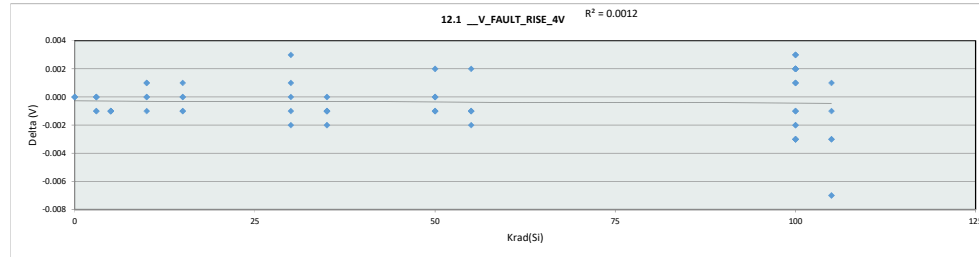
10.7 V_CS_ILIM_OC_Rise											
Test Site											
Tester											
Test Number											
Max Limit	1.09	V									
Min Limit	V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.044	1.050	1.044	1.046	1.050	1.042	1.048	1.046	1.042	1.042	1.040
Average	1.053	1.054	1.049	1.052	1.055	1.050	1.054	1.051	1.048	1.053	1.049
Max	1.062	1.064	1.054	1.058	1.062	1.056	1.062	1.056	1.056	1.068	1.054
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090



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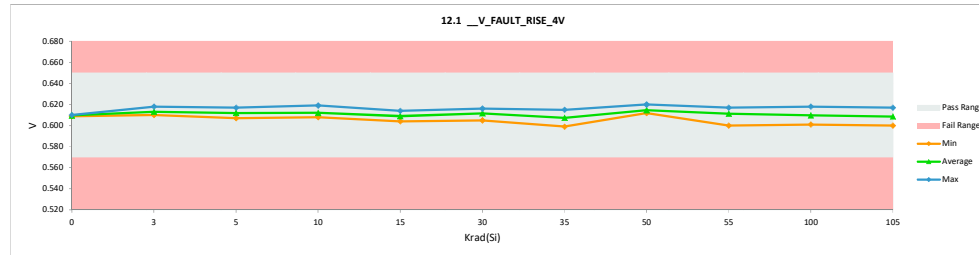
12.1 __V_FAULT_RISE_4V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	0.65 0.65
Min Limit	0.57 0.57

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	0.609	0.609	0.000
0	118	0.610	0.610	0.000
0	122	0.610	0.610	0.000
3	301	0.611	0.610	-0.001
3	306	0.615	0.615	0.000
3	307	0.610	0.610	0.000
3	308	0.614	0.613	-0.001
3	309	0.618	0.618	0.000
5	310	0.611	0.610	-0.001
5	311	0.608	0.607	-0.001
5	312	0.615	0.614	-0.001
5	314	0.618	0.617	-0.001
5	316	0.613	0.612	-0.001
10	317	0.608	0.608	0.000
10	318	0.614	0.615	0.001
10	319	0.618	0.619	0.001
10	321	0.609	0.609	0.000
10	322	0.611	0.610	-0.001
15	323	0.605	0.605	0.000
15	325	0.604	0.604	0.000
15	326	0.612	0.611	-0.001
15	328	0.615	0.614	-0.001
15	333	0.610	0.611	0.001
30	334	0.605	0.608	0.003
30	335	0.615	0.615	0.000
30	337	0.613	0.614	0.001
30	339	0.607	0.605	-0.002
30	341	0.617	0.616	-0.001
35	342	0.604	0.603	-0.001
35	345	0.613	0.611	-0.002
35	346	0.610	0.609	-0.001
35	347	0.616	0.615	-0.001
35	348	0.599	0.599	0.000
50	349	0.614	0.613	-0.001
50	350	0.612	0.612	0.000
50	351	0.611	0.613	0.002
50	353	0.620	0.620	0.000
50	354	0.616	0.615	-0.001
55	355	0.615	0.617	0.002
55	357	0.612	0.611	-0.001
55	359	0.601	0.600	-0.001
55	362	0.614	0.613	-0.001
55	363	0.618	0.616	-0.002
100	1	0.609	0.608	-0.001
100	3	0.604	0.601	-0.003
100	4	0.604	0.606	0.002
100	5	0.610	0.612	0.002
100	6	0.619	0.616	-0.003
100	7	0.611	0.614	0.003
100	8	0.608	0.605	-0.003
100	9	0.602	0.603	0.001
100	10	0.610	0.608	-0.002
100	11	0.613	0.614	0.001
100	12	0.606	0.605	-0.001
100	13	0.615	0.617	0.002
100	14	0.609	0.608	-0.001
100	15	0.619	0.616	-0.003
100	16	0.608	0.609	0.001
100	17	0.606	0.604	-0.002
100	18	0.616	0.618	0.002
100	19	0.601	0.604	0.003
100	20	0.610	0.607	-0.003
100	21	0.608	0.611	0.003
100	22	0.612	0.614	0.002
100	24	0.613	0.615	0.002
105	25	0.618	0.617	-0.001
105	26	0.603	0.600	-0.003
105	27	0.612	0.609	-0.003
105	28	0.611	0.604	-0.007
105	29	0.612	0.613	0.001
Max	0.620	0.620	0.003	
Average	0.611	0.611	0.000	
Min	0.599	0.599	-0.007	
Std Dev	0.005	0.005	0.002	



12.1 __V_FAULT_RISE_4V	
Test Site	
Tester	
Test Number	
Max Limit	0.65 V
Min Limit	0.57 V

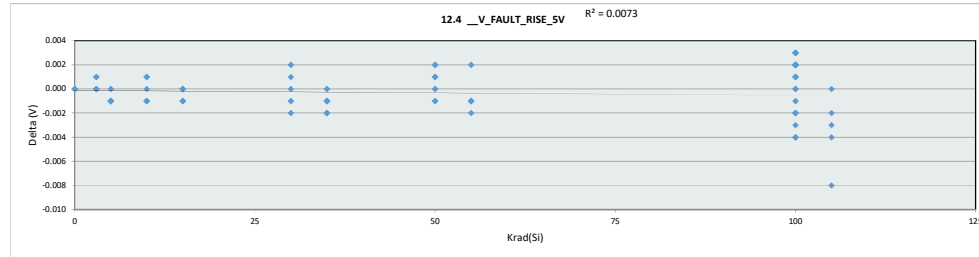
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.609	0.610	0.607	0.608	0.604	0.605	0.599	0.612	0.600	0.601	0.600
Average	0.610	0.613	0.612	0.612	0.609	0.612	0.607	0.615	0.611	0.610	0.609
Max	0.610	0.618	0.617	0.619	0.614	0.616	0.615	0.620	0.617	0.618	0.617
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



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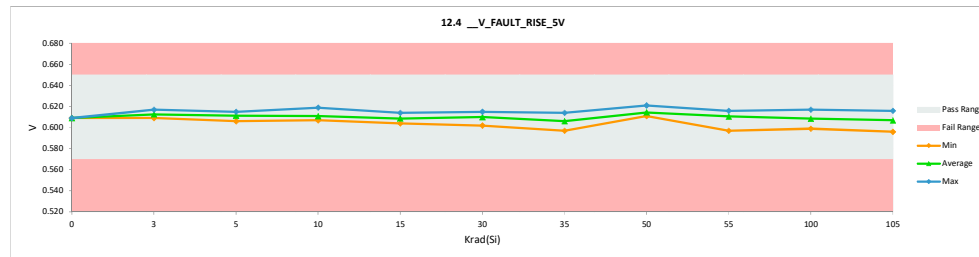
12.4 __V_FAULT_RISE_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.65
Min Limit	0.57

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	0.609	0.609	0.000
0	118	0.609	0.609	0.000
0	122	0.609	0.609	0.000
3	301	0.609	0.609	0.000
3	306	0.614	0.615	0.001
3	307	0.609	0.609	0.000
3	308	0.612	0.613	0.001
3	309	0.617	0.617	0.000
5	310	0.609	0.609	0.000
5	311	0.607	0.606	-0.001
5	312	0.615	0.614	-0.001
5	314	0.616	0.615	-0.001
5	316	0.613	0.612	-0.001
10	317	0.608	0.607	-0.001
10	318	0.613	0.613	0.000
10	319	0.618	0.619	0.001
10	321	0.606	0.607	0.001
10	322	0.610	0.609	-0.001
15	323	0.605	0.604	-0.001
15	325	0.604	0.604	0.000
15	326	0.611	0.611	0.000
15	328	0.615	0.614	-0.001
15	333	0.610	0.610	0.000
30	334	0.604	0.606	0.002
30	335	0.614	0.614	0.000
30	337	0.612	0.613	0.001
30	339	0.604	0.602	-0.002
30	341	0.616	0.615	-0.001
35	342	0.602	0.600	-0.002
35	345	0.613	0.611	-0.002
35	346	0.610	0.609	-0.001
35	347	0.615	0.614	-0.001
35	348	0.597	0.597	0.000
50	349	0.612	0.613	0.001
50	350	0.612	0.611	-0.001
50	351	0.610	0.612	0.002
50	353	0.620	0.621	0.001
50	354	0.615	0.615	0.000
55	355	0.614	0.616	0.002
55	357	0.612	0.611	-0.001
55	359	0.599	0.597	-0.002
55	362	0.614	0.613	-0.001
55	363	0.617	0.616	-0.001
100	1	0.606	0.607	0.001
100	3	0.601	0.599	-0.002
100	4	0.601	0.604	0.003
100	5	0.609	0.612	0.003
100	6	0.618	0.614	-0.004
100	7	0.611	0.613	0.002
100	8	0.607	0.604	-0.003
100	9	0.600	0.601	0.001
100	10	0.608	0.606	-0.002
100	11	0.613	0.613	0.000
100	12	0.603	0.603	0.000
100	13	0.613	0.615	0.002
100	14	0.608	0.607	-0.001
100	15	0.619	0.615	-0.004
100	16	0.607	0.609	0.002
100	17	0.604	0.602	-0.002
100	18	0.614	0.617	0.003
100	19	0.599	0.602	0.003
100	20	0.609	0.605	-0.004
100	21	0.608	0.610	0.002
100	22	0.613	0.614	0.001
100	24	0.613	0.615	0.002
105	25	0.618	0.616	-0.002
105	26	0.600	0.596	-0.004
105	27	0.611	0.608	-0.003
105	28	0.610	0.602	-0.008
105	29	0.613	0.613	0.000
	Max	0.620	0.621	0.003
	Average	0.610	0.610	0.000
	Min	0.597	0.596	-0.008
	Std Dev	0.005	0.006	0.002



12.4 __V_FAULT_RISE_5V	
Test Site	
Tester	
Test Number	
Max Limit	0.65
Min Limit	0.57

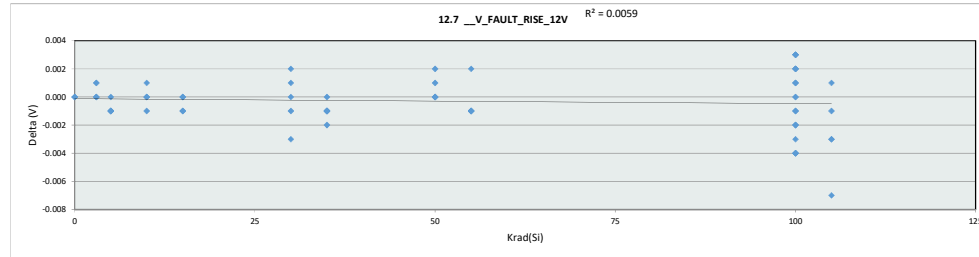
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.609	0.609	0.606	0.607	0.604	0.602	0.597	0.611	0.597	0.599	0.596
Average	0.609	0.613	0.611	0.611	0.609	0.610	0.606	0.614	0.611	0.609	0.607
Max	0.609	0.617	0.615	0.619	0.614	0.615	0.614	0.621	0.616	0.617	0.616
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



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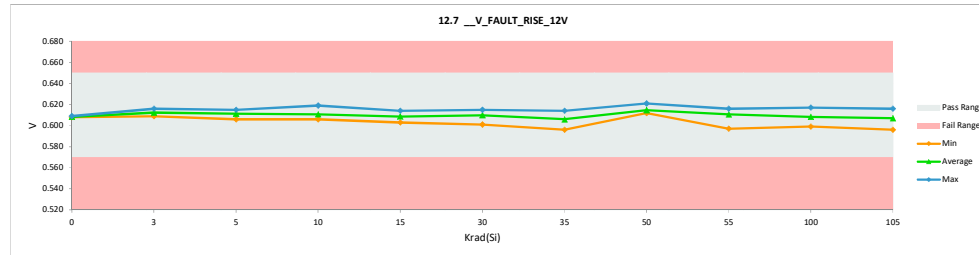
12.7 __V_FAULT_RISE_12V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	0.65 0.65
Min Limit	0.57 0.57

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	0.609	0.609	0.000
0	118	0.608	0.608	0.000
0	122	0.608	0.608	0.000
3	301	0.609	0.609	0.000
3	306	0.614	0.615	0.001
3	307	0.609	0.609	0.000
3	308	0.612	0.613	0.001
3	309	0.616	0.616	0.000
5	310	0.609	0.609	0.000
5	311	0.607	0.606	-0.001
5	312	0.615	0.614	-0.001
5	314	0.616	0.615	-0.001
5	316	0.613	0.612	-0.001
10	317	0.607	0.607	0.000
10	318	0.613	0.613	0.000
10	319	0.618	0.619	0.001
10	321	0.606	0.606	0.000
10	322	0.610	0.609	-0.001
15	323	0.605	0.605	0.000
15	325	0.604	0.603	-0.001
15	326	0.611	0.611	0.000
15	328	0.615	0.614	-0.001
15	333	0.610	0.610	0.000
30	334	0.604	0.606	0.002
30	335	0.614	0.614	0.000
30	337	0.612	0.613	0.001
30	339	0.604	0.601	-0.003
30	341	0.616	0.615	-0.001
35	342	0.601	0.600	-0.001
35	345	0.613	0.611	-0.002
35	346	0.610	0.609	-0.001
35	347	0.615	0.614	-0.001
35	348	0.596	0.596	0.000
50	349	0.612	0.613	0.001
50	350	0.612	0.612	0.000
50	351	0.610	0.612	0.002
50	353	0.621	0.621	0.000
50	354	0.615	0.615	0.000
55	355	0.614	0.616	0.002
55	357	0.612	0.611	-0.001
55	359	0.598	0.597	-0.001
55	362	0.614	0.613	-0.001
55	363	0.617	0.616	-0.001
100	1	0.606	0.606	0.000
100	3	0.601	0.599	-0.002
100	4	0.601	0.604	0.003
100	5	0.609	0.612	0.003
100	6	0.618	0.614	-0.004
100	7	0.611	0.612	0.001
100	8	0.607	0.604	-0.003
100	9	0.600	0.601	0.001
100	10	0.608	0.606	-0.002
100	11	0.612	0.612	0.000
100	12	0.603	0.602	-0.001
100	13	0.613	0.615	0.002
100	14	0.608	0.607	-0.001
100	15	0.619	0.615	-0.004
100	16	0.607	0.609	0.002
100	17	0.604	0.602	-0.002
100	18	0.614	0.617	0.003
100	19	0.599	0.602	0.003
100	20	0.609	0.605	-0.004
100	21	0.608	0.610	0.002
100	22	0.613	0.614	0.001
100	24	0.613	0.615	0.002
105	25	0.617	0.616	-0.001
105	26	0.599	0.596	-0.003
105	27	0.611	0.608	-0.003
105	28	0.609	0.602	-0.007
105	29	0.612	0.613	0.001
	Max	0.621	0.621	0.003
	Average	0.610	0.609	0.000
	Min	0.596	0.596	-0.007
	Std Dev	0.005	0.006	0.002



12.7 __V_FAULT_RISE_12V	
Test Site	
Tester	
Test Number	
Max Limit	0.65 V
Min Limit	0.57 V

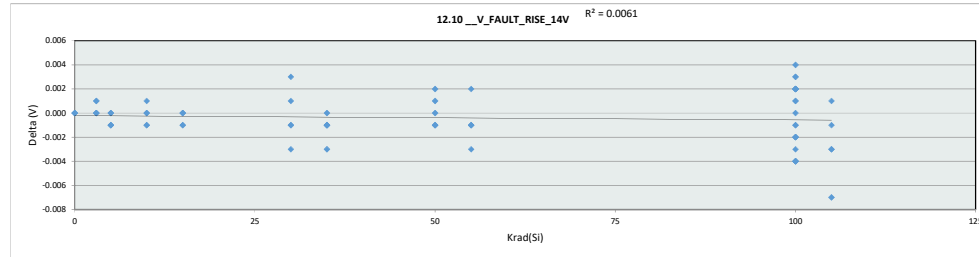
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.608	0.609	0.606	0.606	0.603	0.601	0.596	0.612	0.597	0.599	0.596
Average	0.608	0.612	0.611	0.611	0.609	0.610	0.606	0.615	0.611	0.608	0.607
Max	0.609	0.616	0.615	0.619	0.614	0.615	0.614	0.621	0.616	0.617	0.616
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



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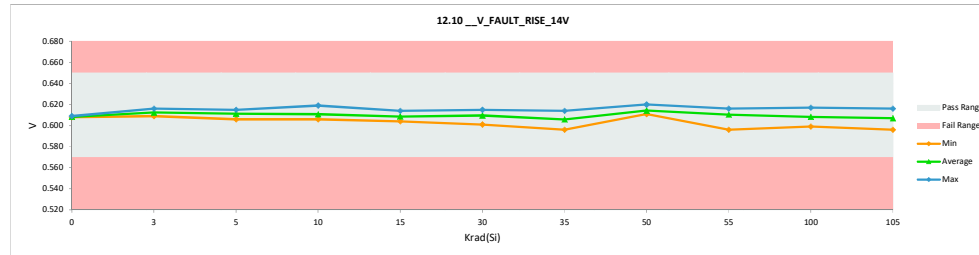
12.10 __V_FAULT_RISE_14V			
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	0.65	0.65	
Min Limit	0.57	0.57	

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	0.609	0.609	0.000
0	118	0.608	0.608	0.000
0	122	0.608	0.608	0.000
3	301	0.609	0.609	0.000
3	306	0.614	0.615	0.001
3	307	0.609	0.609	0.000
3	308	0.612	0.613	0.001
3	309	0.616	0.616	0.000
5	310	0.609	0.609	0.000
5	311	0.607	0.606	-0.001
5	312	0.615	0.614	-0.001
5	314	0.616	0.615	-0.001
5	316	0.612	0.612	0.000
10	317	0.608	0.607	-0.001
10	318	0.613	0.613	0.000
10	319	0.618	0.619	0.001
10	321	0.606	0.606	0.000
10	322	0.610	0.609	-0.001
15	323	0.605	0.604	-0.001
15	325	0.604	0.604	0.000
15	326	0.611	0.611	0.000
15	328	0.615	0.614	-0.001
15	333	0.610	0.610	0.000
30	334	0.603	0.606	0.003
30	335	0.614	0.613	-0.001
30	337	0.612	0.613	0.001
30	339	0.604	0.601	-0.003
30	341	0.616	0.615	-0.001
35	342	0.601	0.600	-0.001
35	345	0.613	0.610	-0.003
35	346	0.610	0.609	-0.001
35	347	0.615	0.614	-0.001
35	348	0.596	0.596	0.000
50	349	0.612	0.613	0.001
50	350	0.612	0.611	-0.001
50	351	0.610	0.612	0.002
50	353	0.621	0.620	-0.001
50	354	0.615	0.615	0.000
55	355	0.614	0.616	0.002
55	357	0.612	0.611	-0.001
55	359	0.599	0.596	-0.003
55	362	0.614	0.613	-0.001
55	363	0.617	0.616	-0.001
100	1	0.606	0.606	0.000
100	3	0.601	0.599	-0.002
100	4	0.601	0.604	0.003
100	5	0.610	0.612	0.002
100	6	0.618	0.614	-0.004
100	7	0.611	0.613	0.002
100	8	0.607	0.604	-0.003
100	9	0.600	0.601	0.001
100	10	0.608	0.606	-0.002
100	11	0.613	0.612	-0.001
100	12	0.603	0.602	-0.001
100	13	0.613	0.615	0.002
100	14	0.608	0.606	-0.002
100	15	0.619	0.615	-0.004
100	16	0.607	0.609	0.002
100	17	0.604	0.602	-0.002
100	18	0.613	0.617	0.004
100	19	0.599	0.602	0.003
100	20	0.609	0.605	-0.004
100	21	0.608	0.610	0.002
100	22	0.613	0.614	0.001
100	24	0.613	0.615	0.002
105	25	0.617	0.616	-0.001
105	26	0.599	0.596	-0.003
105	27	0.611	0.608	-0.003
105	28	0.609	0.602	-0.007
105	29	0.612	0.613	0.001
	Max	0.621	0.620	0.004
	Average	0.610	0.609	0.000
	Min	0.596	0.596	-0.007
	Std Dev	0.005	0.006	0.002



12.10 __V_FAULT_RISE_14V			
Test Site			
Tester			
Test Number			
Max Limit	0.65	V	
Min Limit	0.57	V	

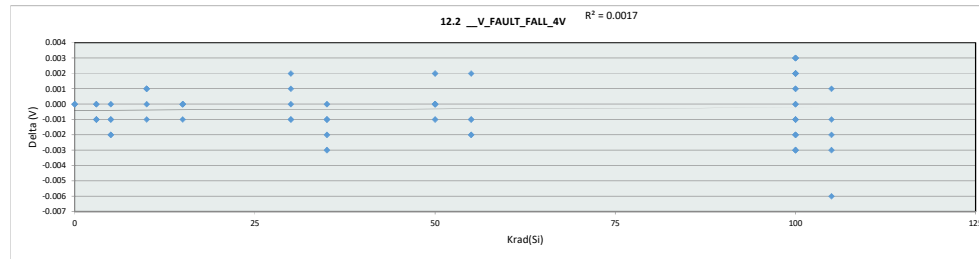
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.608	0.609	0.606	0.606	0.604	0.601	0.596	0.611	0.596	0.599	0.596
Average	0.608	0.612	0.611	0.611	0.609	0.610	0.606	0.614	0.610	0.608	0.607
Max	0.609	0.616	0.615	0.619	0.614	0.615	0.614	0.620	0.616	0.617	0.616
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



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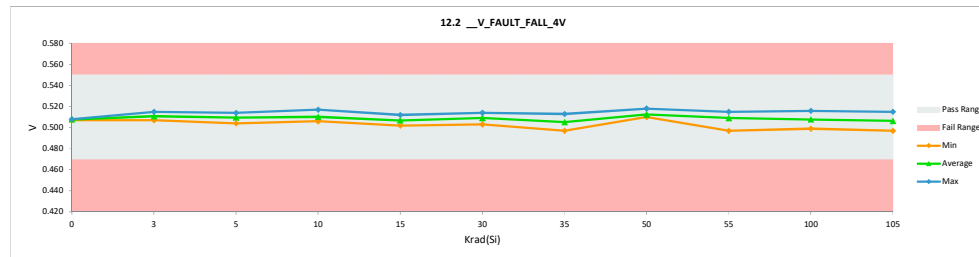
12.2 __V_FAULT_FALL_4V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	0.55 0.55
Min Limit	0.47 0.47

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	0.507	0.507	0.000
0	118	0.508	0.508	0.000
0	122	0.508	0.508	0.000
3	301	0.508	0.508	0.000
3	306	0.513	0.513	0.000
3	307	0.508	0.507	-0.001
3	308	0.512	0.511	-0.001
3	309	0.516	0.515	-0.001
5	310	0.508	0.508	0.000
5	311	0.506	0.504	-0.002
5	312	0.513	0.512	-0.001
5	314	0.516	0.514	-0.002
5	316	0.511	0.510	-0.001
10	317	0.506	0.506	0.000
10	318	0.512	0.513	0.001
10	319	0.516	0.517	0.001
10	321	0.506	0.507	0.001
10	322	0.509	0.508	-0.001
15	323	0.503	0.503	0.000
15	325	0.502	0.502	0.000
15	326	0.509	0.509	0.000
15	328	0.513	0.512	-0.001
15	333	0.508	0.508	0.000
30	334	0.503	0.505	0.002
30	335	0.512	0.512	0.000
30	337	0.511	0.512	0.001
30	339	0.504	0.503	-0.001
30	341	0.515	0.514	-0.001
35	342	0.502	0.500	-0.002
35	345	0.512	0.509	-0.003
35	346	0.508	0.507	-0.001
35	347	0.514	0.513	-0.001
35	348	0.497	0.497	0.000
50	349	0.511	0.510	-0.001
50	350	0.510	0.510	0.000
50	351	0.509	0.511	0.002
50	353	0.518	0.518	0.000
50	354	0.513	0.513	0.000
55	355	0.513	0.515	0.002
55	357	0.510	0.509	-0.001
55	359	0.499	0.497	-0.002
55	362	0.512	0.511	-0.001
55	363	0.516	0.514	-0.002
100	1	0.506	0.506	0.000
100	3	0.501	0.499	-0.002
100	4	0.501	0.504	0.003
100	5	0.508	0.510	0.002
100	6	0.517	0.514	-0.003
100	7	0.509	0.512	0.003
100	8	0.506	0.503	-0.003
100	9	0.500	0.501	0.001
100	10	0.507	0.506	-0.001
100	11	0.511	0.511	0.000
100	12	0.503	0.502	-0.001
100	13	0.512	0.515	0.003
100	14	0.507	0.506	-0.001
100	15	0.517	0.515	-0.002
100	16	0.506	0.508	0.002
100	17	0.504	0.502	-0.002
100	18	0.514	0.516	0.002
100	19	0.499	0.502	0.003
100	20	0.508	0.505	-0.003
100	21	0.506	0.508	0.002
100	22	0.510	0.511	0.001
100	24	0.511	0.513	0.002
105	25	0.516	0.515	-0.001
105	26	0.500	0.497	-0.003
105	27	0.509	0.507	-0.002
105	28	0.508	0.502	-0.006
105	29	0.510	0.511	0.001
Max		0.518	0.518	0.003
Average		0.509	0.508	0.000
Min		0.497	0.497	-0.006
Std Dev		0.005	0.005	0.002



12.2 __V_FAULT_FALL_4V	
Test Site	
Tester	
Test Number	
Max Limit	0.55 V
Min Limit	0.47 V

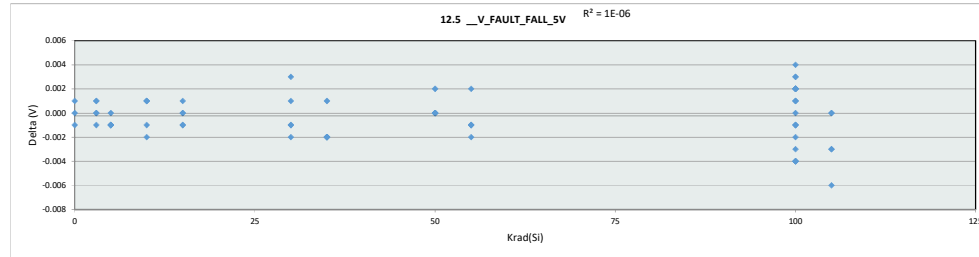
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.507	0.507	0.504	0.506	0.502	0.503	0.497	0.510	0.497	0.499	0.497
Average	0.508	0.511	0.510	0.510	0.507	0.509	0.505	0.512	0.509	0.508	0.506
Max	0.508	0.515	0.514	0.517	0.512	0.514	0.513	0.518	0.515	0.516	0.515
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



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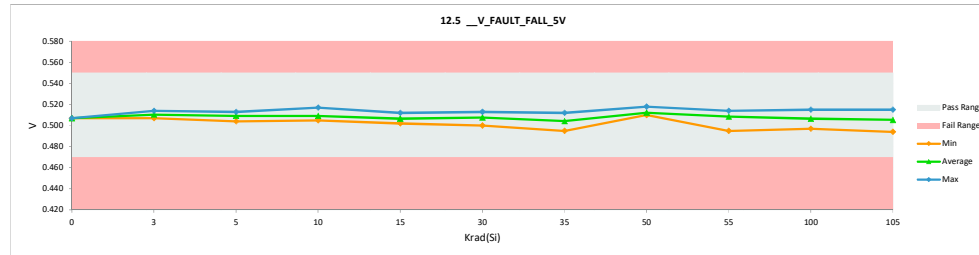
12.5 __V_FAULT_FALL_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.55
Min Limit	0.47

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	0.508	0.507	-0.001
0	118	0.506	0.507	0.001
0	122	0.507	0.507	0.000
3	301	0.507	0.507	0.000
3	306	0.512	0.513	0.001
3	307	0.507	0.507	0.000
3	308	0.510	0.511	0.001
3	309	0.515	0.514	-0.001
5	310	0.507	0.507	0.000
5	311	0.505	0.504	-0.001
5	312	0.513	0.512	-0.001
5	314	0.514	0.513	-0.001
5	316	0.511	0.510	-0.001
10	317	0.506	0.505	-0.001
10	318	0.511	0.512	0.001
10	319	0.516	0.517	0.001
10	321	0.504	0.505	0.001
10	322	0.509	0.507	-0.002
15	323	0.503	0.502	-0.001
15	325	0.502	0.502	0.000
15	326	0.509	0.509	0.000
15	328	0.513	0.512	-0.001
15	333	0.507	0.508	0.001
30	334	0.501	0.504	0.003
30	335	0.512	0.511	-0.001
30	337	0.510	0.511	0.001
30	339	0.501	0.500	-0.001
30	341	0.515	0.513	-0.002
35	342	0.500	0.498	-0.002
35	345	0.511	0.509	-0.002
35	346	0.509	0.507	-0.002
35	347	0.514	0.512	-0.002
35	348	0.494	0.495	0.001
50	349	0.510	0.510	0.000
50	350	0.510	0.510	0.000
50	351	0.508	0.510	0.002
50	353	0.518	0.518	0.000
50	354	0.513	0.513	0.000
55	355	0.512	0.514	0.002
55	357	0.510	0.509	-0.001
55	359	0.497	0.495	-0.002
55	362	0.512	0.511	-0.001
55	363	0.515	0.514	-0.001
100	1	0.504	0.505	0.001
100	3	0.499	0.497	-0.002
100	4	0.500	0.502	0.002
100	5	0.508	0.510	0.002
100	6	0.516	0.512	-0.004
100	7	0.508	0.511	0.003
100	8	0.505	0.502	-0.003
100	9	0.498	0.500	0.002
100	10	0.506	0.505	-0.001
100	11	0.510	0.511	0.001
100	12	0.501	0.501	0.000
100	13	0.511	0.513	0.002
100	14	0.506	0.505	-0.001
100	15	0.517	0.513	-0.004
100	16	0.505	0.507	0.002
100	17	0.502	0.501	-0.001
100	18	0.511	0.515	0.004
100	19	0.497	0.500	0.003
100	20	0.507	0.503	-0.004
100	21	0.506	0.508	0.002
100	22	0.511	0.512	0.001
100	24	0.511	0.513	0.002
105	25	0.515	0.515	0.000
105	26	0.497	0.494	-0.003
105	27	0.509	0.506	-0.003
105	28	0.507	0.501	-0.006
105	29	0.511	0.511	0.000
	Max	0.518	0.518	0.004
	Average	0.508	0.508	0.000
	Min	0.494	0.494	-0.006
	Std Dev	0.005	0.006	0.002



12.5 __V_FAULT_FALL_5V	
Test Site	
Tester	
Test Number	
Max Limit	0.55
Min Limit	0.47

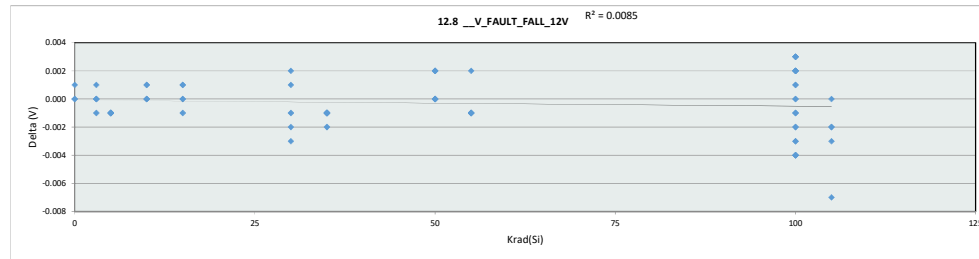
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.507	0.507	0.504	0.505	0.502	0.500	0.495	0.510	0.495	0.497	0.494
Average	0.507	0.510	0.509	0.509	0.507	0.508	0.504	0.512	0.509	0.507	0.505
Max	0.507	0.514	0.513	0.517	0.512	0.513	0.512	0.518	0.514	0.515	0.515
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



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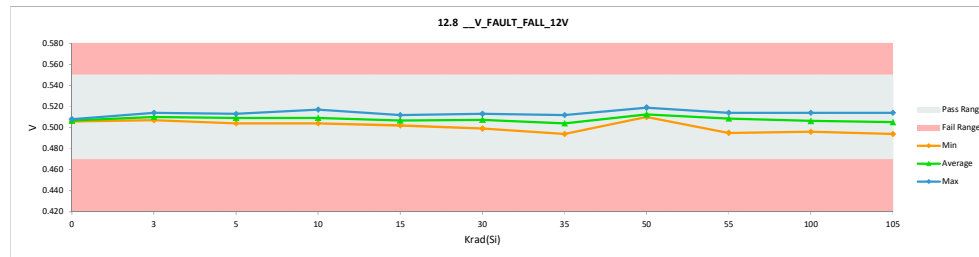
12.8 __V_FAULT_FALL_12V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	0.55 0.55
Min Limit	0.47 0.47

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	0.507	0.508	0.001
0	118	0.506	0.506	0.000
0	122	0.506	0.506	0.000
3	301	0.507	0.507	0.000
3	306	0.512	0.513	0.001
3	307	0.507	0.507	0.000
3	308	0.511	0.510	-0.001
3	309	0.514	0.514	0.000
5	310	0.507	0.506	-0.001
5	311	0.505	0.504	-0.001
5	312	0.514	0.513	-0.001
5	314	0.514	0.513	-0.001
5	316	0.511	0.510	-0.001
10	317	0.505	0.505	0.000
10	318	0.511	0.512	0.001
10	319	0.516	0.517	0.001
10	321	0.504	0.504	0.000
10	322	0.508	0.508	0.000
15	323	0.502	0.503	0.001
15	325	0.501	0.502	0.001
15	326	0.509	0.509	0.000
15	328	0.513	0.512	-0.001
15	333	0.508	0.508	0.000
30	334	0.501	0.502	0.002
30	335	0.512	0.511	-0.001
30	337	0.510	0.511	0.001
30	339	0.502	0.499	-0.003
30	341	0.515	0.513	-0.002
35	342	0.499	0.498	-0.001
35	345	0.511	0.509	-0.002
35	346	0.508	0.507	-0.001
35	347	0.513	0.512	-0.001
35	348	0.495	0.494	-0.001
50	349	0.509	0.511	0.002
50	350	0.510	0.510	0.000
50	351	0.508	0.510	0.002
50	353	0.519	0.519	0.000
50	354	0.513	0.513	0.000
55	355	0.512	0.514	0.002
55	357	0.510	0.509	-0.001
55	359	0.496	0.495	-0.001
55	362	0.512	0.511	-0.001
55	363	0.515	0.514	-0.001
100	1	0.504	0.504	0.000
100	3	0.499	0.496	-0.003
100	4	0.499	0.502	0.003
100	5	0.508	0.510	0.002
100	6	0.516	0.512	-0.004
100	7	0.509	0.511	0.002
100	8	0.505	0.502	-0.003
100	9	0.498	0.499	0.001
100	10	0.506	0.504	-0.002
100	11	0.510	0.510	0.000
100	12	0.501	0.500	-0.001
100	13	0.511	0.513	0.002
100	14	0.506	0.505	-0.001
100	15	0.517	0.513	-0.004
100	16	0.505	0.507	0.002
100	17	0.502	0.500	-0.002
100	18	0.511	0.514	0.003
100	19	0.497	0.500	0.003
100	20	0.507	0.503	-0.004
100	21	0.506	0.508	0.002
100	22	0.511	0.512	0.001
100	24	0.511	0.514	0.003
105	25	0.516	0.514	-0.002
105	26	0.497	0.494	-0.003
105	27	0.509	0.507	-0.002
105	28	0.507	0.500	-0.007
105	29	0.511	0.511	0.000
Max	0.519	0.519	0.003	
Average	0.508	0.508	0.000	
Min	0.495	0.494	-0.007	
Std Dev	0.005	0.006	0.002	



12.8 __V_FAULT_FALL_12V	
Test Site	
Tester	
Test Number	
Max Limit	0.55 V
Min Limit	0.47 V

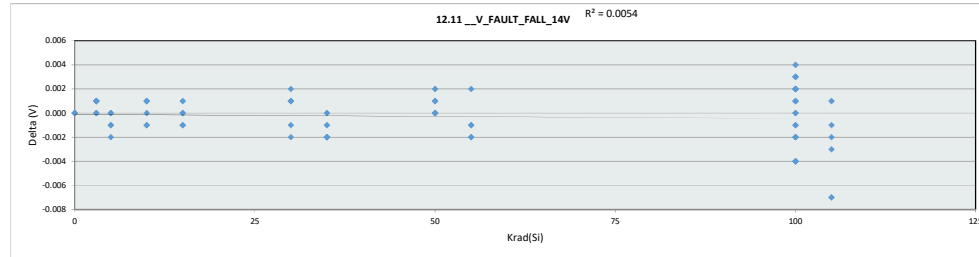
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.506	0.507	0.504	0.504	0.502	0.499	0.494	0.510	0.495	0.496	0.494
Average	0.507	0.510	0.509	0.509	0.507	0.507	0.504	0.513	0.509	0.506	0.505
Max	0.508	0.514	0.513	0.517	0.512	0.513	0.512	0.519	0.514	0.514	0.514
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



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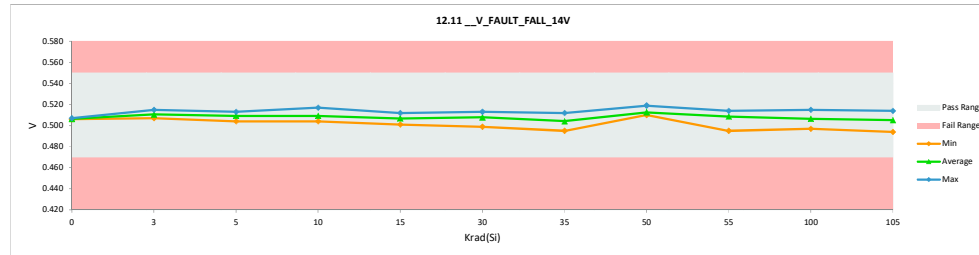
12.11 __V_FAULT_FALL_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.55
Min Limit	0.47

Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	0.507	0.507	0.000
0	118	0.506	0.506	0.000
0	122	0.506	0.506	0.000
3	301	0.507	0.507	0.000
3	306	0.512	0.513	0.001
3	307	0.507	0.507	0.000
3	308	0.510	0.511	0.001
3	309	0.514	0.515	0.001
5	310	0.507	0.507	0.000
5	311	0.505	0.504	-0.001
5	312	0.514	0.512	-0.002
5	314	0.514	0.513	-0.001
5	316	0.510	0.510	0.000
10	317	0.506	0.505	-0.001
10	318	0.511	0.512	0.001
10	319	0.516	0.517	0.001
10	321	0.504	0.504	0.000
10	322	0.509	0.508	-0.001
15	323	0.503	0.503	0.000
15	325	0.502	0.501	-0.001
15	326	0.509	0.509	0.000
15	328	0.513	0.512	-0.001
15	333	0.508	0.509	0.001
30	334	0.502	0.504	0.002
30	335	0.511	0.512	0.001
30	337	0.510	0.511	0.001
30	339	0.501	0.499	-0.002
30	341	0.514	0.513	-0.001
35	342	0.499	0.498	-0.001
35	345	0.511	0.509	-0.002
35	346	0.509	0.507	-0.002
35	347	0.514	0.512	-0.002
35	348	0.495	0.495	0.000
50	349	0.510	0.511	0.001
50	350	0.510	0.510	0.000
50	351	0.508	0.510	0.002
50	353	0.519	0.519	0.000
50	354	0.513	0.513	0.000
55	355	0.512	0.514	0.002
55	357	0.510	0.509	-0.001
55	359	0.497	0.495	-0.002
55	362	0.512	0.511	-0.001
55	363	0.516	0.514	-0.002
100	1	0.504	0.504	0.000
100	3	0.499	0.497	-0.002
100	4	0.499	0.502	0.003
100	5	0.508	0.510	0.002
100	6	0.516	0.512	-0.004
100	7	0.509	0.511	0.002
100	8	0.506	0.502	-0.004
100	9	0.498	0.499	0.001
100	10	0.506	0.504	-0.002
100	11	0.510	0.510	0.000
100	12	0.501	0.500	-0.001
100	13	0.511	0.513	0.002
100	14	0.506	0.505	-0.001
100	15	0.517	0.513	-0.004
100	16	0.505	0.507	0.002
100	17	0.503	0.501	-0.002
100	18	0.512	0.515	0.003
100	19	0.496	0.500	0.004
100	20	0.507	0.503	-0.004
100	21	0.506	0.509	0.003
100	22	0.511	0.512	0.001
100	24	0.511	0.513	0.002
105	25	0.515	0.514	-0.001
105	26	0.497	0.494	-0.003
105	27	0.509	0.507	-0.002
105	28	0.507	0.500	-0.007
105	29	0.510	0.511	0.001
	Max	0.519	0.519	0.004
	Average	0.508	0.508	0.000
	Min	0.495	0.494	-0.007
	Std Dev	0.005	0.006	0.002



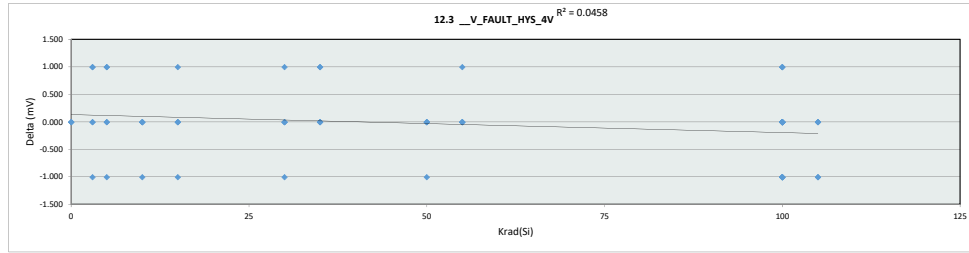
12.11 __V_FAULT_FALL_14V	
Test Site	
Tester	
Test Number	
Max Limit	0.55
Min Limit	0.47

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.506	0.507	0.504	0.504	0.501	0.499	0.495	0.510	0.495	0.497	0.494
Average	0.506	0.511	0.509	0.509	0.507	0.508	0.504	0.513	0.509	0.506	0.505
Max	0.507	0.515	0.513	0.517	0.512	0.513	0.512	0.519	0.514	0.515	0.514
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550

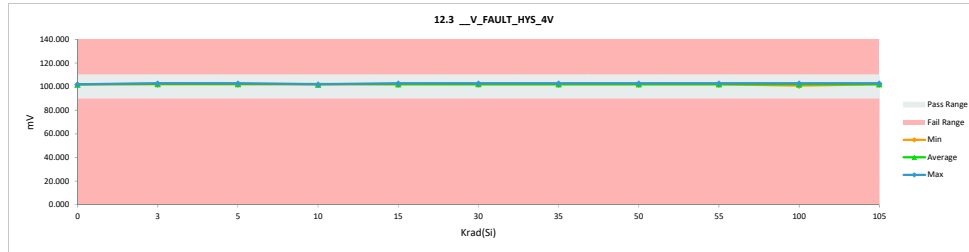


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12.3 V_FAULT_HYS_4V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	110	110		
Min Limit	90	90		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	102.000	102.000	0.000
0	118	102.000	102.000	0.000
0	122	102.000	102.000	0.000
3	301	103.000	102.000	-1.000
3	306	102.000	102.000	0.000
3	307	102.000	103.000	1.000
3	308	102.000	102.000	0.000
3	309	102.000	103.000	1.000
5	310	103.000	102.000	-1.000
5	311	102.000	103.000	1.000
5	312	102.000	102.000	0.000
5	314	102.000	103.000	1.000
5	316	102.000	102.000	0.000
10	317	102.000	102.000	0.000
10	318	102.000	102.000	0.000
10	319	102.000	102.000	0.000
10	321	103.000	102.000	-1.000
10	322	102.000	102.000	0.000
15	323	102.000	102.000	0.000
15	325	102.000	102.000	0.000
15	326	103.000	102.000	-1.000
15	328	102.000	102.000	0.000
15	333	102.000	103.000	1.000
30	334	102.000	103.000	1.000
30	335	103.000	103.000	0.000
30	337	102.000	102.000	0.000
30	339	103.000	102.000	-1.000
30	341	102.000	102.000	0.000
35	342	102.000	103.000	1.000
35	345	101.000	102.000	1.000
35	346	102.000	102.000	0.000
35	347	102.000	102.000	0.000
35	348	102.000	102.000	0.000
50	349	103.000	103.000	0.000
50	350	102.000	102.000	0.000
50	351	102.000	102.000	0.000
50	353	102.000	102.000	0.000
50	354	103.000	102.000	-1.000
55	355	102.000	102.000	0.000
55	357	102.000	102.000	0.000
55	359	102.000	103.000	1.000
55	362	102.000	102.000	0.000
55	363	102.000	102.000	0.000
100	1	103.000	102.000	-1.000
100	3	103.000	102.000	-1.000
100	4	103.000	102.000	-1.000
100	5	102.000	102.000	0.000
100	6	102.000	102.000	0.000
100	7	102.000	102.000	0.000
100	8	102.000	102.000	0.000
100	9	102.000	102.000	0.000
100	10	103.000	102.000	-1.000
100	11	102.000	103.000	1.000
100	12	103.000	103.000	0.000
100	13	103.000	102.000	-1.000
100	14	102.000	102.000	0.000
100	15	102.000	101.000	-1.000
100	16	102.000	101.000	-1.000
100	17	102.000	102.000	0.000
100	18	102.000	102.000	0.000
100	19	102.000	102.000	0.000
100	20	102.000	102.000	0.000
100	21	102.000	103.000	1.000
100	22	102.000	103.000	1.000
100	24	102.000	102.000	0.000
105	25	102.000	102.000	0.000
105	26	103.000	103.000	0.000
105	27	103.000	102.000	-1.000
105	28	103.000	102.000	-1.000
105	29	102.000	102.000	0.000
	Max	103.000	103.000	1.000
	Average	102.229	102.186	-0.043
	Min	101.000	101.000	-1.000
	Std Dev	0.456	0.460	0.624

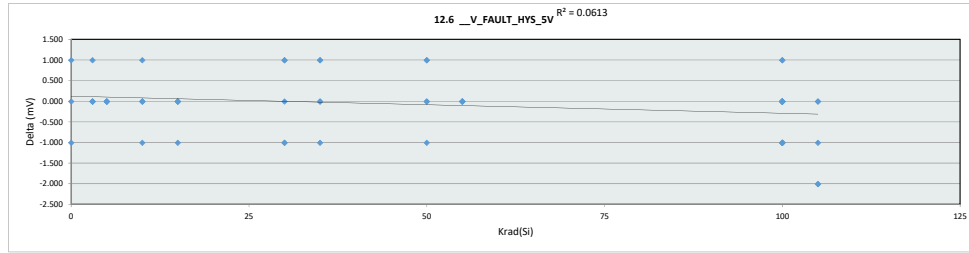


12.3 V_FAULT_HYS_4V											
Test Site											
Tester											
Test Number											
Max Limit	110	mV									
Min Limit	90	mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min	102.000	102.000	102.000	102.000	102.000	102.000	102.000	102.000	102.000	102.000	101.000
Average	102.000	102.400	102.400	102.000	102.200	102.400	102.200	102.200	102.200	102.091	102.200
Max	102.000	103.000	103.000	102.000	103.000	103.000	103.000	103.000	103.000	103.000	103.000
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

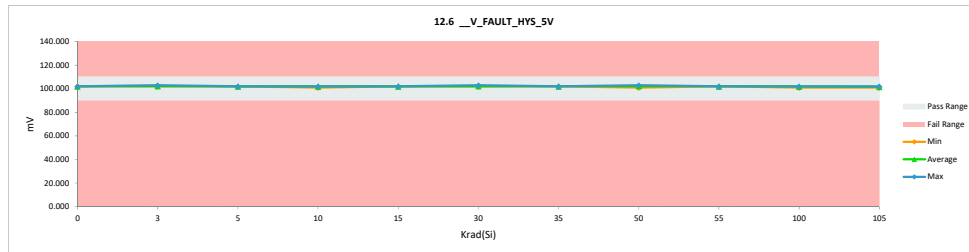


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12.6 V_FAULT_HYS_5V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	110	110		
Min Limit	90	90		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	101.000	102.000	1.000
0	118	103.000	102.000	-1.000
0	122	102.000	102.000	0.000
3	301	102.000	102.000	0.000
3	306	102.000	102.000	0.000
3	307	102.000	102.000	0.000
3	308	102.000	102.000	0.000
3	309	102.000	103.000	1.000
5	310	102.000	102.000	0.000
5	311	102.000	102.000	0.000
5	312	102.000	102.000	0.000
5	314	102.000	102.000	0.000
5	316	102.000	102.000	0.000
10	317	102.000	102.000	0.000
10	318	102.000	101.000	-1.000
10	319	102.000	102.000	0.000
10	321	102.000	102.000	0.000
10	322	101.000	102.000	1.000
15	323	102.000	102.000	0.000
15	325	102.000	102.000	0.000
15	326	102.000	102.000	0.000
15	328	102.000	102.000	0.000
15	333	103.000	102.000	-1.000
30	334	103.000	102.000	-1.000
30	335	102.000	103.000	1.000
30	337	102.000	102.000	0.000
30	339	103.000	102.000	-1.000
30	341	101.000	102.000	1.000
35	342	102.000	102.000	0.000
35	345	102.000	102.000	0.000
35	346	101.000	102.000	1.000
35	347	101.000	102.000	1.000
35	348	103.000	102.000	-1.000
50	349	102.000	103.000	1.000
50	350	102.000	101.000	-1.000
50	351	102.000	102.000	0.000
50	353	102.000	103.000	1.000
50	354	102.000	102.000	0.000
55	355	102.000	102.000	0.000
55	357	102.000	102.000	0.000
55	359	102.000	102.000	0.000
55	362	102.000	102.000	0.000
55	363	102.000	102.000	0.000
100	1	102.000	102.000	0.000
100	3	102.000	102.000	0.000
100	4	101.000	102.000	1.000
100	5	101.000	102.000	1.000
100	6	102.000	102.000	0.000
100	7	103.000	102.000	-1.000
100	8	102.000	102.000	0.000
100	9	102.000	101.000	-1.000
100	10	102.000	101.000	-1.000
100	11	103.000	102.000	-1.000
100	12	102.000	102.000	0.000
100	13	102.000	102.000	0.000
100	14	102.000	102.000	0.000
100	15	102.000	102.000	0.000
100	16	102.000	102.000	0.000
100	17	102.000	101.000	-1.000
100	18	103.000	102.000	-1.000
100	19	102.000	102.000	0.000
100	20	102.000	102.000	0.000
100	21	102.000	102.000	0.000
100	22	102.000	102.000	0.000
100	24	102.000	102.000	0.000
105	25	103.000	101.000	-2.000
105	26	103.000	102.000	-1.000
105	27	102.000	102.000	0.000
105	28	103.000	101.000	-2.000
105	29	102.000	102.000	0.000
Max	103.000	103.000	1.000	
Average	102.057	101.957	-0.100	
Min	101.000	101.000	-2.000	
Std Dev	0.508	0.397	0.684	

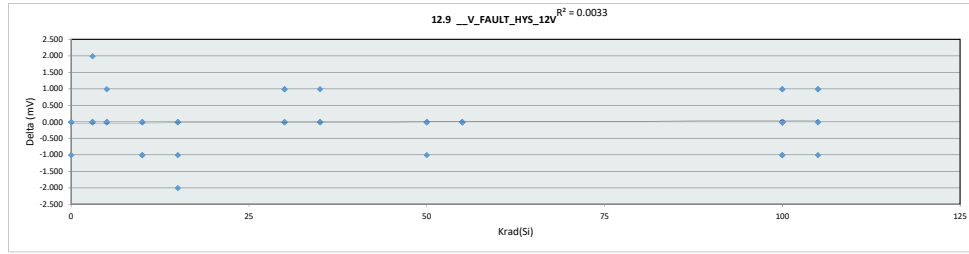


12.6 V_FAULT_HYS_5V											
Test Site											
Tester											
Test Number											
Max Limit	110	mV									
Min Limit	90	mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min	102.000	102.000	102.000	101.000	102.000	102.000	102.000	101.000	102.000	101.000	101.000
Average	102.000	102.200	102.000	101.800	102.000	102.200	102.000	102.200	102.000	101.864	101.600
Max	102.000	103.000	102.000	102.000	102.000	103.000	102.000	103.000	102.000	102.000	102.000
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

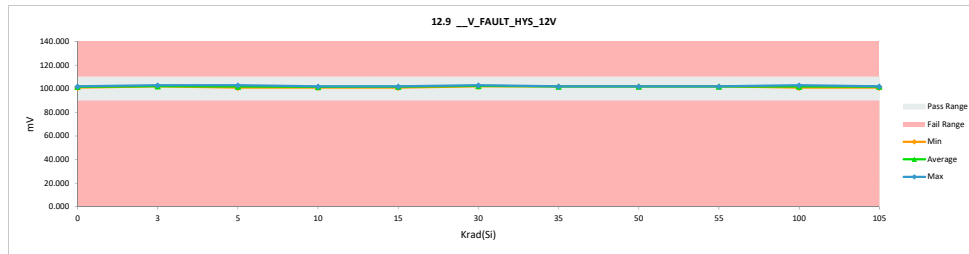


HDR TID Report
TPS7H5001-SP

12.9 V_FAULT_HYS_12V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	110	110		
Min Limit	90	90		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	102.000	101.000	-1.000
0	118	102.000	102.000	0.000
0	122	102.000	102.000	0.000
3	301	102.000	102.000	0.000
3	306	102.000	102.000	0.000
3	307	102.000	102.000	0.000
3	308	101.000	103.000	2.000
3	309	102.000	102.000	0.000
5	310	102.000	103.000	1.000
5	311	102.000	102.000	0.000
5	312	101.000	101.000	0.000
5	314	102.000	102.000	0.000
5	316	102.000	102.000	0.000
10	317	102.000	102.000	0.000
10	318	102.000	101.000	-1.000
10	319	102.000	102.000	0.000
10	321	102.000	102.000	0.000
10	322	102.000	101.000	-1.000
15	323	103.000	102.000	-1.000
15	325	103.000	101.000	-2.000
15	326	102.000	102.000	0.000
15	328	102.000	102.000	0.000
15	333	102.000	102.000	0.000
30	334	103.000	103.000	0.000
30	335	102.000	103.000	1.000
30	337	102.000	102.000	0.000
30	339	102.000	102.000	0.000
30	341	101.000	102.000	1.000
35	342	102.000	102.000	0.000
35	345	102.000	102.000	0.000
35	346	102.000	102.000	0.000
35	347	102.000	102.000	0.000
35	348	101.000	102.000	1.000
50	349	103.000	102.000	-1.000
50	350	102.000	102.000	0.000
50	351	102.000	102.000	0.000
50	353	102.000	102.000	0.000
50	354	102.000	102.000	0.000
55	355	102.000	102.000	0.000
55	357	102.000	102.000	0.000
55	359	102.000	102.000	0.000
55	362	102.000	102.000	0.000
55	363	102.000	102.000	0.000
100	1	102.000	102.000	0.000
100	3	102.000	103.000	1.000
100	4	102.000	102.000	0.000
100	5	101.000	102.000	1.000
100	6	102.000	102.000	0.000
100	7	102.000	101.000	-1.000
100	8	102.000	102.000	0.000
100	9	102.000	102.000	0.000
100	10	102.000	102.000	0.000
100	11	102.000	102.000	0.000
100	12	102.000	102.000	0.000
100	13	102.000	102.000	0.000
100	14	102.000	102.000	0.000
100	15	102.000	102.000	0.000
100	16	102.000	102.000	0.000
100	17	102.000	102.000	0.000
100	18	103.000	103.000	0.000
100	19	102.000	102.000	0.000
100	20	102.000	102.000	0.000
100	21	102.000	102.000	0.000
100	22	102.000	102.000	0.000
100	24	102.000	101.000	-1.000
105	25	101.000	102.000	1.000
105	26	102.000	102.000	0.000
105	27	102.000	101.000	-1.000
105	28	102.000	102.000	0.000
105	29	101.000	102.000	1.000
Max		103.000	103.000	2.000
Average		101.971	101.971	0.000
Min		101.000	101.000	-2.000
Std Dev		0.416	0.450	0.590

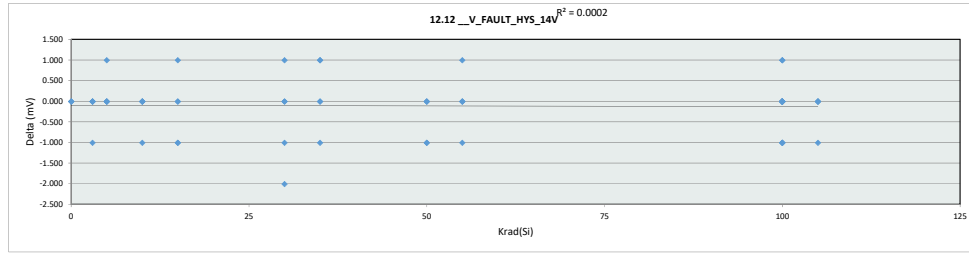


12.9 V_FAULT_HYS_12V											
Test Site											
Tester											
Test Number											
Max Limit	110	mV									
Min Limit	90	mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min	101.000	102.000	101.000	101.000	101.000	101.000	102.000	102.000	102.000	101.000	101.000
Average	101.667	102.200	102.000	101.600	101.800	102.400	102.000	102.000	102.000	102.000	101.800
Max	102.000	103.000	103.000	102.000	102.000	103.000	102.000	102.000	102.000	103.000	102.000
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

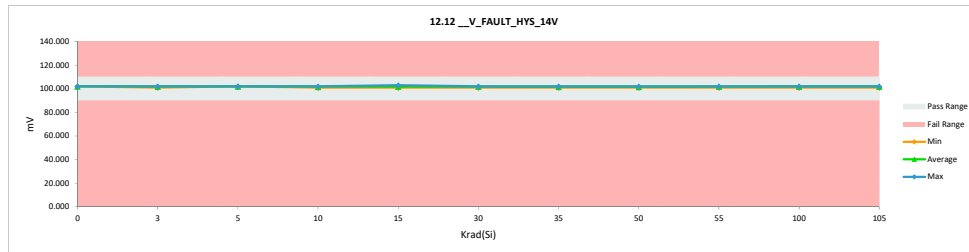


HDR TID Report
TPS7H5001-SP

12.12 V_FAULT_HYS_14V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	110	110		
Min Limit	90	90		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	102.000	102.000	0.000
0	118	102.000	102.000	0.000
0	122	102.000	102.000	0.000
3	301	102.000	102.000	0.000
3	306	102.000	102.000	0.000
3	307	102.000	102.000	0.000
3	308	102.000	102.000	0.000
3	309	102.000	101.000	-1.000
5	310	102.000	102.000	0.000
5	311	102.000	102.000	0.000
5	312	101.000	102.000	1.000
5	314	102.000	102.000	0.000
5	316	102.000	102.000	0.000
10	317	102.000	102.000	0.000
10	318	102.000	101.000	-1.000
10	319	102.000	102.000	0.000
10	321	102.000	102.000	0.000
10	322	101.000	101.000	0.000
15	323	102.000	101.000	-1.000
15	325	102.000	103.000	1.000
15	326	102.000	102.000	0.000
15	328	102.000	102.000	0.000
15	333	102.000	101.000	-1.000
30	334	101.000	102.000	1.000
30	335	103.000	101.000	-2.000
30	337	102.000	102.000	0.000
30	339	103.000	102.000	-1.000
30	341	102.000	102.000	0.000
35	342	102.000	102.000	0.000
35	345	102.000	101.000	-1.000
35	346	101.000	102.000	1.000
35	347	101.000	102.000	1.000
35	348	101.000	101.000	0.000
50	349	102.000	102.000	0.000
50	350	102.000	101.000	-1.000
50	351	102.000	102.000	0.000
50	353	102.000	101.000	-1.000
50	354	102.000	102.000	0.000
55	355	102.000	102.000	0.000
55	357	102.000	102.000	0.000
55	359	102.000	101.000	-1.000
55	362	102.000	102.000	0.000
55	363	101.000	102.000	1.000
100	1	102.000	102.000	0.000
100	3	102.000	102.000	0.000
100	4	102.000	102.000	0.000
100	5	102.000	102.000	0.000
100	6	102.000	102.000	0.000
100	7	102.000	102.000	0.000
100	8	101.000	102.000	1.000
100	9	102.000	102.000	0.000
100	10	102.000	102.000	0.000
100	11	103.000	102.000	-1.000
100	12	102.000	102.000	0.000
100	13	102.000	102.000	0.000
100	14	102.000	101.000	-1.000
100	15	102.000	102.000	0.000
100	16	102.000	102.000	0.000
100	17	101.000	101.000	0.000
100	18	101.000	102.000	1.000
100	19	103.000	102.000	-1.000
100	20	102.000	102.000	0.000
100	21	102.000	101.000	-1.000
100	22	102.000	102.000	0.000
100	24	102.000	102.000	0.000
105	25	102.000	102.000	0.000
105	26	102.000	102.000	0.000
105	27	102.000	101.000	-1.000
105	28	102.000	102.000	0.000
105	29	102.000	102.000	0.000
Max	103.000	103.000	1.000	
Average	101.914	101.800	-0.114	
Min	101.000	101.000	-2.000	
Std Dev	0.442	0.437	0.603	

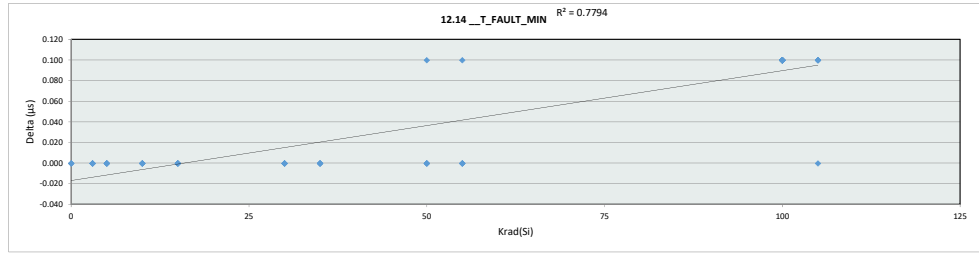


12.12 V_FAULT_HYS_14V											
Test Site											
Tester											
Test Number											
Max Limit	110	mV									
Min Limit	90	mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min	102.000	101.000	102.000	101.000	101.000	101.000	101.000	101.000	101.000	101.000	101.000
Average	102.000	101.800	102.000	101.600	101.800	101.800	101.600	101.600	101.800	101.864	101.800
Max	102.000	102.000	102.000	102.000	103.000	102.000	102.000	102.000	102.000	102.000	102.000
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

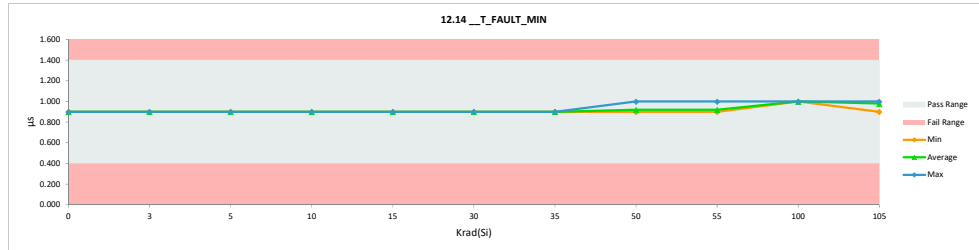


HDR TID Report
TPS7H5001-SP

12.14 T_FAULT_MIN				
Test Site	Tester	Test Number	Unit	Max Limit
			µs	1.4
			µs	0.4
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	0.900	0.900	0.000
0	118	0.900	0.900	0.000
0	122	0.900	0.900	0.000
3	301	0.900	0.900	0.000
3	306	0.900	0.900	0.000
3	307	0.900	0.900	0.000
3	308	0.900	0.900	0.000
3	309	0.900	0.900	0.000
5	310	0.900	0.900	0.000
5	311	0.900	0.900	0.000
5	312	0.900	0.900	0.000
5	314	0.900	0.900	0.000
5	316	0.900	0.900	0.000
10	317	0.900	0.900	0.000
10	318	0.900	0.900	0.000
10	319	0.900	0.900	0.000
10	321	0.900	0.900	0.000
10	322	0.900	0.900	0.000
15	323	0.900	0.900	0.000
15	325	0.900	0.900	0.000
15	326	0.900	0.900	0.000
15	328	0.900	0.900	0.000
15	333	0.900	0.900	0.000
30	334	0.900	0.900	0.000
30	335	0.900	0.900	0.000
30	337	0.900	0.900	0.000
30	339	0.900	0.900	0.000
30	341	0.900	0.900	0.000
35	342	0.900	0.900	0.000
35	345	0.900	0.900	0.000
35	346	0.900	0.900	0.000
35	347	0.900	0.900	0.000
35	348	0.900	0.900	0.000
50	349	0.900	0.900	0.000
50	350	0.900	0.900	0.000
50	351	0.900	0.900	0.000
50	353	0.900	0.900	0.000
50	354	0.900	1.000	0.100
55	355	0.900	1.000	0.100
55	357	0.900	0.900	0.000
55	359	0.900	0.900	0.000
55	362	0.900	0.900	0.000
55	363	0.900	0.900	0.000
100	1	0.900	1.000	0.100
100	3	0.900	1.000	0.100
100	4	0.900	1.000	0.100
100	5	0.900	1.000	0.100
100	6	0.900	1.000	0.100
100	7	0.900	1.000	0.100
100	8	0.900	1.000	0.100
100	9	0.900	1.000	0.100
100	10	0.900	1.000	0.100
100	11	0.900	1.000	0.100
100	12	0.900	1.000	0.100
100	13	0.900	1.000	0.100
100	14	0.900	1.000	0.100
100	15	0.900	1.000	0.100
100	16	0.900	1.000	0.100
100	17	0.900	1.000	0.100
100	18	0.900	1.000	0.100
100	19	0.900	1.000	0.100
100	20	0.900	1.000	0.100
100	21	0.900	1.000	0.100
100	22	0.900	1.000	0.100
100	24	0.900	1.000	0.100
105	25	0.900	1.000	0.100
105	26	0.900	0.900	0.000
105	27	0.900	1.000	0.100
105	28	0.900	1.000	0.100
105	29	0.900	1.000	0.100
	Max	0.900	1.000	0.100
	Average	0.900	0.940	0.040
	Min	0.900	0.900	0.000
	Std Dev	0.000	0.049	0.049

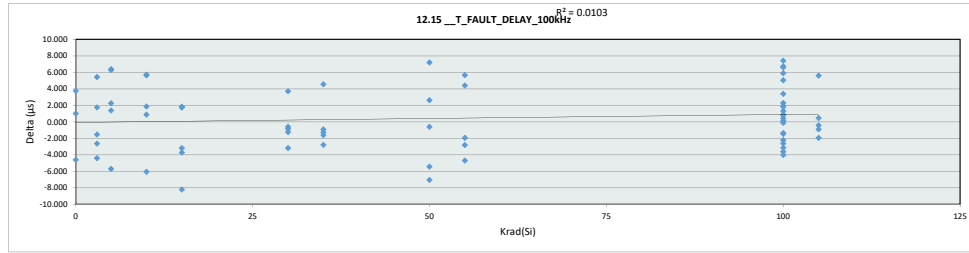


12.14 T_FAULT_MIN																
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit	0	3	5	10	15	30	35	50	55	100	105
			µs	1.4	0.4	0.400	0.400	0.400	0.400	0.400	0.400	0.400	0.400	0.400	0.400	0.400
			µs	0.4	0.4	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900
						0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.920	0.920	1.000	0.980
						0.900	0.900	0.900	0.900	0.900	0.900	0.900	1.000	1.000	1.000	1.000
						1.400	1.400	1.400	1.400	1.400	1.400	1.400	1.400	1.400	1.400	1.400

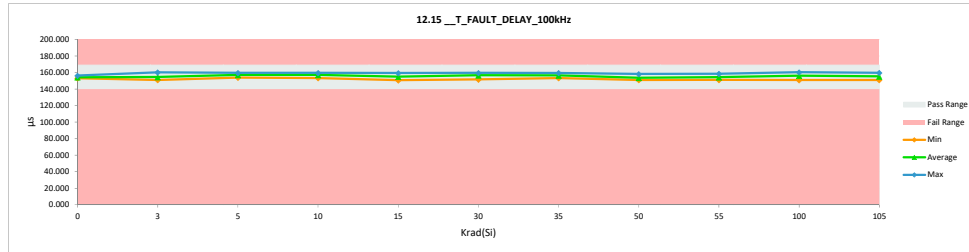


HDR TID Report
TPS7H5001-SP

12.15 T_FAULT_DELAY_100kHz				
Test Site				
Tester				
Test Number				
Unit	µs	µs		
Max Limit	169	169		
Min Limit	140	140		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	152.512	156.320	3.808
0	118	157.757	153.199	-4.558
0	122	152.431	153.485	1.054
3	301	153.045	151.558	-1.487
3	306	153.689	151.088	-2.601
3	307	158.553	160.338	1.785
3	308	152.433	157.904	5.471
3	309	156.920	152.554	-4.366
5	310	152.505	158.824	6.319
5	311	157.346	159.645	2.299
5	312	150.601	157.030	6.429
5	314	154.928	156.339	1.411
5	316	159.640	153.973	-5.667
10	317	153.994	159.687	5.693
10	318	150.707	156.466	5.759
10	319	159.408	153.387	-6.021
10	321	158.292	159.195	0.903
10	322	155.152	157.061	1.909
15	323	157.549	159.439	1.890
15	325	160.169	152.004	-8.165
15	326	157.697	159.463	1.766
15	328	156.977	153.287	-3.690
15	333	153.853	150.707	-3.146
30	334	154.832	158.585	3.753
30	335	154.907	151.764	-3.143
30	337	160.128	158.918	-1.210
30	339	160.302	159.739	-0.563
30	341	155.689	154.875	-0.814
35	342	157.339	156.458	-0.881
35	345	154.968	159.563	4.595
35	346	159.614	156.854	-2.760
35	347	155.000	153.422	-1.578
35	348	158.053	156.826	-1.227
50	349	152.029	151.463	-0.566
50	350	153.106	155.759	2.653
50	351	157.423	152.026	-5.397
50	353	158.099	151.094	-7.005
50	354	151.082	158.319	7.237
55	355	153.932	151.157	-2.775
55	357	157.435	152.784	-4.651
55	359	154.464	152.560	-1.904
55	362	154.140	158.584	4.444
55	363	151.332	157.029	5.697
100	1	152.173	152.094	-0.079
100	3	159.519	160.457	0.938
100	4	151.183	154.621	3.438
100	5	158.933	159.392	0.459
100	6	155.724	151.746	-3.978
100	7	152.728	157.818	5.090
100	8	155.244	155.393	0.149
100	9	153.627	160.390	6.763
100	10	153.828	152.503	-1.325
100	11	150.929	158.382	7.453
100	12	158.788	160.139	1.351
100	13	156.289	154.877	-1.412
100	14	156.960	154.373	-2.587
100	15	157.711	160.030	2.319
100	16	152.741	159.382	6.641
100	17	152.243	154.225	1.982
100	18	151.522	152.353	0.831
100	19	152.803	158.740	5.937
100	20	157.132	154.951	-2.181
100	21	155.554	157.412	1.858
100	22	154.075	150.969	-3.106
100	24	160.271	156.693	-3.578
105	25	158.911	157.013	-1.898
105	26	156.172	155.781	-0.391
105	27	150.579	151.075	0.496
105	28	154.160	159.805	5.645
105	29	154.238	153.363	-0.875
Max		160.302	160.457	7.453
Average		155.344	155.782	0.438
Min		150.579	150.707	-8.165
Std Dev		2.839	3.110	3.837

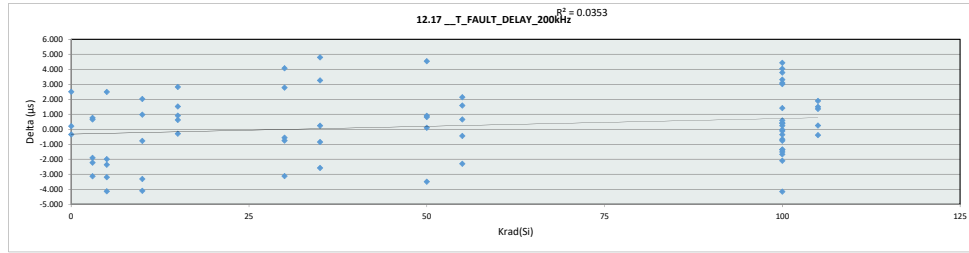


12.15 T_FAULT_DELAY_100											
Test Site											
Tester											
Test Number											
Max Limit	169	µs									
Min Limit	140	µs									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000
Min	153.199	151.088	153.973	153.387	150.707	151.764	153.422	151.094	151.157	150.969	151.075
Average	154.335	154.688	157.162	157.159	154.980	156.776	156.625	153.732	154.423	156.225	155.407
Max	156.320	160.338	159.645	159.687	159.463	159.739	159.563	158.319	158.584	160.457	159.805
UL	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000

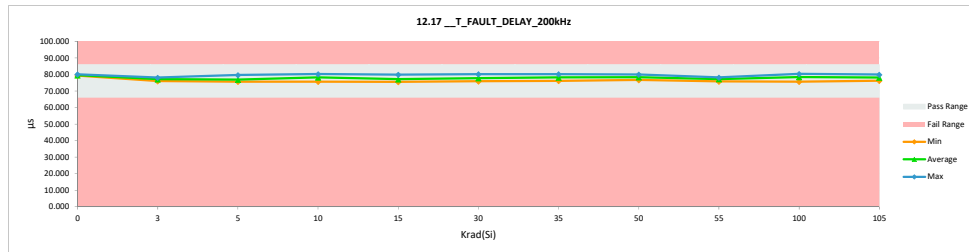


HDR TID Report
TPS7H5001-SP

12.17 T FAULT DELAY 200kHz				
Test Site				
Tester				
Test Number				
Unit	µs	µs		
Max Limit	86	86		
Min Limit	66	66		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	79.704	79.377	-0.327
0	118	76.793	79.320	2.527
0	122	79.946	80.184	0.238
3	301	77.325	78.118	0.793
3	306	78.771	76.894	-1.877
3	307	79.171	76.066	-3.105
3	308	80.369	78.173	-2.196
3	309	76.279	76.960	0.681
5	310	80.305	76.198	-4.107
5	311	78.914	75.738	-3.176
5	312	78.357	76.397	-1.960
5	314	79.066	76.732	-2.334
5	316	77.219	79.740	2.521
10	317	79.035	75.744	-3.291
10	318	79.335	80.339	1.004
10	319	80.334	76.252	-4.082
10	321	78.174	80.225	2.051
10	322	79.854	79.108	-0.746
15	323	75.836	75.570	-0.266
15	325	75.938	76.587	0.649
15	326	76.021	76.962	0.941
15	328	75.531	77.073	1.542
15	333	77.153	80.001	2.848
30	334	77.479	80.280	2.801
30	335	79.868	76.772	-3.096
30	337	76.695	75.964	-0.731
30	339	76.013	80.112	4.099
30	341	76.669	76.129	-0.540
35	342	79.445	78.622	-0.823
35	345	75.489	80.315	4.826
35	346	80.019	77.465	-2.554
35	347	75.603	78.889	3.286
35	348	75.989	76.252	0.263
50	349	77.820	78.658	0.838
50	350	75.835	76.767	0.932
50	351	79.717	79.843	0.126
50	353	75.500	80.070	4.570
50	354	80.363	76.891	-3.472
55	355	77.627	78.312	0.685
55	357	75.776	77.946	2.170
55	359	76.471	78.085	1.614
55	362	76.494	76.072	-0.422
55	363	78.162	75.889	-2.273
100	1	77.957	76.469	-1.488
100	3	79.298	78.967	-0.331
100	4	80.402	80.316	-0.086
100	5	78.715	76.638	-2.077
100	6	78.604	77.863	-0.741
100	7	76.106	76.720	0.614
100	8	80.077	80.344	0.267
100	9	76.057	79.866	3.809
100	10	77.354	76.034	-1.320
100	11	76.719	80.061	3.342
100	12	78.239	76.593	-1.646
100	13	79.892	75.761	-4.131
100	14	76.678	77.098	0.420
100	15	75.752	80.208	4.456
100	16	79.844	79.846	0.002
100	17	79.880	80.322	0.442
100	18	77.392	80.434	3.042
100	19	78.653	80.092	1.439
100	20	75.567	79.633	4.066
100	21	80.417	79.080	-1.337
100	22	76.538	75.898	-0.640
100	24	76.969	80.081	3.112
105	25	75.671	77.055	1.384
105	26	80.184	79.821	-0.363
105	27	78.514	80.041	1.527
105	28	76.056	77.978	1.922
105	29	76.036	76.317	0.281
	Max	80.417	80.434	4.826
	Average	77.858	78.095	0.237
	Min	75.489	75.570	-4.131
	Std Dev	1.673	1.684	2.273

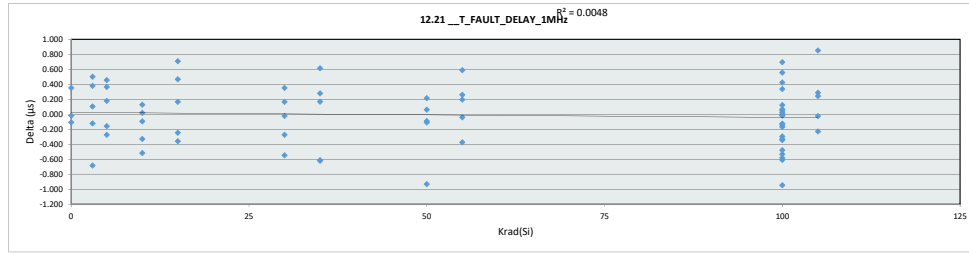


12.17 T FAULT DELAY 200											
Test Site											
Tester											
Test Number											
Max Limit	86	µs									
Min Limit	66	µs									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000
Min	79.320	76.066	75.738	75.744	75.570	75.964	76.252	76.767	75.889	75.761	76.317
Average	79.627	77.242	76.961	78.334	77.239	77.851	78.309	78.446	77.261	78.560	78.242
Max	80.184	78.173	79.740	80.339	80.001	80.280	80.315	80.070	78.312	80.434	80.041
UL	86.000	86.000	86.000	86.000	86.000	86.000	86.000	86.000	86.000	86.000	86.000

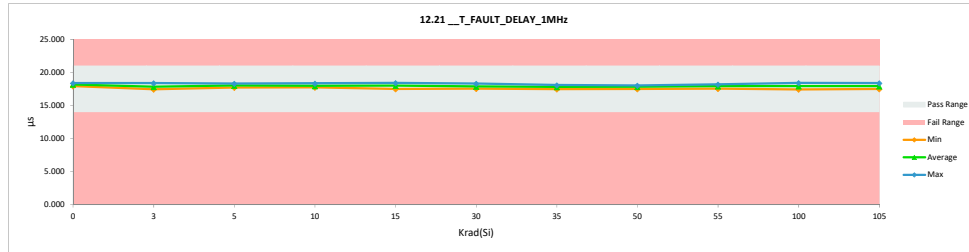


HDR TID Report
TPS7H5001-SP

12.21 T_FAULT_DELAY_1MHz				
Test Site				
Tester				
Test Number				
Unit	µs	µs		
Max Limit	21	21		
Min Limit	14	14		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	18.047	17.944	-0.103
0	118	18.282	18.270	-0.012
0	122	18.045	18.401	0.356
3	301	17.903	18.409	0.506
3	306	17.542	17.927	0.385
3	307	17.750	17.632	-0.118
3	308	18.148	17.469	-0.679
3	309	17.667	17.777	0.110
5	310	17.805	18.175	0.370
5	311	18.168	17.899	-0.269
5	312	17.879	18.340	0.461
5	314	18.153	18.000	-0.153
5	316	17.548	17.732	0.184
10	317	18.106	18.130	0.024
10	318	17.845	17.755	-0.090
10	319	18.328	17.817	-0.511
10	321	18.150	17.826	-0.324
10	322	18.238	18.370	0.132
15	323	17.951	18.423	0.472
15	325	18.094	18.265	0.171
15	326	17.529	18.242	0.713
15	328	17.739	17.499	-0.240
15	333	17.978	17.623	-0.355
30	334	17.835	17.857	0.022
30	335	17.968	18.325	0.357
30	337	17.888	18.059	0.171
30	339	17.826	17.810	-0.016
30	341	18.229	17.687	-0.542
35	342	17.538	17.822	0.284
35	345	17.848	18.022	0.174
35	346	18.284	17.676	-0.608
35	347	17.483	18.103	0.620
35	348	18.108	17.494	-0.614
50	349	18.427	17.500	-0.927
50	350	17.849	17.764	-0.085
50	351	17.823	18.044	0.221
50	353	18.106	18.002	-0.104
50	354	17.822	17.889	0.067
55	355	17.499	18.092	0.593
55	357	17.813	18.078	0.265
55	359	17.927	17.558	-0.369
55	362	18.001	18.201	0.200
55	363	17.810	17.775	-0.035
100	1	18.263	18.287	0.024
100	3	18.094	17.568	-0.526
100	4	17.657	18.357	0.700
100	5	17.677	17.540	-0.137
100	6	18.104	18.109	0.005
100	7	18.362	17.888	-0.474
100	8	17.868	18.430	0.562
100	9	18.398	17.457	-0.941
100	10	18.308	17.970	-0.338
100	11	17.813	17.524	-0.289
100	12	17.732	18.074	0.342
100	13	17.778	17.659	-0.119
100	14	18.211	18.204	-0.007
100	15	18.109	18.096	-0.013
100	16	17.793	17.922	0.129
100	17	18.081	17.478	-0.603
100	18	18.006	18.057	0.051
100	19	18.136	17.559	-0.577
100	20	18.197	17.872	-0.325
100	21	17.865	18.294	0.429
100	22	18.164	18.237	0.073
100	24	18.158	17.994	-0.164
105	25	17.736	17.513	-0.223
105	26	17.906	17.887	-0.019
105	27	17.883	18.176	0.293
105	28	17.531	17.778	0.247
105	29	17.551	18.407	0.856
Max		18.427	18.430	0.856
Average		17.948	17.939	-0.009
Min		17.483	17.457	-0.026
Std Dev		0.243	0.293	0.393

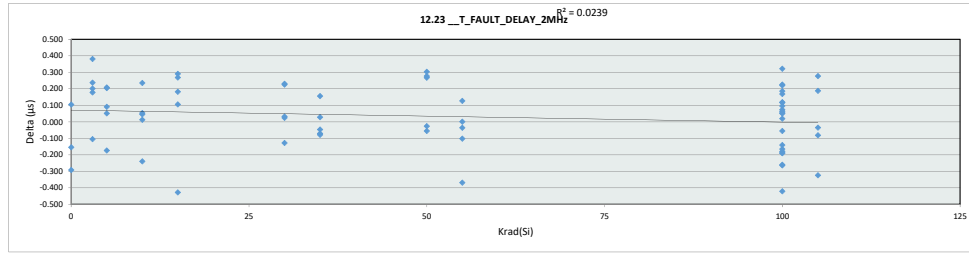


12.21 T_FAULT_DELAY_1MHz											
Test Site											
Tester											
Test Number											
Max Limit	21	µs									
Min Limit	14	µs									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000
Min	17.944	17.469	17.732	17.755	17.499	17.557	17.494	17.500	17.558	17.457	17.513
Average	18.205	17.843	18.029	17.980	18.010	17.888	17.823	17.840	17.941	17.935	17.952
Max	18.401	18.409	18.340	18.370	18.423	18.325	18.103	18.044	18.201	18.430	18.407
UL	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000

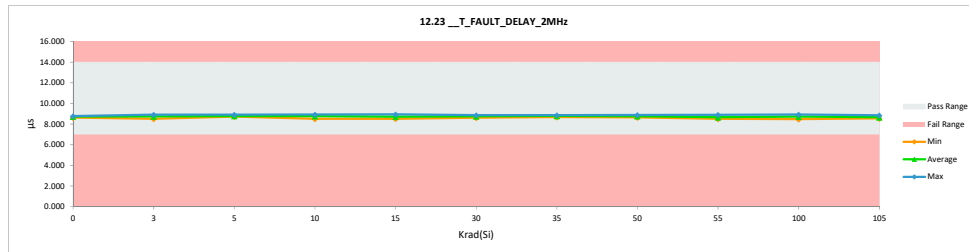


HDR TID Report
TPS7H5001-SP

12.23 T_FAULT_DELAY_2MHz				
Test Site				
Tester				
Test Number				
Unit	µs	µs		
Max Limit	14	14		
Min Limit	7	7		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.675	8.782	0.107
0	118	8.923	8.633	-0.290
0	122	8.909	8.757	-0.152
3	301	8.499	8.703	0.204
3	306	8.736	8.916	0.180
3	307	8.481	8.721	0.240
3	308	8.609	8.506	-0.103
3	309	8.506	8.889	0.383
5	310	8.707	8.914	0.207
5	311	8.933	8.761	-0.172
5	312	8.544	8.755	0.211
5	314	8.705	8.758	0.053
5	316	8.635	8.728	0.093
10	317	8.776	8.831	0.055
10	318	8.824	8.872	0.048
10	319	8.690	8.928	0.238
10	321	8.746	8.508	-0.238
10	322	8.738	8.753	0.015
15	323	8.524	8.817	0.293
15	325	8.491	8.599	0.108
15	326	8.946	8.520	-0.426
15	328	8.679	8.950	0.271
15	333	8.481	8.665	0.184
30	334	8.598	8.524	-0.074
30	335	8.818	8.852	0.034
30	337	8.579	8.812	0.233
30	339	8.746	8.620	-0.126
30	341	8.644	8.872	0.228
35	342	8.906	8.837	-0.069
35	345	8.900	8.823	-0.077
35	346	8.521	8.679	0.158
35	347	8.701	8.731	0.030
35	348	8.916	8.871	-0.045
50	349	8.524	8.803	0.279
50	350	8.579	8.885	0.306
50	351	8.571	8.841	0.270
50	353	8.710	8.657	-0.053
50	354	8.675	8.651	-0.024
55	355	8.891	8.894	0.003
55	357	8.633	8.599	-0.034
55	359	8.876	8.509	-0.367
55	362	8.692	8.821	0.129
55	363	8.645	8.545	-0.100
100	1	8.768	8.580	-0.188
100	3	8.790	8.866	0.076
100	4	8.640	8.738	0.098
100	5	8.512	8.836	0.324
100	6	8.892	8.630	-0.262
100	7	8.670	8.691	0.021
100	8	8.544	8.610	0.066
100	9	8.701	8.929	0.228
100	10	8.678	8.730	0.052
100	11	8.546	8.664	0.118
100	12	8.866	8.813	-0.053
100	13	8.824	8.661	-0.163
100	14	8.641	8.864	0.223
100	15	8.832	8.693	-0.139
100	16	8.612	8.733	0.121
100	17	8.930	8.752	-0.178
100	18	8.689	8.860	0.171
100	19	8.900	8.481	-0.419
100	20	8.720	8.776	0.056
100	21	8.622	8.810	0.188
100	22	8.747	8.561	-0.186
100	24	8.944	8.686	-0.258
105	25	8.658	8.578	-0.080
105	26	8.579	8.858	0.279
105	27	8.562	8.752	0.190
105	28	8.585	8.552	-0.033
105	29	8.938	8.616	-0.322
	Max	8.946	8.950	0.383
	Average	8.704	8.736	0.032
	Min	8.481	8.481	-0.426
	Std Dev	0.139	0.123	0.192

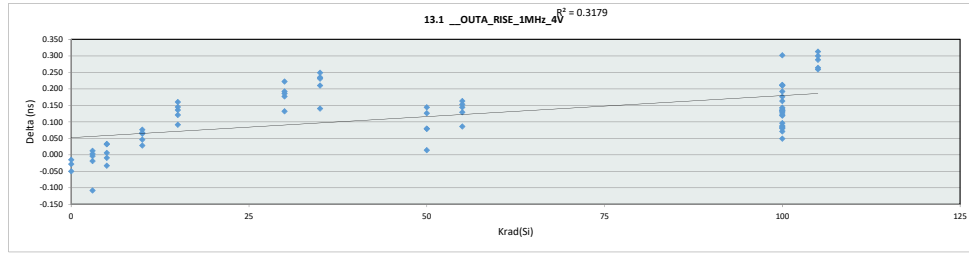


12.23 T_FAULT_DELAY_2MHz											
Test Site											
Tester											
Test Number											
Max Limit	14	µs									
Min Limit	7	µs									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000
Min	8.633	8.506	8.728	8.508	8.520	8.620	8.679	8.651	8.509	8.481	8.552
Average	8.724	8.747	8.783	8.778	8.710	8.756	8.788	8.767	8.674	8.726	8.671
Max	8.782	8.916	8.914	8.928	8.950	8.872	8.871	8.885	8.894	8.929	8.858
UL	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000

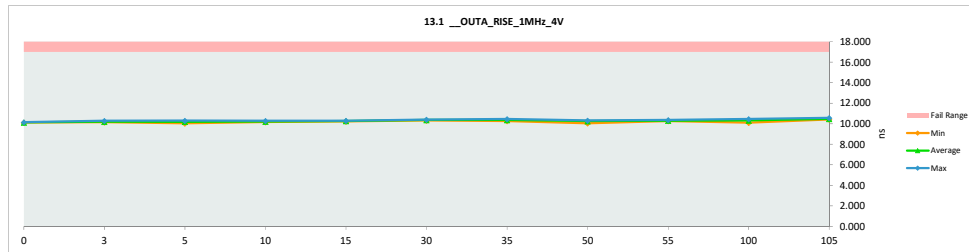


HDR TID Report
TPS7H5001-SP

13.1 OUTA_RISE_1MHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	10.177	10.150	-0.027
0	118	10.159	10.145	-0.014
0	122	10.170	10.121	-0.049
3	301	10.292	10.185	-0.107
3	306	10.192	10.174	-0.018
3	307	10.149	10.153	0.004
3	308	10.277	10.290	0.013
3	309	10.204	10.201	-0.003
5	310	10.069	10.037	-0.032
5	311	10.181	10.188	0.007
5	312	10.198	10.231	0.033
5	314	10.273	10.307	0.034
5	316	10.197	10.189	-0.008
10	317	10.176	10.223	0.047
10	318	10.134	10.202	0.068
10	319	10.140	10.169	0.029
10	321	10.121	10.186	0.065
10	322	10.209	10.286	0.077
15	323	10.138	10.299	0.161
15	325	10.158	10.250	0.092
15	326	10.151	10.288	0.137
15	328	10.079	10.225	0.146
15	333	10.162	10.284	0.122
30	334	10.198	10.321	0.123
30	335	10.202	10.380	0.178
30	337	10.167	10.390	0.223
30	339	10.213	10.406	0.193
30	341	10.136	10.323	0.187
35	342	10.149	10.385	0.236
35	345	10.249	10.460	0.211
35	346	10.122	10.372	0.250
35	347	10.113	10.345	0.232
35	348	10.124	10.265	0.141
50	349	10.245	10.325	0.080
50	350	10.196	10.323	0.127
50	351	10.198	10.278	0.080
50	353	10.164	10.309	0.145
50	354	10.031	10.046	0.015
55	355	10.104	10.268	0.164
55	357	10.220	10.365	0.145
55	359	10.162	10.292	0.130
55	362	10.191	10.278	0.087
55	363	10.123	10.277	0.154
100	1	10.258	10.422	0.164
100	3	10.188	10.316	0.128
100	4	10.333	10.455	0.122
100	5	10.260	10.396	0.136
100	6	10.084	10.217	0.133
100	7	10.161	10.354	0.193
100	8	10.260	10.395	0.135
100	9	10.284	10.381	0.097
100	10	10.251	10.463	0.212
100	11	10.238	10.321	0.083
100	12	10.029	10.101	0.072
100	13	10.074	10.214	0.140
100	14	10.160	10.246	0.086
100	15	10.227	10.438	0.211
100	16	10.138	10.188	0.050
100	17	10.160	10.241	0.081
100	18	10.278	10.367	0.089
100	19	10.082	10.295	0.213
100	20	10.158	10.302	0.144
100	21	10.229	10.406	0.177
100	22	10.091	10.394	0.303
100	24	10.151	10.271	0.120
105	25	10.161	10.421	0.260
105	26	10.136	10.437	0.301
105	27	10.128	10.442	0.314
105	28	10.287	10.576	0.289
105	29	10.216	10.481	0.265
Max		10.333	10.576	0.314
Average		10.176	10.296	0.120
Min		10.029	10.037	-0.107
Std Dev		0.064	0.108	0.092

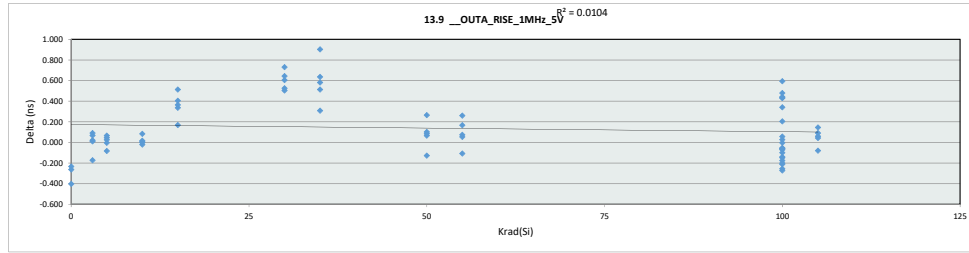


13.1 OUTA_RISE_1MHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	10.121	10.153	10.037	10.169	10.225	10.321	10.265	10.046	10.268	10.101	10.421
Average	10.139	10.201	10.190	10.213	10.269	10.364	10.365	10.256	10.296	10.327	10.471
Max	10.150	10.290	10.307	10.286	10.299	10.406	10.460	10.325	10.365	10.463	10.576
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

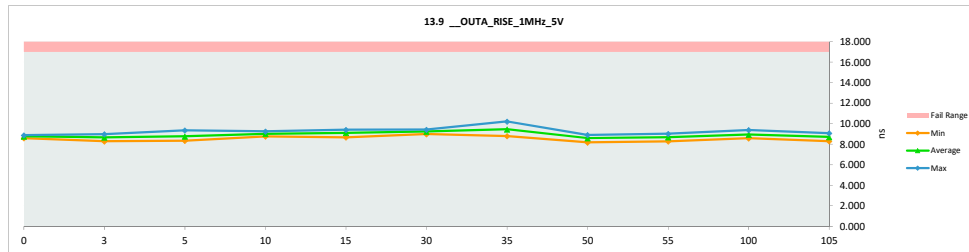


HDR TID Report
TPS7H5001-SP

		13.9 OUTA_RISE_1MHz_5V		
Test Site				
Tester				
Test Number				
Unit		ns	ns	
Max Limit		17	17	
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	9.248	8.849	-0.399
0	118	8.831	8.601	-0.230
0	122	9.144	8.885	-0.259
3	301	8.465	8.295	-0.170
3	306	8.375	8.470	0.095
3	307	8.974	8.988	0.014
3	308	8.635	8.659	0.024
3	309	8.900	8.971	0.071
5	310	8.680	8.730	0.050
5	311	8.321	8.350	0.029
5	312	8.367	8.436	0.069
5	314	9.364	9.364	0.000
5	316	9.106	9.026	-0.080
10	317	9.108	9.090	-0.018
10	318	8.748	8.768	0.020
10	319	8.969	8.972	0.003
10	321	8.924	9.011	0.087
10	322	9.254	9.263	0.009
15	323	8.868	9.384	0.516
15	325	8.756	9.164	0.408
15	326	8.502	8.674	0.172
15	328	8.599	8.938	0.339
15	333	9.053	9.420	0.367
30	334	8.590	9.324	0.734
30	335	8.905	9.454	0.549
30	337	8.711	9.320	0.609
30	339	8.492	9.000	0.508
30	341	8.515	9.162	0.647
35	342	8.491	8.802	0.311
35	345	9.317	10.223	0.906
35	346	8.851	9.436	0.585
35	347	9.043	9.682	0.639
35	348	8.647	9.163	0.516
50	349	8.842	8.912	0.070
50	350	8.048	8.317	0.269
50	351	8.754	8.860	0.106
50	353	8.098	8.186	0.088
50	354	8.852	8.728	-0.124
55	355	8.857	9.028	0.171
55	357	8.450	8.713	0.263
55	359	9.035	8.930	-0.105
55	362	8.433	8.490	0.057
55	363	8.213	8.291	0.078
100	1	8.563	8.908	0.345
100	3	8.937	8.793	-0.144
100	4	8.840	9.285	0.445
100	5	8.763	9.361	0.598
100	6	8.925	8.924	-0.001
100	7	9.058	8.849	-0.209
100	8	8.528	8.962	0.434
100	9	9.506	9.254	-0.252
100	10	9.448	9.398	-0.050
100	11	9.043	8.984	-0.059
100	12	8.762	8.695	-0.067
100	13	8.847	8.878	0.031
100	14	9.209	9.072	-0.137
100	15	8.537	9.019	0.482
100	16	9.048	8.876	-0.172
100	17	9.055	8.786	-0.269
100	18	8.900	8.960	0.060
100	19	8.758	8.661	-0.097
100	20	9.125	8.928	-0.197
100	21	8.157	8.593	0.436
100	22	8.782	8.989	0.207
100	24	8.748	8.693	-0.055
105	25	9.156	9.080	-0.076
105	26	8.934	9.083	0.149
105	27	8.707	8.771	0.064
105	28	8.260	8.307	0.047
105	29	8.239	8.334	0.095
Max		9.506	10.223	0.906
Average		8.788	8.925	0.137
Min		8.048	8.186	-0.399
Std Dev		0.326	0.362	0.281

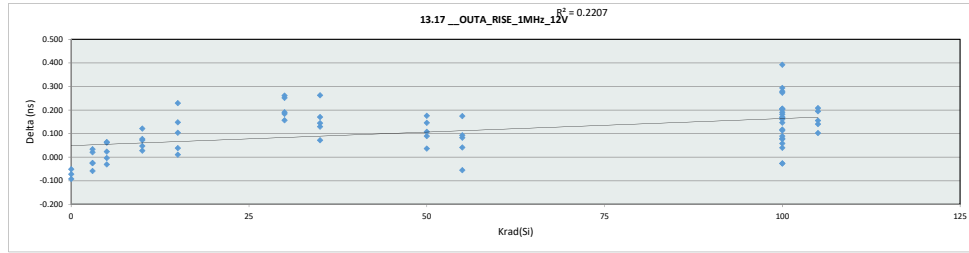


		13.9 OUTA_RISE_1MHz_5V										
Test Site												
Tester												
Test Number												
Max Limit		17	ns									
Min Limit			ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL												
Min	8.601	8.295	8.350	8.768	8.674	9.000	8.802	8.186	8.291	8.593	8.307	
Average	8.778	8.677	8.781	9.021	9.116	9.248	9.461	8.601	8.690	8.949	8.715	
Max	8.885	8.988	9.364	9.263	9.420	9.434	10.223	8.912	9.028	9.398	9.083	
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	

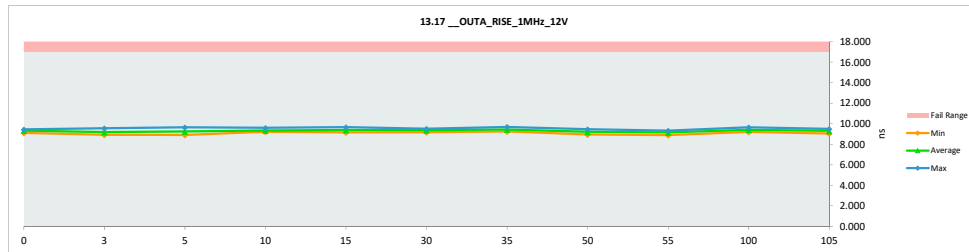


HDR TID Report
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13.17 OUTA_RISE_1MHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	9.500	9.450	-0.050
0	118	9.169	9.098	-0.071
0	122	9.496	9.405	-0.091
3	301	8.973	8.916	-0.057
3	306	9.029	9.051	0.022
3	307	9.578	9.555	-0.023
3	308	9.148	9.124	-0.024
3	309	9.228	9.263	0.035
5	310	9.308	9.279	-0.029
5	311	8.906	8.972	0.066
5	312	8.837	8.900	0.063
5	314	9.621	9.646	0.025
5	316	9.445	9.443	-0.002
10	317	9.214	9.288	0.074
10	318	9.181	9.210	0.029
10	319	9.470	9.593	0.123
10	321	9.138	9.217	0.079
10	322	9.312	9.361	0.049
15	323	9.432	9.663	0.231
15	325	9.364	9.469	0.105
15	326	9.104	9.144	0.040
15	328	9.149	9.161	0.012
15	333	9.342	9.491	0.149
30	334	9.130	9.383	0.253
30	335	9.155	9.418	0.263
30	337	9.313	9.505	0.192
30	339	9.012	9.170	0.158
30	341	9.158	9.343	0.185
35	342	9.169	9.242	0.073
35	345	9.321	9.493	0.172
35	346	9.254	9.400	0.146
35	347	9.424	9.688	0.264
35	348	9.248	9.379	0.131
50	349	9.166	9.275	0.109
50	350	8.795	8.942	0.147
50	351	9.287	9.464	0.177
50	353	8.898	8.989	0.091
50	354	9.303	9.341	0.038
55	355	9.278	9.321	0.043
55	357	8.956	9.132	0.176
55	359	9.241	9.325	0.084
55	362	9.011	9.105	0.094
55	363	8.945	8.891	-0.054
100	1	9.088	9.296	0.208
100	3	9.303	9.472	0.169
100	4	9.232	9.507	0.275
100	5	9.044	9.438	0.394
100	6	9.213	9.332	0.119
100	7	9.278	9.356	0.078
100	8	9.152	9.447	0.295
100	9	9.486	9.648	0.162
100	10	9.597	9.638	0.041
100	11	9.110	9.258	0.148
100	12	9.228	9.433	0.205
100	13	9.185	9.390	0.205
100	14	9.429	9.544	0.115
100	15	9.057	9.249	0.192
100	16	9.572	9.653	0.081
100	17	9.378	9.353	-0.025
100	18	9.257	9.232	-0.025
100	19	9.283	9.343	0.060
100	20	9.328	9.500	0.172
100	21	8.929	9.210	0.281
100	22	9.210	9.393	0.183
100	24	9.109	9.200	0.091
105	25	9.281	9.491	0.210
105	26	9.263	9.459	0.196
105	27	9.273	9.430	0.157
105	28	8.917	9.059	0.142
105	29	8.933	9.037	0.104
Max		9.621	9.688	0.394
Average		9.216	9.327	0.110
Min		8.795	8.891	-0.091
Std Dev		0.188	0.198	0.100

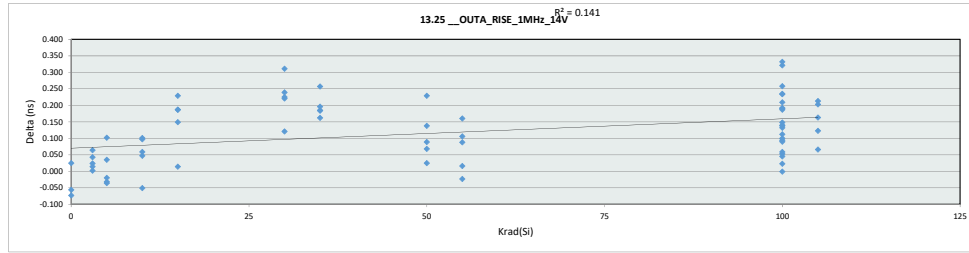


13.17 OUTA_RISE_1MHz_12V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.098	8.916	8.900	9.210	9.144	9.170	9.242	8.942	8.891	9.200	9.037
Average	9.318	9.182	9.248	9.334	9.386	9.364	9.440	9.202	9.155	9.404	9.295
Max	9.450	9.555	9.646	9.593	9.663	9.505	9.688	9.464	9.325	9.653	9.491
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

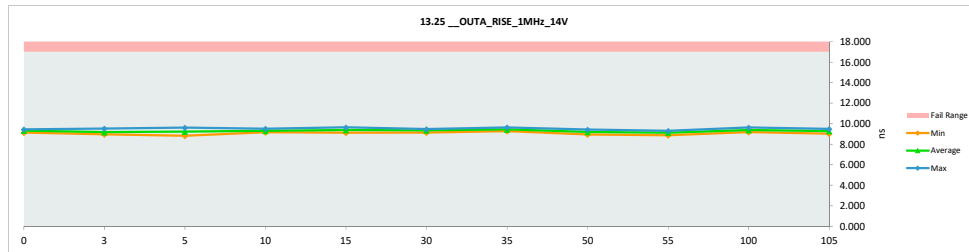


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13.25 OUTA_RISE_1MHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	9.509	9.454	-0.055
0	118	9.103	9.129	0.026
0	122	9.455	9.383	-0.072
3	301	8.957	8.960	0.003
3	306	9.035	9.060	0.025
3	307	9.467	9.532	0.065
3	308	9.096	9.111	0.015
3	309	9.204	9.248	0.044
5	310	9.298	9.267	-0.031
5	311	8.865	8.968	0.103
5	312	8.864	8.829	-0.035
5	314	9.587	9.623	0.036
5	316	9.458	9.439	-0.019
10	317	9.221	9.269	0.048
10	318	9.276	9.226	-0.050
10	319	9.457	9.517	0.060
10	321	9.068	9.170	0.102
10	322	9.249	9.347	0.098
15	323	9.419	9.649	0.230
15	325	9.277	9.465	0.188
15	326	9.044	9.232	0.188
15	328	9.101	9.116	0.015
15	333	9.312	9.462	0.150
30	334	9.064	9.376	0.312
30	335	9.185	9.411	0.226
30	337	9.238	9.478	0.240
30	339	9.014	9.136	0.122
30	341	9.146	9.368	0.222
35	342	9.081	9.278	0.197
35	345	9.266	9.452	0.186
35	346	9.251	9.414	0.163
35	347	9.383	9.641	0.258
35	348	9.246	9.431	0.185
50	349	9.143	9.169	0.026
50	350	8.884	9.023	0.139
50	351	9.207	9.437	0.230
50	353	8.882	8.951	0.069
50	354	9.298	9.388	0.090
55	355	9.200	9.307	0.107
55	357	8.917	9.078	0.161
55	359	9.281	9.298	0.017
55	362	9.000	9.089	0.089
55	363	8.895	8.873	-0.022
100	1	9.074	9.310	0.236
100	3	9.262	9.453	0.191
100	4	9.170	9.429	0.259
100	5	9.070	9.392	0.322
100	6	9.239	9.378	0.139
100	7	9.220	9.333	0.113
100	8	9.096	9.429	0.333
100	9	9.484	9.633	0.149
100	10	9.537	9.628	0.091
100	11	9.117	9.212	0.095
100	12	9.210	9.445	0.235
100	13	9.132	9.320	0.188
100	14	9.391	9.445	0.054
100	15	9.082	9.215	0.133
100	16	9.566	9.626	0.060
100	17	9.358	9.358	0.000
100	18	9.188	9.234	0.046
100	19	9.277	9.301	0.024
100	20	9.308	9.501	0.193
100	21	8.972	9.182	0.210
100	22	9.207	9.348	0.141
100	24	9.123	9.224	0.101
105	25	9.283	9.497	0.214
105	26	9.293	9.417	0.124
105	27	9.218	9.422	0.204
105	28	8.887	9.051	0.164
105	29	8.960	9.027	0.067
Max		9.587	9.649	0.333
Average		9.195	9.312	0.118
Min		8.864	8.829	-0.072
Std Dev		0.180	0.192	0.097

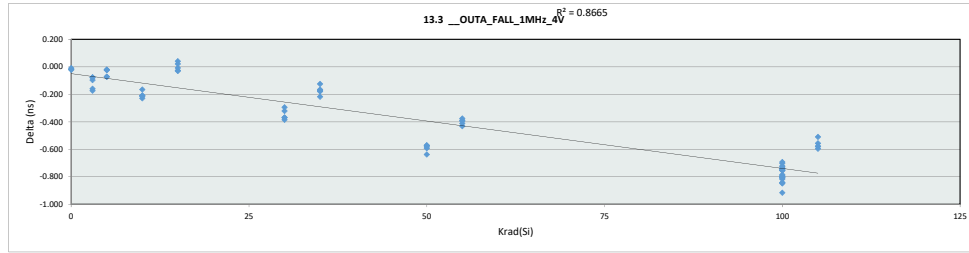


13.25 OUTA_RISE_1MHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.129	8.960	8.829	9.170	9.116	9.136	9.278	8.951	8.873	9.182	9.027
Average	9.322	9.182	9.225	9.306	9.385	9.354	9.443	9.194	9.129	9.382	9.283
Max	9.454	9.532	9.623	9.517	9.649	9.478	9.641	9.437	9.307	9.633	9.497
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

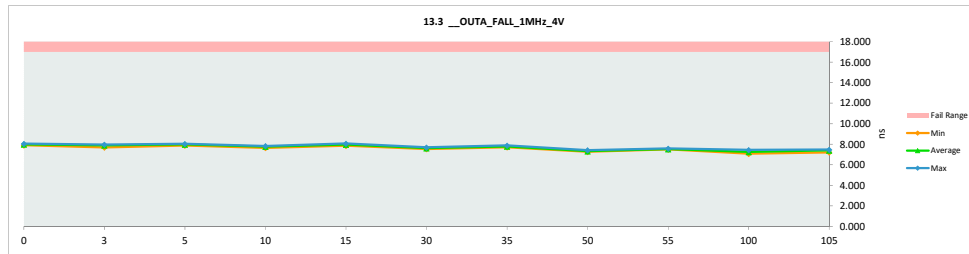


HDR TID Report
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13.3 OUTA_FALL_1MHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.082	8.063	-0.019
0	118	7.920	7.905	-0.015
0	122	8.003	7.997	-0.006
3	301	8.051	7.894	-0.157
3	306	7.867	7.696	-0.171
3	307	8.030	7.957	-0.073
3	308	8.067	7.973	-0.094
3	309	8.032	7.956	-0.076
5	310	8.038	8.017	-0.021
5	311	8.065	8.045	-0.020
5	312	7.988	7.971	-0.017
5	314	7.950	7.880	-0.070
5	316	8.044	7.976	-0.068
10	317	8.030	7.804	-0.226
10	318	8.009	7.802	-0.207
10	319	7.861	7.649	-0.212
10	321	7.884	7.722	-0.162
10	322	8.042	7.836	-0.206
15	323	7.955	7.931	-0.024
15	325	8.027	8.070	0.043
15	326	8.083	8.078	-0.005
15	328	7.830	7.854	0.024
15	333	7.908	7.881	-0.027
30	334	7.850	7.558	-0.292
30	335	7.902	7.536	-0.366
30	337	8.055	7.689	-0.366
30	339	8.024	7.706	-0.318
30	341	8.030	7.647	-0.383
35	342	7.930	7.808	-0.122
35	345	7.885	7.708	-0.177
35	346	7.960	7.791	-0.169
35	347	7.943	7.728	-0.215
35	348	8.054	7.892	-0.162
50	349	7.864	7.285	-0.579
50	350	7.998	7.429	-0.569
50	351	7.856	7.266	-0.590
50	353	7.909	7.340	-0.569
50	354	7.940	7.305	-0.635
55	355	7.939	7.530	-0.409
55	357	7.951	7.559	-0.392
55	359	8.023	7.594	-0.429
55	362	7.869	7.496	-0.373
55	363	7.882	7.496	-0.386
100	1	8.200	7.287	-0.913
100	3	7.923	7.171	-0.752
100	4	7.984	7.265	-0.719
100	5	8.295	7.449	-0.846
100	6	7.965	7.275	-0.690
100	7	8.200	7.452	-0.748
100	8	8.125	7.319	-0.806
100	9	8.208	7.393	-0.815
100	10	8.017	7.214	-0.803
100	11	8.065	7.274	-0.791
100	12	7.981	7.245	-0.736
100	13	8.056	7.216	-0.840
100	14	8.211	7.371	-0.840
100	15	7.883	7.131	-0.752
100	16	8.012	7.230	-0.782
100	17	7.924	7.131	-0.793
100	18	7.890	7.191	-0.699
100	19	7.967	7.173	-0.794
100	20	7.929	7.185	-0.744
100	21	8.008	7.270	-0.738
100	22	7.882	7.078	-0.804
100	24	7.934	7.209	-0.725
105	25	7.789	7.212	-0.577
105	26	8.021	7.446	-0.575
105	27	8.054	7.461	-0.593
105	28	7.989	7.482	-0.507
105	29	7.969	7.415	-0.554
	Max	8.295	8.078	0.043
	Average	7.987	7.570	-0.418
	Min	7.789	7.078	-0.913
	Std Dev	0.098	0.301	0.303

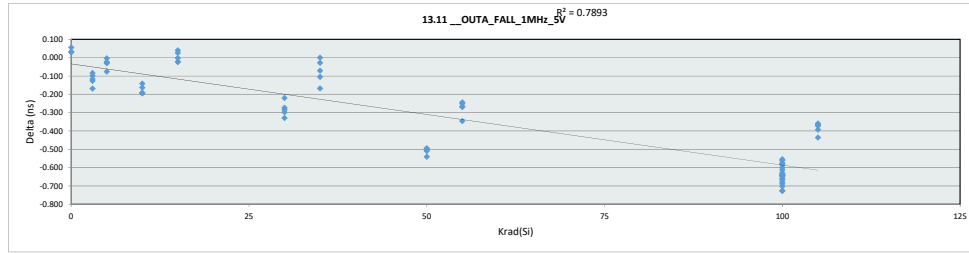


13.3 OUTA_FALL_1MHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.905	7.696	7.880	7.649	7.854	7.536	7.708	7.266	7.496	7.078	7.212
Average	7.988	7.895	7.978	7.763	7.963	7.627	7.785	7.325	7.535	7.251	7.403
Max	8.063	7.973	8.045	7.836	8.078	7.706	7.892	7.429	7.594	7.452	7.482
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

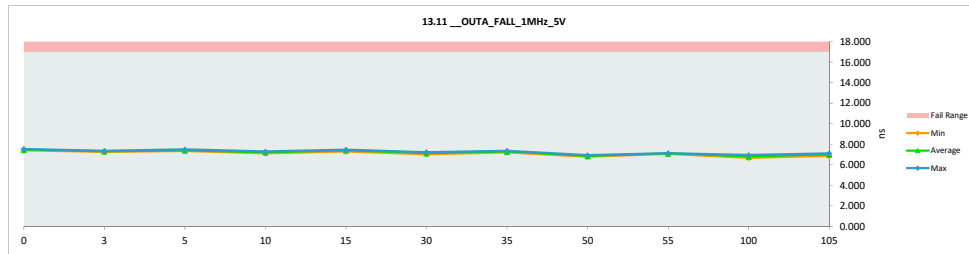


HDR TID Report
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		13.11 OUTA_FALL_1MHz_5V		
Test Site	Tester			
Test Number	Unit	ns	ns	
Max Limit	Min Limit	17	17	
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	7.479	7.537	0.058
0	118	7.376	7.408	0.032
0	122	7.458	7.492	0.034
3	301	7.476	7.309	-0.167
3	306	7.377	7.263	-0.114
3	307	7.412	7.315	-0.097
3	308	7.490	7.365	-0.125
3	309	7.439	7.357	-0.082
5	310	7.523	7.501	-0.022
5	311	7.493	7.465	-0.028
5	312	7.375	7.373	-0.002
5	314	7.377	7.351	-0.026
5	316	7.473	7.399	-0.074
10	317	7.438	7.298	-0.140
10	318	7.403	7.210	-0.193
10	319	7.311	7.120	-0.191
10	321	7.381	7.220	-0.161
10	322	7.472	7.283	-0.189
15	323	7.387	7.366	-0.021
15	325	7.431	7.473	0.042
15	326	7.494	7.473	-0.021
15	328	7.304	7.304	0.000
15	333	7.434	7.462	0.028
30	334	7.356	7.029	-0.327
30	335	7.378	7.107	-0.271
30	337	7.469	7.173	-0.296
30	339	7.449	7.231	-0.218
30	341	7.439	7.157	-0.282
35	342	7.367	7.264	-0.103
35	345	7.382	7.216	-0.166
35	346	7.338	7.340	0.002
35	347	7.356	7.287	-0.069
35	348	7.399	7.373	-0.026
50	349	7.290	6.796	-0.494
50	350	7.428	6.935	-0.493
50	351	7.345	6.806	-0.539
50	353	7.377	6.870	-0.507
50	354	7.396	6.896	-0.500
55	355	7.469	7.125	-0.344
55	357	7.402	7.137	-0.265
55	359	7.387	7.138	-0.249
55	362	7.355	7.088	-0.267
55	363	7.309	7.067	-0.242
100	1	7.553	6.828	-0.725
100	3	7.373	6.794	-0.579
100	4	7.427	6.767	-0.660
100	5	7.598	6.958	-0.640
100	6	7.506	6.830	-0.676
100	7	7.495	6.923	-0.572
100	8	7.503	6.862	-0.641
100	9	7.500	6.852	-0.648
100	10	7.493	6.830	-0.663
100	11	7.449	6.814	-0.635
100	12	7.381	6.798	-0.583
100	13	7.540	6.854	-0.686
100	14	7.613	6.913	-0.700
100	15	7.303	6.705	-0.598
100	16	7.442	6.831	-0.611
100	17	7.404	6.679	-0.725
100	18	7.388	6.752	-0.636
100	19	7.367	6.809	-0.558
100	20	7.428	6.792	-0.636
100	21	7.434	6.807	-0.627
100	22	7.275	6.722	-0.553
100	24	7.402	6.759	-0.643
105	25	7.311	6.877	-0.434
105	26	7.390	7.033	-0.357
105	27	7.486	7.122	-0.364
105	28	7.414	7.023	-0.391
105	29	7.396	7.026	-0.370
Max		7.613	7.537	0.058
Average		7.419	7.091	-0.329
Min		7.275	6.679	-0.725
Std Dev		0.070	0.251	0.254

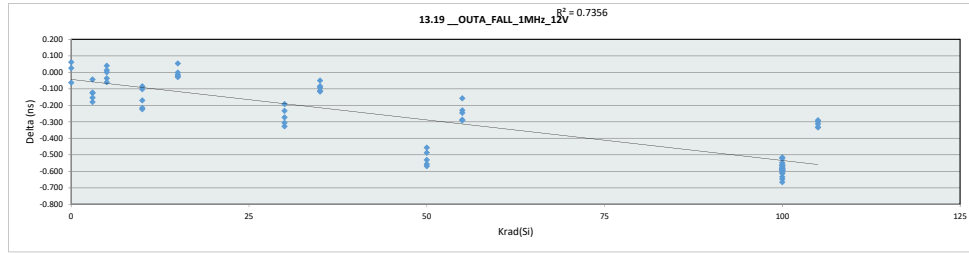


		13.11 OUTA_FALL_1MHz_5V										
Test Site	Tester											
Test Number	Unit	17	ns									
Max Limit	Min Limit	17	ns									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		7.408	7.263	7.351	7.120	7.304	7.029	7.216	6.796	7.067	6.679	6.877
Average		7.479	7.322	7.418	7.226	7.416	7.139	7.296	6.861	7.111	6.813	7.016
Max		7.537	7.365	7.501	7.298	7.473	7.231	7.373	6.935	7.138	6.958	7.122
UL		17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

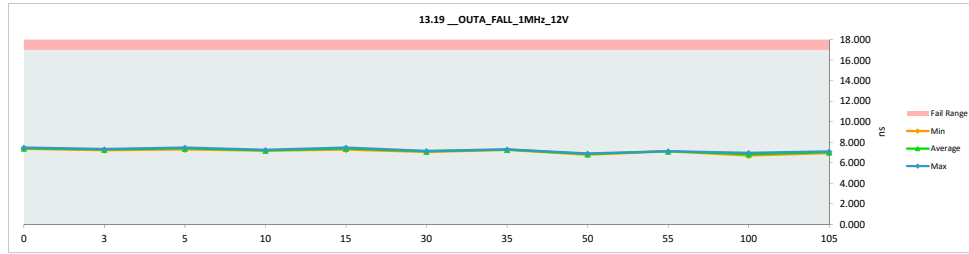


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13.19 OUTA_FALL_1MHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	7.455	7.483	0.028
0	118	7.301	7.365	0.064
0	122	7.452	7.392	-0.060
3	301	7.442	7.264	-0.178
3	306	7.359	7.207	-0.152
3	307	7.368	7.327	-0.041
3	308	7.451	7.330	-0.121
3	309	7.454	7.331	-0.123
5	310	7.475	7.478	0.003
5	311	7.375	7.417	0.042
5	312	7.351	7.294	-0.057
5	314	7.379	7.394	0.015
5	316	7.468	7.434	-0.034
10	317	7.414	7.200	-0.214
10	318	7.267	7.184	-0.083
10	319	7.387	7.166	-0.221
10	321	7.300	7.200	-0.100
10	322	7.420	7.252	-0.168
15	323	7.422	7.396	-0.026
15	325	7.420	7.476	0.056
15	326	7.452	7.430	-0.022
15	328	7.288	7.275	-0.013
15	333	7.375	7.376	0.001
30	334	7.237	7.047	-0.190
30	335	7.361	7.057	-0.304
30	337	7.483	7.158	-0.325
30	339	7.366	7.135	-0.231
30	341	7.412	7.141	-0.271
35	342	7.325	7.277	-0.048
35	345	7.365	7.250	-0.115
35	346	7.394	7.312	-0.082
35	347	7.381	7.271	-0.110
35	348	7.367	7.276	-0.091
50	349	7.261	6.776	-0.485
50	350	7.450	6.883	-0.567
50	351	7.355	6.901	-0.454
50	353	7.385	6.856	-0.529
50	354	7.359	6.805	-0.554
55	355	7.416	7.127	-0.289
55	357	7.405	7.120	-0.285
55	359	7.342	7.114	-0.228
55	362	7.349	7.105	-0.244
55	363	7.279	7.124	-0.155
100	1	7.511	6.921	-0.590
100	3	7.357	6.844	-0.513
100	4	7.425	6.843	-0.582
100	5	7.456	6.936	-0.520
100	6	7.478	6.846	-0.632
100	7	7.475	6.896	-0.579
100	8	7.458	6.884	-0.574
100	9	7.508	6.953	-0.555
100	10	7.503	6.839	-0.664
100	11	7.404	6.793	-0.611
100	12	7.354	6.791	-0.563
100	13	7.464	6.900	-0.564
100	14	7.564	6.960	-0.604
100	15	7.306	6.713	-0.593
100	16	7.434	6.869	-0.565
100	17	7.344	6.699	-0.645
100	18	7.378	6.787	-0.591
100	19	7.329	6.784	-0.545
100	20	7.391	6.800	-0.591
100	21	7.418	6.839	-0.579
100	22	7.330	6.745	-0.585
100	24	7.324	6.735	-0.589
105	25	7.243	6.932	-0.311
105	26	7.324	7.036	-0.288
105	27	7.401	7.105	-0.296
105	28	7.368	7.037	-0.331
105	29	7.382	7.051	-0.331
Max		7.564	7.483	0.064
Average		7.390	7.085	-0.305
Min		7.237	6.699	-0.664
Std Dev		0.068	0.231	0.234

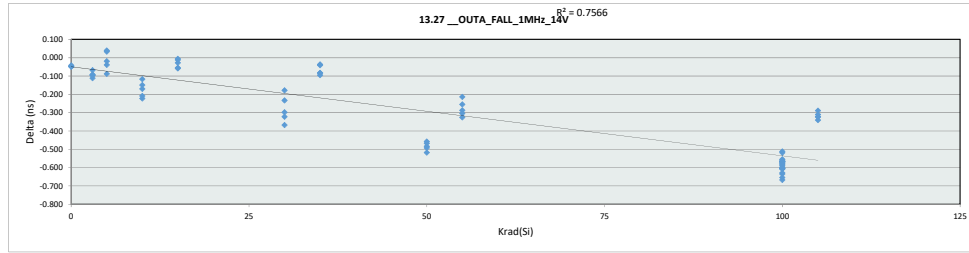


13.19 OUTA_FALL_1MHz_12V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.365	7.207	7.294	7.166	7.275	7.047	7.250	6.776	7.105	6.699	6.932
Average	7.413	7.292	7.403	7.200	7.391	7.108	7.277	6.844	7.118	6.835	7.032
Max	7.483	7.331	7.478	7.252	7.476	7.158	7.312	6.901	7.127	6.960	7.105
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

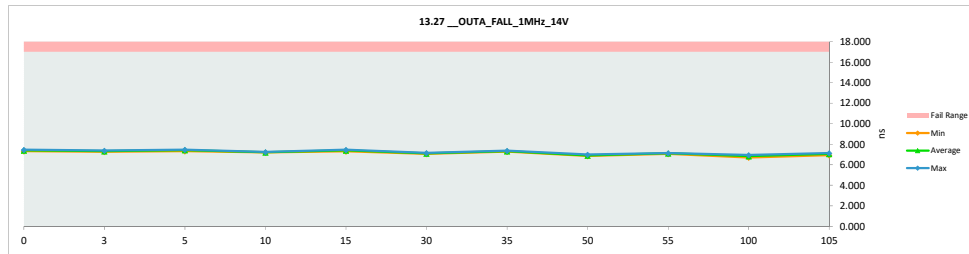


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13.27 OUTA_FALL_1MHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	7.514	7.467	-0.047
0	118	7.363	7.323	-0.040
0	122	7.479	7.432	-0.047
3	301	7.398	7.303	-0.095
3	306	7.349	7.259	-0.090
3	307	7.476	7.384	-0.092
3	308	7.424	7.358	-0.066
3	309	7.440	7.330	-0.110
5	310	7.503	7.466	-0.037
5	311	7.560	7.474	-0.086
5	312	7.341	7.324	-0.017
5	314	7.392	7.427	0.035
5	316	7.407	7.448	0.041
10	317	7.455	7.248	-0.207
10	318	7.355	7.208	-0.147
10	319	7.370	7.202	-0.168
10	321	7.330	7.215	-0.115
10	322	7.454	7.233	-0.221
15	323	7.450	7.397	-0.053
15	325	7.480	7.470	-0.010
15	326	7.483	7.457	-0.026
15	328	7.363	7.306	-0.057
15	333	7.386	7.382	-0.004
30	334	7.359	7.074	-0.176
30	335	7.352	7.056	-0.296
30	337	7.519	7.153	-0.366
30	339	7.360	7.129	-0.231
30	341	7.453	7.133	-0.320
35	342	7.379	7.297	-0.082
35	345	7.367	7.286	-0.081
35	346	7.397	7.362	-0.035
35	347	7.386	7.293	-0.093
35	348	7.410	7.371	-0.039
50	349	7.328	6.872	-0.456
50	350	7.459	6.994	-0.465
50	351	7.403	6.921	-0.482
50	353	7.388	6.872	-0.516
50	354	7.355	6.864	-0.491
55	355	7.391	7.138	-0.253
55	357	7.424	7.139	-0.285
55	359	7.441	7.139	-0.302
55	362	7.325	7.113	-0.212
55	363	7.389	7.065	-0.324
100	1	7.509	6.934	-0.575
100	3	7.389	6.824	-0.565
100	4	7.397	6.840	-0.557
100	5	7.534	6.933	-0.601
100	6	7.470	6.867	-0.603
100	7	7.479	6.893	-0.586
100	8	7.517	6.954	-0.563
100	9	7.539	6.944	-0.595
100	10	7.461	6.892	-0.569
100	11	7.387	6.781	-0.606
100	12	7.359	6.849	-0.510
100	13	7.469	6.916	-0.553
100	14	7.510	6.909	-0.601
100	15	7.361	6.758	-0.603
100	16	7.479	6.852	-0.627
100	17	7.377	6.712	-0.665
100	18	7.436	6.784	-0.652
100	19	7.324	6.807	-0.517
100	20	7.423	6.790	-0.633
100	21	7.453	6.887	-0.566
100	22	7.343	6.763	-0.580
100	24	7.324	6.758	-0.566
105	25	7.252	6.932	-0.320
105	26	7.381	7.060	-0.321
105	27	7.449	7.141	-0.308
105	28	7.363	7.076	-0.287
105	29	7.408	7.069	-0.339
	Max	7.560	7.474	0.041
	Average	7.413	7.104	-0.309
	Min	7.250	6.712	-0.665
	Std Dev	0.066	0.231	0.229

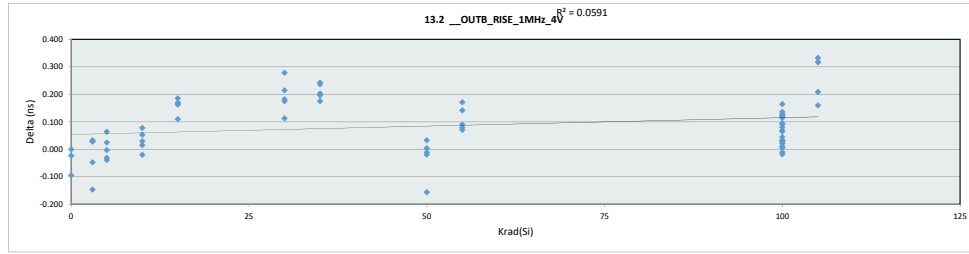


13.27 OUTA_FALL_1MHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.323	7.259	7.324	7.202	7.306	7.056	7.286	6.864	7.065	6.712	6.932
Average	7.407	7.327	7.428	7.221	7.402	7.109	7.322	6.905	7.119	6.848	7.056
Max	7.467	7.384	7.474	7.248	7.470	7.153	7.371	6.994	7.139	6.954	7.141
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

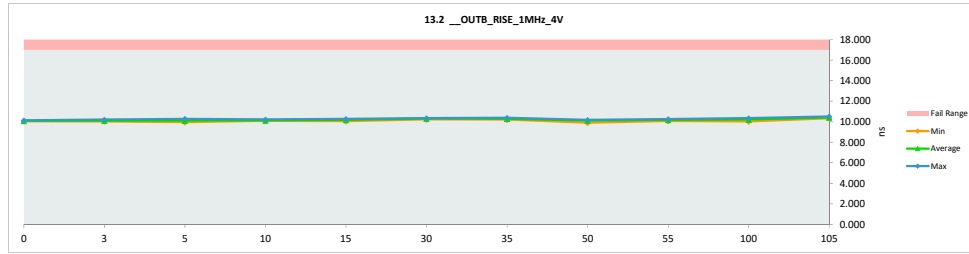


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13.2 OUTB_RISE_1MHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	10.140	10.047	-0.093
0	118	10.107	10.085	-0.022
0	122	10.129	10.130	0.001
3	301	10.214	10.069	-0.145
3	306	10.080	10.034	-0.046
3	307	10.046	10.076	0.030
3	308	10.182	10.211	0.029
3	309	10.083	10.117	0.034
5	310	9.992	9.962	-0.030
5	311	10.091	10.117	0.026
5	312	10.150	10.112	-0.038
5	314	10.215	10.280	0.065
5	316	10.145	10.144	-0.001
10	317	10.101	10.082	-0.019
10	318	10.069	10.123	0.054
10	319	10.006	10.085	0.079
10	321	10.089	10.120	0.031
10	322	10.215	10.231	0.016
15	323	10.087	10.274	0.187
15	325	10.045	10.214	0.169
15	326	10.044	10.208	0.164
15	328	9.883	10.054	0.171
15	333	10.039	10.150	0.111
30	334	10.133	10.349	0.216
30	335	10.151	10.265	0.114
30	337	10.061	10.341	0.280
30	339	10.165	10.348	0.183
30	341	10.056	10.232	0.176
35	342	10.039	10.283	0.244
35	345	10.177	10.381	0.204
35	346	10.073	10.250	0.177
35	347	10.148	10.345	0.197
35	348	9.970	10.208	0.238
50	349	10.200	10.182	-0.018
50	350	10.145	10.150	0.005
50	351	10.124	10.114	-0.010
50	353	10.113	10.147	0.034
50	354	10.059	9.904	-0.155
55	355	9.996	10.076	0.080
55	357	10.162	10.253	0.091
55	359	10.061	10.204	0.143
55	362	10.144	10.216	0.072
55	363	10.009	10.181	0.172
100	1	10.188	10.284	0.096
100	3	10.140	10.163	0.023
100	4	10.232	10.347	0.115
100	5	10.225	10.349	0.124
100	6	10.050	10.119	0.069
100	7	10.106	10.198	0.092
100	8	10.172	10.295	0.123
100	9	10.266	10.289	0.023
100	10	10.194	10.240	0.046
100	11	10.241	10.272	0.031
100	12	10.029	10.020	-0.009
100	13	10.018	10.099	0.081
100	14	10.056	10.222	0.166
100	15	10.131	10.197	0.066
100	16	10.124	10.136	0.012
100	17	10.113	10.096	-0.017
100	18	10.093	10.230	0.137
100	19	10.067	10.196	0.129
100	20	10.141	10.175	0.034
100	21	10.212	10.245	0.033
100	22	10.195	10.201	0.006
100	24	10.044	10.172	0.128
105	25	10.173	10.334	0.161
105	26	10.104	10.422	0.318
105	27	10.048	10.382	0.334
105	28	10.307	10.517	0.210
105	29	10.139	10.457	0.318
Max	10.307	10.517	0.334	
Average	10.114	10.200	0.087	
Min	9.883	9.904	-0.155	
Std Dev	0.077	0.116	0.103	

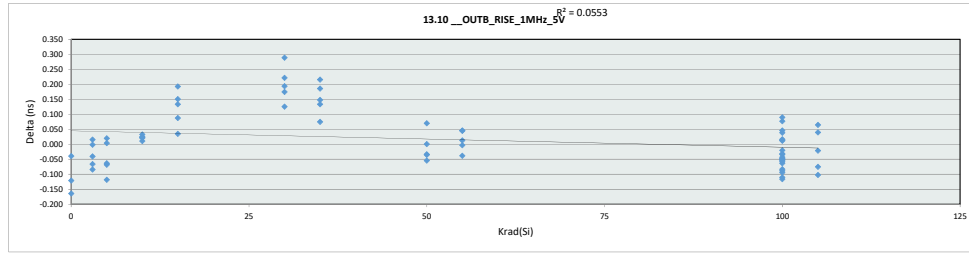


13.2 OUTB_RISE_1MHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	10.047	10.034	9.962	10.082	10.054	10.232	10.208	9.904	10.076	10.020	10.334
Average	10.087	10.101	10.123	10.128	10.180	10.307	10.293	10.099	10.186	10.207	10.422
Max	10.130	10.211	10.280	10.231	10.274	10.349	10.381	10.182	10.253	10.349	10.517
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

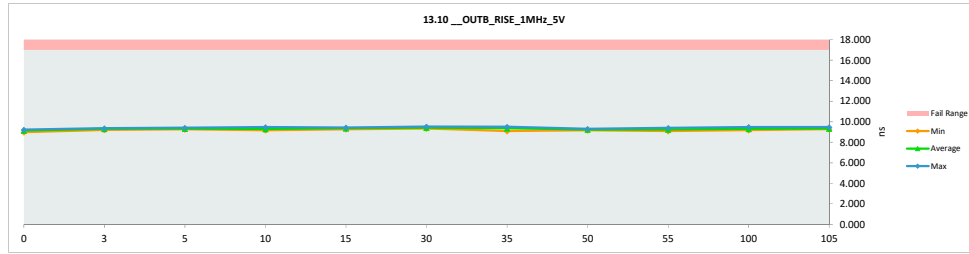


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13.10 OUTB_RISE_1MHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	9.347	9.227	-0.120
0	118	9.013	8.975	-0.038
0	122	9.391	9.228	-0.163
3	301	9.438	9.355	-0.083
3	306	9.299	9.260	-0.039
3	307	9.271	9.206	-0.065
3	308	9.351	9.368	0.017
3	309	9.348	9.348	0.000
5	310	9.259	9.264	0.005
5	311	9.347	9.368	0.021
5	312	9.381	9.264	-0.117
5	314	9.489	9.422	-0.067
5	316	9.380	9.318	-0.062
10	317	9.264	9.290	0.026
10	318	9.244	9.256	0.012
10	319	9.141	9.175	0.034
10	321	9.246	9.271	0.025
10	322	9.455	9.477	0.022
15	323	9.223	9.417	0.194
15	325	9.305	9.440	0.135
15	326	9.308	9.397	0.089
15	328	9.117	9.269	0.152
15	333	9.311	9.347	0.036
30	334	9.174	9.369	0.195
30	335	9.354	9.481	0.127
30	337	9.357	9.533	0.176
30	339	9.062	9.352	0.290
30	341	9.125	9.348	0.223
35	342	8.953	9.088	0.135
35	345	9.297	9.514	0.217
35	346	9.288	9.475	0.187
35	347	9.359	9.435	0.076
35	348	9.205	9.354	0.149
50	349	9.318	9.265	-0.053
50	350	9.098	9.169	0.071
50	351	9.289	9.257	-0.032
50	353	9.310	9.312	0.002
50	354	9.221	9.187	-0.034
55	355	9.229	9.275	0.046
55	357	9.357	9.404	0.047
55	359	9.143	9.157	0.014
55	362	9.339	9.337	-0.002
55	363	9.131	9.094	-0.037
100	1	9.421	9.334	-0.087
100	3	9.326	9.280	-0.046
100	4	9.304	9.395	0.091
100	5	9.388	9.406	0.018
100	6	9.344	9.262	-0.082
100	7	9.346	9.298	-0.048
100	8	9.382	9.422	0.040
100	9	9.410	9.488	0.078
100	10	9.420	9.358	-0.062
100	11	9.180	9.193	0.013
100	12	9.214	9.195	-0.019
100	13	9.295	9.186	-0.109
100	14	9.384	9.351	-0.033
100	15	9.303	9.251	-0.052
100	16	9.287	9.245	-0.042
100	17	9.328	9.278	-0.050
100	18	9.404	9.347	-0.057
100	19	9.288	9.195	-0.093
100	20	9.430	9.315	-0.115
100	21	9.403	9.418	0.015
100	22	9.280	9.327	0.047
100	24	9.286	9.256	-0.030
105	25	9.352	9.278	-0.074
105	26	9.234	9.300	0.066
105	27	9.361	9.341	-0.020
105	28	9.442	9.483	0.041
105	29	9.418	9.317	-0.101
Max		9.489	9.533	0.290
Average		9.297	9.312	0.016
Min		8.953	8.975	-0.163
Std Dev		0.107	0.106	0.095

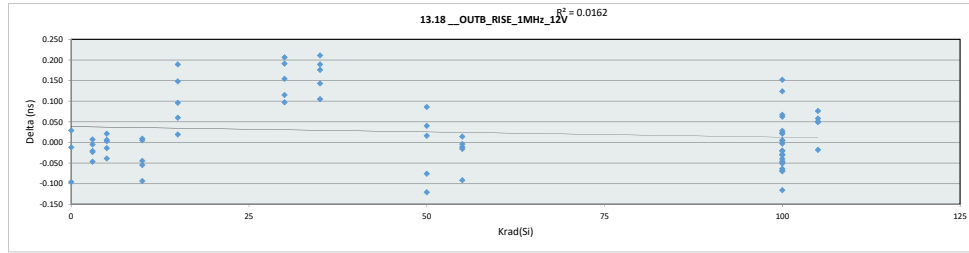


13.10 OUTB_RISE_1MHz_5V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.975	9.206	9.264	9.175	9.269	9.348	9.088	9.169	9.094	9.186	9.278
Average	9.143	9.307	9.327	9.294	9.374	9.417	9.373	9.238	9.253	9.309	9.344
Max	9.228	9.368	9.422	9.477	9.440	9.533	9.514	9.312	9.404	9.488	9.483
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

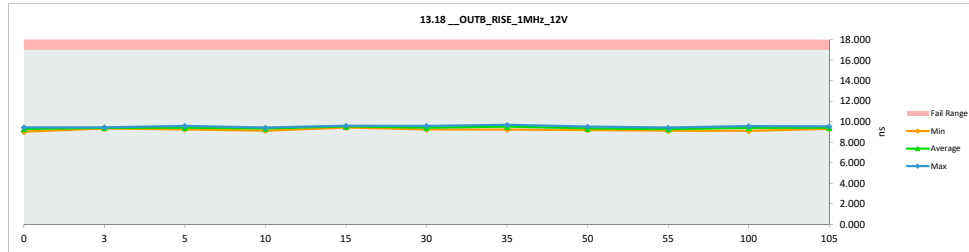


HDR TID Report
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		13.18 OUTB_RISE_1MHz_12V		
Test Site				
Tester				
Test Number				
Unit		ns	ns	
Max Limit		17	17	
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	9.431	9.461	0.030
0	118	9.039	9.028	-0.011
0	122	9.512	9.417	-0.095
3	301	9.414	9.392	-0.022
3	306	9.368	9.348	-0.020
3	307	9.460	9.414	-0.046
3	308	9.410	9.406	-0.004
3	309	9.451	9.459	0.008
5	310	9.390	9.377	-0.013
5	311	9.342	9.346	0.004
5	312	9.250	9.257	0.007
5	314	9.638	9.600	-0.038
5	316	9.491	9.513	0.022
10	317	9.407	9.413	0.006
10	318	9.374	9.330	-0.044
10	319	9.399	9.409	0.010
10	321	9.221	9.128	-0.093
10	322	9.489	9.435	-0.054
15	323	9.552	9.613	0.061
15	325	9.468	9.617	0.149
15	326	9.462	9.559	0.097
15	328	9.274	9.464	0.190
15	333	9.416	9.436	0.020
30	334	9.146	9.353	0.207
30	335	9.401	9.517	0.116
30	337	9.499	9.597	0.098
30	339	9.105	9.260	0.155
30	341	9.228	9.420	0.192
35	342	9.069	9.246	0.177
35	345	9.411	9.623	0.212
35	346	9.475	9.581	0.106
35	347	9.502	9.692	0.190
35	348	9.389	9.533	0.144
50	349	9.312	9.329	0.017
50	350	9.112	9.199	0.087
50	351	9.603	9.528	-0.075
50	353	9.526	9.406	-0.120
50	354	9.278	9.319	0.041
55	355	9.367	9.357	-0.010
55	357	9.363	9.348	-0.015
55	359	9.152	9.149	-0.003
55	362	9.420	9.435	0.015
55	363	9.222	9.131	-0.091
100	1	9.469	9.474	0.005
100	3	9.434	9.440	0.006
100	4	9.420	9.483	0.063
100	5	9.318	9.471	0.153
100	6	9.390	9.321	-0.069
100	7	9.273	9.206	-0.067
100	8	9.533	9.557	0.024
100	9	9.598	9.559	-0.039
100	10	9.615	9.587	-0.028
100	11	9.158	9.108	-0.050
100	12	9.375	9.355	-0.020
100	13	9.335	9.357	0.022
100	14	9.467	9.420	-0.047
100	15	9.471	9.356	-0.115
100	16	9.481	9.510	0.029
100	17	9.343	9.280	-0.063
100	18	9.433	9.388	-0.045
100	19	9.283	9.253	-0.030
100	20	9.400	9.468	0.068
100	21	9.542	9.523	-0.019
100	22	9.401	9.526	0.125
100	24	9.262	9.260	-0.002
105	25	9.311	9.361	0.050
105	26	9.237	9.314	0.077
105	27	9.497	9.556	0.059
105	28	9.452	9.435	-0.017
105	29	9.418	9.470	0.052
Max		9.638	9.692	0.212
Average		9.382	9.407	0.025
Min		9.039	9.028	-0.120
Std Dev		0.132	0.137	0.082

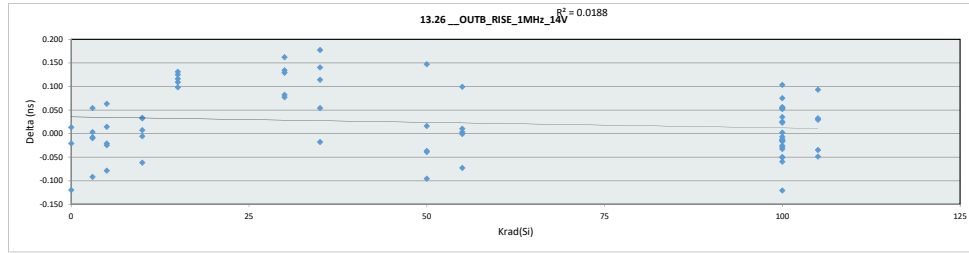


		13.18 OUTB_RISE_1MHz_12V												
Test Site														
Tester														
Test Number														
Max Limit		17	ns											
Min Limit			ns											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105			
LL														
Min	9.028	9.348	9.257	9.128	9.436	9.260	9.246	9.199	9.131	9.108	9.314			
Average	9.302	9.404	9.419	9.343	9.538	9.429	9.535	9.356	9.284	9.405	9.427			
Max	9.461	9.459	9.600	9.435	9.617	9.597	9.692	9.528	9.435	9.587	9.556			
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000			

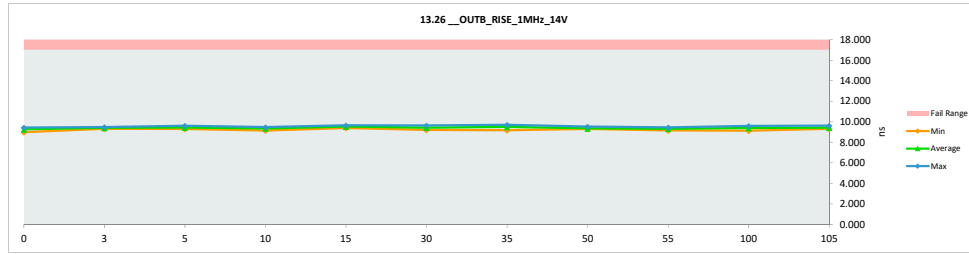


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13.26 OUTB_RISE_1MHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	9.493	9.374	-0.119
0	118	9.000	8.980	-0.020
0	122	9.414	9.428	0.014
3	301	9.416	9.409	-0.007
3	306	9.393	9.302	-0.091
3	307	9.416	9.471	0.055
3	308	9.386	9.390	0.004
3	309	9.461	9.452	-0.009
5	310	9.343	9.407	0.064
5	311	9.362	9.284	-0.078
5	312	9.364	9.340	-0.024
5	314	9.622	9.602	-0.020
5	316	9.492	9.507	0.015
10	317	9.392	9.400	0.008
10	318	9.308	9.342	0.034
10	319	9.415	9.354	-0.061
10	321	9.144	9.139	-0.005
10	322	9.436	9.469	0.033
15	323	9.518	9.650	0.132
15	325	9.483	9.582	0.099
15	326	9.419	9.536	0.117
15	328	9.279	9.389	0.110
15	333	9.331	9.457	0.126
30	334	9.240	9.375	0.135
30	335	9.411	9.489	0.078
30	337	9.503	9.633	0.130
30	339	9.033	9.196	0.163
30	341	9.270	9.353	0.083
35	342	9.184	9.167	-0.017
35	345	9.378	9.556	0.178
35	346	9.443	9.558	0.115
35	347	9.556	9.697	0.141
35	348	9.423	9.478	0.055
50	349	9.324	9.288	-0.036
50	350	9.161	9.309	0.148
50	351	9.556	9.518	-0.038
50	353	9.447	9.352	-0.095
50	354	9.278	9.295	0.017
55	355	9.364	9.375	0.011
55	357	9.334	9.334	0.000
55	359	9.203	9.207	0.004
55	362	9.341	9.441	0.100
55	363	9.221	9.149	-0.072
100	1	9.450	9.422	-0.028
100	3	9.416	9.384	-0.032
100	4	9.421	9.415	-0.006
100	5	9.355	9.431	0.076
100	6	9.420	9.361	-0.059
100	7	9.259	9.245	-0.014
100	8	9.457	9.460	0.003
100	9	9.559	9.583	0.024
100	10	9.462	9.519	0.057
100	11	9.124	9.113	-0.011
100	12	9.407	9.287	-0.120
100	13	9.303	9.359	0.056
100	14	9.354	9.458	0.104
100	15	9.411	9.362	-0.049
100	16	9.449	9.502	0.053
100	17	9.326	9.276	-0.050
100	18	9.413	9.439	0.026
100	19	9.249	9.285	0.036
100	20	9.391	9.445	0.054
100	21	9.500	9.487	-0.013
100	22	9.445	9.429	-0.016
100	24	9.290	9.265	-0.025
105	25	9.388	9.340	-0.048
105	26	9.269	9.302	0.033
105	27	9.526	9.620	0.094
105	28	9.438	9.468	0.030
105	29	9.407	9.373	-0.034
Max		9.622	9.697	0.178
Average		9.372	9.395	0.023
Min		9.000	8.980	-0.120
Std Dev		0.119	0.134	0.070

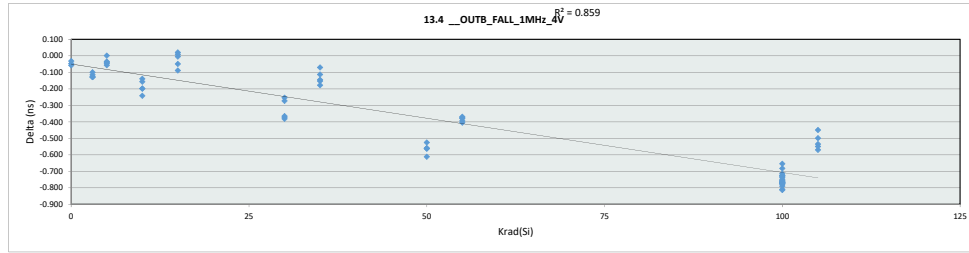


13.26 OUTB_RISE_1MHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.980	9.302	9.284	9.139	9.389	9.196	9.167	9.288	9.149	9.113	9.302
Average	9.261	9.405	9.428	9.341	9.523	9.409	9.491	9.352	9.301	9.388	9.421
Max	9.428	9.471	9.602	9.469	9.650	9.633	9.697	9.518	9.441	9.583	9.620
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

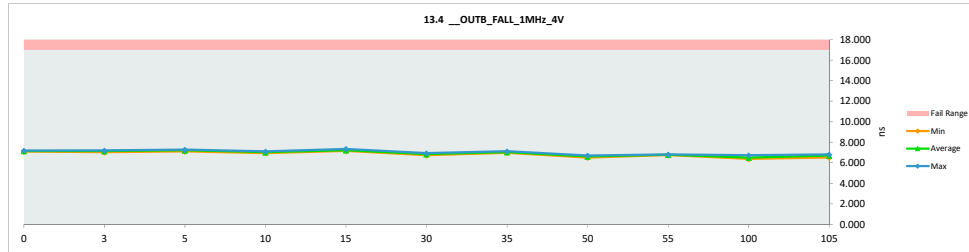


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13.4 OUTB_FALL_1MHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	7.244	7.188	-0.056
0	118	7.120	7.091	-0.029
0	122	7.210	7.165	-0.045
3	301	7.322	7.196	-0.126
3	306	7.129	7.032	-0.097
3	307	7.263	7.138	-0.125
3	308	7.268	7.155	-0.113
3	309	7.295	7.169	-0.126
5	310	7.262	7.207	-0.055
5	311	7.298	7.254	-0.044
5	312	7.282	7.286	0.004
5	314	7.145	7.113	-0.032
5	316	7.242	7.205	-0.037
10	317	7.344	7.104	-0.240
10	318	7.247	7.051	-0.196
10	319	7.141	6.945	-0.196
10	321	7.107	6.952	-0.155
10	322	7.227	7.090	-0.137
15	323	7.208	7.161	-0.047
15	325	7.298	7.211	-0.087
15	326	7.326	7.349	0.023
15	328	7.174	7.185	0.011
15	333	7.228	7.226	-0.002
30	334	7.113	6.801	-0.312
30	335	7.110	6.740	-0.370
30	337	7.265	6.902	-0.363
30	339	7.209	6.938	-0.271
30	341	7.240	6.860	-0.380
35	342	7.120	6.977	-0.143
35	345	7.138	6.987	-0.151
35	346	7.223	7.112	-0.111
35	347	7.166	6.990	-0.176
35	348	7.198	7.130	-0.068
50	349	7.117	6.507	-0.610
50	350	7.268	6.711	-0.557
50	351	7.149	6.589	-0.560
50	353	7.232	6.672	-0.560
50	354	7.113	6.590	-0.523
55	355	7.207	6.815	-0.392
55	357	7.181	6.778	-0.403
55	359	7.144	6.766	-0.378
55	362	7.169	6.796	-0.373
55	363	7.115	6.748	-0.367
100	1	7.406	6.642	-0.764
100	3	7.200	6.452	-0.748
100	4	7.219	6.463	-0.756
100	5	7.496	6.740	-0.756
100	6	7.289	6.566	-0.723
100	7	7.356	6.633	-0.723
100	8	7.339	6.568	-0.771
100	9	7.372	6.642	-0.730
100	10	7.235	6.468	-0.767
100	11	7.244	6.433	-0.811
100	12	7.136	6.484	-0.652
100	13	7.272	6.494	-0.778
100	14	7.387	6.599	-0.788
100	15	7.180	6.409	-0.771
100	16	7.163	6.483	-0.680
100	17	7.163	6.395	-0.768
100	18	7.244	6.437	-0.807
100	19	7.117	6.380	-0.737
100	20	7.143	6.431	-0.712
100	21	7.240	6.481	-0.759
100	22	7.149	6.380	-0.769
100	24	7.291	6.523	-0.768
105	25	7.053	6.520	-0.533
105	26	7.192	6.695	-0.497
105	27	7.320	6.752	-0.568
105	28	7.264	6.816	-0.448
105	29	7.254	6.707	-0.547
	Max	7.496	7.349	0.023
	Average	7.222	6.821	-0.401
	Min	7.053	6.380	-0.811
	Std Dev	0.086	0.289	0.290

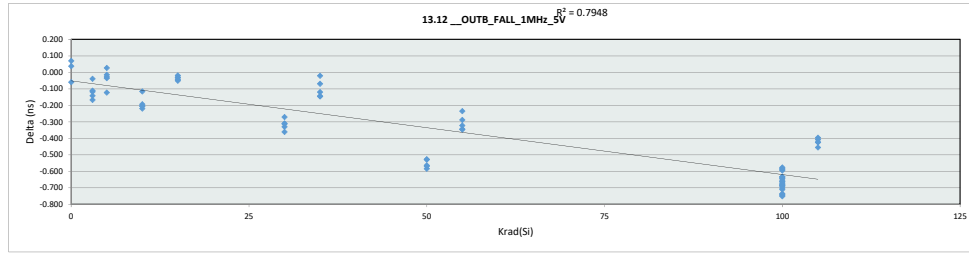


13.4 OUTB_FALL_1MHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.091	7.032	7.113	6.945	7.161	6.740	6.977	6.507	6.748	6.380	6.520
Average	7.148	7.138	7.213	7.028	7.226	6.848	7.039	6.614	6.781	6.505	6.698
Max	7.188	7.196	7.286	7.104	7.349	6.938	7.130	6.711	6.815	6.740	6.816
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

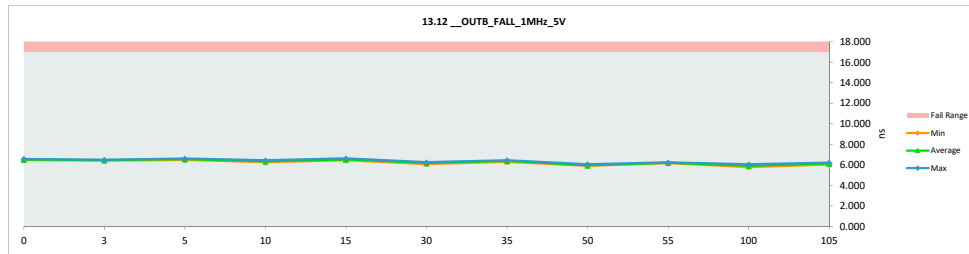


HDR TID Report
TPS7H5001-SP

13.12 OUTB_FALL_1MHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	6.547	6.490	-0.057
0	118	6.417	6.457	0.040
0	122	6.499	6.571	0.072
3	301	6.627	6.462	-0.165
3	306	6.507	6.398	-0.109
3	307	6.582	6.466	-0.116
3	308	6.609	6.469	-0.140
3	309	6.530	6.493	-0.037
5	310	6.636	6.611	-0.025
5	311	6.608	6.595	-0.013
5	312	6.580	6.609	0.029
5	314	6.589	6.468	-0.121
5	316	6.525	6.492	-0.033
10	317	6.550	6.436	-0.114
10	318	6.551	6.348	-0.203
10	319	6.445	6.254	-0.191
10	321	6.509	6.291	-0.218
10	322	6.557	6.355	-0.202
15	323	6.496	6.447	-0.049
15	325	6.519	6.492	-0.027
15	326	6.664	6.622	-0.042
15	328	6.494	6.463	-0.031
15	333	6.566	6.549	-0.017
30	334	6.401	6.073	-0.328
30	335	6.468	6.157	-0.311
30	337	6.553	6.194	-0.359
30	339	6.487	6.218	-0.269
30	341	6.557	6.249	-0.308
35	342	6.432	6.290	-0.142
35	345	6.505	6.387	-0.118
35	346	6.514	6.447	-0.067
35	347	6.486	6.342	-0.144
35	348	6.457	6.438	-0.019
50	349	6.475	5.893	-0.582
50	350	6.581	6.056	-0.525
50	351	6.452	5.925	-0.527
50	353	6.522	5.955	-0.567
50	354	6.493	5.931	-0.562
55	355	6.579	6.234	-0.345
55	357	6.501	6.181	-0.320
55	359	6.452	6.166	-0.286
55	362	6.528	6.186	-0.342
55	363	6.452	6.219	-0.233
100	1	6.685	6.034	-0.651
100	3	6.492	5.852	-0.640
100	4	6.492	5.860	-0.632
100	5	6.712	6.050	-0.662
100	6	6.657	5.949	-0.708
100	7	6.602	6.021	-0.581
100	8	6.573	5.873	-0.700
100	9	6.687	5.949	-0.738
100	10	6.624	5.939	-0.685
100	11	6.604	5.870	-0.734
100	12	6.460	5.885	-0.575
100	13	6.633	5.952	-0.681
100	14	6.718	5.969	-0.749
100	15	6.467	5.836	-0.631
100	16	6.532	5.893	-0.639
100	17	6.482	5.821	-0.661
100	18	6.517	5.774	-0.743
100	19	6.435	5.842	-0.593
100	20	6.520	5.849	-0.671
100	21	6.551	5.873	-0.678
100	22	6.407	5.820	-0.587
100	24	6.580	5.892	-0.688
105	25	6.480	6.027	-0.453
105	26	6.466	6.072	-0.394
105	27	6.624	6.203	-0.421
105	28	6.573	6.167	-0.406
105	29	6.574	6.152	-0.422
	Max	6.718	6.622	0.072
	Average	6.538	6.183	-0.355
	Min	6.401	5.774	-0.749
	Std Dev	0.074	0.257	0.261

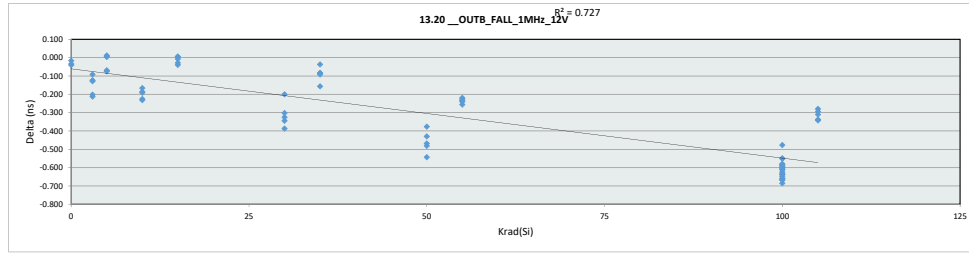


13.12 OUTB_FALL_1MHz_5V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.457	6.398	6.468	6.254	6.447	6.073	6.290	5.893	6.166	5.774	6.027
Average	6.506	6.458	6.555	6.337	6.515	6.178	6.381	5.952	6.197	5.900	6.124
Max	6.571	6.493	6.611	6.436	6.622	6.249	6.447	6.056	6.234	6.050	6.203
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

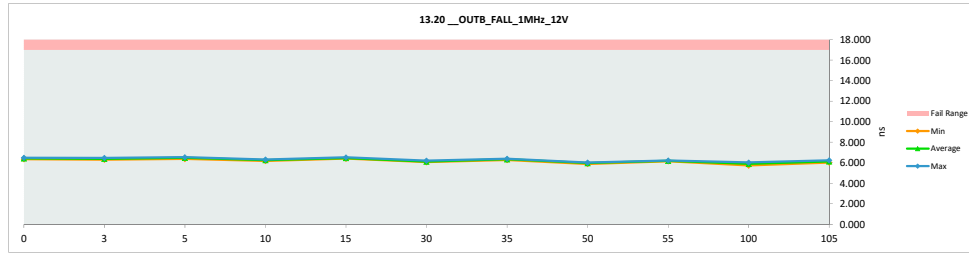


HDR TID Report
TPS7H5001-SP

		13.20	OUTB	FALL	1MHz	12V
Test Site						
Tester						
Test Number						
Unit		ns		ns		
Max Limit		17		17		
Min Limit						
Krad(Si)	Serial #	PRE_HDR		POST_HDR		Delta
0	117	6.532		6.499		-0.033
0	118	6.372		6.356		-0.016
0	122	6.488		6.449		-0.039
3	301	6.560		6.349		-0.211
3	306	6.515		6.314		-0.201
3	307	6.567		6.476		-0.091
3	308	6.534		6.412		-0.122
3	309	6.522		6.395		-0.127
5	310	6.545		6.553		0.008
5	311	6.505		6.518		0.013
5	312	6.446		6.378		-0.068
5	314	6.483		6.488		0.005
5	316	6.558		6.485		-0.073
10	317	6.544		6.320		-0.224
10	318	6.455		6.290		-0.165
10	319	6.437		6.252		-0.185
10	321	6.411		6.181		-0.230
10	322	6.474		6.284		-0.190
15	323	6.465		6.427		-0.038
15	325	6.466		6.472		0.006
15	326	6.540		6.534		-0.006
15	328	6.435		6.408		-0.027
15	333	6.458		6.465		0.007
30	334	6.267		6.068		-0.199
30	335	6.391		6.090		-0.301
30	337	6.560		6.217		-0.343
30	339	6.410		6.086		-0.324
30	341	6.522		6.136		-0.386
35	342	6.411		6.328		-0.083
35	345	6.435		6.280		-0.155
35	346	6.478		6.387		-0.091
35	347	6.441		6.405		-0.036
35	348	6.425		6.343		-0.082
50	349	6.431		5.889		-0.542
50	350	6.408		6.033		-0.375
50	351	6.467		5.987		-0.480
50	353	6.456		5.989		-0.467
50	354	6.397		5.968		-0.429
55	355	6.455		6.230		-0.225
55	357	6.422		6.167		-0.255
55	359	6.382		6.149		-0.233
55	362	6.430		6.191		-0.239
55	363	6.434		6.216		-0.218
100	1	6.612		5.965		-0.647
100	3	6.377		5.901		-0.476
100	4	6.470		5.870		-0.600
100	5	6.580		5.997		-0.583
100	6	6.532		5.923		-0.609
100	7	6.524		5.924		-0.600
100	8	6.604		5.971		-0.633
100	9	6.630		6.029		-0.601
100	10	6.596		5.938		-0.658
100	11	6.474		5.806		-0.668
100	12	6.417		5.869		-0.548
100	13	6.498		5.948		-0.550
100	14	6.631		5.947		-0.684
100	15	6.444		5.858		-0.586
100	16	6.576		5.913		-0.663
100	17	6.393		5.734		-0.659
100	18	6.414		5.791		-0.623
100	19	6.383		5.746		-0.637
100	20	6.486		5.876		-0.610
100	21	6.566		5.929		-0.637
100	22	6.427		5.834		-0.593
100	24	6.462		5.884		-0.578
105	25	6.370		6.029		-0.341
105	26	6.375		6.097		-0.278
105	27	6.558		6.249		-0.309
105	28	6.462		6.169		-0.293
105	29	6.481		6.145		-0.336
Max		6.631		6.553		0.013
Average		6.475		6.154		-0.321
Min		6.267		5.734		-0.684
Std Dev		0.073		0.232		0.234

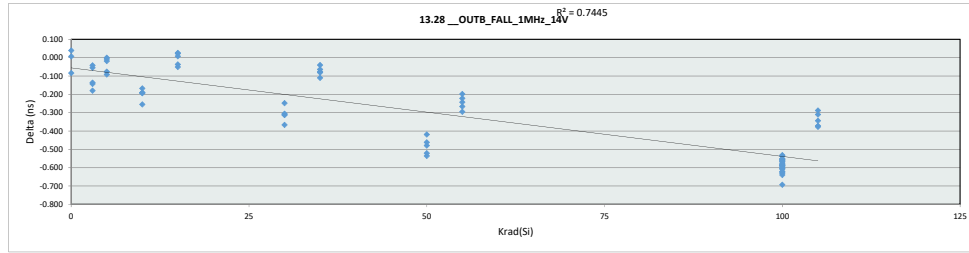


		13.20	OUTB	FALL	1MHz	12V						
Test Site												
Tester												
Test Number												
Max Limit		17	ns									
Min Limit			ns									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		6.356	6.314	6.378	6.181	6.408	6.068	6.280	5.889	6.149	5.734	6.029
Average		6.435	6.389	6.484	6.265	6.461	6.119	6.349	5.973	6.191	5.893	6.138
Max		6.499	6.476	6.553	6.320	6.534	6.217	6.405	6.033	6.230	6.029	6.249
UL		17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

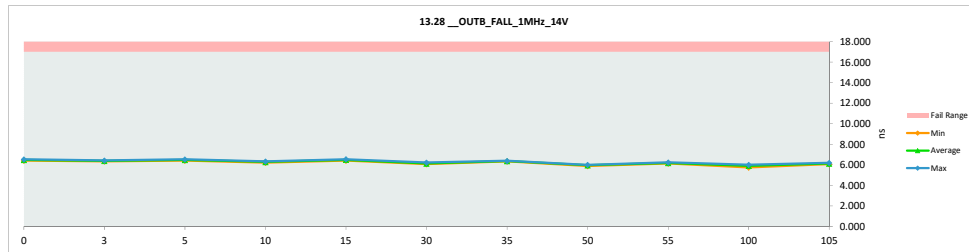


HDR TID Report
TPS7H5001-SP

13.28 OUTB FALL 1MHz 14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	6.528	6.536	0.008
0	118	6.369	6.409	0.040
0	122	6.555	6.472	-0.083
3	301	6.503	6.324	-0.179
3	306	6.435	6.381	-0.054
3	307	6.458	6.417	-0.041
3	308	6.515	6.380	-0.135
3	309	6.510	6.368	-0.142
5	310	6.515	6.516	0.001
5	311	6.531	6.525	-0.006
5	312	6.489	6.398	-0.091
5	314	6.498	6.481	-0.017
5	316	6.520	6.445	-0.075
10	317	6.576	6.322	-0.254
10	318	6.478	6.289	-0.189
10	319	6.427	6.260	-0.167
10	321	6.388	6.198	-0.190
10	322	6.485	6.292	-0.193
15	323	6.467	6.494	0.027
15	325	6.503	6.467	-0.036
15	326	6.589	6.539	-0.050
15	328	6.389	6.398	0.009
15	333	6.467	6.491	0.024
30	334	6.368	6.063	-0.305
30	335	6.403	6.091	-0.312
30	337	6.533	6.222	-0.311
30	339	6.355	6.108	-0.247
30	341	6.537	6.171	-0.366
35	342	6.384	6.321	-0.063
35	345	6.415	6.376	-0.039
35	346	6.491	6.382	-0.109
35	347	6.415	6.339	-0.076
35	348	6.449	6.369	-0.080
50	349	6.397	5.877	-0.520
50	350	6.452	5.991	-0.461
50	351	6.483	5.948	-0.535
50	353	6.472	5.994	-0.478
50	354	6.402	5.984	-0.418
55	355	6.436	6.239	-0.197
55	357	6.424	6.159	-0.265
55	359	6.405	6.184	-0.221
55	362	6.421	6.127	-0.294
55	363	6.362	6.120	-0.242
100	1	6.613	5.990	-0.623
100	3	6.454	5.850	-0.604
100	4	6.495	5.866	-0.629
100	5	6.572	5.997	-0.575
100	6	6.484	5.901	-0.583
100	7	6.516	5.896	-0.620
100	8	6.562	5.968	-0.594
100	9	6.549	5.998	-0.551
100	10	6.496	5.965	-0.531
100	11	6.414	5.826	-0.588
100	12	6.448	5.885	-0.563
100	13	6.526	5.942	-0.584
100	14	6.567	5.966	-0.601
100	15	6.407	5.852	-0.555
100	16	6.564	5.925	-0.639
100	17	6.433	5.741	-0.692
100	18	6.431	5.824	-0.607
100	19	6.365	5.829	-0.536
100	20	6.459	5.892	-0.567
100	21	6.495	5.934	-0.561
100	22	6.470	5.883	-0.587
100	24	6.458	5.906	-0.552
105	25	6.348	6.061	-0.287
105	26	6.445	6.074	-0.371
105	27	6.526	6.183	-0.343
105	28	6.487	6.111	-0.376
105	29	6.465	6.156	-0.309
Max		6.613	6.539	0.040
Average		6.469	6.156	-0.314
Min		6.348	5.741	-0.692
Std Dev		0.063	0.230	0.229

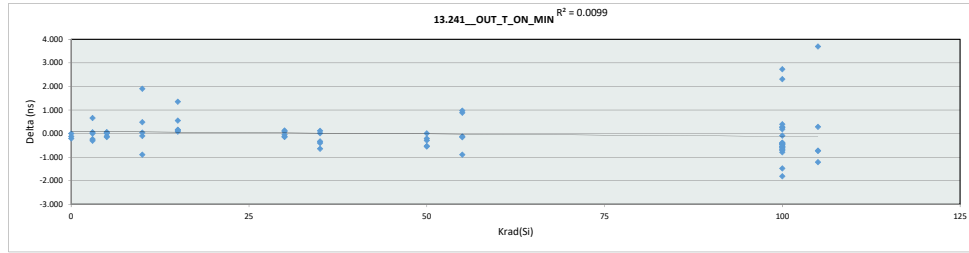


13.28 OUTB_FALL_1MHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.409	6.324	6.398	6.198	6.398	6.063	6.321	5.877	6.120	5.741	6.061
Average	6.472	6.374	6.473	6.272	6.478	6.131	6.357	5.959	6.166	5.902	6.117
Max	6.536	6.417	6.525	6.322	6.539	6.222	6.382	5.994	6.239	5.998	6.183
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

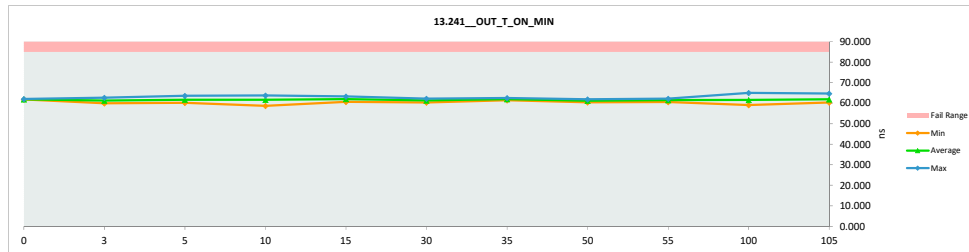


HDR TID Report
TPS7H5001-SP

		13.241 OUT T ON MIN		
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	85			
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	62.179	62.068	-0.111
0	118	61.876	61.885	0.009
0	122	62.013	61.811	-0.202
3	301	61.511	61.287	-0.224
3	306	61.275	61.289	0.014
3	307	60.491	61.159	0.668
3	308	62.931	62.635	-0.296
3	309	59.893	59.958	0.065
5	310	61.377	61.415	0.038
5	311	61.439	61.333	-0.106
5	312	60.309	60.169	-0.140
5	314	61.918	61.938	0.020
5	316	63.536	63.607	0.071
10	317	61.419	61.468	0.049
10	318	61.584	62.079	0.495
10	319	61.861	63.773	1.912
10	321	59.581	58.695	-0.886
10	322	62.422	62.334	-0.088
15	323	63.207	63.300	0.093
15	325	62.096	62.660	0.564
15	326	60.471	60.648	0.177
15	328	61.239	62.604	1.365
15	333	60.661	60.818	0.157
30	334	60.976	61.113	0.137
30	335	62.339	62.205	-0.134
30	337	62.081	62.174	0.093
30	339	60.896	60.858	-0.038
30	341	60.473	60.351	-0.122
35	342	62.009	62.133	0.124
35	345	62.037	61.648	-0.389
35	346	62.583	62.262	-0.321
35	347	62.026	61.392	-0.634
35	348	62.468	62.492	0.024
50	349	62.407	61.891	-0.516
50	350	62.055	61.513	-0.542
50	351	61.707	61.426	-0.281
50	353	60.707	60.724	0.017
50	354	60.593	60.395	-0.198
55	355	61.883	61.771	-0.112
55	357	60.620	61.599	0.979
55	359	61.481	60.593	-0.888
55	362	62.326	62.178	-0.148
55	363	60.598	61.489	0.891
100	1	61.786	61.004	-0.782
100	3	61.648	61.231	-0.417
100	4	60.941	60.263	-0.678
100	5	61.201	60.826	-0.375
100	6	61.025	60.464	-0.561
100	7	63.017	62.463	-0.554
100	8	62.671	64.991	2.320
100	9	62.588	62.511	-0.077
100	10	59.986	60.253	0.267
100	11	61.064	61.355	0.291
100	12	62.237	64.979	2.742
100	13	60.894	59.091	-1.803
100	14	61.527	60.825	-0.702
100	15	60.369	59.798	-0.571
100	16	61.608	61.155	-0.453
100	17	64.536	64.137	-0.399
100	18	63.199	62.682	-0.517
100	19	62.019	62.211	0.192
100	20	60.972	60.331	-0.641
100	21	61.200	61.608	0.408
100	22	62.748	61.278	-1.470
100	24	62.359	61.918	-0.441
105	25	61.402	61.696	0.294
105	26	61.946	61.221	-0.725
105	27	61.512	60.310	-1.202
105	28	62.300	61.583	-0.717
105	29	60.991	64.699	3.708
	Max	64.536	64.991	3.708
	Average	61.647	61.629	-0.018
	Min	59.581	58.695	-1.803
	Std Dev	0.915	1.214	0.857

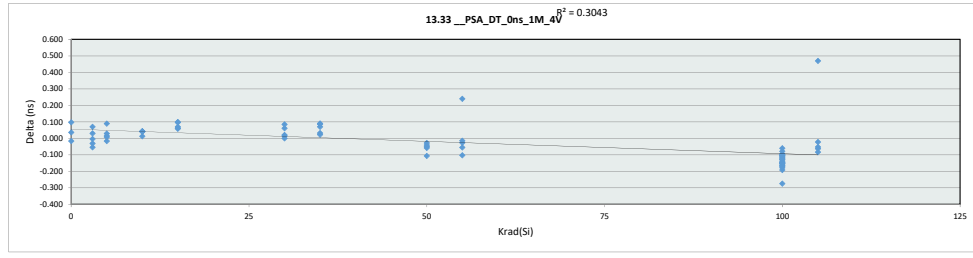


		13.241 OUT T ON MIN									
Test Site											
Tester											
Test Number											
Max Limit	85	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	61.811	59.958	60.169	58.695	60.648	60.351	61.392	60.395	60.593	59.091	60.310
Average	61.921	61.266	61.692	61.670	62.006	61.340	61.985	61.190	61.526	61.608	61.902
Max	62.068	62.635	63.607	63.773	63.300	62.205	62.492	61.891	62.178	64.991	64.699
UL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000

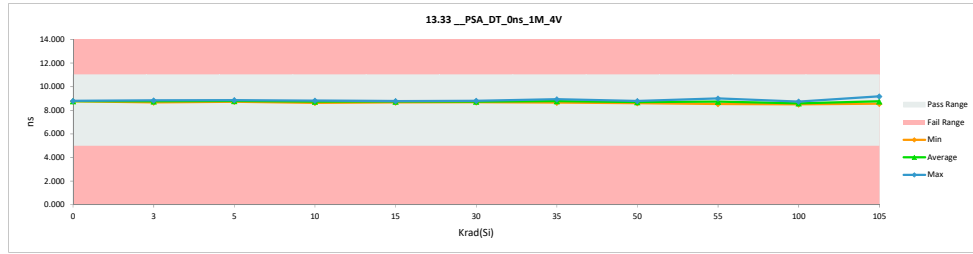


HDR TID Report
TPS7H5001-SP

13.33 PSA_DT_Ons_1M_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.724	8.761	0.037
0	118	8.706	8.804	0.098
0	122	8.786	8.771	-0.015
3	301	8.797	8.794	-0.003
3	306	8.775	8.846	0.071
3	307	8.718	8.665	-0.053
3	308	8.753	8.723	-0.030
3	309	8.756	8.787	0.031
5	310	8.732	8.822	0.090
5	311	8.717	8.733	0.016
5	312	8.744	8.729	-0.015
5	314	8.855	8.864	0.009
5	316	8.780	8.810	0.030
10	317	8.712	8.756	0.044
10	318	8.590	8.634	0.044
10	319	8.732	8.774	0.042
10	321	8.721	8.735	0.014
10	322	8.783	8.825	0.042
15	323	8.697	8.768	0.071
15	325	8.683	8.783	0.100
15	326	8.676	8.739	0.063
15	328	8.613	8.672	0.059
15	333	8.687	8.784	0.097
30	334	8.524	8.586	0.062
30	335	8.785	8.806	0.021
30	337	8.809	8.810	0.001
30	339	8.640	8.725	0.085
30	341	8.675	8.691	0.016
35	342	8.628	8.719	0.091
35	345	8.853	8.939	0.086
35	346	8.673	8.706	0.033
35	347	8.783	8.854	0.071
35	348	8.631	8.654	0.023
50	349	8.828	8.788	-0.040
50	350	8.730	8.703	-0.027
50	351	8.763	8.706	-0.057
50	353	8.714	8.608	-0.106
50	354	8.726	8.677	-0.049
55	355	8.729	8.675	-0.054
55	357	8.763	8.704	0.241
55	359	8.642	8.540	-0.102
55	362	8.739	8.725	-0.014
55	363	8.770	8.744	-0.026
100	1	8.707	8.530	-0.177
100	3	8.681	8.584	-0.097
100	4	8.764	8.616	-0.148
100	5	8.718	8.612	-0.106
100	6	8.749	8.619	-0.130
100	7	8.698	8.572	-0.126
100	8	8.738	8.589	-0.149
100	9	8.663	8.543	-0.120
100	10	8.843	8.652	-0.191
100	11	8.775	8.668	-0.107
100	12	8.686	8.583	-0.103
100	13	8.777	8.503	-0.274
100	14	8.724	8.574	-0.150
100	15	8.738	8.679	-0.059
100	16	8.705	8.590	-0.115
100	17	8.783	8.619	-0.164
100	18	8.836	8.742	-0.094
100	19	8.685	8.544	-0.141
100	20	8.797	8.628	-0.169
100	21	8.627	8.549	-0.078
100	22	8.726	8.580	-0.146
100	24	8.675	8.515	-0.160
105	25	8.783	8.762	-0.021
105	26	8.624	8.562	-0.062
105	27	8.694	8.643	-0.051
105	28	8.772	8.690	-0.082
105	29	8.713	9.184	0.471
Max		8.855	9.184	0.471
Average		8.727	8.704	-0.024
Min		8.590	8.503	-0.274
Std Dev		0.060	0.119	0.111

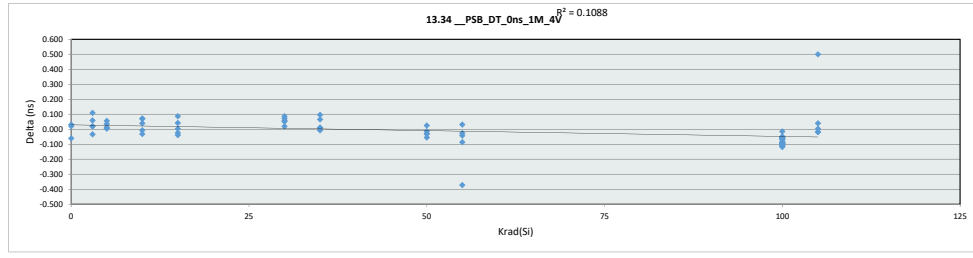


13.33 PSA_DT_Ons_1M_4V											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	8.761	8.665	8.729	8.634	8.672	8.686	8.654	8.608	8.540	8.503	8.562
Average	8.779	8.763	8.792	8.745	8.749	8.744	8.774	8.696	8.738	8.595	8.768
Max	8.804	8.846	8.864	8.825	8.784	8.810	8.939	8.788	9.004	8.742	9.184
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

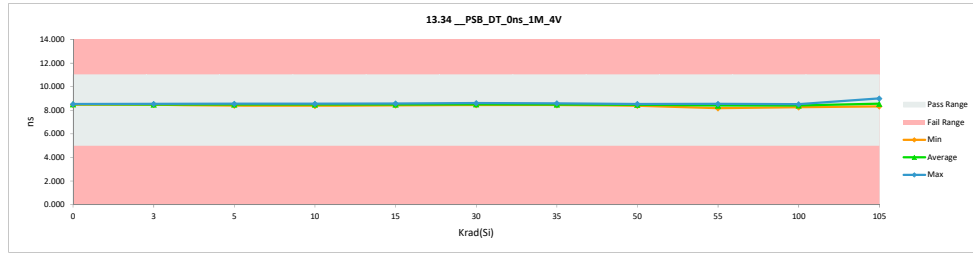


HDR TID Report
TPS7H5001-SP

13.34 PSB_DT_Ons_1M_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	8.543	8.484	-0.059
0	118	8.505	8.538	0.033
0	122	8.516	8.538	0.022
3	301	8.488	8.509	0.021
3	306	8.536	8.504	-0.032
3	307	8.459	8.481	0.022
3	308	8.438	8.549	0.111
3	309	8.423	8.484	0.061
5	310	8.491	8.497	0.006
5	311	8.399	8.407	0.008
5	312	8.475	8.512	0.037
5	314	8.507	8.564	0.057
5	316	8.510	8.525	0.015
10	317	8.441	8.512	0.071
10	318	8.419	8.389	-0.030
10	319	8.489	8.563	0.074
10	321	8.508	8.503	-0.005
10	322	8.512	8.553	0.041
15	323	8.526	8.570	0.044
15	325	8.448	8.425	-0.023
15	326	8.498	8.586	0.088
15	328	8.508	8.471	-0.037
15	333	8.452	8.457	0.005
30	334	8.440	8.493	0.053
30	335	8.560	8.618	0.058
30	337	8.397	8.471	0.074
30	339	8.364	8.452	0.088
30	341	8.523	8.544	0.021
35	342	8.380	8.478	0.098
35	345	8.526	8.593	0.067
35	346	8.484	8.479	-0.005
35	347	8.505	8.517	0.012
35	348	8.427	8.440	0.013
50	349	8.533	8.506	-0.027
50	350	8.489	8.459	-0.030
50	351	8.535	8.482	-0.053
50	353	8.402	8.390	-0.012
50	354	8.514	8.541	0.027
55	355	8.537	8.452	-0.085
55	357	8.556	8.186	-0.370
55	359	8.438	8.413	-0.025
55	362	8.519	8.478	-0.041
55	363	8.516	8.549	0.033
100	1	8.423	8.312	-0.111
100	3	8.533	8.433	-0.100
100	4	8.442	8.380	-0.062
100	5	8.424	8.320	-0.104
100	6	8.505	8.397	-0.108
100	7	8.449	8.358	-0.091
100	8	8.499	8.486	-0.013
100	9	8.464	8.406	-0.058
100	10	8.583	8.516	-0.067
100	11	8.499	8.435	-0.064
100	12	8.452	8.405	-0.047
100	13	8.506	8.413	-0.093
100	14	8.455	8.407	-0.048
100	15	8.550	8.458	-0.092
100	16	8.537	8.450	-0.087
100	17	8.571	8.482	-0.089
100	18	8.592	8.505	-0.087
100	19	8.499	8.444	-0.055
100	20	8.588	8.490	-0.098
100	21	8.445	8.389	-0.056
100	22	8.539	8.457	-0.082
100	24	8.380	8.263	-0.117
105	25	8.507	8.490	-0.017
105	26	8.356	8.341	-0.015
105	27	8.420	8.425	0.005
105	28	8.488	8.529	0.041
105	29	8.506	9.007	0.501
Max		8.592	9.007	0.501
Average		8.485	8.474	-0.011
Min		8.356	8.186	-0.370
Std Dev		0.055	0.100	0.096

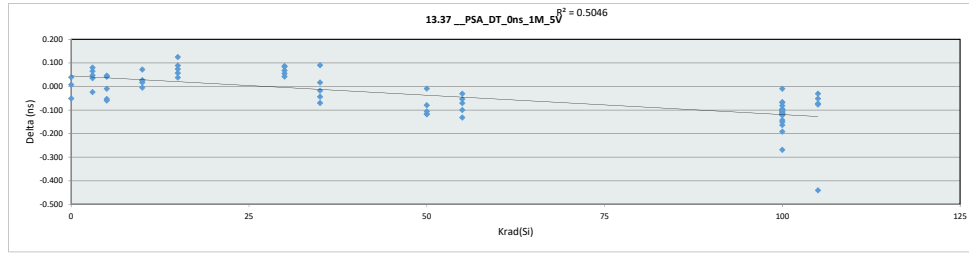


13.34 PSB_DT_Ons_1M_4V											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	8.484	8.481	8.407	8.389	8.425	8.452	8.440	8.390	8.186	8.263	8.341
Average	8.520	8.505	8.501	8.504	8.502	8.516	8.501	8.476	8.416	8.418	8.558
Max	8.538	8.549	8.564	8.563	8.586	8.618	8.593	8.541	8.549	8.516	9.007
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

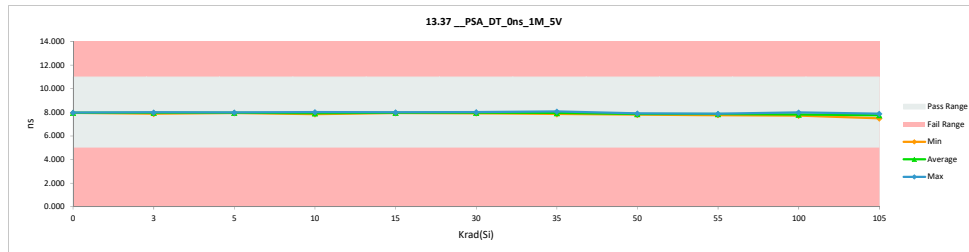


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13.37 PSA_DT_Ons_1M_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	7.931	7.971	0.040
0	118	7.997	7.947	-0.050
0	122	7.969	7.977	0.008
3	301	7.943	7.991	0.048
3	306	7.913	7.949	0.036
3	307	7.895	7.872	-0.023
3	308	7.908	7.974	0.066
3	309	7.914	7.995	0.081
5	310	7.991	7.932	-0.059
5	311	7.921	7.963	0.042
5	312	7.942	7.933	-0.009
5	314	8.028	7.976	-0.052
5	316	7.940	7.987	0.047
10	317	7.922	7.949	0.027
10	318	7.813	7.839	0.026
10	319	7.901	7.919	0.018
10	321	7.896	7.892	-0.004
10	322	7.938	8.011	0.073
15	323	7.887	7.977	0.090
15	325	7.939	7.997	0.058
15	326	7.897	7.936	0.039
15	328	7.817	7.943	0.126
15	333	7.902	7.977	0.075
30	334	7.847	7.916	0.069
30	335	7.956	8.012	0.056
30	337	7.914	8.002	0.088
30	339	7.884	7.927	0.043
30	341	7.851	7.935	0.084
35	342	7.933	7.916	-0.017
35	345	7.982	8.073	0.091
35	346	7.956	7.887	-0.069
35	347	7.968	7.986	0.018
35	348	7.890	7.847	-0.043
50	349	8.009	7.904	-0.105
50	350	7.924	7.845	-0.079
50	351	7.901	7.893	-0.008
50	353	7.905	7.789	-0.116
50	354	7.961	7.844	-0.117
55	355	7.989	7.858	-0.131
55	357	7.938	7.885	-0.053
55	359	7.806	7.776	-0.030
55	362	7.963	7.864	-0.099
55	363	7.947	7.877	-0.070
100	1	7.899	7.818	-0.081
100	3	7.839	7.773	-0.066
100	4	7.905	7.808	-0.097
100	5	7.880	7.774	-0.106
100	6	7.928	7.828	-0.100
100	7	8.050	7.782	-0.268
100	8	7.889	7.781	-0.108
100	9	7.888	7.787	-0.101
100	10	7.983	7.888	-0.095
100	11	7.922	7.810	-0.112
100	12	7.909	7.789	-0.120
100	13	7.924	7.806	-0.118
100	14	7.904	7.806	-0.098
100	15	7.995	7.986	-0.009
100	16	7.893	7.772	-0.121
100	17	8.014	7.851	-0.163
100	18	8.021	7.907	-0.114
100	19	7.875	7.759	-0.116
100	20	7.968	7.818	-0.150
100	21	7.910	7.719	-0.191
100	22	7.955	7.813	-0.142
100	24	7.818	7.751	-0.067
105	25	7.950	7.879	-0.071
105	26	7.801	7.725	-0.076
105	27	7.892	7.841	-0.051
105	28	7.869	7.839	-0.030
105	29	7.931	7.491	-0.440
Max		8.050	8.073	0.126
Average		7.922	7.879	-0.043
Min		7.801	7.491	-0.440
Std Dev		0.053	0.095	0.095

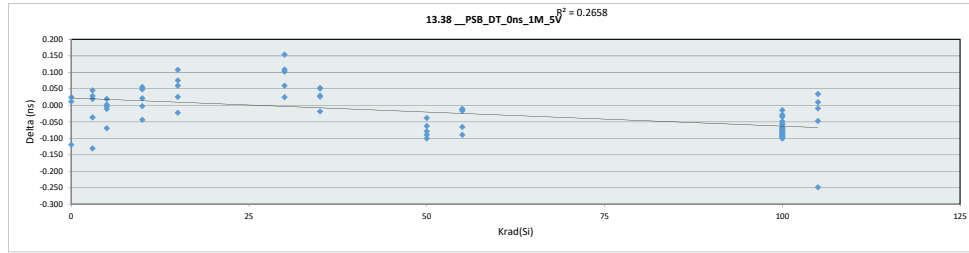


13.37 PSA_DT_Ons_1M_5V											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.947	7.872	7.932	7.839	7.936	7.916	7.847	7.789	7.776	7.719	7.491
Average	7.965	7.956	7.958	7.922	7.966	7.958	7.942	7.855	7.852	7.810	7.755
Max	7.977	7.995	7.987	8.011	7.997	8.012	8.073	7.904	7.885	7.986	7.879
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

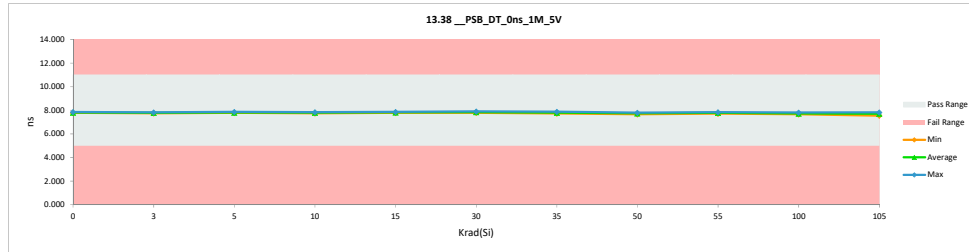


HDR TID Report
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13.38 PSB_DT_Ons_1M_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	7.835	7.860	0.025
0	118	7.903	7.784	-0.119
0	122	7.801	7.813	0.012
3	301	7.784	7.830	0.046
3	306	7.876	7.746	-0.130
3	307	7.764	7.784	0.020
3	308	7.792	7.756	-0.036
3	309	7.760	7.789	0.029
5	310	7.872	7.803	-0.069
5	311	7.785	7.788	0.003
5	312	7.791	7.781	-0.010
5	314	7.843	7.863	0.020
5	316	7.805	7.802	-0.003
10	317	7.787	7.843	0.056
10	318	7.724	7.746	0.022
10	319	7.768	7.817	0.049
10	321	7.803	7.801	-0.002
10	322	7.839	7.796	-0.043
15	323	7.861	7.839	-0.022
15	325	7.705	7.813	0.108
15	326	7.759	7.819	0.060
15	328	7.759	7.785	0.026
15	333	7.796	7.872	0.076
30	334	7.720	7.874	0.154
30	335	7.847	7.907	0.060
30	337	7.736	7.839	0.103
30	339	7.770	7.795	0.025
30	341	7.712	7.821	0.109
35	342	7.756	7.738	-0.018
35	345	7.857	7.887	0.030
35	346	7.728	7.781	0.053
35	347	7.801	7.828	0.027
35	348	7.742	7.794	0.052
50	349	7.860	7.782	-0.078
50	350	7.798	7.736	-0.062
50	351	7.847	7.758	-0.089
50	353	7.766	7.666	-0.100
50	354	7.835	7.797	-0.038
55	355	7.826	7.761	-0.065
55	357	7.834	7.822	-0.012
55	359	7.734	7.718	-0.016
55	362	7.859	7.770	-0.089
55	363	7.845	7.835	-0.010
100	1	7.761	7.713	-0.048
100	3	7.859	7.777	-0.082
100	4	7.834	7.750	-0.084
100	5	7.775	7.675	-0.100
100	6	7.785	7.694	-0.091
100	7	7.709	7.681	-0.028
100	8	7.782	7.687	-0.095
100	9	7.792	7.711	-0.081
100	10	7.835	7.768	-0.067
100	11	7.805	7.747	-0.058
100	12	7.802	7.727	-0.075
100	13	7.806	7.773	-0.033
100	14	7.803	7.724	-0.079
100	15	7.810	7.796	-0.014
100	16	7.788	7.689	-0.099
100	17	7.883	7.815	-0.068
100	18	7.886	7.812	-0.074
100	19	7.802	7.731	-0.071
100	20	7.873	7.779	-0.094
100	21	7.742	7.709	-0.033
100	22	7.797	7.710	-0.087
100	24	7.721	7.666	-0.055
105	25	7.813	7.823	0.010
105	26	7.680	7.671	-0.009
105	27	7.716	7.751	0.035
105	28	7.819	7.772	-0.047
105	29	7.799	7.551	-0.248
	Max	7.903	7.907	0.154
	Average	7.797	7.773	-0.023
	Min	7.680	7.551	-0.248
	Std Dev	0.050	0.062	0.068

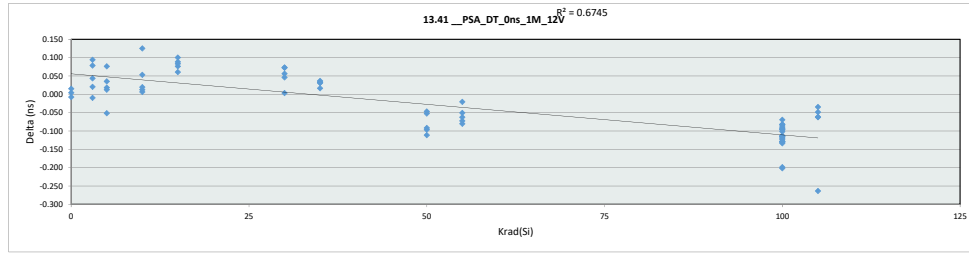


13.38 PSB_DT_Ons_1M_5V											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.784	7.746	7.781	7.746	7.785	7.795	7.738	7.666	7.718	7.666	7.551
Average	7.819	7.781	7.807	7.801	7.826	7.847	7.806	7.748	7.781	7.733	7.714
Max	7.860	7.830	7.863	7.843	7.872	7.907	7.887	7.797	7.835	7.815	7.823
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

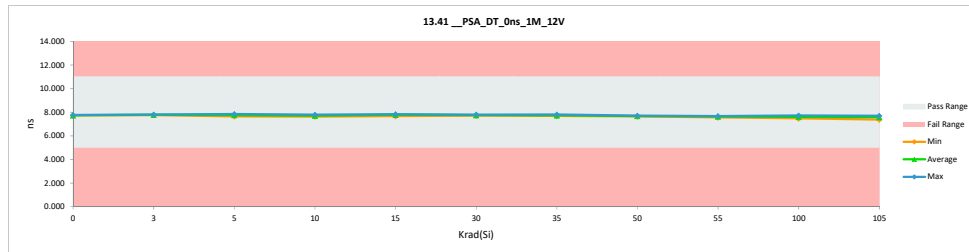


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13.41 PSA_DT_Ons_1M_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	7.768	7.773	0.005
0	118	7.717	7.733	0.016
0	122	7.757	7.750	-0.007
3	301	7.729	7.808	0.079
3	306	7.753	7.774	0.021
3	307	7.698	7.793	0.095
3	308	7.775	7.766	-0.009
3	309	7.776	7.820	0.044
5	310	7.726	7.803	0.077
5	311	7.703	7.722	0.019
5	312	7.710	7.659	-0.051
5	314	7.845	7.858	0.013
5	316	7.757	7.793	0.036
10	317	7.727	7.734	0.007
10	318	7.613	7.667	0.054
10	319	7.673	7.799	0.126
10	321	7.631	7.644	0.013
10	322	7.760	7.780	0.020
15	323	7.752	7.841	0.089
15	325	7.736	7.820	0.084
15	326	7.748	7.809	0.061
15	328	7.606	7.683	0.077
15	333	7.670	7.771	0.101
30	334	7.649	7.722	0.073
30	335	7.735	7.809	0.074
30	337	7.760	7.807	0.047
30	339	7.667	7.724	0.057
30	341	7.723	7.727	0.004
35	342	7.678	7.710	0.032
35	345	7.797	7.814	0.017
35	346	7.673	7.707	0.034
35	347	7.776	7.813	0.037
35	348	7.658	7.690	0.032
50	349	7.812	7.716	-0.096
50	350	7.736	7.690	-0.046
50	351	7.772	7.720	-0.052
50	353	7.761	7.650	-0.111
50	354	7.755	7.664	-0.091
55	355	7.710	7.630	-0.080
55	357	7.719	7.669	-0.050
55	359	7.635	7.573	-0.062
55	362	7.700	7.680	-0.020
55	363	7.741	7.669	-0.072
100	1	7.717	7.621	-0.096
100	3	7.721	7.608	-0.113
100	4	7.758	7.689	-0.069
100	5	7.693	7.564	-0.129
100	6	7.718	7.617	-0.101
100	7	7.687	7.486	-0.201
100	8	7.742	7.646	-0.096
100	9	7.732	7.599	-0.133
100	10	7.860	7.730	-0.130
100	11	7.708	7.595	-0.113
100	12	7.715	7.599	-0.116
100	13	7.730	7.602	-0.128
100	14	7.718	7.590	-0.128
100	15	7.740	7.658	-0.082
100	16	7.728	7.606	-0.122
100	17	7.805	7.607	-0.198
100	18	7.812	7.731	-0.081
100	19	7.664	7.578	-0.086
100	20	7.736	7.645	-0.091
100	21	7.712	7.593	-0.119
100	22	7.735	7.643	-0.092
100	24	7.635	7.521	-0.114
105	25	7.750	7.702	-0.048
105	26	7.597	7.535	-0.062
105	27	7.697	7.663	-0.034
105	28	7.732	7.671	-0.061
105	29	7.650	7.387	-0.263
	Max	7.860	7.858	0.126
	Average	7.723	7.690	-0.033
	Min	7.597	7.387	-0.263
	Std Dev	0.053	0.093	0.083

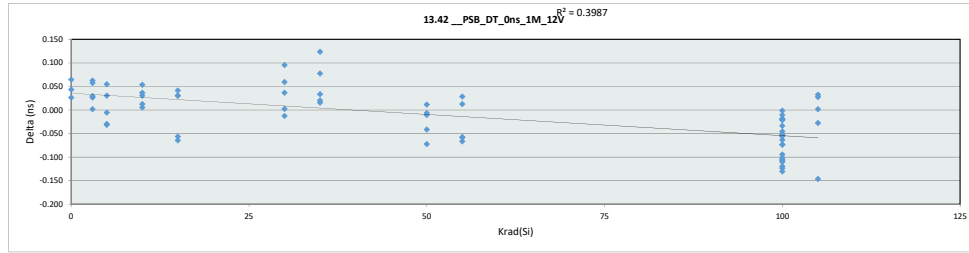


13.41 PSA_DT_Ons_1M_12V											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.733	7.766	7.659	7.644	7.683	7.722	7.690	7.650	7.573	7.486	7.387
Average	7.752	7.792	7.767	7.725	7.785	7.758	7.747	7.688	7.644	7.615	7.592
Max	7.773	7.820	7.858	7.799	7.841	7.809	7.814	7.720	7.680	7.731	7.702
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

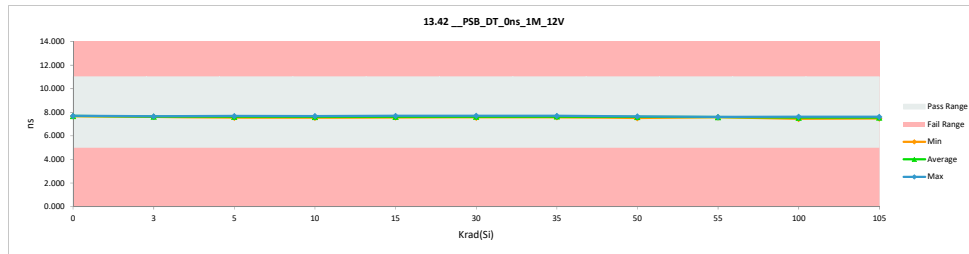


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13.42 PSB_DT_Ons_1M_12V				
Test Site				
Tester				
Test Number				
Unit	ns		ns	
Max Limit	11		11	
Min Limit	5		5	
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	7.629	7.694	0.065
0	118	7.645	7.672	0.027
0	122	7.663	7.707	0.044
3	301	7.606	7.633	0.027
3	306	7.666	7.668	0.002
3	307	7.599	7.662	0.063
3	308	7.593	7.623	0.030
3	309	7.586	7.644	0.058
5	310	7.606	7.661	0.055
5	311	7.581	7.552	-0.029
5	312	7.563	7.558	-0.005
5	314	7.727	7.696	-0.031
5	316	7.628	7.659	0.031
10	317	7.601	7.632	0.031
10	318	7.560	7.566	0.006
10	319	7.636	7.690	0.054
10	321	7.536	7.549	0.013
10	322	7.608	7.645	0.037
15	323	7.675	7.706	0.031
15	325	7.606	7.648	0.042
15	326	7.666	7.602	-0.064
15	328	7.546	7.577	0.031
15	333	7.665	7.609	-0.056
30	334	7.563	7.659	0.096
30	335	7.709	7.712	0.003
30	337	7.590	7.627	0.037
30	339	7.568	7.556	-0.012
30	341	7.592	7.652	0.060
35	342	7.575	7.591	0.016
35	345	7.659	7.693	0.034
35	346	7.584	7.708	0.124
35	347	7.663	7.684	0.021
35	348	7.537	7.615	0.078
50	349	7.664	7.658	-0.006
50	350	7.635	7.563	-0.072
50	351	7.673	7.632	-0.041
50	353	7.507	7.519	0.012
50	354	7.672	7.662	-0.010
55	355	7.568	7.597	0.029
55	357	7.645	7.588	-0.057
55	359	7.598	7.611	0.013
55	362	7.654	7.588	-0.066
55	363	7.648	7.590	-0.058
100	1	7.550	7.495	-0.055
100	3	7.650	7.549	-0.101
100	4	7.597	7.546	-0.051
100	5	7.598	7.504	-0.094
100	6	7.605	7.486	-0.119
100	7	7.578	7.472	-0.106
100	8	7.567	7.566	-0.001
100	9	7.613	7.595	-0.018
100	10	7.707	7.597	-0.110
100	11	7.600	7.579	-0.021
100	12	7.610	7.591	-0.019
100	13	7.641	7.586	-0.055
100	14	7.650	7.545	-0.105
100	15	7.649	7.586	-0.063
100	16	7.642	7.609	-0.033
100	17	7.727	7.597	-0.130
100	18	7.702	7.629	-0.073
100	19	7.604	7.584	-0.020
100	20	7.641	7.631	-0.010
100	21	7.645	7.522	-0.123
100	22	7.631	7.586	-0.045
100	24	7.518	7.445	-0.073
105	25	7.645	7.618	-0.027
105	26	7.483	7.511	0.028
105	27	7.582	7.584	0.002
105	28	7.596	7.629	0.033
105	29	7.631	7.485	-0.146
	Max	7.727	7.712	0.124
	Average	7.617	7.604	-0.012
	Min	7.483	7.445	-0.146
	Std Dev	0.051	0.062	0.058

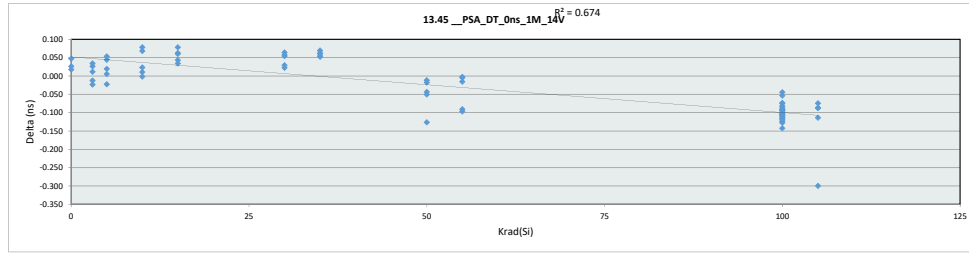


13.42 PSB_DT_Ons_1M_12V											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.672	7.623	7.552	7.549	7.577	7.556	7.591	7.519	7.588	7.445	7.485
Average	7.691	7.646	7.625	7.616	7.628	7.641	7.658	7.607	7.595	7.559	7.565
Max	7.707	7.668	7.696	7.690	7.706	7.712	7.708	7.662	7.611	7.631	7.629
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

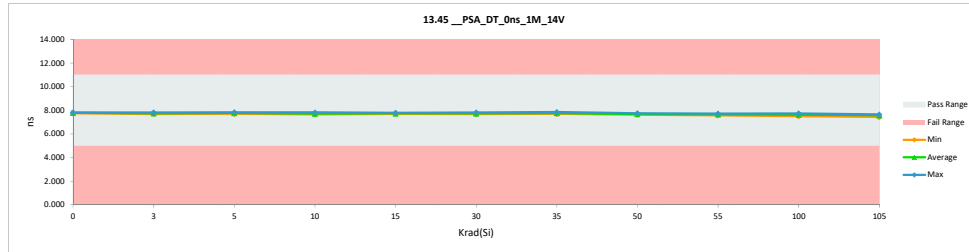


HDR TID Report
TPS7H5001-SP

		13.45 PSA_DT_Ons_1M_14V		
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	7.756	7.783	0.027
0	118	7.726	7.744	0.018
0	122	7.777	7.825	0.048
3	301	7.711	7.723	0.012
3	306	7.789	7.824	0.035
3	307	7.714	7.691	-0.023
3	308	7.737	7.764	0.027
3	309	7.797	7.785	-0.012
5	310	7.760	7.805	0.045
5	311	7.672	7.726	0.054
5	312	7.741	7.719	-0.022
5	314	7.836	7.842	0.006
5	316	7.785	7.805	0.020
10	317	7.718	7.729	0.011
10	318	7.662	7.661	-0.001
10	319	7.748	7.772	0.024
10	321	7.601	7.670	0.069
10	322	7.753	7.832	0.079
15	323	7.724	7.803	0.079
15	325	7.724	7.768	0.044
15	326	7.689	7.724	0.035
15	328	7.639	7.702	0.063
15	333	7.700	7.761	0.061
30	334	7.629	7.684	0.055
30	335	7.756	7.821	0.065
30	337	7.792	7.815	0.023
30	339	7.651	7.709	0.058
30	341	7.690	7.720	0.030
35	342	7.668	7.738	0.070
35	345	7.796	7.859	0.063
35	346	7.667	7.722	0.055
35	347	7.769	7.830	0.061
35	348	7.658	7.711	0.053
50	349	7.765	7.722	-0.043
50	350	7.747	7.697	-0.050
50	351	7.770	7.753	-0.017
50	353	7.642	7.631	-0.011
50	354	7.782	7.656	-0.126
55	355	7.752	7.656	-0.096
55	357	7.695	7.691	-0.004
55	359	7.585	7.583	-0.002
55	362	7.750	7.735	-0.015
55	363	7.758	7.668	-0.090
100	1	7.736	7.613	-0.123
100	3	7.697	7.606	-0.091
100	4	7.763	7.690	-0.073
100	5	7.702	7.586	-0.116
100	6	7.746	7.645	-0.101
100	7	7.726	7.613	-0.113
100	8	7.768	7.660	-0.108
100	9	7.724	7.582	-0.142
100	10	7.792	7.707	-0.085
100	11	7.690	7.609	-0.081
100	12	7.674	7.621	-0.053
100	13	7.706	7.616	-0.090
100	14	7.712	7.638	-0.074
100	15	7.748	7.648	-0.100
100	16	7.746	7.653	-0.093
100	17	7.748	7.653	-0.095
100	18	7.785	7.741	-0.044
100	19	7.676	7.557	-0.119
100	20	7.762	7.658	-0.104
100	21	7.688	7.579	-0.109
100	22	7.755	7.628	-0.127
100	24	7.615	7.510	-0.105
105	25	7.768	7.655	-0.113
105	26	7.615	7.529	-0.086
105	27	7.702	7.628	-0.074
105	28	7.741	7.654	-0.087
105	29	7.738	7.439	-0.299
	Max	7.836	7.859	0.079
	Average	7.723	7.694	-0.029
	Min	7.585	7.439	-0.299
	Std Dev	0.053	0.086	0.076

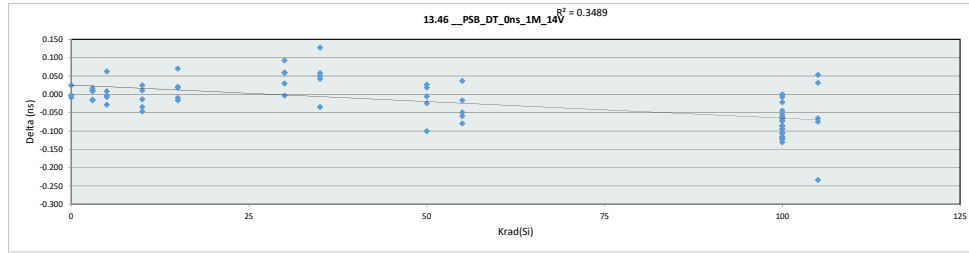


		13.45 PSA_DT_Ons_1M_14V									
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.744	7.691	7.719	7.661	7.702	7.684	7.711	7.631	7.583	7.510	7.439
Average	7.784	7.757	7.779	7.733	7.752	7.750	7.772	7.692	7.667	7.628	7.581
Max	7.825	7.824	7.842	7.832	7.803	7.821	7.859	7.753	7.735	7.741	7.655
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

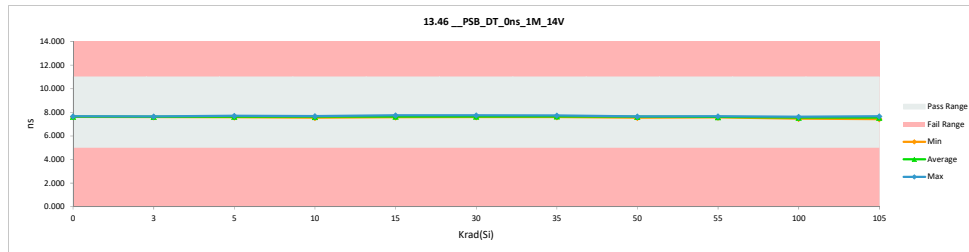


HDR TID Report
TPS7H5001-SP

13.46 PSB_DT_Ons_1M_14V				
Test Site				
Tester				
Test Number				
Unit	ns		ns	
Max Limit	11		11	
Min Limit	5		5	
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	7.642	7.667	0.025
0	118	7.621	7.619	-0.002
0	122	7.627	7.619	-0.008
3	301	7.612	7.621	0.009
3	306	7.644	7.630	-0.014
3	307	7.645	7.656	0.011
3	308	7.626	7.610	-0.016
3	309	7.583	7.600	0.017
5	310	7.654	7.647	-0.007
5	311	7.552	7.615	0.063
5	312	7.607	7.579	-0.028
5	314	7.704	7.701	-0.003
5	316	7.640	7.649	0.009
10	317	7.617	7.604	-0.013
10	318	7.558	7.569	0.011
10	319	7.706	7.672	-0.034
10	321	7.591	7.545	-0.046
10	322	7.608	7.633	0.025
15	323	7.669	7.740	0.071
15	325	7.569	7.590	0.021
15	326	7.701	7.692	-0.009
15	328	7.570	7.589	0.019
15	333	7.633	7.617	-0.016
30	334	7.586	7.646	0.060
30	335	7.713	7.743	0.030
30	337	7.568	7.661	0.093
30	339	7.540	7.599	0.059
30	341	7.607	7.604	-0.003
35	342	7.624	7.590	-0.034
35	345	7.599	7.727	0.128
35	346	7.600	7.651	0.051
35	347	7.664	7.722	0.058
35	348	7.581	7.624	0.043
50	349	7.653	7.648	-0.005
50	350	7.638	7.657	0.019
50	351	7.663	7.639	-0.024
50	353	7.519	7.546	0.027
50	354	7.714	7.614	-0.100
55	355	7.674	7.595	-0.079
55	357	7.633	7.574	-0.059
55	359	7.534	7.571	0.037
55	362	7.678	7.662	-0.016
55	363	7.650	7.601	-0.049
100	1	7.601	7.479	-0.122
100	3	7.660	7.575	-0.085
100	4	7.620	7.620	0.000
100	5	7.569	7.548	-0.021
100	6	7.632	7.517	-0.115
100	7	7.582	7.466	-0.116
100	8	7.645	7.541	-0.104
100	9	7.673	7.579	-0.094
100	10	7.720	7.626	-0.094
100	11	7.653	7.523	-0.130
100	12	7.630	7.544	-0.086
100	13	7.633	7.579	-0.054
100	14	7.665	7.559	-0.106
100	15	7.590	7.583	-0.007
100	16	7.647	7.574	-0.073
100	17	7.696	7.576	-0.120
100	18	7.664	7.594	-0.070
100	19	7.613	7.569	-0.044
100	20	7.637	7.577	-0.060
100	21	7.555	7.554	-0.001
100	22	7.651	7.589	-0.062
100	24	7.547	7.483	-0.064
105	25	7.666	7.601	-0.065
105	26	7.544	7.470	-0.074
105	27	7.604	7.658	0.054
105	28	7.614	7.646	0.032
105	29	7.660	7.427	-0.233
	Max	7.720	7.743	0.128
	Average	7.626	7.603	-0.023
	Min	7.519	7.427	-0.233
	Std Dev	0.047	0.063	0.063

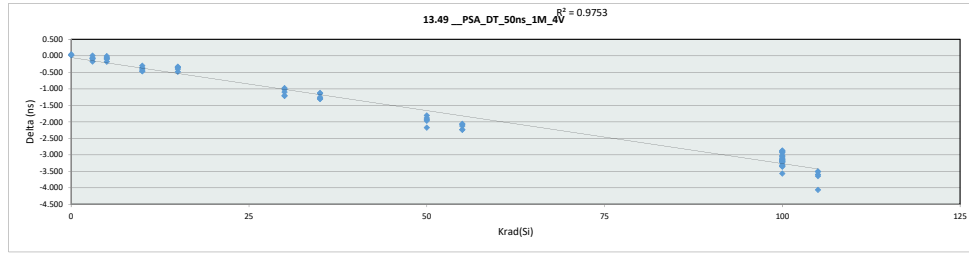


13.46 PSB_DT_Ons_1M_14V											
Test Site											
Tester											
Test Number											
Max Limit	11		ns								
Min Limit	5		ns								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.619	7.600	7.579	7.545	7.589	7.599	7.590	7.546	7.571	7.466	7.427
Average	7.635	7.623	7.638	7.605	7.646	7.651	7.663	7.621	7.601	7.557	7.560
Max	7.667	7.656	7.701	7.672	7.740	7.743	7.727	7.657	7.662	7.626	7.658
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

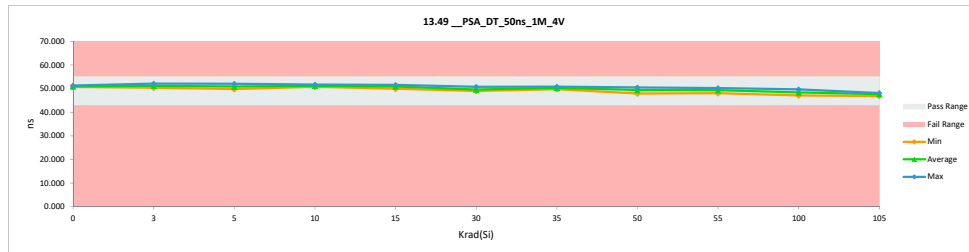


HDR TID Report
TPS7H5001-SP

13.49 PSA_DT_50ns_1M_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	55	55		
Min Limit	43	43		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	50.696	50.747	0.051
0	118	51.325	51.358	0.033
0	122	50.838	50.862	0.024
3	301	51.650	51.570	-0.080
3	306	52.233	52.178	-0.055
3	307	50.654	50.570	-0.084
3	308	50.360	50.372	0.012
3	309	51.690	51.523	-0.167
5	310	50.621	50.529	-0.092
5	311	52.008	51.943	-0.065
5	312	52.073	52.070	-0.003
5	314	50.027	49.854	-0.173
5	316	50.216	50.160	-0.056
10	317	51.336	50.941	-0.395
10	318	51.799	51.507	-0.292
10	319	51.276	50.823	-0.453
10	321	52.187	51.723	-0.464
10	322	51.368	51.006	-0.362
15	323	52.090	51.614	-0.476
15	325	50.304	49.963	-0.341
15	326	51.419	51.068	-0.351
15	328	51.219	50.899	-0.320
15	333	51.671	51.279	-0.392
30	334	50.685	49.478	-1.207
30	335	50.244	49.045	-1.199
30	337	50.126	49.031	-1.095
30	339	50.726	49.756	-0.970
30	341	51.800	50.787	-1.013
35	342	51.042	49.792	-1.250
35	345	51.546	50.246	-1.300
35	346	51.364	50.068	-1.296
35	347	51.836	50.720	-1.116
35	348	51.994	50.856	-1.138
50	349	51.328	49.408	-1.920
50	350	52.684	50.514	-2.170
50	351	49.777	47.976	-1.801
50	353	51.036	49.076	-1.960
50	354	52.049	50.168	-1.881
55	355	51.182	49.103	-2.079
55	357	52.014	49.960	-2.054
55	359	52.478	50.244	-2.234
55	362	51.634	49.512	-2.122
55	363	50.323	48.090	-2.233
100	1	51.766	48.648	-3.118
100	3	50.624	47.435	-3.189
100	4	50.995	47.688	-3.307
100	5	52.479	49.126	-3.353
100	6	52.069	48.926	-3.143
100	7	50.331	47.452	-2.879
100	8	51.965	48.627	-3.338
100	9	50.060	47.150	-2.910
100	10	51.633	48.595	-3.038
100	11	52.733	49.717	-3.016
100	12	51.736	48.837	-2.899
100	13	52.173	49.011	-3.162
100	14	52.076	48.953	-3.123
100	15	51.491	48.163	-3.328
100	16	52.335	49.187	-3.148
100	17	50.915	47.707	-3.208
100	18	52.442	49.379	-3.063
100	19	50.348	47.485	-2.863
100	20	51.676	48.114	-3.562
100	21	51.359	48.160	-3.199
100	22	51.636	48.408	-3.228
100	24	51.151	48.221	-2.930
105	25	51.323	47.264	-4.059
105	26	50.501	46.912	-3.589
105	27	51.591	47.951	-3.640
105	28	51.648	48.145	-3.503
105	29	51.615	48.116	-3.499
Max		52.733	52.178	0.051
Average		51.366	49.597	-1.769
Min		49.777	46.912	-4.059
Std Dev		0.724	1.389	1.332

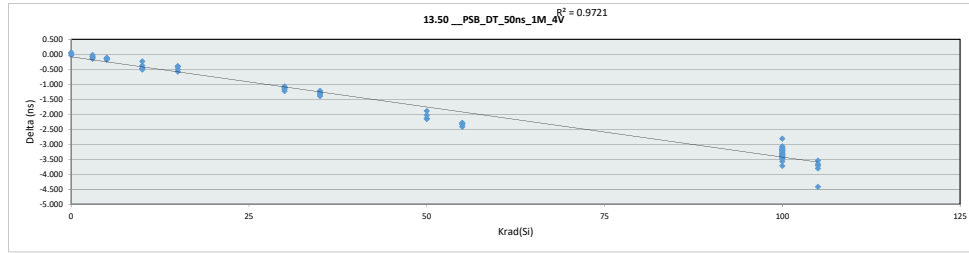


13.49 PSA_DT_50ns_1M_4V											
Test Site											
Tester											
Test Number											
Max Limit	55	ns									
Min Limit	43	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	50.747	50.372	49.854	50.823	49.963	49.031	49.792	47.976	48.090	47.150	46.912
Average	50.989	51.243	50.911	51.200	50.965	49.619	50.336	49.428	49.382	48.409	47.678
Max	51.358	52.178	52.070	51.723	51.614	50.787	50.856	50.514	50.244	49.717	48.145
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

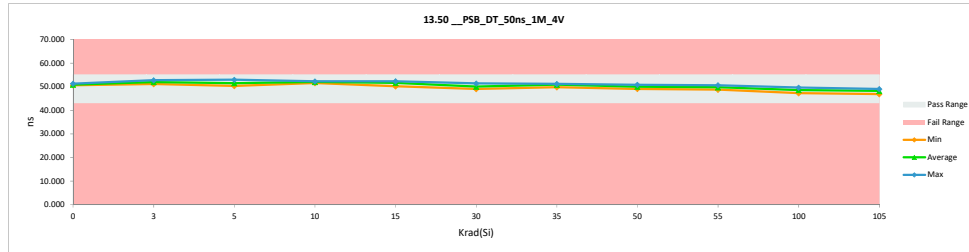


HDR TID Report
TPS7H5001-SP

13.50 PSB DT 50ns 1M 4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	55	55		
Min Limit	43	43		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	50.663	50.637	-0.026
0	118	51.258	51.328	0.070
0	122	50.724	50.760	0.036
3	301	52.607	52.593	-0.014
3	306	52.901	52.757	-0.144
3	307	51.583	51.462	-0.121
3	308	51.242	51.158	-0.084
3	309	52.098	52.040	-0.058
5	310	51.281	51.174	-0.107
5	311	53.085	52.949	-0.136
5	312	52.808	52.665	-0.143
5	314	50.526	50.350	-0.176
5	316	50.569	50.419	-0.150
10	317	52.678	52.310	-0.368
10	318	52.345	52.119	-0.226
10	319	52.558	52.089	-0.469
10	321	52.450	51.951	-0.499
10	322	52.010	51.557	-0.453
15	323	52.367	51.797	-0.570
15	325	50.710	50.215	-0.495
15	326	52.726	52.320	-0.406
15	328	52.553	52.158	-0.395
15	333	51.994	51.603	-0.391
30	334	50.257	49.049	-1.218
30	335	51.025	49.910	-1.115
30	337	50.758	49.653	-1.105
30	339	51.386	50.224	-1.162
30	341	52.465	51.399	-1.066
35	342	51.042	49.779	-1.263
35	345	52.311	51.014	-1.297
35	346	52.588	51.202	-1.386
35	347	52.408	51.201	-1.207
35	348	52.427	51.088	-1.339
50	349	51.611	49.472	-2.139
50	350	52.966	50.828	-2.138
50	351	51.211	49.067	-2.144
50	353	52.752	50.734	-2.018
50	354	51.985	50.102	-1.883
55	355	52.047	49.637	-2.410
55	357	52.914	50.632	-2.282
55	359	52.012	49.732	-2.280
55	362	52.588	50.266	-2.322
55	363	51.088	48.754	-2.334
100	1	52.309	48.936	-3.373
100	3	51.249	47.860	-3.389
100	4	51.045	47.618	-3.427
100	5	52.701	49.243	-3.458
100	6	52.077	48.927	-3.150
100	7	50.525	47.273	-3.252
100	8	52.522	48.811	-3.711
100	9	50.556	47.378	-3.178
100	10	51.726	48.495	-3.231
100	11	52.444	49.244	-3.200
100	12	52.222	49.148	-3.074
100	13	52.215	49.099	-3.116
100	14	52.197	48.906	-3.291
100	15	52.812	49.321	-3.491
100	16	51.759	48.449	-3.310
100	17	51.163	47.854	-3.309
100	18	52.709	49.643	-3.066
100	19	50.102	47.295	-2.807
100	20	51.987	48.423	-3.564
100	21	52.739	49.413	-3.326
100	22	52.263	48.894	-3.369
100	24	52.017	48.862	-3.155
105	25	52.318	47.906	-4.412
105	26	50.384	46.849	-3.535
105	27	52.166	48.367	-3.799
105	28	52.696	49.043	-3.653
105	29	52.759	49.054	-3.705
	Max	53.085	52.949	0.070
	Average	51.904	50.035	-1.868
	Min	50.102	46.849	-4.412
	Std Dev	0.804	1.545	1.386

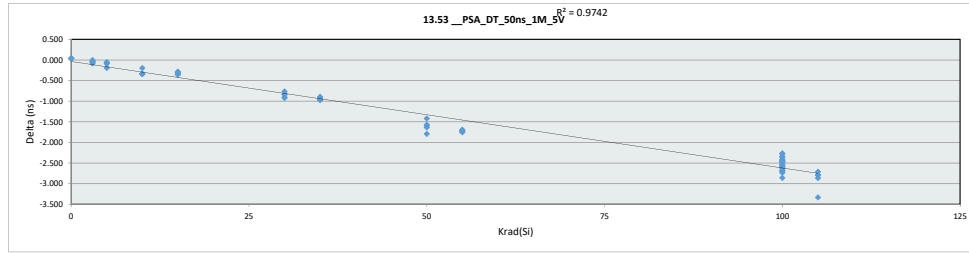


13.50 PSB DT 50ns 1M 4V											
Test Site											
Tester											
Test Number											
Max Limit	55	ns									
Min Limit	43	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	50.637	51.158	50.350	51.557	50.215	49.049	49.779	49.067	48.754	47.273	46.849
Average	50.908	52.002	51.511	52.005	51.619	50.047	50.857	50.041	49.804	48.595	48.244
Max	51.328	52.757	52.949	52.310	52.320	51.399	51.202	50.828	50.632	49.643	49.054
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

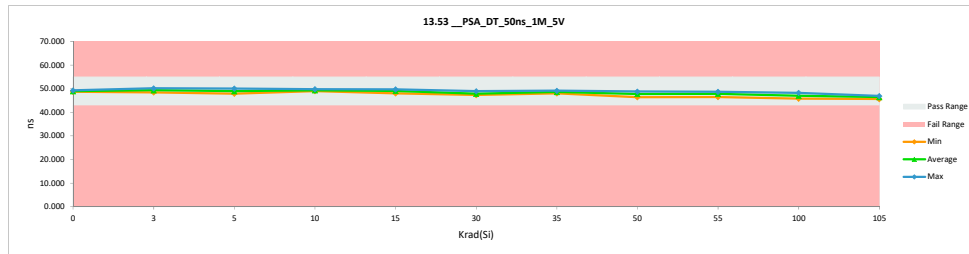


HDR TID Report
TPS7H5001-SP

13.53 PSA_DT_50ns_1M_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	55	55		
Min Limit	43	43		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	48.695	48.749	0.054
0	118	49.286	49.327	0.041
0	122	48.863	48.900	0.037
3	301	49.747	49.690	-0.057
3	306	50.233	50.200	-0.033
3	307	48.816	48.782	-0.034
3	308	48.464	48.466	0.002
3	309	49.740	49.662	-0.078
5	310	48.715	48.629	-0.086
5	311	50.073	50.009	-0.064
5	312	50.159	50.093	-0.066
5	314	48.134	47.947	-0.187
5	316	48.294	48.247	-0.047
10	317	49.424	49.096	-0.328
10	318	49.773	49.582	-0.191
10	319	49.341	49.008	-0.333
10	321	50.177	49.846	-0.331
10	322	49.512	49.177	-0.335
15	323	50.123	49.777	-0.346
15	325	48.395	48.084	-0.311
15	326	49.538	49.262	-0.276
15	328	49.345	49.040	-0.305
15	333	49.727	49.378	-0.349
30	334	48.648	47.751	-0.897
30	335	48.285	47.367	-0.918
30	337	48.218	47.391	-0.827
30	339	48.773	48.014	-0.759
30	341	49.809	48.974	-0.835
35	342	48.970	48.078	-0.892
35	345	49.550	48.603	-0.947
35	346	49.406	48.439	-0.967
35	347	49.916	48.963	-0.953
35	348	50.095	49.189	-0.906
50	349	49.257	47.666	-1.591
50	350	50.643	48.854	-1.789
50	351	47.844	46.431	-1.413
50	353	49.082	47.516	-1.566
50	354	50.168	48.535	-1.633
55	355	49.317	47.629	-1.688
55	357	50.092	48.379	-1.713
55	359	50.482	48.732	-1.750
55	362	49.739	48.010	-1.729
55	363	48.245	46.520	-1.725
100	1	49.870	47.383	-2.487
100	3	48.696	46.109	-2.587
100	4	48.946	46.268	-2.678
100	5	50.539	47.861	-2.678
100	6	50.109	47.651	-2.458
100	7	48.467	46.108	-2.359
100	8	50.111	47.379	-2.732
100	9	48.176	45.843	-2.333
100	10	49.678	47.252	-2.426
100	11	50.680	48.271	-2.409
100	12	49.749	47.476	-2.273
100	13	50.265	47.637	-2.628
100	14	50.041	47.558	-2.483
100	15	49.560	46.864	-2.696
100	16	50.340	47.812	-2.528
100	17	48.869	46.301	-2.568
100	18	50.437	47.987	-2.450
100	19	48.391	46.129	-2.262
100	20	49.710	46.856	-2.854
100	21	49.383	46.837	-2.546
100	22	49.691	47.091	-2.600
100	24	49.312	46.869	-2.443
105	25	49.436	46.109	-3.327
105	26	48.433	45.645	-2.788
105	27	49.632	46.772	-2.860
105	28	49.738	46.952	-2.786
105	29	49.621	46.914	-2.707
Max		50.680	50.200	0.054
Average		49.414	47.999	-1.415
Min		47.844	45.645	-3.327
Std Dev		0.716	1.170	1.070

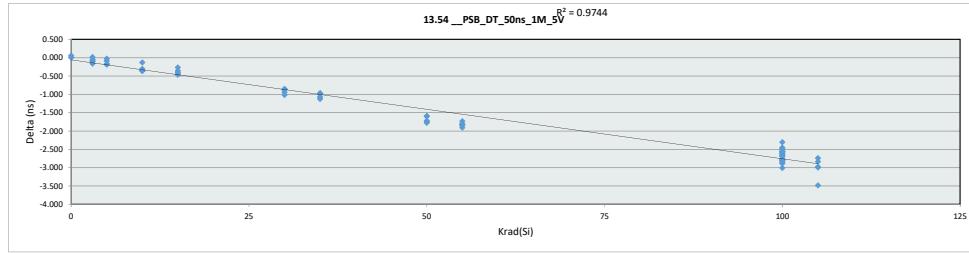


13.53 PSA_DT_50ns_1M_5V											
Test Site											
Tester											
Test Number											
Max Limit	55	ns									
Min Limit	43	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	48.749	48.466	47.947	49.008	48.084	47.367	48.078	46.431	46.520	45.843	45.645
Average	48.992	49.360	48.985	49.342	49.108	47.899	48.654	47.800	47.854	47.070	46.478
Max	49.327	50.200	50.093	49.846	49.777	48.974	49.189	48.854	48.732	48.271	46.952
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

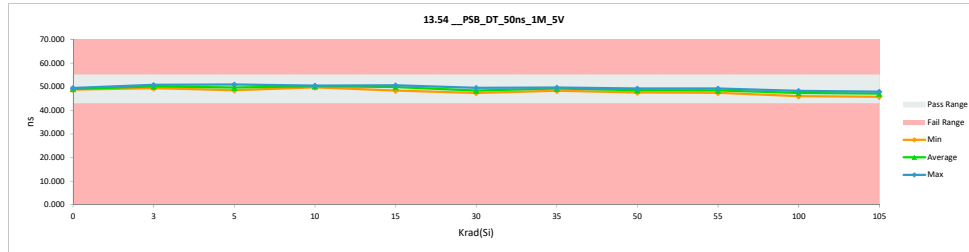


HDR TID Report
TPS7H5001-SP

13.54 PSB DT 50ns 1M 5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	55	55		
Min Limit	43	43		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	48.760	48.779	0.019
0	118	49.376	49.436	0.060
0	122	48.787	48.798	0.011
3	301	50.734	50.653	-0.081
3	306	50.915	50.757	-0.158
3	307	49.726	49.632	-0.094
3	308	49.327	49.350	0.023
3	309	50.234	50.189	-0.045
5	310	49.398	49.220	-0.178
5	311	51.121	50.962	-0.159
5	312	50.859	50.776	-0.083
5	314	48.671	48.491	-0.180
5	316	48.643	48.624	-0.019
10	317	50.775	50.473	-0.302
10	318	50.426	50.298	-0.128
10	319	50.620	50.272	-0.348
10	321	50.463	50.116	-0.347
10	322	50.100	49.741	-0.359
15	323	50.561	50.116	-0.445
15	325	48.831	48.374	-0.457
15	326	50.837	50.577	-0.260
15	328	50.666	50.311	-0.355
15	333	50.142	49.747	-0.395
30	334	48.370	47.359	-1.011
30	335	49.082	48.216	-0.866
30	337	48.912	47.972	-0.940
30	339	49.463	48.617	-0.846
30	341	50.380	49.522	-0.858
35	342	49.210	48.248	-0.962
35	345	50.352	49.287	-1.065
35	346	50.712	49.592	-1.120
35	347	50.477	49.500	-0.977
35	348	50.604	49.633	-0.971
50	349	49.635	48.037	-1.598
50	350	50.949	49.212	-1.737
50	351	49.291	47.517	-1.774
50	353	50.779	49.058	-1.721
50	354	50.139	48.554	-1.585
55	355	50.104	48.200	-1.904
55	357	51.076	49.235	-1.841
55	359	50.203	48.391	-1.812
55	362	50.553	48.755	-1.798
55	363	49.137	47.409	-1.728
100	1	50.469	47.705	-2.764
100	3	49.385	46.577	-2.808
100	4	49.146	46.335	-2.811
100	5	50.793	47.911	-2.882
100	6	50.252	47.691	-2.561
100	7	48.696	46.069	-2.627
100	8	50.692	47.689	-3.003
100	9	48.847	46.252	-2.595
100	10	49.868	47.207	-2.661
100	11	50.591	48.040	-2.551
100	12	50.299	47.822	-2.477
100	13	50.337	47.888	-2.449
100	14	50.186	47.638	-2.548
100	15	50.801	48.001	-2.800
100	16	49.879	47.202	-2.677
100	17	49.294	46.604	-2.690
100	18	50.705	48.200	-2.505
100	19	48.244	45.945	-2.299
100	20	50.055	47.192	-2.863
100	21	50.855	48.078	-2.777
100	22	50.348	47.596	-2.752
100	24	50.153	47.561	-2.592
105	25	50.373	46.894	-3.479
105	26	48.464	45.729	-2.735
105	27	50.267	47.278	-2.989
105	28	50.701	47.870	-2.831
105	29	50.751	47.778	-2.973
Max		51.121	50.962	0.060
Average		49.998	48.497	-1.501
Min		48.244	45.729	-3.479
Std Dev		0.783	1.307	1.117

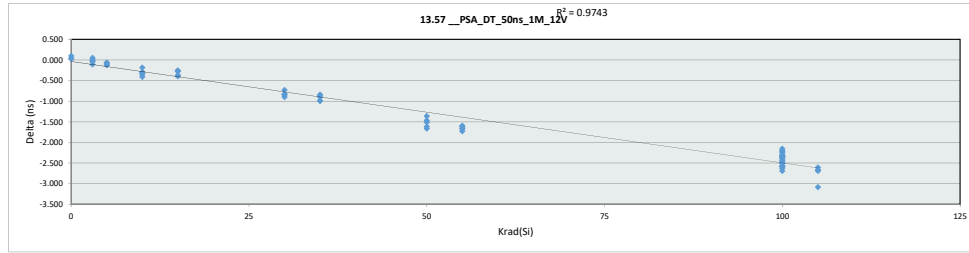


13.54 PSB DT 50ns 1M 5V											
Test Site											
Tester											
Test Number											
Max Limit	55	ns									
Min Limit	43	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	48.779	49.350	48.491	49.741	48.374	47.359	48.248	47.517	47.409	45.945	45.729
Average	49.004	50.116	49.615	50.180	49.825	48.337	49.252	48.476	48.398	47.327	47.110
Max	49.436	50.757	50.962	50.473	50.577	49.522	49.633	49.212	49.235	48.200	47.870
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

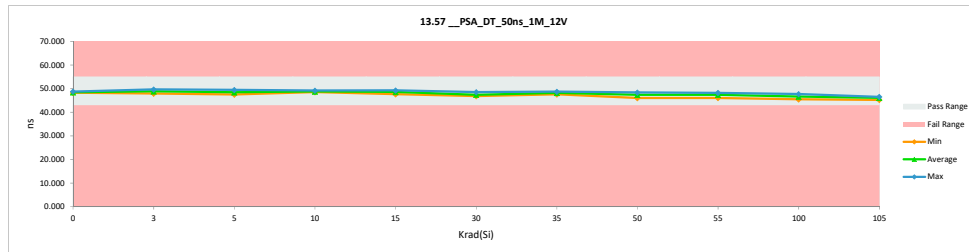


HDR TID Report
TPS7H5001-SP

13.57 PSA_DT_50ns_1M_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	55	55		
Min Limit	43	43		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	48.227	48.271	0.044
0	118	48.695	48.798	0.103
0	122	48.381	48.412	0.031
3	301	49.171	49.226	0.055
3	306	49.738	49.702	-0.036
3	307	48.405	48.301	-0.104
3	308	47.966	47.977	0.011
3	309	49.204	49.194	-0.010
5	310	48.227	48.108	-0.119
5	311	49.536	49.470	-0.066
5	312	49.595	49.529	-0.066
5	314	47.679	47.565	-0.114
5	316	47.814	47.749	-0.065
10	317	48.904	48.620	-0.284
10	318	49.247	49.066	-0.181
10	319	48.917	48.570	-0.347
10	321	49.653	49.247	-0.406
10	322	48.961	48.652	-0.309
15	323	49.701	49.312	-0.389
15	325	47.949	47.676	-0.273
15	326	49.039	48.788	-0.251
15	328	48.829	48.571	-0.258
15	333	49.249	48.886	-0.363
30	334	48.158	47.285	-0.873
30	335	47.710	46.877	-0.833
30	337	47.790	46.971	-0.819
30	339	48.241	47.517	-0.724
30	341	49.403	48.564	-0.839
35	342	48.471	47.631	-0.840
35	345	49.088	48.099	-0.989
35	346	48.949	47.967	-0.982
35	347	49.382	48.543	-0.839
35	348	49.682	48.787	-0.895
50	349	48.701	47.195	-1.506
50	350	50.078	48.420	-1.658
50	351	47.437	46.079	-1.358
50	353	48.684	47.072	-1.612
50	354	49.595	48.133	-1.462
55	355	48.815	47.201	-1.614
55	357	49.506	47.917	-1.589
55	359	49.977	48.251	-1.726
55	362	49.215	47.569	-1.646
55	363	47.752	46.084	-1.668
100	1	49.342	46.991	-2.351
100	3	48.226	45.723	-2.503
100	4	48.483	45.909	-2.574
100	5	49.999	47.411	-2.588
100	6	49.595	47.221	-2.374
100	7	47.942	45.724	-2.218
100	8	49.563	47.002	-2.561
100	9	47.762	45.518	-2.244
100	10	49.188	46.885	-2.303
100	11	50.111	47.772	-2.339
100	12	49.290	47.101	-2.189
100	13	49.683	47.218	-2.465
100	14	49.470	47.169	-2.301
100	15	49.051	46.431	-2.620
100	16	49.872	47.476	-2.396
100	17	48.374	45.885	-2.489
100	18	49.891	47.546	-2.345
100	19	47.871	45.723	-2.148
100	20	49.150	46.463	-2.687
100	21	48.945	46.462	-2.483
100	22	49.156	46.725	-2.431
100	24	48.824	46.507	-2.317
105	25	48.807	45.727	-3.080
105	26	47.946	45.284	-2.662
105	27	49.167	46.484	-2.683
105	28	49.164	46.561	-2.603
105	29	49.139	46.467	-2.672
	Max	50.111	49.702	0.103
	Average	48.910	47.560	-1.350
	Min	47.437	45.284	-3.080
	Std Dev	0.697	1.126	1.019

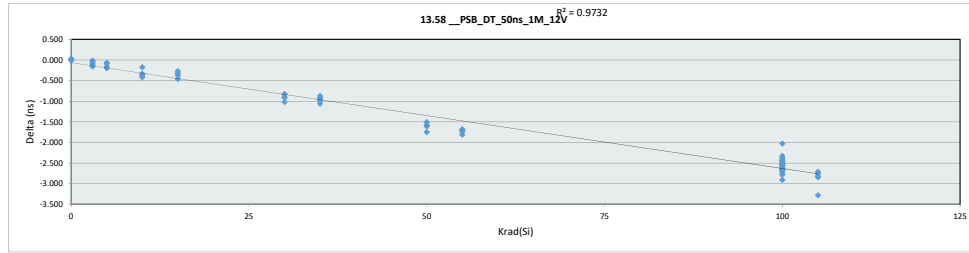


13.57 PSA_DT_50ns_1M_12V											
Test Site											
Tester											
Test Number											
Max Limit	55	ns									
Min Limit	43	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	48.271	47.977	47.565	48.570	47.676	46.877	47.631	46.079	46.084	45.518	45.284
Average	48.494	48.880	48.484	48.831	48.647	47.439	48.205	47.380	47.404	46.676	46.105
Max	48.798	49.702	49.529	49.247	49.312	48.564	48.787	48.420	48.251	47.772	46.561
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

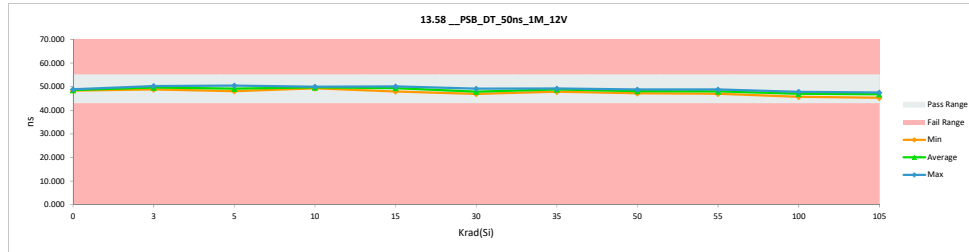


HDR TID Report
TPS7H5001-SP

13.58 PSB DT 50ns 1M 12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	55	55		
Min Limit	43	43		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	48.360	48.350	-0.010
0	118	48.864	48.898	0.034
0	122	48.388	48.412	0.024
3	301	50.215	50.170	-0.045
3	306	50.455	50.300	-0.155
3	307	49.349	49.279	-0.070
3	308	48.937	48.826	-0.111
3	309	49.765	49.754	-0.011
5	310	48.955	48.757	-0.198
5	311	50.569	50.496	-0.073
5	312	50.342	50.276	-0.066
5	314	48.277	48.121	-0.156
5	316	48.312	48.229	-0.083
10	317	50.359	50.024	-0.335
10	318	50.008	49.834	-0.174
10	319	50.291	49.879	-0.412
10	321	49.959	49.624	-0.335
10	322	49.652	49.288	-0.364
15	323	50.136	49.681	-0.455
15	325	48.376	48.002	-0.374
15	326	50.454	50.131	-0.323
15	328	50.166	49.900	-0.266
15	333	49.643	49.325	-0.318
30	334	47.964	46.948	-1.016
30	335	48.637	47.818	-0.819
30	337	48.465	47.563	-0.902
30	339	49.006	48.109	-0.897
30	341	50.005	49.171	-0.834
35	342	48.772	47.903	-0.869
35	345	49.851	48.875	-0.976
35	346	50.251	49.196	-1.055
35	347	50.028	49.113	-0.915
35	348	50.209	49.258	-0.951
50	349	49.157	47.653	-1.504
50	350	50.448	48.845	-1.603
50	351	48.954	47.211	-1.743
50	353	50.307	48.726	-1.581
50	354	49.719	48.142	-1.577
55	355	49.659	47.853	-1.806
55	357	50.521	48.845	-1.676
55	359	49.709	47.971	-1.738
55	362	50.094	48.394	-1.700
55	363	48.712	46.997	-1.715
100	1	50.001	47.356	-2.645
100	3	48.925	46.260	-2.665
100	4	48.736	46.054	-2.682
100	5	50.335	47.554	-2.781
100	6	49.763	47.334	-2.429
100	7	48.209	45.706	-2.503
100	8	50.307	47.401	-2.906
100	9	48.476	46.015	-2.461
100	10	49.409	46.961	-2.448
100	11	50.120	47.579	-2.541
100	12	49.857	47.491	-2.366
100	13	49.807	47.485	-2.322
100	14	49.745	47.200	-2.545
100	15	50.359	47.684	-2.675
100	16	49.529	46.964	-2.565
100	17	48.777	46.270	-2.507
100	18	50.257	47.859	-2.398
100	19	47.778	45.755	-2.023
100	20	49.588	46.849	-2.739
100	21	50.432	47.794	-2.638
100	22	49.909	47.310	-2.599
100	24	49.719	47.172	-2.547
105	25	49.857	46.579	-3.278
105	26	48.020	45.313	-2.707
105	27	49.840	47.000	-2.840
105	28	50.297	47.551	-2.746
105	29	50.280	47.467	-2.813
	Max	50.569	50.496	0.034
	Average	49.552	48.116	-1.436
	Min	47.778	45.313	-3.278
	Std Dev	0.769	1.260	1.064

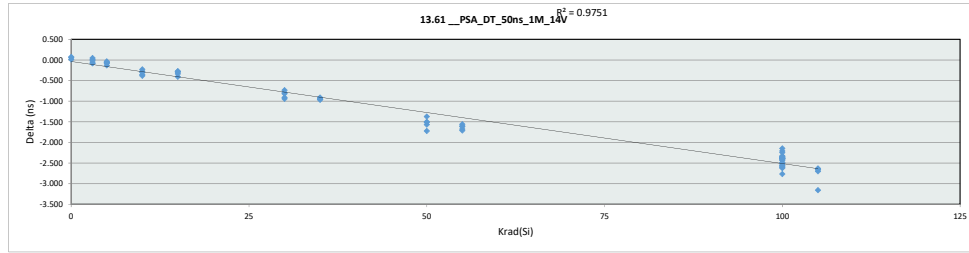


13.58 PSB DT 50ns 1M 12V											
Test Site											
Tester											
Test Number											
Max Limit	55	ns									
Min Limit	43	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	48.350	48.826	48.121	49.288	48.002	46.948	47.903	47.211	46.997	45.706	45.313
Average	48.553	49.666	49.176	49.730	49.408	47.922	48.869	48.115	48.012	47.002	46.782
Max	48.898	50.300	50.496	50.024	50.131	49.171	49.258	48.845	48.845	47.859	47.551
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

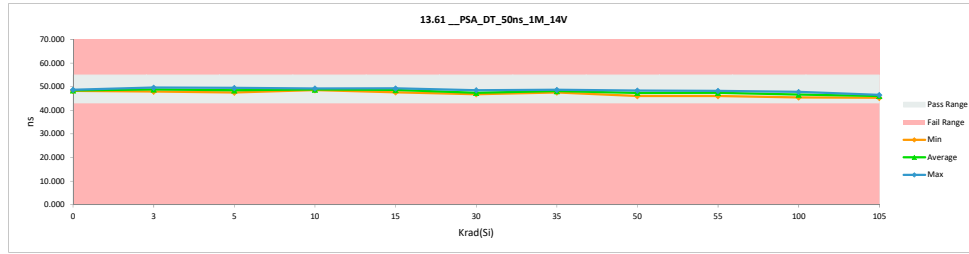


HDR TID Report
TPS7H5001-SP

13.61 PSA_DT_50ns_1M_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	55	55		
Min Limit	43	43		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	48.211	48.289	0.078
0	118	48.741	48.757	0.016
0	122	48.362	48.422	0.060
3	301	49.138	49.191	0.053
3	306	49.722	49.653	-0.069
3	307	48.355	48.338	-0.017
3	308	47.946	47.959	0.013
3	309	49.281	49.206	-0.075
5	310	48.220	48.093	-0.127
5	311	49.562	49.507	-0.055
5	312	49.585	49.525	-0.060
5	314	47.647	47.571	-0.076
5	316	47.828	47.800	-0.028
10	317	48.956	48.603	-0.353
10	318	49.292	49.067	-0.225
10	319	48.953	48.574	-0.379
10	321	49.597	49.299	-0.298
10	322	48.940	48.669	-0.271
15	323	49.712	49.310	-0.402
15	325	47.930	47.665	-0.265
15	326	49.042	48.759	-0.283
15	328	48.864	48.579	-0.285
15	333	49.250	48.918	-0.332
30	334	48.174	47.269	-0.905
30	335	47.777	46.839	-0.938
30	337	47.786	46.973	-0.813
30	339	48.238	47.511	-0.727
30	341	49.320	48.529	-0.791
35	342	48.505	47.600	-0.905
35	345	49.046	48.099	-0.947
35	346	48.949	48.003	-0.946
35	347	49.437	48.485	-0.952
35	348	49.676	48.721	-0.955
50	349	48.709	47.207	-1.502
50	350	50.113	48.396	-1.717
50	351	47.438	46.069	-1.369
50	353	48.647	47.083	-1.564
50	354	49.606	48.110	-1.496
55	355	48.802	47.204	-1.598
55	357	49.492	47.939	-1.553
55	359	49.960	48.255	-1.705
55	362	49.184	47.572	-1.612
55	363	47.750	46.081	-1.669
100	1	49.340	46.954	-2.386
100	3	48.217	45.672	-2.545
100	4	48.491	45.914	-2.577
100	5	50.007	47.461	-2.546
100	6	49.570	47.186	-2.384
100	7	47.923	45.722	-2.201
100	8	49.607	47.006	-2.601
100	9	47.844	45.506	-2.338
100	10	49.172	46.849	-2.323
100	11	50.189	47.831	-2.358
100	12	49.307	47.065	-2.242
100	13	49.679	47.248	-2.431
100	14	49.488	47.138	-2.350
100	15	49.050	46.433	-2.617
100	16	49.867	47.474	-2.393
100	17	48.380	45.894	-2.486
100	18	49.923	47.536	-2.387
100	19	47.906	45.766	-2.140
100	20	49.212	46.451	-2.761
100	21	48.926	46.435	-2.491
100	22	49.175	46.763	-2.412
100	24	48.853	46.479	-2.374
105	25	48.846	45.693	-3.153
105	26	47.939	45.289	-2.650
105	27	49.129	46.435	-2.694
105	28	49.183	46.556	-2.627
105	29	49.142	46.487	-2.655
Max	50.189	49.653	0.078	
Average	48.916	47.556	-1.360	
Min	47.438	45.289	-3.153	
Std Dev	0.698	1.128	1.026	

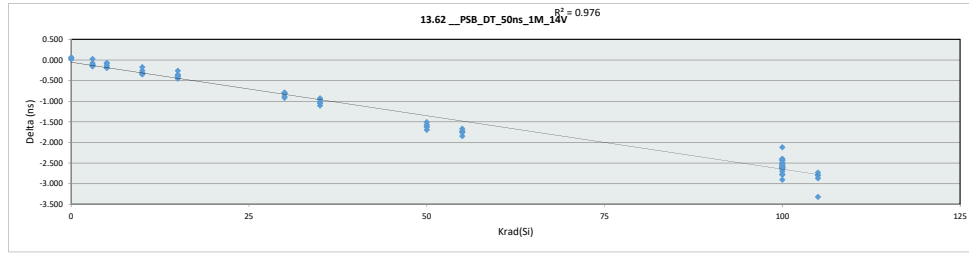


13.61 PSA_DT_50ns_1M_14V											
Test Site											
Tester											
Test Number											
Max Limit	55	ns									
Min Limit	43	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	48.289	47.959	47.571	48.574	47.665	46.839	47.600	46.069	46.081	45.506	45.289
Average	48.489	48.869	48.499	48.842	48.646	47.424	48.182	47.373	47.410	46.672	46.092
Max	48.757	49.653	49.525	49.299	49.310	48.529	48.721	48.396	48.255	47.831	46.556
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

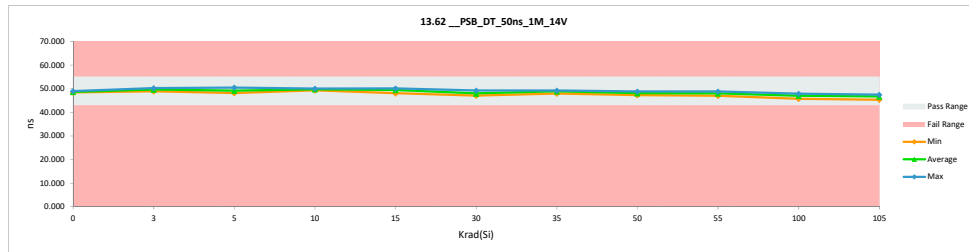


HDR TID Report
TPS7H5001-SP

13.62 PSB_DT_50ns_1M_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	55	55		
Min Limit	43	43		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	48.383	48.404	0.021
0	118	48.909	48.975	0.066
0	122	48.358	48.415	0.057
3	301	50.293	50.159	-0.134
3	306	50.422	50.271	-0.151
3	307	49.323	49.213	-0.110
3	308	48.937	48.866	-0.071
3	309	49.757	49.786	0.029
5	310	48.913	48.787	-0.126
5	311	50.566	50.498	-0.068
5	312	50.392	50.225	-0.167
5	314	48.322	48.134	-0.188
5	316	48.257	48.185	-0.072
10	317	50.315	50.062	-0.253
10	318	50.026	49.858	-0.168
10	319	50.239	49.908	-0.331
10	321	49.968	49.658	-0.310
10	322	49.651	49.307	-0.344
15	323	50.115	49.712	-0.403
15	325	48.394	48.044	-0.350
15	326	50.493	50.124	-0.369
15	328	50.194	49.935	-0.259
15	333	49.704	49.256	-0.448
30	334	47.953	47.039	-0.914
30	335	48.654	47.853	-0.801
30	337	48.480	47.605	-0.875
30	339	48.943	48.158	-0.785
30	341	50.030	49.228	-0.802
35	342	48.837	47.912	-0.925
35	345	49.971	48.925	-1.046
35	346	50.252	49.152	-1.100
35	347	50.066	49.112	-0.954
35	348	50.260	49.244	-1.016
50	349	49.178	47.613	-1.565
50	350	50.486	48.797	-1.689
50	351	48.872	47.250	-1.622
50	353	50.277	48.676	-1.601
50	354	49.669	48.165	-1.504
55	355	49.695	47.855	-1.840
55	357	50.571	48.837	-1.734
55	359	49.648	47.985	-1.663
55	362	50.098	48.341	-1.757
55	363	48.731	47.011	-1.720
100	1	49.999	47.396	-2.603
100	3	48.930	46.287	-2.643
100	4	48.694	45.996	-2.698
100	5	50.322	47.553	-2.769
100	6	49.777	47.279	-2.498
100	7	48.275	45.717	-2.558
100	8	50.263	47.362	-2.901
100	9	48.461	45.960	-2.501
100	10	49.417	46.924	-2.493
100	11	50.111	47.533	-2.578
100	12	49.880	47.449	-2.431
100	13	49.855	47.454	-2.401
100	14	49.748	47.224	-2.524
100	15	50.334	47.716	-2.618
100	16	49.507	46.938	-2.569
100	17	48.834	46.223	-2.611
100	18	50.244	47.850	-2.394
100	19	47.826	45.714	-2.112
100	20	49.591	46.812	-2.779
100	21	50.429	47.812	-2.617
100	22	49.927	47.344	-2.583
100	24	49.629	47.200	-2.429
105	25	49.822	46.507	-3.315
105	26	48.076	45.350	-2.726
105	27	49.870	47.005	-2.865
105	28	50.254	47.463	-2.791
105	29	50.268	47.473	-2.795
Max		50.571	50.498	0.066
Average		49.556	48.115	-1.441
Min		47.826	45.350	-3.315
Std Dev		0.767	1.266	1.073

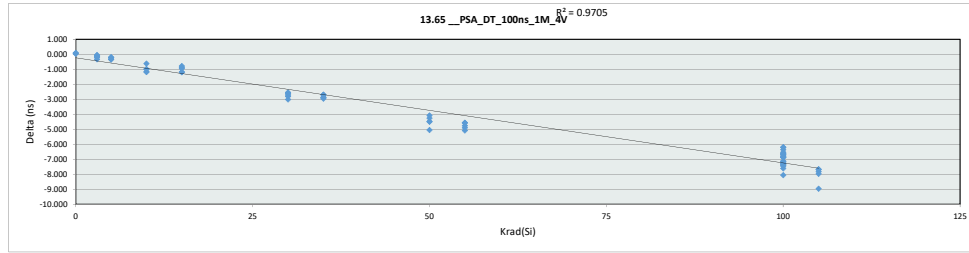


13.62 PSB_DT_50ns_1M_14V											
Test Site											
Tester											
Test Number											
Max Limit	55	ns									
Min Limit	43	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	48.404	48.866	48.134	49.307	48.044	47.039	47.912	47.250	47.011	45.714	45.350
Average	48.598	49.659	49.166	49.759	49.414	47.977	48.869	48.100	48.006	46.988	46.760
Max	48.975	50.271	50.498	50.062	50.124	49.228	49.244	48.797	48.837	47.850	47.473
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

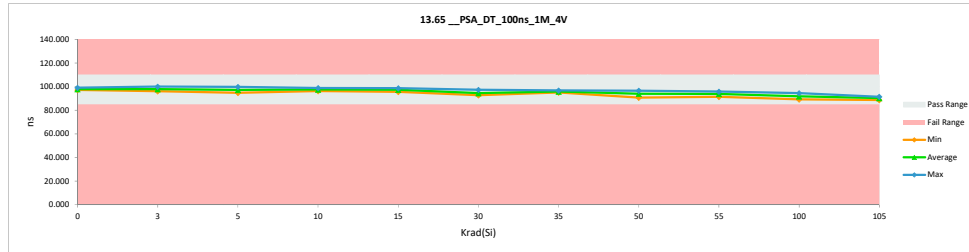


HDR TID Report
TPS7H5001-SP

13.65 PSA_DT_100ns_1M_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	110	110		
Min Limit	85	85		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	97.119	97.226	0.107
0	118	99.069	99.116	0.047
0	122	97.383	97.477	0.094
3	301	98.457	98.425	-0.032
3	306	100.263	100.132	-0.131
3	307	96.482	96.293	-0.189
3	308	96.257	96.170	-0.087
3	309	98.949	98.628	-0.321
5	310	96.989	96.676	-0.313
5	311	99.766	99.567	-0.199
5	312	99.985	99.809	-0.176
5	314	95.158	94.830	-0.328
5	316	95.591	95.382	-0.209
10	317	98.103	97.135	-0.968
10	318	99.527	98.926	-0.601
10	319	97.513	96.356	-1.157
10	321	99.944	98.786	-1.158
10	322	97.929	96.955	-0.974
15	323	99.881	98.698	-1.183
15	325	96.600	95.665	-0.935
15	326	98.192	97.344	-0.848
15	328	98.178	97.413	-0.765
15	333	98.931	97.774	-1.157
30	334	97.558	94.575	-2.983
30	335	95.676	92.881	-2.795
30	337	95.535	92.840	-2.695
30	339	97.371	94.848	-2.523
30	341	99.976	97.371	-2.605
35	342	97.951	95.093	-2.858
35	345	98.467	95.530	-2.937
35	346	98.437	95.583	-2.854
35	347	99.036	96.365	-2.671
35	348	99.541	96.858	-2.683
50	349	98.513	94.040	-4.473
50	350	101.678	96.645	-5.033
50	351	94.810	90.750	-4.060
50	353	97.626	93.151	-4.475
50	354	99.770	95.522	-4.248
55	355	97.789	93.239	-4.550
55	357	99.394	94.838	-4.556
55	359	100.969	95.908	-5.061
55	362	98.740	93.997	-4.743
55	363	96.282	91.393	-4.889
100	1	99.273	92.472	-6.801
100	3	96.941	89.501	-7.440
100	4	97.720	90.404	-7.316
100	5	101.041	93.741	-7.300
100	6	99.522	92.654	-6.868
100	7	96.106	89.759	-6.347
100	8	99.663	92.262	-7.401
100	9	95.811	89.296	-6.515
100	10	98.564	91.966	-6.598
100	11	101.221	94.562	-6.659
100	12	98.880	92.701	-6.179
100	13	100.092	92.919	-7.173
100	14	100.414	93.700	-6.714
100	15	98.241	90.650	-7.591
100	16	100.565	93.725	-6.840
100	17	97.624	90.354	-7.270
100	18	100.632	93.782	-6.850
100	19	96.160	89.959	-6.201
100	20	99.263	91.225	-8.038
100	21	98.350	91.201	-7.149
100	22	98.787	91.680	-7.107
100	24	97.660	91.026	-6.634
105	25	97.679	88.730	-8.949
105	26	96.931	88.972	-7.959
105	27	99.315	91.511	-7.804
105	28	98.729	91.085	-7.644
105	29	99.082	91.414	-7.668
Max		101.678	100.132	0.107
Average		98.367	94.392	-3.974
Min		94.810	88.730	-8.949
Std Dev		1.579	3.032	2.911

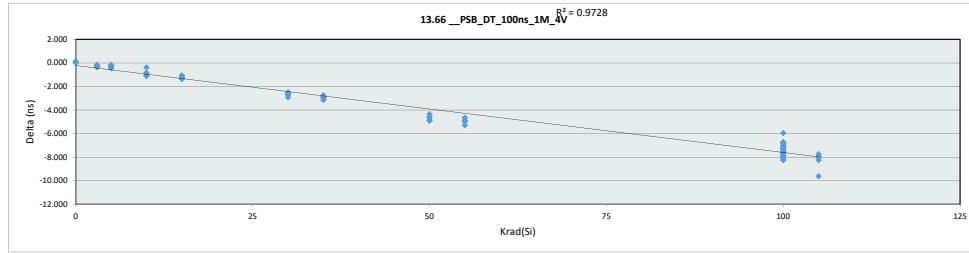


13.65 PSA_DT_100ns_1M_4V											
Test Site											
Tester											
Test Number											
Max Limit	110	ns									
Min Limit	85	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	97.226	96.170	94.830	96.356	95.665	92.840	95.093	90.750	91.393	89.296	88.730
Average	97.940	97.930	97.253	97.632	97.379	94.503	95.886	94.022	93.875	91.797	90.342
Max	99.116	100.132	99.809	98.926	98.698	97.371	96.858	96.645	95.908	94.562	91.511
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

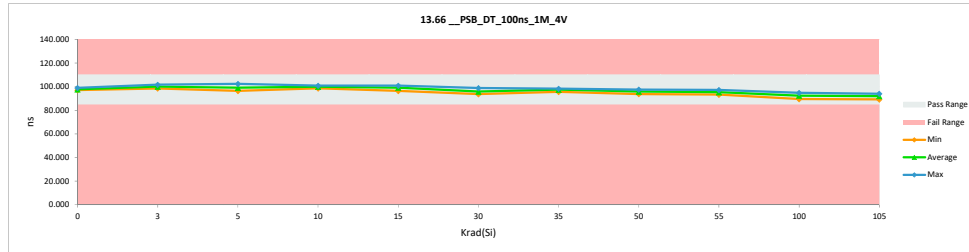


HDR TID Report
TPS7H5001-SP

13.66 PSB_DT_100ns_1M_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	110	110		
Min Limit	85	85		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	97.042	97.172	0.130
0	118	99.005	99.025	0.020
0	122	97.212	97.349	0.137
3	301	101.408	101.257	-0.151
3	306	102.109	101.784	-0.325
3	307	98.881	98.538	-0.343
3	308	98.908	98.721	-0.187
3	309	100.242	100.021	-0.221
5	310	99.021	98.588	-0.433
5	311	102.543	102.388	-0.155
5	312	101.828	101.569	-0.259
5	314	96.836	96.422	-0.414
5	316	97.056	96.777	-0.279
10	317	101.722	100.908	-0.814
10	318	100.860	100.496	-0.364
10	319	101.007	99.910	-1.097
10	321	100.819	99.812	-1.007
10	322	99.684	98.667	-1.017
15	323	100.797	99.466	-1.331
15	325	97.820	96.492	-1.328
15	326	101.990	100.917	-1.073
15	328	101.553	100.506	-1.047
15	333	100.009	98.827	-1.182
30	334	96.653	93.749	-2.904
30	335	98.333	95.636	-2.697
30	337	97.526	95.004	-2.522
30	339	98.728	96.240	-2.488
30	341	101.460	98.934	-2.526
35	342	98.510	95.678	-2.832
35	345	100.555	97.631	-2.924
35	346	101.455	98.324	-3.131
35	347	100.640	97.913	-2.727
35	348	100.928	98.008	-2.920
50	349	99.548	94.941	-4.607
50	350	102.398	97.505	-4.893
50	351	98.656	93.810	-4.846
50	353	102.189	97.603	-4.586
50	354	99.954	95.621	-4.333
55	355	100.158	94.880	-5.278
55	357	101.886	97.246	-4.640
55	359	99.959	95.087	-4.872
55	362	101.350	96.376	-4.974
55	363	98.191	93.323	-4.868
100	1	100.819	93.339	-7.480
100	3	98.361	90.783	-7.578
100	4	98.512	90.649	-7.863
100	5	101.823	93.961	-7.862
100	6	100.008	93.027	-6.981
100	7	96.928	89.686	-7.242
100	8	101.493	93.247	-8.246
100	9	96.856	89.820	-7.036
100	10	99.027	91.846	-7.181
100	11	100.735	93.514	-7.221
100	12	100.459	93.755	-6.704
100	13	100.406	93.612	-6.794
100	14	100.841	93.346	-7.495
100	15	102.171	94.048	-8.123
100	16	99.437	91.942	-7.485
100	17	98.265	90.963	-7.302
100	18	101.561	94.800	-6.761
100	19	95.489	89.558	-5.931
100	20	100.009	92.064	-7.945
100	21	102.228	94.518	-7.710
100	22	100.592	92.952	-7.640
100	24	100.057	93.016	-7.041
105	25	100.446	90.840	-9.606
105	26	96.969	89.236	-7.733
105	27	100.429	92.195	-8.234
105	28	101.710	93.809	-7.901
105	29	102.094	94.066	-8.028
	Max	102.543	102.388	0.137
	Average	99.931	95.767	-4.163
	Min	95.489	89.236	-6.253
	Std Dev	1.744	3.405	3.060

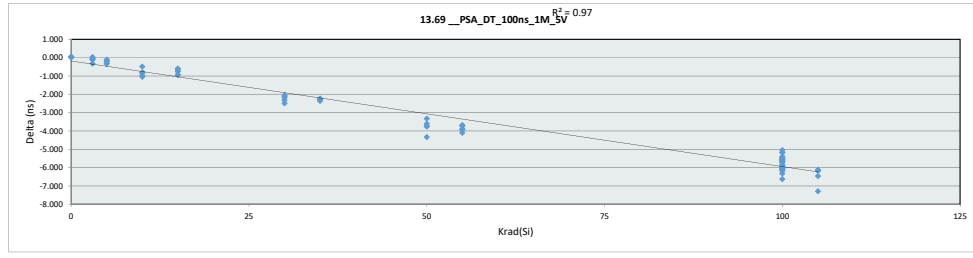


13.66 PSB_DT_100ns_1M_4V											
Test Site											
Tester											
Test Number											
Max Limit	110	ns									
Min Limit	85	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	97.172	98.538	96.422	98.667	96.492	93.749	95.678	93.810	93.323	89.558	89.236
Average	97.849	100.064	99.149	99.959	99.242	95.913	97.511	95.896	95.382	92.475	92.029
Max	99.025	101.784	102.388	100.908	100.917	98.934	98.324	97.603	97.246	94.800	94.066
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

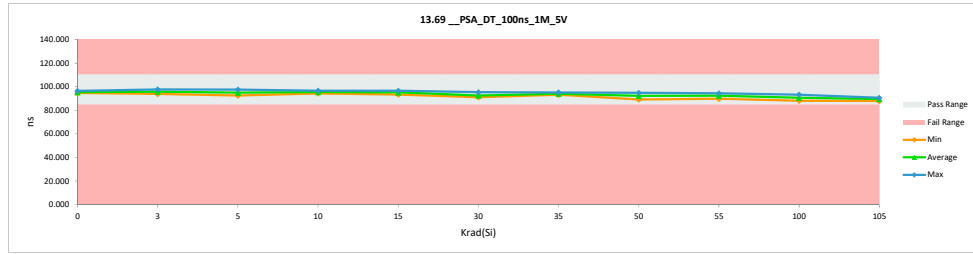


HDR TID Report
TPS7H5001-SP

13.69 PSA_DT_100ns_1M_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	110	110		
Min Limit	85	85		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	94.670	94.733	0.063
0	118	96.438	96.497	0.059
0	122	94.948	94.967	0.019
3	301	96.208	96.250	0.042
3	306	97.839	97.746	-0.093
3	307	94.406	94.329	-0.077
3	308	93.945	93.922	-0.023
3	309	96.714	96.404	-0.310
5	310	94.635	94.319	-0.316
5	311	97.491	97.292	-0.199
5	312	97.698	97.605	-0.093
5	314	92.853	92.556	-0.297
5	316	93.309	93.153	-0.156
10	317	95.820	95.035	-0.785
10	318	97.111	96.636	-0.475
10	319	95.386	94.358	-1.028
10	321	97.636	96.668	-0.968
10	322	95.733	94.909	-0.824
15	323	97.561	96.633	-0.928
15	325	94.162	93.412	-0.750
15	326	95.954	95.299	-0.655
15	328	95.836	95.249	-0.587
15	333	96.615	95.687	-0.928
30	334	94.942	92.462	-2.480
30	335	93.393	91.087	-2.306
30	337	93.236	91.081	-2.155
30	339	94.919	92.880	-2.039
30	341	97.489	95.372	-2.117
35	342	95.434	93.196	-2.238
35	345	96.070	93.825	-2.245
35	346	96.211	93.866	-2.345
35	347	96.789	94.564	-2.225
35	348	97.447	95.169	-2.278
50	349	95.969	92.236	-3.733
50	350	99.169	94.849	-4.320
50	351	92.545	89.224	-3.321
50	353	95.248	91.508	-3.740
50	354	97.452	93.853	-3.599
55	355	95.547	91.877	-3.669
55	357	97.098	93.378	-3.721
55	359	98.506	94.423	-4.083
55	362	96.500	92.625	-3.875
55	363	93.744	89.814	-3.930
100	1	97.027	91.447	-5.580
100	3	94.547	88.387	-6.160
100	4	95.188	89.166	-6.022
100	5	98.718	92.663	-6.055
100	6	97.259	91.595	-5.664
100	7	93.873	88.703	-5.170
100	8	97.428	91.321	-6.107
100	9	93.536	88.114	-5.422
100	10	96.191	90.807	-5.384
100	11	98.862	93.305	-5.557
100	12	96.517	91.482	-5.035
100	13	97.720	91.816	-5.904
100	14	97.821	92.333	-5.488
100	15	96.034	89.720	-6.314
100	16	98.175	92.456	-5.719
100	17	95.108	89.078	-6.030
100	18	98.216	92.581	-5.635
100	19	93.830	88.668	-5.162
100	20	96.771	90.158	-6.613
100	21	96.036	90.139	-5.897
100	22	96.482	90.655	-5.827
100	24	95.504	89.935	-5.569
105	25	95.402	88.121	-7.281
105	26	94.408	87.956	-6.452
105	27	96.803	90.662	-6.141
105	28	96.433	90.318	-6.115
105	29	96.670	90.504	-6.166
Max		99.169	97.746	0.063
Average		96.018	92.758	-3.260
Min		92.545	87.956	-7.281
Std Dev		1.564	2.602	2.392

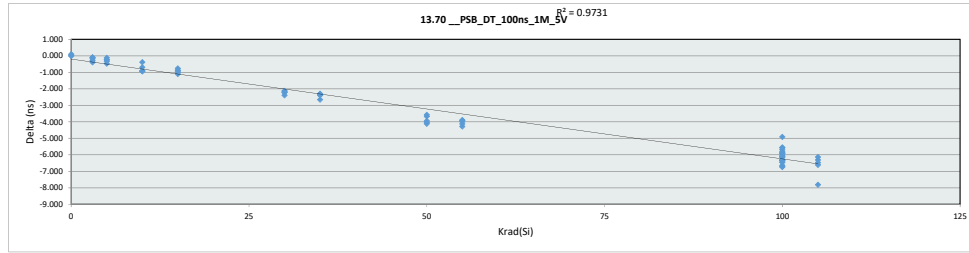


13.69 PSA_DT_100ns_1M_5V											
Test Site											
Tester											
Test Number											
Max Limit	110	ns									
Min Limit	85	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.733	93.922	92.556	94.358	93.412	91.081	93.196	89.224	89.814	88.114	87.956
Average	95.399	95.730	94.985	95.521	95.256	92.576	94.124	92.334	92.423	90.660	89.512
Max	96.497	97.746	97.605	96.668	96.633	95.372	95.169	94.849	94.423	93.305	90.662
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

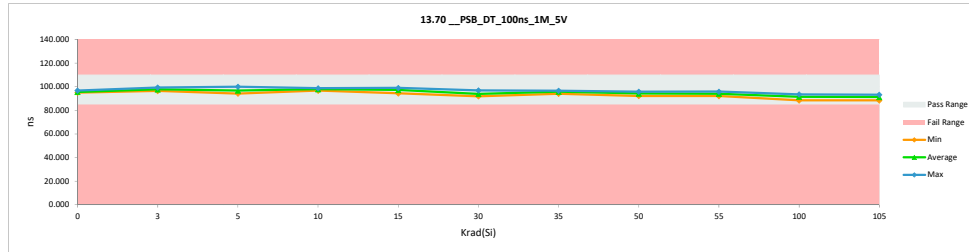


HDR TID Report
TPS7H5001-SP

		13.70 PSB_DT_100ns_1M_5V		
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	110	110		
Min Limit	85	85		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	94.901	95.015	0.114
0	118	96.798	96.791	-0.007
0	122	95.008	95.051	0.043
3	301	99.109	99.024	-0.085
3	306	99.736	99.357	-0.379
3	307	96.743	96.482	-0.261
3	308	96.554	96.470	-0.084
3	309	98.054	97.906	-0.148
5	310	96.622	96.167	-0.455
5	311	100.143	100.034	-0.109
5	312	99.634	99.404	-0.230
5	314	94.545	94.236	-0.309
5	316	94.749	94.526	-0.223
10	317	99.474	98.796	-0.678
10	318	98.766	98.399	-0.367
10	319	98.847	97.926	-0.921
10	321	98.655	97.756	-0.899
10	322	97.623	96.705	-0.918
15	323	98.620	97.534	-1.086
15	325	95.438	94.462	-0.976
15	326	99.918	99.065	-0.853
15	328	99.284	98.537	-0.747
15	333	97.823	96.918	-0.905
30	334	94.338	91.950	-2.378
30	335	95.905	93.697	-2.208
30	337	95.334	93.127	-2.207
30	339	96.528	94.388	-2.140
30	341	99.036	96.931	-2.105
35	342	96.472	94.164	-2.308
35	345	98.315	95.971	-2.344
35	346	99.197	96.553	-2.644
35	347	98.406	96.125	-2.281
35	348	99.043	96.663	-2.380
50	349	97.167	93.507	-3.660
50	350	100.023	95.920	-4.103
50	351	96.290	92.264	-4.026
50	353	99.699	95.763	-3.936
50	354	97.700	94.139	-3.561
55	355	97.917	93.645	-4.272
55	357	99.839	95.940	-3.899
55	359	97.910	93.928	-3.982
55	362	98.955	94.842	-4.113
55	363	96.014	92.135	-3.879
100	1	98.622	92.530	-6.092
100	3	96.144	89.806	-6.338
100	4	96.176	89.828	-6.348
100	5	99.631	93.002	-6.629
100	6	97.967	92.194	-5.773
100	7	94.787	88.680	-6.107
100	8	99.257	92.533	-6.724
100	9	94.972	89.123	-5.849
100	10	97.015	91.084	-5.931
100	11	98.754	92.717	-6.037
100	12	98.220	92.674	-5.546
100	13	98.218	92.661	-5.557
100	14	98.472	92.539	-5.933
100	15	99.725	93.301	-6.424
100	16	97.337	91.028	-6.309
100	17	96.043	89.900	-6.143
100	18	99.243	93.588	-5.655
100	19	93.349	88.443	-4.906
100	20	97.738	91.032	-6.706
100	21	99.888	93.446	-6.442
100	22	98.408	92.125	-6.283
100	24	97.898	92.016	-5.882
105	25	98.164	90.356	-7.808
105	26	94.622	88.493	-6.129
105	27	98.162	91.558	-6.604
105	28	99.362	93.067	-6.295
105	29	99.740	93.271	-6.469
Max		100.143	100.034	0.114
Average		97.701	94.275	-3.426
Min		93.349	88.443	-7.808
Std Dev		1.725	2.933	2.509

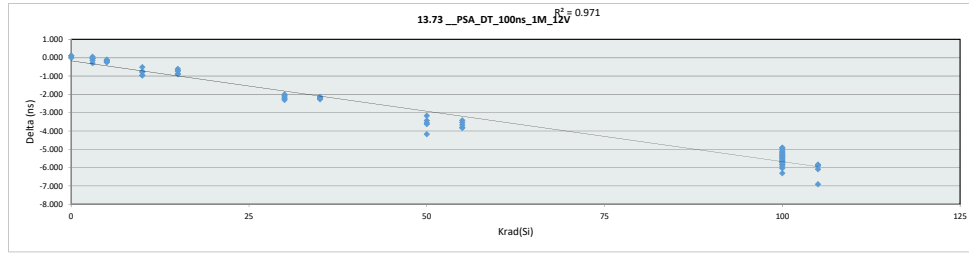


		13.70 PSB_DT_100ns_1M_5V									
Test Site											
Tester											
Test Number											
Max Limit	110	ns									
Min Limit	85	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	95.015	96.470	94.236	96.705	94.462	91.960	94.164	92.264	92.135	88.443	88.493
Average	95.619	97.848	96.873	97.916	97.303	94.021	95.895	94.319	94.098	91.557	91.349
Max	96.791	99.357	100.034	98.796	99.065	96.931	96.663	95.920	95.940	93.588	93.271
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

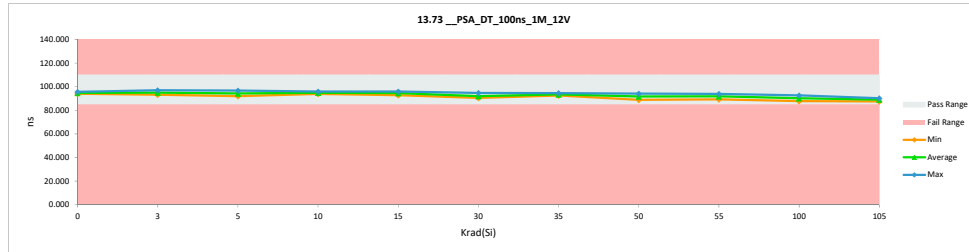


HDR TID Report
TPS7H5001-SP

13.73 PSA_DT_100ns_1M_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	110	110		
Min Limit	85	85		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	93.923	94.027	0.104
0	118	95.596	95.596	0.000
0	122	94.146	94.269	0.123
3	301	95.408	95.474	0.066
3	306	96.997	96.964	-0.033
3	307	93.824	93.683	-0.141
3	308	93.183	93.149	-0.034
3	309	95.922	95.633	-0.289
5	310	93.815	93.571	-0.244
5	311	96.634	96.460	-0.174
5	312	96.849	96.751	-0.098
5	314	92.165	91.948	-0.217
5	316	92.686	92.543	-0.143
10	317	95.140	94.396	-0.744
10	318	96.359	95.856	-0.503
10	319	94.755	93.798	-0.957
10	321	96.762	95.822	-0.940
10	322	94.951	94.171	-0.780
15	323	96.809	95.948	-0.861
15	325	93.455	92.742	-0.713
15	326	95.290	94.605	-0.685
15	328	95.198	94.596	-0.602
15	333	95.943	95.052	-0.891
30	334	94.133	91.827	-2.306
30	335	92.678	90.455	-2.223
30	337	92.577	90.520	-2.057
30	339	94.147	92.153	-1.994
30	341	96.744	94.607	-2.137
35	342	94.635	92.494	-2.141
35	345	95.360	93.142	-2.218
35	346	95.526	93.263	-2.263
35	347	96.071	93.943	-2.128
35	348	96.693	94.534	-2.159
50	349	95.076	91.524	-3.552
50	350	98.275	94.115	-4.160
50	351	91.939	88.781	-3.158
50	353	94.549	90.933	-3.616
50	354	96.706	93.287	-3.419
55	355	94.829	91.418	-3.411
55	357	96.275	92.745	-3.530
55	359	97.686	93.859	-3.827
55	362	95.699	92.045	-3.654
55	363	93.028	89.229	-3.799
100	1	96.223	90.881	-5.342
100	3	93.890	87.983	-5.907
100	4	94.374	88.673	-5.701
100	5	97.838	92.035	-5.803
100	6	96.511	91.071	-5.440
100	7	93.091	88.071	-5.020
100	8	96.609	90.799	-5.810
100	9	92.825	87.718	-5.107
100	10	95.496	90.329	-5.167
100	11	97.907	92.697	-5.210
100	12	95.824	90.931	-4.893
100	13	96.925	91.298	-5.627
100	14	97.017	91.718	-5.299
100	15	95.311	89.290	-6.021
100	16	97.415	91.926	-5.489
100	17	94.281	88.594	-5.687
100	18	97.405	92.000	-5.405
100	19	93.107	88.186	-4.921
100	20	96.010	89.714	-6.296
100	21	95.331	89.699	-5.632
100	22	95.741	90.176	-5.565
100	24	94.790	89.483	-5.307
105	25	94.686	87.786	-6.900
105	26	93.582	87.502	-6.080
105	27	96.094	90.200	-5.894
105	28	95.638	89.817	-5.821
105	29	95.911	90.061	-5.850
Max		98.275	96.964	0.123
Average		95.261	92.151	-3.110
Min		91.939	87.502	-6.900
Std Dev		1.524	2.495	2.282

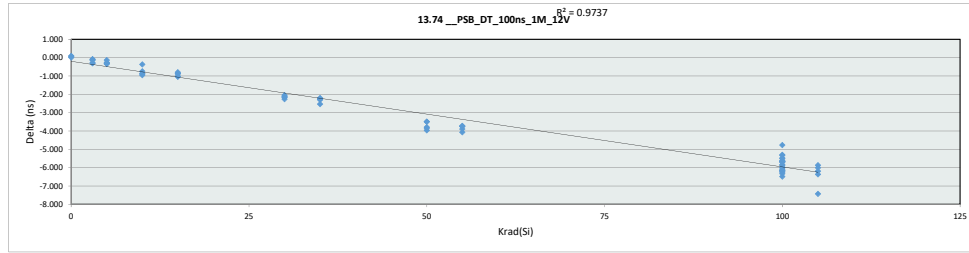


13.73 PSA_DT_100ns_1M_12V											
Test Site											
Tester											
Test Number											
Max Limit	110	ns									
Min Limit	85	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.027	93.149	91.948	93.798	92.742	90.455	92.494	88.781	89.229	87.718	87.502
Average	94.631	94.981	94.255	94.809	94.589	91.912	93.475	91.728	91.859	90.149	89.073
Max	95.596	96.964	96.751	95.856	95.948	94.607	94.534	94.115	93.859	92.697	90.200
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

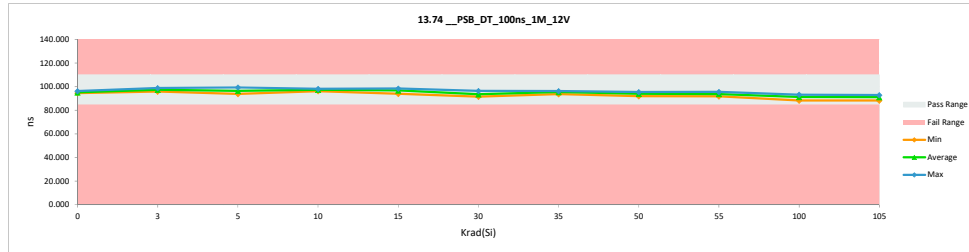


HDR TID Report
TPS7H5001-SP

13.74 PSB DT 100ns 1M 12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	110	110		
Min Limit	85	85		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	94.512	94.621	0.109
0	118	96.195	96.286	0.091
0	122	94.619	94.634	0.015
3	301	98.563	98.494	-0.069
3	306	99.150	98.858	-0.292
3	307	96.426	96.159	-0.267
3	308	96.083	95.951	-0.132
3	309	97.509	97.424	-0.085
5	310	96.100	95.769	-0.331
5	311	99.507	99.386	-0.121
5	312	99.063	98.775	-0.288
5	314	94.195	93.885	-0.310
5	316	94.435	94.182	-0.253
10	317	98.937	98.209	-0.728
10	318	98.267	97.908	-0.359
10	319	98.413	97.475	-0.938
10	321	98.100	97.221	-0.879
10	322	97.089	96.239	-0.850
15	323	98.187	97.188	-0.999
15	325	95.051	94.021	-1.030
15	326	99.294	98.511	-0.783
15	328	98.770	97.919	-0.851
15	333	97.342	96.467	-0.875
30	334	93.839	91.589	-2.250
30	335	95.436	93.302	-2.134
30	337	94.869	92.840	-2.029
30	339	96.041	93.893	-2.148
30	341	98.505	96.407	-2.098
35	342	95.974	93.788	-2.186
35	345	97.818	95.561	-2.257
35	346	98.707	96.186	-2.521
35	347	97.990	95.746	-2.244
35	348	98.588	96.299	-2.289
50	349	96.634	93.152	-3.482
50	350	99.421	95.463	-3.958
50	351	95.845	92.010	-3.835
50	353	99.238	95.456	-3.782
50	354	97.282	93.800	-3.482
55	355	97.418	93.362	-4.056
55	357	99.328	95.600	-3.728
55	359	97.351	93.641	-3.710
55	362	98.452	94.571	-3.881
55	363	95.547	91.800	-3.747
100	1	98.155	92.292	-5.863
100	3	95.751	89.672	-6.079
100	4	95.782	89.632	-6.150
100	5	99.011	92.775	-6.236
100	6	97.439	91.936	-5.503
100	7	94.259	88.572	-5.687
100	8	98.785	92.309	-6.476
100	9	94.646	88.998	-5.648
100	10	96.538	90.881	-5.657
100	11	98.210	92.529	-5.681
100	12	97.745	92.426	-5.319
100	13	97.686	92.397	-5.289
100	14	97.885	92.247	-5.638
100	15	99.167	92.984	-6.183
100	16	96.914	90.947	-5.967
100	17	95.555	89.730	-5.825
100	18	98.732	93.270	-5.462
100	19	93.044	88.288	-4.756
100	20	97.224	90.904	-6.320
100	21	99.366	93.205	-6.161
100	22	97.899	91.900	-5.999
100	24	97.363	91.750	-5.613
105	25	97.595	90.175	-7.420
105	26	94.176	88.319	-5.857
105	27	97.687	91.333	-6.354
105	28	98.858	92.864	-5.994
105	29	99.148	92.969	-6.179
Max		99.507	99.386	0.109
Average		97.210	93.934	-3.277
Min		93.044	88.288	-7.420
Std Dev		1.675	2.810	2.389

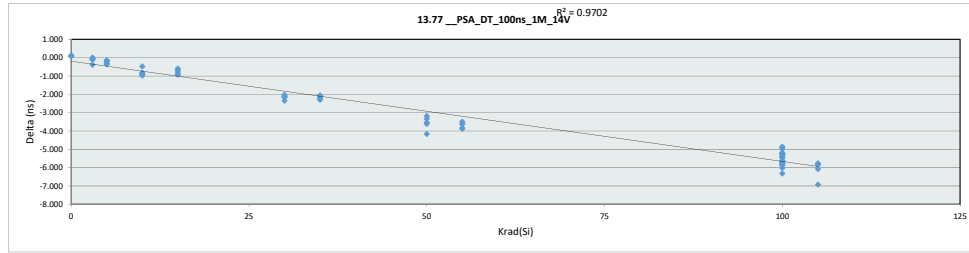


13.74 PSB DT 100ns 1M 12V											
Test Site											
Tester											
Test Number											
Max Limit	110	ns									
Min Limit	85	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.621	95.951	93.885	96.239	94.021	91.589	93.788	92.010	91.800	88.288	88.319
Average	95.180	97.377	96.399	97.410	96.821	93.606	95.516	93.976	93.795	91.347	91.132
Max	96.286	98.858	99.386	98.209	98.511	96.407	96.299	95.463	95.600	93.270	92.969
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

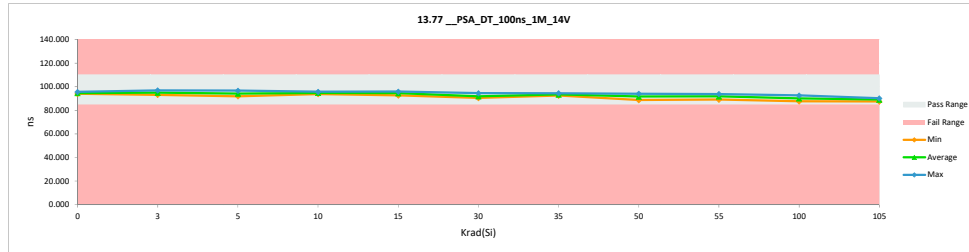


HDR TID Report
TPS7H5001-SP

13.77 PSA_DT_100ns_1M_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	110	110		
Min Limit	85	85		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	93.874	94.011	0.137
0	118	95.530	95.617	0.087
0	122	94.195	94.295	0.100
3	301	95.436	95.459	0.023
3	306	96.986	96.912	-0.074
3	307	93.768	93.675	-0.093
3	308	93.198	93.162	-0.036
3	309	95.999	95.629	-0.370
5	310	93.898	93.559	-0.339
5	311	96.617	96.453	-0.164
5	312	96.855	96.723	-0.132
5	314	92.239	91.954	-0.285
5	316	92.675	92.501	-0.174
10	317	95.205	94.340	-0.865
10	318	96.290	95.820	-0.470
10	319	94.773	93.801	-0.972
10	321	96.730	95.809	-0.921
10	322	94.959	94.148	-0.811
15	323	96.812	95.926	-0.886
15	325	93.457	92.690	-0.767
15	326	95.271	94.608	-0.663
15	328	95.137	94.553	-0.584
15	333	95.935	95.026	-0.909
30	334	94.168	91.828	-2.340
30	335	92.642	90.483	-2.159
30	337	92.586	90.502	-2.084
30	339	94.148	92.131	-2.017
30	341	96.682	94.552	-2.130
35	342	94.586	92.536	-2.050
35	345	95.402	93.120	-2.282
35	346	95.512	93.270	-2.242
35	347	96.064	93.992	-2.072
35	348	96.694	94.531	-2.163
50	349	95.067	91.545	-3.522
50	350	98.262	94.108	-4.154
50	351	91.949	88.770	-3.179
50	353	94.523	90.917	-3.606
50	354	96.603	93.280	-3.323
55	355	94.869	91.384	-3.485
55	357	96.294	92.694	-3.590
55	359	97.674	93.847	-3.827
55	362	95.708	92.090	-3.618
55	363	93.045	89.177	-3.868
100	1	96.214	90.937	-5.277
100	3	93.797	87.934	-5.863
100	4	94.396	88.661	-5.735
100	5	97.848	92.087	-5.761
100	6	96.478	91.094	-5.384
100	7	93.075	88.096	-4.979
100	8	96.616	90.841	-5.775
100	9	92.823	87.641	-5.182
100	10	95.514	90.322	-5.192
100	11	97.924	92.654	-5.270
100	12	95.801	90.917	-4.884
100	13	96.949	91.279	-5.670
100	14	96.998	91.728	-5.270
100	15	95.282	89.269	-6.013
100	16	97.368	91.908	-5.450
100	17	94.363	88.572	-5.791
100	18	97.402	91.978	-5.424
100	19	93.057	88.208	-4.849
100	20	95.993	89.684	-6.309
100	21	95.303	89.659	-5.644
100	22	95.734	90.139	-5.595
100	24	94.700	89.466	-5.234
105	25	94.708	87.796	-6.912
105	26	93.566	87.503	-6.063
105	27	96.061	90.231	-5.830
105	28	95.632	89.817	-5.815
105	29	95.825	90.071	-5.754
Max		98.262	96.912	0.137
Average		95.253	92.142	-3.111
Min		91.949	87.503	-6.912
Std Dev		1.518	2.493	2.272

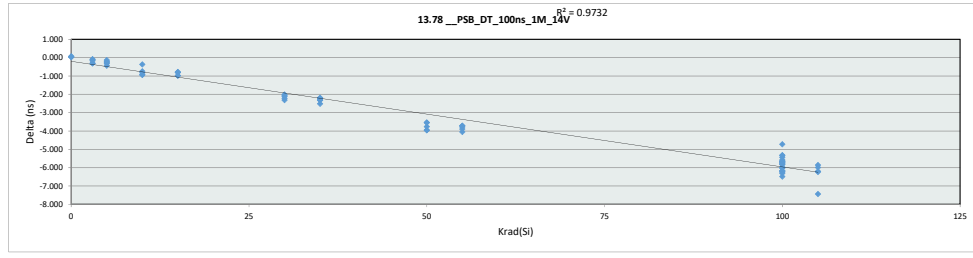


13.77 PSA_DT_100ns_1M_14V											
Test Site											
Tester											
Test Number											
Max Limit	110	ns									
Min Limit	85	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.011	93.162	91.954	93.801	92.690	90.483	92.536	88.770	89.177	87.641	87.503
Average	94.641	94.967	94.238	94.784	94.561	91.899	93.490	91.724	91.838	90.140	89.084
Max	95.617	96.912	96.723	95.820	95.926	94.552	94.531	94.108	93.847	92.654	90.231
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

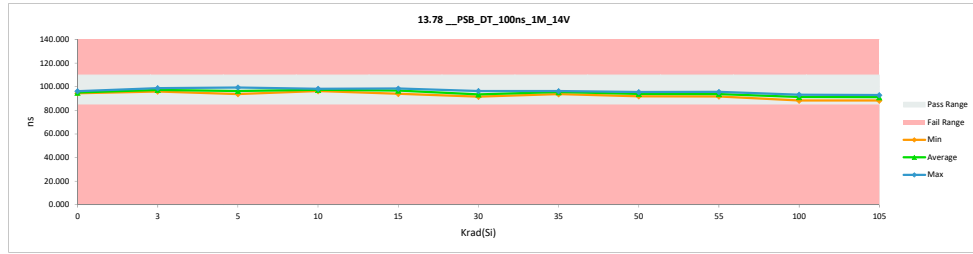


HDR TID Report
TPS7H5001-SP

13.78 PSB_DT_100ns_1M_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	110	110		
Min Limit	85	85		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	94.463	94.527	0.064
0	118	96.229	96.265	0.036
0	122	94.543	94.628	0.085
3	301	98.607	98.450	-0.157
3	306	99.161	98.865	-0.296
3	307	96.378	96.085	-0.293
3	308	96.046	95.981	-0.065
3	309	97.539	97.433	-0.106
5	310	96.147	95.722	-0.425
5	311	99.554	99.355	-0.199
5	312	99.044	98.791	-0.253
5	314	94.174	93.864	-0.310
5	316	94.341	94.216	-0.125
10	317	99.018	98.284	-0.734
10	318	98.250	97.894	-0.356
10	319	98.420	97.499	-0.921
10	321	98.073	97.150	-0.923
10	322	97.085	96.277	-0.808
15	323	98.176	97.206	-0.970
15	325	95.023	94.058	-0.965
15	326	99.300	98.504	-0.796
15	328	98.774	97.981	-0.793
15	333	97.280	96.511	-0.769
30	334	93.899	91.594	-2.305
30	335	95.455	93.247	-2.208
30	337	94.860	92.764	-2.096
30	339	95.911	93.913	-1.998
30	341	98.505	96.441	-2.064
35	342	96.065	93.754	-2.311
35	345	97.781	95.552	-2.229
35	346	98.681	96.179	-2.502
35	347	97.972	95.795	-2.177
35	348	98.621	96.327	-2.294
50	349	96.687	93.155	-3.532
50	350	99.458	95.514	-3.944
50	351	95.882	91.941	-3.941
50	353	99.194	95.438	-3.756
50	354	97.313	93.786	-3.527
55	355	97.394	93.356	-4.038
55	357	99.367	95.658	-3.709
55	359	97.387	93.617	-3.770
55	362	98.418	94.532	-3.886
55	363	95.503	91.801	-3.702
100	1	98.154	92.327	-5.827
100	3	95.686	89.675	-6.011
100	4	95.752	89.593	-6.159
100	5	98.991	92.807	-6.184
100	6	97.473	91.907	-5.566
100	7	94.343	88.583	-5.760
100	8	98.748	92.273	-6.475
100	9	94.626	88.967	-5.659
100	10	96.569	90.887	-5.682
100	11	98.199	92.473	-5.726
100	12	97.705	92.399	-5.306
100	13	97.746	92.415	-5.331
100	14	97.907	92.289	-5.618
100	15	99.178	92.946	-6.232
100	16	96.905	90.947	-5.958
100	17	95.580	89.775	-5.805
100	18	98.704	93.277	-5.427
100	19	92.989	88.273	-4.716
100	20	97.176	90.877	-6.299
100	21	99.327	93.152	-6.175
100	22	97.890	91.905	-5.985
100	24	97.421	91.738	-5.683
105	25	97.630	90.199	-7.431
105	26	94.195	88.348	-5.847
105	27	97.669	91.442	-6.227
105	28	98.871	92.892	-5.979
105	29	99.147	92.954	-6.193
	Max	99.554	99.355	0.085
	Average	97.208	93.932	-3.276
	Min	92.989	88.273	-7.431
	Std Dev	1.683	2.813	2.390

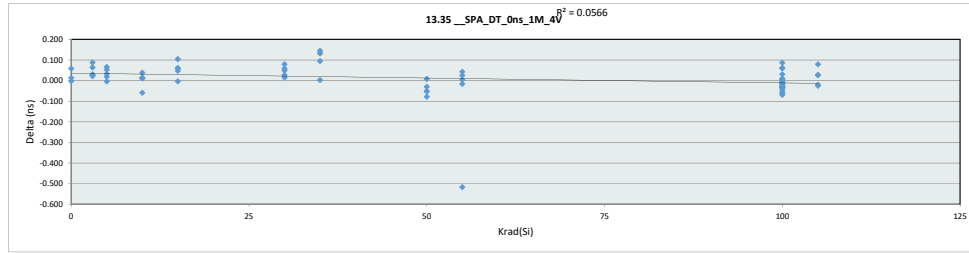


13.78 PSB_DT_100ns_1M_14V											
Test Site											
Tester											
Test Number											
Max Limit	110	ns									
Min Limit	85	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.527	95.981	93.864	96.277	94.058	91.594	93.754	91.941	91.801	88.273	88.348
Average	95.140	97.363	96.390	97.421	96.852	93.592	95.521	93.967	93.793	91.340	91.167
Max	96.265	98.865	99.355	98.284	98.504	96.441	96.327	95.514	95.658	93.277	92.954
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

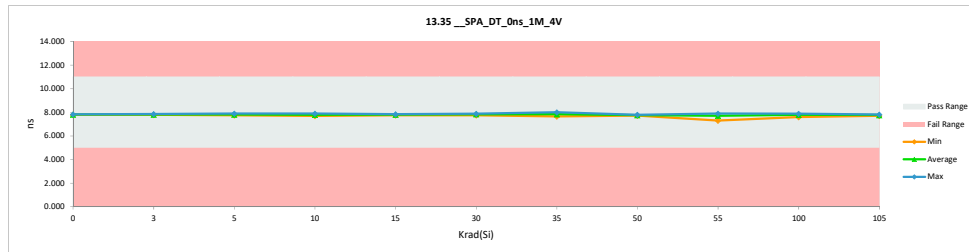


HDR TID Report
TPS7H5001-SP

13.35 SPA_DT_Ons_1M_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	7.810	7.824	0.014
0	118	7.789	7.788	-0.001
0	122	7.756	7.815	0.059
3	301	7.778	7.805	0.027
3	306	7.784	7.815	0.031
3	307	7.697	7.786	0.089
3	308	7.774	7.797	0.023
3	309	7.793	7.858	0.065
5	310	7.875	7.895	0.020
5	311	7.770	7.767	-0.003
5	312	7.785	7.852	0.067
5	314	7.793	7.827	0.034
5	316	7.688	7.742	0.054
10	317	7.760	7.702	-0.058
10	318	7.670	7.685	0.015
10	319	7.840	7.853	0.013
10	321	7.854	7.893	0.039
10	322	7.797	7.809	0.012
15	323	7.741	7.789	0.048
15	325	7.784	7.846	0.062
15	326	7.722	7.827	0.105
15	328	7.681	7.740	0.059
15	333	7.816	7.813	-0.003
30	334	7.796	7.846	0.050
30	335	7.870	7.886	0.016
30	337	7.721	7.749	0.028
30	339	7.765	7.825	0.060
30	341	7.794	7.874	0.080
35	342	7.669	7.803	0.134
35	345	7.876	7.879	0.003
35	346	7.700	7.846	0.146
35	347	7.869	8.002	0.133
35	348	7.553	7.649	0.096
50	349	7.743	7.752	0.009
50	350	7.823	7.772	-0.051
50	351	7.844	7.793	-0.051
50	353	7.793	7.716	-0.077
50	354	7.819	7.790	-0.029
55	355	7.886	7.891	0.005
55	357	7.816	7.300	-0.516
55	359	7.707	7.733	0.026
55	362	7.777	7.821	0.044
55	363	7.793	7.778	-0.015
100	1	7.758	7.715	-0.043
100	3	7.847	7.840	-0.007
100	4	7.746	7.833	0.087
100	5	7.624	7.597	-0.027
100	6	7.823	7.793	-0.030
100	7	7.724	7.735	0.011
100	8	7.777	7.761	-0.016
100	9	7.711	7.718	0.007
100	10	7.875	7.840	-0.035
100	11	7.776	7.774	-0.002
100	12	7.817	7.805	-0.012
100	13	7.822	7.853	0.031
100	14	7.829	7.833	0.004
100	15	7.767	7.827	0.060
100	16	7.888	7.833	-0.055
100	17	7.797	7.776	-0.021
100	18	7.866	7.838	-0.028
100	19	7.723	7.655	-0.068
100	20	7.916	7.883	-0.033
100	21	7.836	7.772	-0.064
100	22	7.811	7.874	0.063
100	24	7.780	7.771	-0.009
105	25	7.762	7.789	0.027
105	26	7.675	7.704	0.029
105	27	7.719	7.799	0.080
105	28	7.834	7.815	-0.019
105	29	7.745	7.721	-0.024
	Max	7.916	8.002	0.146
	Average	7.780	7.791	0.011
	Min	7.553	7.300	-0.516
	Std Dev	0.067	0.089	0.081

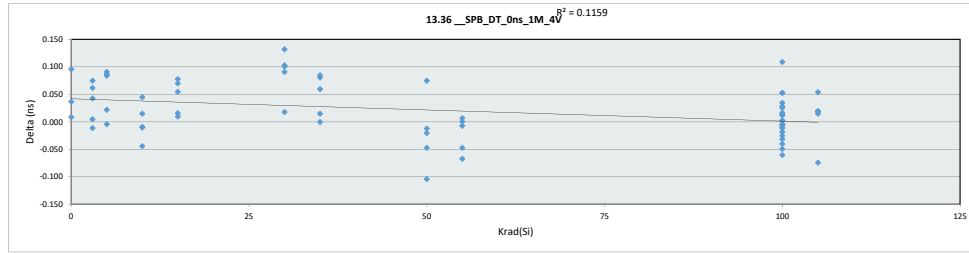


13.35 SPA_DT_Ons_1M_4V											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.788	7.786	7.742	7.685	7.740	7.749	7.649	7.716	7.300	7.597	7.704
Average	7.809	7.812	7.817	7.788	7.803	7.836	7.836	7.765	7.705	7.788	7.766
Max	7.824	7.858	7.895	7.893	7.846	7.886	8.002	7.793	7.891	7.883	7.815
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

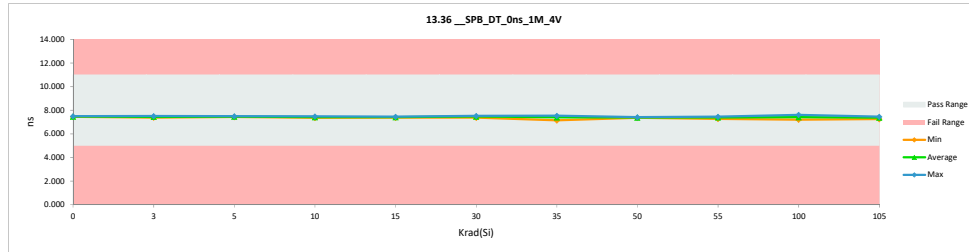


HDR TID Report
TPS7H5001-SP

13.36 SPB_DT_Ons_1M_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	7.425	7.462	0.037
0	118	7.405	7.501	0.096
0	122	7.448	7.457	0.009
3	301	7.388	7.450	0.062
3	306	7.400	7.443	0.043
3	307	7.410	7.415	0.005
3	308	7.398	7.387	-0.011
3	309	7.436	7.511	0.075
5	310	7.441	7.437	-0.004
5	311	7.359	7.450	0.091
5	312	7.485	7.507	0.022
5	314	7.406	7.493	0.087
5	316	7.373	7.457	0.084
10	317	7.397	7.388	-0.009
10	318	7.324	7.369	0.045
10	319	7.454	7.444	-0.010
10	321	7.469	7.484	0.015
10	322	7.486	7.442	-0.044
15	323	7.390	7.406	0.016
15	325	7.366	7.376	0.010
15	326	7.370	7.448	0.078
15	328	7.327	7.397	0.070
15	333	7.402	7.457	0.055
30	334	7.391	7.482	0.091
30	335	7.397	7.529	0.132
30	337	7.358	7.376	0.018
30	339	7.363	7.466	0.103
30	341	7.399	7.500	0.101
35	342	7.333	7.414	0.081
35	345	7.479	7.479	0.000
35	346	7.337	7.422	0.085
35	347	7.487	7.547	0.060
35	348	7.138	7.153	0.015
50	349	7.396	7.384	-0.012
50	350	7.474	7.370	-0.104
50	351	7.445	7.425	-0.020
50	353	7.326	7.401	0.075
50	354	7.453	7.406	-0.047
55	355	7.419	7.420	0.001
55	357	7.457	7.464	0.007
55	359	7.346	7.279	-0.067
55	362	7.430	7.423	-0.007
55	363	7.470	7.423	-0.047
100	1	7.366	7.419	0.053
100	3	7.483	7.434	-0.049
100	4	7.440	7.466	0.026
100	5	7.230	7.212	-0.018
100	6	7.492	7.494	0.002
100	7	7.300	7.303	0.003
100	8	7.426	7.438	0.012
100	9	7.281	7.276	-0.005
100	10	7.552	7.566	0.014
100	11	7.458	7.433	-0.025
100	12	7.454	7.444	-0.010
100	13	7.510	7.619	0.109
100	14	7.429	7.398	-0.031
100	15	7.427	7.480	0.053
100	16	7.501	7.441	-0.060
100	17	7.402	7.391	-0.011
100	18	7.429	7.389	-0.040
100	19	7.346	7.375	0.029
100	20	7.508	7.503	-0.005
100	21	7.392	7.427	0.035
100	22	7.432	7.427	-0.005
100	24	7.397	7.414	0.017
105	25	7.428	7.448	0.020
105	26	7.312	7.327	0.015
105	27	7.446	7.465	0.019
105	28	7.385	7.439	0.054
105	29	7.336	7.262	-0.074
	Max	7.552	7.619	0.132
	Average	7.406	7.426	0.020
	Min	7.138	7.153	-0.104
	Std Dev	0.068	0.075	0.049

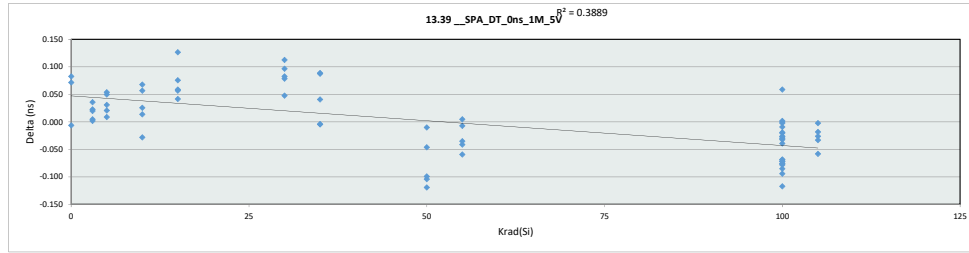


13.36 SPB_DT_Ons_1M_4V											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.457	7.387	7.437	7.369	7.376	7.376	7.153	7.370	7.279	7.212	7.262
Average	7.473	7.441	7.469	7.425	7.417	7.471	7.403	7.397	7.402	7.425	7.388
Max	7.501	7.511	7.507	7.484	7.457	7.529	7.547	7.425	7.464	7.619	7.465
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

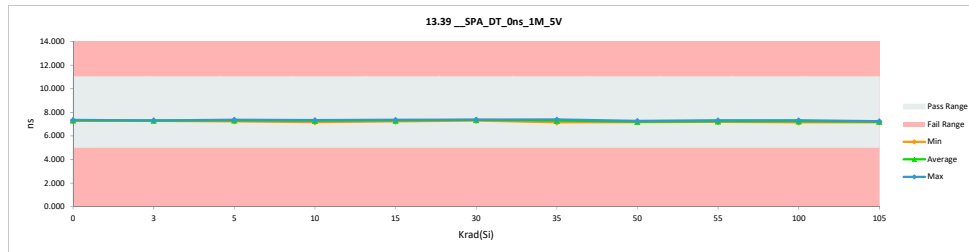


HDR TID Report
TPS7H5001-SP

13.39 SPA_DT_Ons_1M_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	7.285	7.357	0.072
0	118	7.282	7.276	-0.006
0	122	7.248	7.331	0.083
3	301	7.303	7.308	0.005
3	306	7.316	7.318	0.002
3	307	7.285	7.321	0.036
3	308	7.255	7.278	0.023
3	309	7.298	7.318	0.020
5	310	7.343	7.374	0.031
5	311	7.267	7.317	0.050
5	312	7.315	7.369	0.054
5	314	7.296	7.305	0.009
5	316	7.217	7.238	0.021
10	317	7.206	7.274	0.068
10	318	7.192	7.164	-0.028
10	319	7.320	7.334	0.014
10	321	7.318	7.344	0.026
10	322	7.248	7.305	0.057
15	323	7.252	7.309	0.057
15	325	7.244	7.371	0.127
15	326	7.245	7.321	0.076
15	328	7.177	7.236	0.059
15	333	7.307	7.349	0.042
30	334	7.276	7.373	0.097
30	335	7.304	7.387	0.083
30	337	7.187	7.300	0.113
30	339	7.264	7.343	0.079
30	341	7.301	7.349	0.048
35	342	7.214	7.303	0.089
35	345	7.351	7.392	0.041
35	346	7.274	7.270	-0.004
35	347	7.296	7.384	0.088
35	348	7.157	7.153	-0.004
50	349	7.277	7.173	-0.104
50	350	7.321	7.222	-0.099
50	351	7.288	7.169	-0.119
50	353	7.253	7.207	-0.046
50	354	7.281	7.271	-0.010
55	355	7.360	7.325	-0.035
55	357	7.304	7.245	-0.059
55	359	7.239	7.198	-0.041
55	362	7.244	7.249	0.005
55	363	7.242	7.235	-0.007
100	1	7.247	7.245	-0.002
100	3	7.328	7.330	0.002
100	4	7.274	7.254	-0.020
100	5	7.226	7.154	-0.072
100	6	7.334	7.258	-0.076
100	7	7.143	7.202	0.059
100	8	7.328	7.211	-0.117
100	9	7.266	7.172	-0.094
100	10	7.400	7.332	-0.068
100	11	7.219	7.219	0.000
100	12	7.300	7.228	-0.072
100	13	7.305	7.296	-0.009
100	14	7.307	7.231	-0.076
100	15	7.282	7.197	-0.085
100	16	7.356	7.317	-0.039
100	17	7.354	7.276	-0.078
100	18	7.329	7.310	-0.019
100	19	7.237	7.168	-0.069
100	20	7.344	7.317	-0.027
100	21	7.302	7.276	-0.026
100	22	7.274	7.244	-0.030
100	24	7.232	7.200	-0.032
105	25	7.250	7.232	-0.018
105	26	7.225	7.199	-0.026
105	27	7.260	7.227	-0.033
105	28	7.246	7.244	-0.002
105	29	7.217	7.159	-0.058
	Max	7.400	7.392	0.127
	Average	7.275	7.274	-0.001
	Min	7.143	7.153	-0.119
	Std Dev	0.050	0.066	0.059

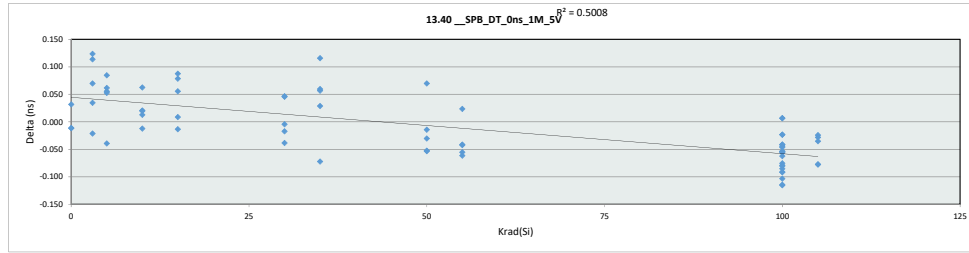


13.39 SPA_DT_Ons_1M_5V											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.276	7.278	7.238	7.164	7.236	7.300	7.153	7.169	7.198	7.154	7.159
Average	7.321	7.309	7.321	7.284	7.317	7.350	7.300	7.208	7.250	7.247	7.212
Max	7.357	7.321	7.374	7.344	7.371	7.387	7.392	7.271	7.325	7.332	7.244
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

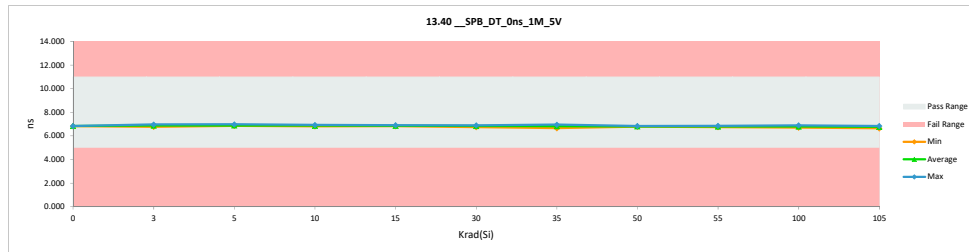


HDR TID Report
TPS7H5001-SP

13.40 SPB_DT_Ons_1M_5V				
Test Site				
Tester				
Test Number				
Unit	ns		ns	
Max Limit	11		11	
Min Limit	5		5	
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	6.864	6.853	-0.011
0	118	6.817	6.849	0.032
0	122	6.847	6.836	-0.011
3	301	6.796	6.920	0.124
3	306	6.803	6.782	-0.021
3	307	6.848	6.918	0.070
3	308	6.826	6.861	0.035
3	309	6.864	6.978	0.114
5	310	6.928	6.889	-0.039
5	311	6.837	6.893	0.056
5	312	6.923	6.985	0.062
5	314	6.848	6.933	0.085
5	316	6.806	6.859	0.053
10	317	6.835	6.823	-0.012
10	318	6.798	6.818	0.020
10	319	6.837	6.858	0.021
10	321	6.872	6.935	0.063
10	322	6.859	6.872	0.013
15	323	6.817	6.873	0.056
15	325	6.840	6.849	0.009
15	326	6.840	6.919	0.079
15	328	6.743	6.831	0.088
15	333	6.879	6.866	-0.013
30	334	6.836	6.882	0.046
30	335	6.874	6.857	-0.017
30	337	6.795	6.757	-0.038
30	339	6.809	6.856	0.047
30	341	6.903	6.899	-0.004
35	342	6.721	6.837	0.116
35	345	6.887	6.916	0.029
35	346	6.853	6.781	-0.072
35	347	6.906	6.963	0.057
35	348	6.606	6.666	0.060
50	349	6.782	6.852	0.070
50	350	6.834	6.804	-0.030
50	351	6.894	6.842	-0.052
50	353	6.811	6.797	-0.014
50	354	6.879	6.826	-0.053
55	355	6.879	6.818	-0.061
55	357	6.844	6.802	-0.042
55	359	6.805	6.750	-0.055
55	362	6.863	6.822	-0.041
55	363	6.837	6.861	0.024
100	1	6.841	6.800	-0.041
100	3	6.898	6.905	0.007
100	4	6.905	6.851	-0.054
100	5	6.767	6.711	-0.056
100	6	6.937	6.822	-0.115
100	7	6.814	6.760	-0.054
100	8	6.876	6.773	-0.103
100	9	6.830	6.739	-0.091
100	10	6.996	6.882	-0.114
100	11	6.853	6.774	-0.079
100	12	6.864	6.841	-0.023
100	13	6.984	6.893	-0.091
100	14	6.857	6.864	0.007
100	15	6.829	6.749	-0.080
100	16	6.892	6.851	-0.041
100	17	6.859	6.815	-0.044
100	18	6.822	6.799	-0.023
100	19	6.771	6.725	-0.046
100	20	6.932	6.857	-0.075
100	21	6.841	6.779	-0.062
100	22	6.867	6.814	-0.053
100	24	6.861	6.776	-0.085
105	25	6.847	6.823	-0.024
105	26	6.725	6.648	-0.077
105	27	6.873	6.796	-0.077
105	28	6.874	6.846	-0.028
105	29	6.781	6.746	-0.035
Max		6.996	6.985	0.124
Average		6.845	6.835	-0.010
Min		6.606	6.648	-0.115
Std Dev		0.059	0.066	0.059

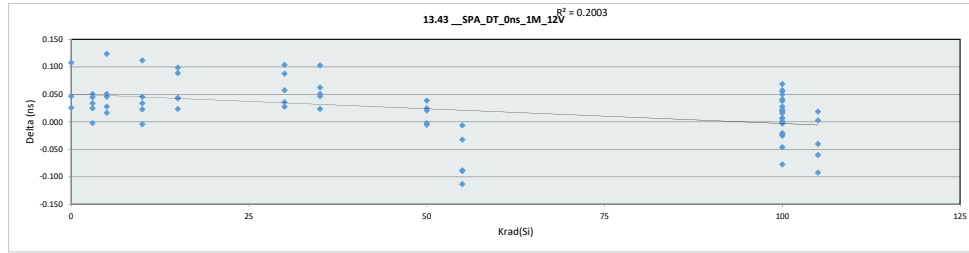


13.40 SPB_DT_Ons_1M_5V											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	6.836	6.782	6.859	6.818	6.831	6.757	6.666	6.797	6.750	6.711	6.648
Average	6.846	6.892	6.912	6.861	6.868	6.850	6.833	6.824	6.811	6.808	6.772
Max	6.853	6.978	6.985	6.935	6.919	6.899	6.963	6.852	6.861	6.905	6.846
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

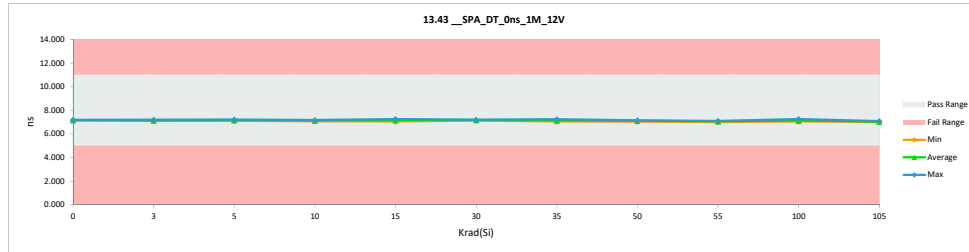


HDR TID Report
TPS7H5001-SP

13.43 SPA_DT_Ons_1M_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	7.139	7.186	0.047
0	118	7.116	7.142	0.026
0	122	7.065	7.173	0.108
3	301	7.090	7.141	0.051
3	306	7.086	7.120	0.034
3	307	7.095	7.120	0.025
3	308	7.108	7.106	-0.002
3	309	7.158	7.203	0.045
5	310	7.183	7.211	0.028
5	311	7.074	7.125	0.051
5	312	7.049	7.173	0.124
5	314	7.151	7.168	0.017
5	316	7.058	7.104	0.046
10	317	7.057	7.169	0.112
10	318	7.049	7.072	0.023
10	319	7.160	7.156	-0.004
10	321	7.105	7.139	0.034
10	322	7.094	7.140	0.046
15	323	7.117	7.161	0.044
15	325	7.154	7.253	0.099
15	326	7.086	7.175	0.089
15	328	7.027	7.051	0.024
15	333	7.149	7.192	0.043
30	334	7.115	7.203	0.088
30	335	7.096	7.124	0.028
30	337	7.119	7.177	0.058
30	339	7.089	7.193	0.104
30	341	7.164	7.200	0.036
35	342	7.060	7.111	0.051
35	345	7.166	7.190	0.024
35	346	7.093	7.140	0.047
35	347	7.138	7.241	0.103
35	348	6.993	7.056	0.063
50	349	7.041	7.039	-0.002
50	350	7.129	7.124	-0.005
50	351	7.113	7.152	0.039
50	353	7.086	7.111	0.025
50	354	7.103	7.124	0.021
55	355	7.188	7.099	-0.089
55	357	7.094	7.088	-0.006
55	359	7.114	7.001	-0.113
55	362	7.117	7.085	-0.032
55	363	7.134	7.046	-0.088
100	1	7.122	7.123	0.001
100	3	7.128	7.197	0.069
100	4	7.138	7.113	-0.025
100	5	7.080	7.096	0.016
100	6	7.151	7.179	0.028
100	7	7.035	7.073	0.038
100	8	7.112	7.113	0.001
100	9	7.171	7.125	-0.046
100	10	7.204	7.262	0.058
100	11	7.073	7.095	0.022
100	12	7.161	7.141	-0.020
100	13	7.116	7.138	0.022
100	14	7.195	7.172	-0.023
100	15	7.051	7.107	0.056
100	16	7.191	7.190	-0.001
100	17	7.140	7.063	-0.077
100	18	7.128	7.125	-0.003
100	19	7.033	7.052	0.019
100	20	7.192	7.199	0.007
100	21	7.149	7.156	0.007
100	22	7.096	7.137	0.041
100	24	7.041	7.091	0.050
105	25	7.011	7.014	0.003
105	26	7.019	7.038	0.019
105	27	7.125	7.085	-0.040
105	28	7.157	7.065	-0.092
105	29	7.083	7.023	-0.060
	Max	7.204	7.262	0.124
	Average	7.109	7.131	0.022
	Min	6.993	7.001	-0.113
	Std Dev	0.049	0.057	0.049

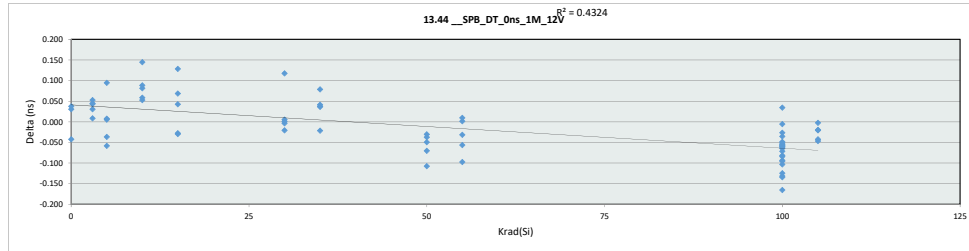


13.43 SPA_DT_Ons_1M_12V											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.142	7.106	7.104	7.072	7.051	7.124	7.056	7.039	7.001	7.052	7.014
Average	7.167	7.138	7.156	7.135	7.166	7.179	7.148	7.110	7.064	7.134	7.045
Max	7.186	7.203	7.211	7.169	7.253	7.203	7.241	7.152	7.099	7.262	7.085
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

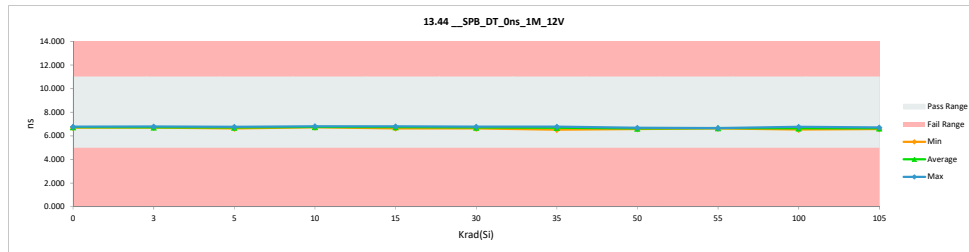


HDR TID Report
TPS7H5001-SP

13.44 SPB_DT_Ons_1M_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	6.749	6.780	0.031
0	118	6.747	6.705	-0.042
0	122	6.701	6.739	0.038
3	301	6.679	6.710	0.031
3	306	6.656	6.701	0.045
3	307	6.704	6.748	0.044
3	308	6.666	6.719	0.053
3	309	6.784	6.793	0.009
5	310	6.734	6.742	0.008
5	311	6.691	6.697	0.006
5	312	6.672	6.767	0.095
5	314	6.781	6.745	-0.036
5	316	6.693	6.635	-0.058
10	317	6.642	6.731	0.089
10	318	6.646	6.728	0.082
10	319	6.673	6.818	0.145
10	321	6.721	6.780	0.059
10	322	6.683	6.736	0.053
15	323	6.703	6.746	0.043
15	325	6.707	6.776	0.069
15	326	6.791	6.762	-0.029
15	328	6.668	6.641	-0.027
15	333	6.687	6.816	0.129
30	334	6.738	6.744	0.006
30	335	6.663	6.781	0.118
30	337	6.634	6.631	-0.003
30	339	6.706	6.707	0.001
30	341	6.757	6.737	-0.020
35	342	6.652	6.631	-0.021
35	345	6.698	6.740	0.042
35	346	6.666	6.745	0.079
35	347	6.747	6.785	0.038
35	348	6.495	6.532	0.037
50	349	6.684	6.577	-0.107
50	350	6.706	6.636	-0.070
50	351	6.724	6.687	-0.037
50	353	6.670	6.621	-0.049
50	354	6.698	6.668	-0.030
55	355	6.730	6.633	-0.097
55	357	6.724	6.668	-0.056
55	359	6.652	6.662	0.010
55	362	6.665	6.667	0.002
55	363	6.685	6.654	-0.031
100	1	6.661	6.613	-0.048
100	3	6.790	6.697	-0.093
100	4	6.739	6.683	-0.056
100	5	6.672	6.618	-0.054
100	6	6.731	6.672	-0.059
100	7	6.641	6.557	-0.084
100	8	6.763	6.632	-0.131
100	9	6.738	6.604	-0.134
100	10	6.828	6.767	-0.061
100	11	6.706	6.642	-0.064
100	12	6.738	6.657	-0.081
100	13	6.795	6.700	-0.095
100	14	6.730	6.695	-0.035
100	15	6.638	6.673	0.035
100	16	6.742	6.684	-0.058
100	17	6.756	6.591	-0.165
100	18	6.660	6.589	-0.071
100	19	6.665	6.541	-0.124
100	20	6.804	6.753	-0.051
100	21	6.667	6.641	-0.026
100	22	6.734	6.631	-0.103
100	24	6.637	6.632	-0.005
105	25	6.669	6.650	-0.019
105	26	6.602	6.582	-0.020
105	27	6.757	6.715	-0.042
105	28	6.682	6.680	-0.002
105	29	6.682	6.636	-0.046
Max		6.828	6.818	0.145
Average		6.701	6.687	-0.015
Min		6.495	6.532	-0.165
Std Dev		0.054	0.067	0.065

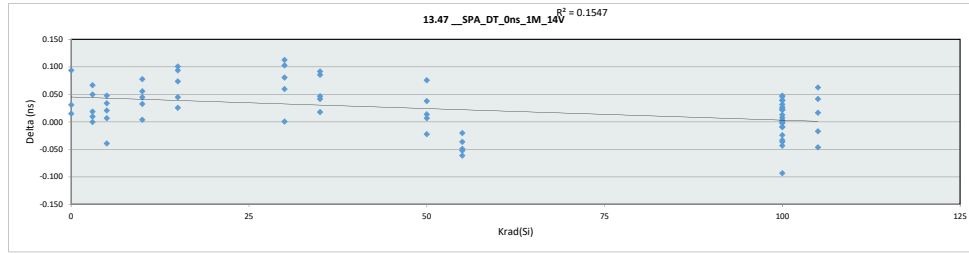


13.44 SPB_DT_Ons_1M_12V											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	6.705	6.701	6.635	6.728	6.641	6.631	6.532	6.577	6.633	6.541	6.582
Average	6.741	6.734	6.717	6.759	6.748	6.720	6.687	6.638	6.657	6.649	6.653
Max	6.780	6.793	6.767	6.818	6.816	6.781	6.785	6.687	6.668	6.767	6.715
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

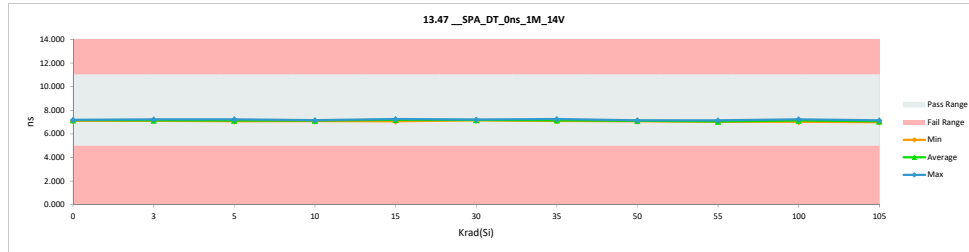


HDR TID Report
TPS7H5001-SP

13.47 SPA_DT_Ons_1M_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	7.144	7.175	0.031
0	118	7.099	7.114	0.015
0	122	7.100	7.194	0.094
3	301	7.133	7.143	0.010
3	306	7.077	7.127	0.050
3	307	7.075	7.142	0.067
3	308	7.117	7.117	0.000
3	309	7.210	7.229	0.019
5	310	7.206	7.240	0.034
5	311	7.068	7.116	0.048
5	312	7.106	7.113	0.007
5	314	7.145	7.166	0.021
5	316	7.105	7.066	-0.039
10	317	7.046	7.124	0.078
10	318	7.026	7.082	0.056
10	319	7.160	7.164	0.004
10	321	7.100	7.145	0.045
10	322	7.131	7.164	0.033
15	323	7.124	7.198	0.074
15	325	7.160	7.261	0.101
15	326	7.181	7.207	0.026
15	328	7.025	7.070	0.045
15	333	7.106	7.200	0.094
30	334	7.066	7.169	0.103
30	335	7.109	7.169	0.060
30	337	7.085	7.166	0.081
30	339	7.146	7.147	0.001
30	341	7.110	7.223	0.113
35	342	7.071	7.118	0.047
35	345	7.155	7.173	0.018
35	346	7.054	7.140	0.086
35	347	7.169	7.261	0.092
35	348	7.059	7.101	0.042
50	349	7.002	7.078	0.076
50	350	7.139	7.153	0.014
50	351	7.101	7.139	0.038
50	353	7.141	7.119	-0.022
50	354	7.088	7.095	0.007
55	355	7.202	7.153	-0.049
55	357	7.085	7.049	-0.036
55	359	7.092	7.040	-0.052
55	362	7.093	7.032	-0.061
55	363	7.046	7.026	-0.020
100	1	7.134	7.132	-0.002
100	3	7.174	7.198	0.024
100	4	7.123	7.145	0.022
100	5	7.070	7.072	0.002
100	6	7.170	7.077	-0.093
100	7	7.039	7.061	0.022
100	8	7.152	7.151	-0.001
100	9	7.161	7.128	-0.033
100	10	7.215	7.223	0.008
100	11	7.074	7.065	-0.009
100	12	7.145	7.136	-0.009
100	13	7.121	7.148	0.027
100	14	7.204	7.180	-0.024
100	15	7.079	7.119	0.040
100	16	7.225	7.238	0.013
100	17	7.139	7.103	-0.036
100	18	7.143	7.146	0.003
100	19	7.040	7.072	0.032
100	20	7.152	7.200	0.048
100	21	7.138	7.184	0.046
100	22	7.100	7.139	0.039
100	24	7.076	7.033	-0.043
105	25	6.989	7.031	0.042
105	26	7.003	7.066	0.063
105	27	7.134	7.151	0.017
105	28	7.128	7.111	-0.017
105	29	7.037	6.991	-0.046
	Max	7.225	7.261	0.113
	Average	7.112	7.134	0.023
	Min	6.989	6.991	-0.093
	Std Dev	0.054	0.060	0.044

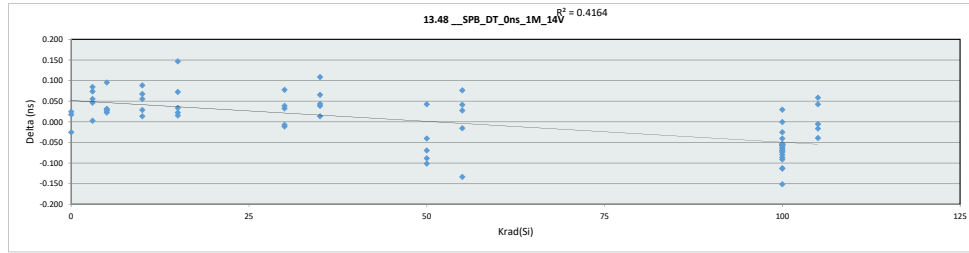


13.47 SPA_DT_Ons_1M_14V											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.114	7.117	7.066	7.082	7.070	7.147	7.101	7.078	7.026	7.033	6.991
Average	7.161	7.152	7.140	7.136	7.187	7.175	7.159	7.117	7.060	7.134	7.070
Max	7.194	7.229	7.240	7.164	7.261	7.223	7.261	7.153	7.153	7.238	7.151
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

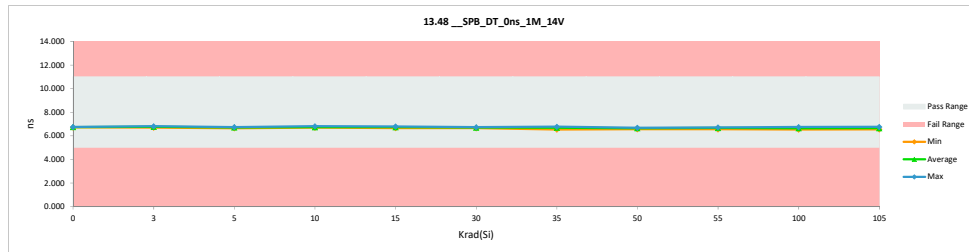


HDR TID Report
TPS7H5001-SP

13.48 SPB DT Ons 1M 14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	6.790	6.765	-0.025
0	118	6.732	6.757	0.025
0	122	6.708	6.726	0.018
3	301	6.681	6.755	0.074
3	306	6.670	6.717	0.047
3	307	6.732	6.817	0.085
3	308	6.713	6.716	0.003
3	309	6.774	6.830	0.056
5	310	6.699	6.731	0.032
5	311	6.634	6.730	0.096
5	312	6.724	6.755	0.031
5	314	6.714	6.741	0.027
5	316	6.634	6.657	0.023
10	317	6.641	6.709	0.068
10	318	6.680	6.709	0.029
10	319	6.737	6.826	0.089
10	321	6.704	6.718	0.014
10	322	6.713	6.769	0.056
15	323	6.694	6.767	0.073
15	325	6.701	6.735	0.034
15	326	6.663	6.810	0.147
15	328	6.650	6.673	0.023
15	333	6.720	6.736	0.016
30	334	6.702	6.741	0.039
30	335	6.702	6.735	0.033
30	337	6.674	6.667	-0.007
30	339	6.726	6.715	-0.011
30	341	6.667	6.745	0.078
35	342	6.589	6.698	0.109
35	345	6.774	6.788	0.014
35	346	6.656	6.695	0.039
35	347	6.697	6.763	0.066
35	348	6.508	6.552	0.044
50	349	6.665	6.577	-0.088
50	350	6.653	6.696	0.043
50	351	6.736	6.696	-0.040
50	353	6.753	6.652	-0.101
50	354	6.736	6.667	-0.069
55	355	6.693	6.721	0.028
55	357	6.675	6.660	-0.015
55	359	6.703	6.570	-0.133
55	362	6.652	6.729	0.077
55	363	6.668	6.710	0.042
100	1	6.708	6.654	-0.054
100	3	6.783	6.722	-0.061
100	4	6.737	6.668	-0.069
100	5	6.645	6.575	-0.070
100	6	6.731	6.761	0.030
100	7	6.659	6.547	-0.112
100	8	6.743	6.592	-0.151
100	9	6.657	6.601	-0.056
100	10	6.829	6.743	-0.086
100	11	6.722	6.667	-0.055
100	12	6.712	6.633	-0.079
100	13	6.746	6.655	-0.091
100	14	6.753	6.680	-0.073
100	15	6.676	6.636	-0.040
100	16	6.782	6.726	-0.056
100	17	6.713	6.600	-0.113
100	18	6.677	6.621	-0.056
100	19	6.641	6.588	-0.053
100	20	6.758	6.758	0.000
100	21	6.748	6.683	-0.065
100	22	6.722	6.668	-0.054
100	24	6.646	6.621	-0.025
105	25	6.662	6.646	-0.016
105	26	6.602	6.563	-0.039
105	27	6.733	6.776	0.043
105	28	6.680	6.675	-0.005
105	29	6.613	6.672	0.059
	Max	6.829	6.830	0.147
	Average	6.697	6.695	-0.002
	Min	6.508	6.547	-0.151
	Std Dev	0.052	0.068	0.064

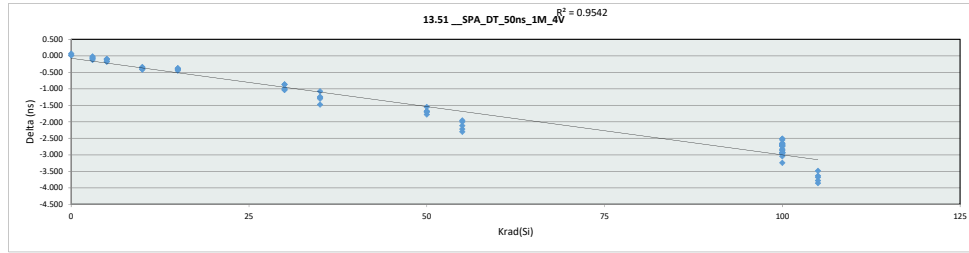


13.48 SPB DT Ons 1M 14V											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	6.726	6.716	6.657	6.709	6.673	6.667	6.552	6.577	6.570	6.547	6.563
Average	6.749	6.767	6.723	6.746	6.744	6.721	6.699	6.658	6.678	6.655	6.666
Max	6.765	6.830	6.755	6.826	6.810	6.745	6.788	6.696	6.729	6.761	6.776
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

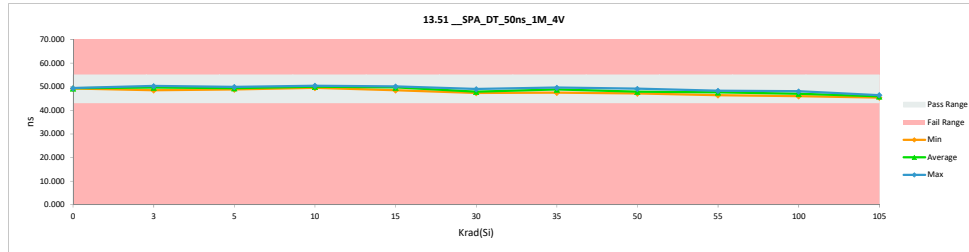


HDR TID Report
TPS7H5001-SP

13.51 SPA DT 50ns 1M 4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	55	55		
Min Limit	43	43		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	49.326	49.401	0.075
0	118	49.221	49.240	0.019
0	122	49.318	49.351	0.033
3	301	49.714	49.648	-0.066
3	306	50.072	50.061	-0.011
3	307	50.312	50.274	-0.038
3	308	48.586	48.501	-0.085
3	309	49.702	49.582	-0.120
5	310	49.564	49.476	-0.088
5	311	49.922	49.808	-0.114
5	312	49.971	49.882	-0.089
5	314	49.006	48.836	-0.170
5	316	49.026	48.866	-0.160
10	317	50.386	50.030	-0.356
10	318	50.096	49.767	-0.329
10	319	50.833	50.425	-0.408
10	321	50.567	50.166	-0.401
10	322	49.878	49.519	-0.359
15	323	50.319	49.939	-0.380
15	325	48.828	48.462	-0.366
15	326	50.117	49.706	-0.411
15	328	50.566	50.121	-0.445
15	333	50.492	50.097	-0.395
30	334	48.321	47.346	-0.975
30	335	48.821	47.794	-1.027
30	337	48.610	47.587	-1.023
30	339	48.468	47.606	-0.862
30	341	49.856	48.995	-0.861
35	342	48.947	47.478	-1.469
35	345	50.259	49.002	-1.257
35	346	50.177	48.887	-1.290
35	347	50.672	49.602	-1.070
35	348	50.253	49.017	-1.236
50	349	49.207	47.538	-1.669
50	350	49.475	47.800	-1.675
50	351	48.832	47.132	-1.700
50	353	49.538	47.768	-1.770
50	354	50.677	49.132	-1.545
55	355	49.939	47.642	-2.297
55	357	50.252	48.302	-1.950
55	359	50.251	48.037	-2.214
55	362	49.650	47.654	-1.996
55	363	48.472	46.359	-2.113
100	1	49.537	46.611	-2.926
100	3	49.042	46.501	-2.541
100	4	48.638	45.963	-2.675
100	5	49.919	47.273	-2.646
100	6	50.047	47.338	-2.709
100	7	48.952	46.241	-2.711
100	8	49.862	46.824	-3.038
100	9	48.586	46.079	-2.507
100	10	50.008	46.771	-3.237
100	11	50.180	47.259	-2.921
100	12	50.542	48.036	-2.506
100	13	50.424	47.781	-2.643
100	14	50.536	47.564	-2.972
100	15	50.126	47.183	-2.943
100	16	50.707	47.864	-2.843
100	17	49.817	46.954	-2.863
100	18	50.218	47.288	-2.930
100	19	49.052	46.377	-2.675
100	20	50.184	47.364	-2.820
100	21	49.211	46.473	-2.738
100	22	49.797	46.874	-2.923
100	24	49.821	47.087	-2.734
105	25	50.212	46.437	-3.775
105	26	48.885	45.407	-3.478
105	27	49.745	45.893	-3.852
105	28	49.493	45.865	-3.628
105	29	49.621	45.945	-3.676
Max		50.833	50.425	0.075
Average		49.709	48.073	-1.637
Min		48.321	45.407	-3.852
Std Dev		0.658	1.377	1.225

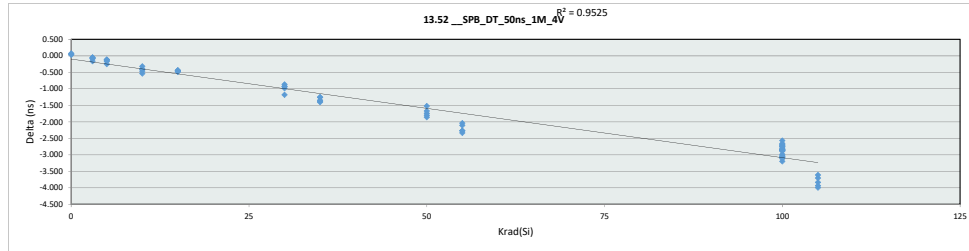


13.51 SPA DT 50ns 1M 4V											
Test Site											
Tester											
Test Number											
Max Limit	55	ns									
Min Limit	43	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	49.240	48.501	48.836	49.519	48.462	47.346	47.478	47.132	46.359	45.963	45.407
Average	49.331	49.613	49.374	49.981	49.665	47.866	48.797	47.874	47.599	46.987	45.909
Max	49.401	50.274	49.882	50.425	50.121	48.995	49.602	49.132	48.302	48.036	46.437
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

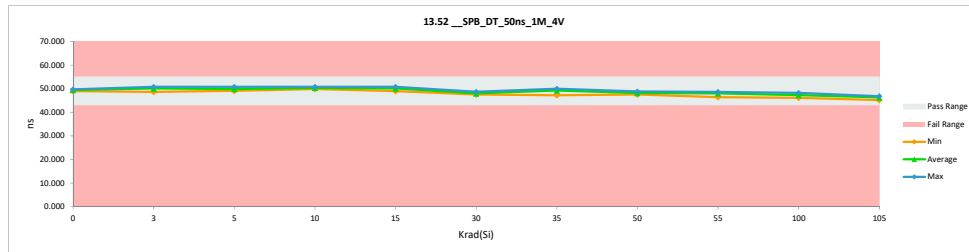


HDR TID Report
TPS7H5001-SP

13.52 SPB DT 50ns 1M 4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	55	55		
Min Limit	43	43		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	49.586	49.666	0.080
0	118	48.878	48.933	0.055
0	122	49.668	49.703	0.035
3	301	50.524	50.486	-0.038
3	306	50.841	50.762	-0.079
3	307	50.616	50.458	-0.158
3	308	48.665	48.620	-0.045
3	309	50.381	50.309	-0.072
5	310	49.449	49.204	-0.245
5	311	50.813	50.711	-0.102
5	312	50.930	50.814	-0.116
5	314	49.367	49.244	-0.123
5	316	49.294	49.132	-0.162
10	317	51.105	50.576	-0.529
10	318	50.490	50.100	-0.390
10	319	51.251	50.793	-0.458
10	321	50.770	50.366	-0.404
10	322	50.194	49.884	-0.310
15	323	50.359	49.892	-0.467
15	325	49.372	48.933	-0.439
15	326	50.786	50.347	-0.439
15	328	51.175	50.692	-0.483
15	333	51.248	50.814	-0.434
30	334	48.722	47.547	-1.175
30	335	49.190	48.214	-0.976
30	337	48.983	48.053	-0.930
30	339	48.430	47.569	-0.861
30	341	49.595	48.677	-0.918
35	342	48.596	47.198	-1.398
35	345	51.294	49.952	-1.342
35	346	50.915	49.544	-1.371
35	347	51.123	49.876	-1.247
35	348	50.697	49.447	-1.250
50	349	49.343	47.546	-1.797
50	350	50.100	48.362	-1.738
50	351	49.329	47.665	-1.664
50	353	50.181	48.328	-1.853
50	354	50.343	48.827	-1.516
55	355	50.929	48.599	-2.330
55	357	50.722	48.618	-2.104
55	359	50.225	47.957	-2.268
55	362	50.601	48.566	-2.035
55	363	48.659	46.399	-2.260
100	1	50.569	47.516	-3.053
100	3	49.410	46.759	-2.651
100	4	49.194	46.354	-2.840
100	5	50.829	48.127	-2.702
100	6	51.048	48.189	-2.859
100	7	49.195	46.336	-2.859
100	8	50.200	47.178	-3.022
100	9	48.711	46.149	-2.562
100	10	50.510	47.317	-3.193
100	11	50.180	47.201	-2.979
100	12	50.280	47.596	-2.684
100	13	50.897	48.155	-2.742
100	14	51.007	47.897	-3.110
100	15	51.207	48.141	-3.066
100	16	50.693	47.889	-2.804
100	17	49.555	46.827	-2.728
100	18	51.061	48.003	-3.058
100	19	48.858	46.101	-2.757
100	20	50.528	47.646	-2.882
100	21	49.707	46.838	-2.869
100	22	50.087	47.253	-2.834
100	24	49.915	47.083	-2.832
105	25	50.775	46.844	-3.931
105	26	48.898	45.198	-3.700
105	27	50.543	46.554	-3.989
105	28	50.057	46.450	-3.607
105	29	50.369	46.540	-3.829
	Max	51.294	50.814	0.080
	Average	50.115	48.422	-1.693
	Min	48.430	45.198	-3.989
	Std Dev	0.817	1.459	1.252

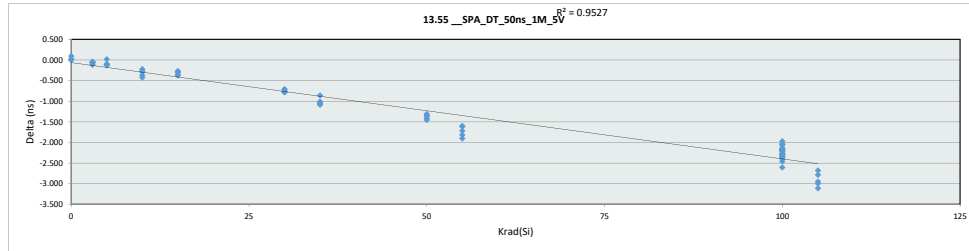


13.52 SPB DT 50ns 1M 4V											
Test Site											
Tester											
Test Number											
Max Limit	55	ns									
Min Limit	43	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	48.933	48.620	49.132	49.884	48.933	47.547	47.198	47.546	46.399	46.101	45.198
Average	49.434	50.127	49.821	50.344	50.136	48.012	49.203	48.146	48.028	47.298	46.317
Max	49.703	50.762	50.814	50.793	50.814	48.677	49.952	48.827	48.618	48.189	46.844
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

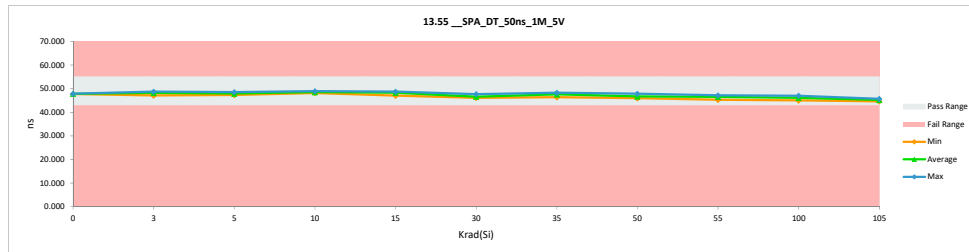


HDR TID Report
TPS7H5001-SP

13.55 SPA DT 50ns 1M 5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	55	55		
Min Limit	43	43		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	47.901	47.902	0.001
0	118	47.739	47.769	0.030
0	122	47.767	47.865	0.098
3	301	48.345	48.298	-0.047
3	306	48.706	48.643	-0.063
3	307	48.902	48.792	-0.110
3	308	47.165	47.127	-0.038
3	309	48.341	48.275	-0.066
5	310	47.981	47.890	-0.091
5	311	48.559	48.439	-0.120
5	312	48.567	48.588	0.021
5	314	47.481	47.361	-0.120
5	316	47.552	47.452	-0.100
10	317	48.924	48.651	-0.273
10	318	48.677	48.395	-0.282
10	319	49.397	48.976	-0.421
10	321	49.083	48.720	-0.363
10	322	48.392	48.173	-0.219
15	323	48.904	48.602	-0.302
15	325	47.368	47.091	-0.277
15	326	48.734	48.373	-0.361
15	328	49.128	48.759	-0.369
15	333	48.953	48.687	-0.266
30	334	46.930	46.183	-0.747
30	335	47.350	46.576	-0.774
30	337	47.161	46.388	-0.773
30	339	47.040	46.336	-0.704
30	341	48.439	47.722	-0.717
35	342	47.483	46.404	-1.079
35	345	48.796	47.733	-1.063
35	346	48.683	47.677	-1.006
35	347	49.150	48.293	-0.857
35	348	48.940	47.908	-1.032
50	349	47.798	46.387	-1.411
50	350	48.115	46.756	-1.359
50	351	47.339	45.997	-1.342
50	353	48.142	46.694	-1.448
50	354	49.217	47.911	-1.306
55	355	48.484	46.586	-1.898
55	357	48.762	47.186	-1.596
55	359	48.841	47.028	-1.813
55	362	48.273	46.663	-1.610
55	363	47.045	45.330	-1.715
100	1	48.164	45.878	-2.286
100	3	47.559	45.592	-1.967
100	4	47.177	45.043	-2.134
100	5	48.619	46.439	-2.180
100	6	48.612	46.411	-2.201
100	7	47.494	45.343	-2.151
100	8	48.555	46.097	-2.458
100	9	47.277	45.214	-2.063
100	10	48.595	45.993	-2.602
100	11	48.705	46.432	-2.273
100	12	49.069	47.036	-2.033
100	13	48.925	46.884	-2.041
100	14	48.941	46.609	-2.332
100	15	48.684	46.327	-2.357
100	16	49.173	46.904	-2.269
100	17	48.281	45.976	-2.305
100	18	48.767	46.375	-2.392
100	19	47.615	45.438	-2.177
100	20	48.658	46.461	-2.197
100	21	47.879	45.620	-2.259
100	22	48.374	46.044	-2.330
100	24	48.367	46.148	-2.219
105	25	48.727	45.733	-2.994
105	26	47.341	44.664	-2.677
105	27	48.348	45.243	-3.105
105	28	48.099	45.319	-2.780
105	29	48.208	45.259	-2.949
	Max	49.397	48.976	0.098
	Average	48.268	46.958	-1.311
	Min	46.930	44.664	-3.105
	Std Dev	0.653	1.155	0.978

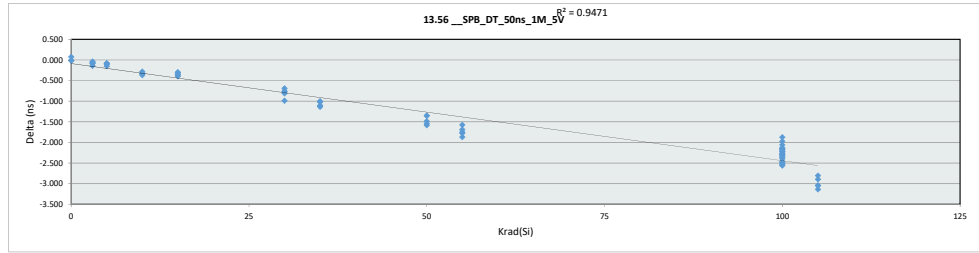


13.55 SPA DT 50ns 1M 5V											
Test Site											
Tester											
Test Number											
Max Limit	55	ns									
Min Limit	43	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	47.769	47.127	47.361	48.173	47.091	46.163	46.404	45.997	45.330	45.043	44.664
Average	47.845	48.227	47.946	48.583	48.302	46.637	47.603	46.749	46.559	46.103	45.244
Max	47.902	48.792	48.588	48.976	48.759	47.722	48.293	47.911	47.186	47.036	45.733
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

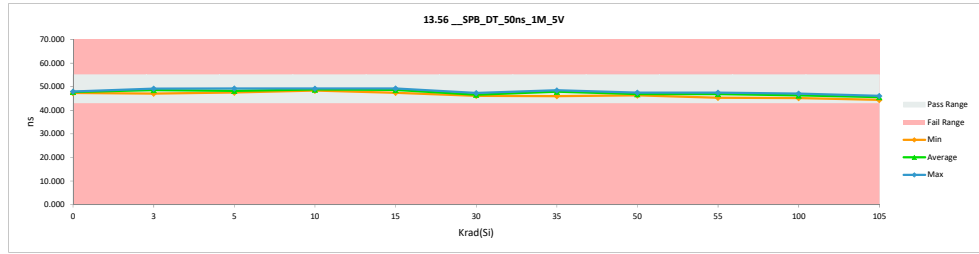


HDR TID Report
TPS7H5001-SP

13.56 SPB_DT_50ns_1M_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	55	55		
Min Limit	43	43		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	47.920	48.000	0.080
0	118	47.350	47.349	-0.001
0	122	48.010	47.996	-0.014
3	301	48.955	48.922	-0.033
3	306	49.308	49.227	-0.081
3	307	49.059	48.984	-0.075
3	308	47.174	47.122	-0.052
3	309	48.914	48.778	-0.136
5	310	47.815	47.726	-0.089
5	311	49.255	49.183	-0.072
5	312	49.368	49.280	-0.088
5	314	47.674	47.577	-0.097
5	316	47.687	47.540	-0.147
10	317	49.511	49.181	-0.330
10	318	48.924	48.605	-0.319
10	319	49.615	49.253	-0.362
10	321	49.142	48.814	-0.328
10	322	48.653	48.376	-0.277
15	323	48.816	48.427	-0.389
15	325	47.758	47.459	-0.299
15	326	49.177	48.837	-0.340
15	328	49.517	49.159	-0.358
15	333	49.568	49.275	-0.293
30	334	47.163	46.180	-0.983
30	335	47.618	46.867	-0.751
30	337	47.429	46.624	-0.805
30	339	46.906	46.221	-0.685
30	341	48.116	47.348	-0.768
35	342	47.135	46.032	-1.103
35	345	49.561	48.455	-1.106
35	346	49.382	48.250	-1.132
35	347	49.521	48.505	-1.016
35	348	49.182	48.185	-0.997
50	349	47.815	46.336	-1.479
50	350	48.686	47.147	-1.539
50	351	47.728	46.378	-1.350
50	353	48.655	47.076	-1.579
50	354	48.867	47.524	-1.343
55	355	49.257	47.393	-1.864
55	357	49.193	47.511	-1.682
55	359	48.684	46.914	-1.770
55	362	48.971	47.403	-1.568
55	363	47.096	45.353	-1.743
100	1	48.989	46.532	-2.457
100	3	47.824	45.854	-1.970
100	4	47.583	45.454	-2.129
100	5	49.244	47.067	-2.177
100	6	49.443	47.112	-2.331
100	7	47.656	45.497	-2.159
100	8	48.681	46.289	-2.392
100	9	47.196	45.326	-1.870
100	10	48.899	46.344	-2.555
100	11	48.687	46.323	-2.364
100	12	48.771	46.564	-2.207
100	13	49.300	47.036	-2.264
100	14	49.375	46.836	-2.539
100	15	49.597	47.084	-2.513
100	16	49.059	46.776	-2.283
100	17	47.993	45.855	-2.138
100	18	49.472	46.987	-2.485
100	19	47.268	45.214	-2.054
100	20	48.963	46.654	-2.309
100	21	48.218	46.004	-2.214
100	22	48.567	46.284	-2.283
100	24	48.444	46.217	-2.227
105	25	49.159	46.116	-3.043
105	26	47.338	44.447	-2.891
105	27	48.994	45.859	-3.135
105	28	48.607	45.807	-2.800
105	29	48.868	45.830	-3.038
	Max	49.615	49.280	0.080
	Average	48.548	47.202	-1.346
	Min	46.906	44.447	-3.135
	Std Dev	0.800	1.217	0.989

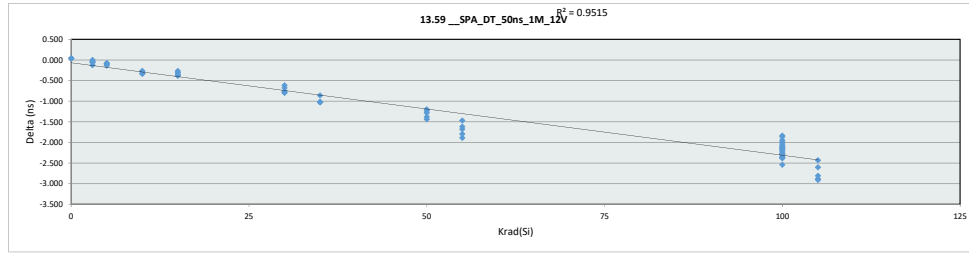


13.56 SPB_DT_50ns_1M_5V											
Test Site											
Tester											
Test Number											
Max Limit	55	ns									
Min Limit	43	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	47.349	47.122	47.540	48.376	47.459	46.180	46.032	46.336	45.353	45.214	44.447
Average	47.782	48.607	48.261	48.846	48.631	46.648	47.885	46.892	46.915	46.332	45.612
Max	48.000	49.227	49.280	49.253	49.275	47.348	48.505	47.524	47.511	47.112	46.116
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

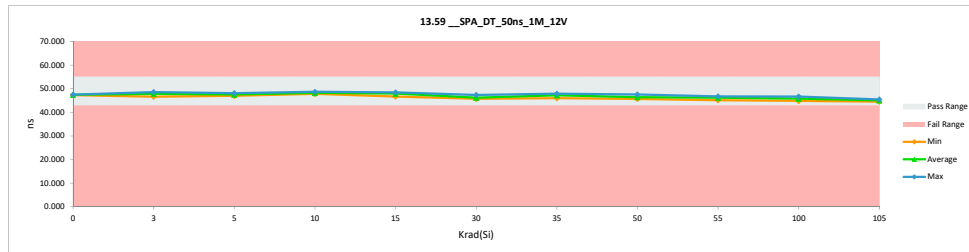


HDR TID Report
TPS7H5001-SP

13.59 SPA DT 50ns 1M 12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	55	55		
Min Limit	43	43		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	47.504	47.540	0.036
0	118	47.279	47.327	0.048
0	122	47.468	47.511	0.043
3	301	47.957	47.918	-0.039
3	306	48.320	48.286	-0.034
3	307	48.588	48.594	0.006
3	308	46.731	46.672	-0.059
3	309	48.038	47.915	-0.123
5	310	47.621	47.542	-0.079
5	311	48.175	48.078	-0.097
5	312	48.232	48.159	-0.073
5	314	47.097	47.022	-0.075
5	316	47.143	47.014	-0.129
10	317	48.615	48.296	-0.319
10	318	48.334	48.071	-0.263
10	319	49.031	48.710	-0.321
10	321	48.674	48.342	-0.332
10	322	48.092	47.821	-0.271
15	323	48.647	48.272	-0.375
15	325	47.021	46.723	-0.298
15	326	48.390	48.015	-0.375
15	328	48.793	48.455	-0.338
15	333	48.613	48.350	-0.263
30	334	46.459	45.787	-0.672
30	335	46.902	46.107	-0.795
30	337	46.786	46.035	-0.751
30	339	46.585	45.974	-0.611
30	341	48.154	47.374	-0.780
35	342	47.044	46.030	-1.014
35	345	48.418	47.401	-1.017
35	346	48.389	47.368	-1.021
35	347	48.807	47.950	-0.857
35	348	48.633	47.617	-1.016
50	349	47.372	46.094	-1.278
50	350	47.780	46.406	-1.374
50	351	46.945	45.715	-1.230
50	353	47.784	46.351	-1.433
50	354	48.787	47.592	-1.195
55	355	48.117	46.231	-1.886
55	357	48.422	46.810	-1.612
55	359	48.448	46.657	-1.791
55	362	47.960	46.287	-1.673
55	363	46.630	45.164	-1.466
100	1	47.771	45.615	-2.156
100	3	47.172	45.343	-1.829
100	4	46.787	44.849	-1.938
100	5	48.279	46.148	-2.131
100	6	48.250	46.063	-2.187
100	7	47.078	45.091	-1.987
100	8	48.228	45.866	-2.362
100	9	46.923	45.061	-1.862
100	10	48.263	45.728	-2.535
100	11	48.351	46.074	-2.277
100	12	48.771	46.721	-2.050
100	13	48.608	46.464	-2.144
100	14	48.651	46.269	-2.382
100	15	48.341	45.998	-2.343
100	16	48.865	46.577	-2.282
100	17	47.885	45.661	-2.224
100	18	48.429	46.072	-2.357
100	19	47.168	45.159	-2.009
100	20	48.375	46.086	-2.289
100	21	47.521	45.420	-2.101
100	22	48.050	45.779	-2.271
100	24	47.972	45.879	-2.093
105	25	48.360	45.484	-2.876
105	26	46.962	44.535	-2.427
105	27	48.046	45.141	-2.905
105	28	47.773	45.173	-2.600
105	29	47.875	45.070	-2.805
	Max	49.031	48.710	0.048
	Average	47.908	46.642	-1.266
	Min	46.459	44.535	-2.905
	Std Dev	0.682	1.114	0.941

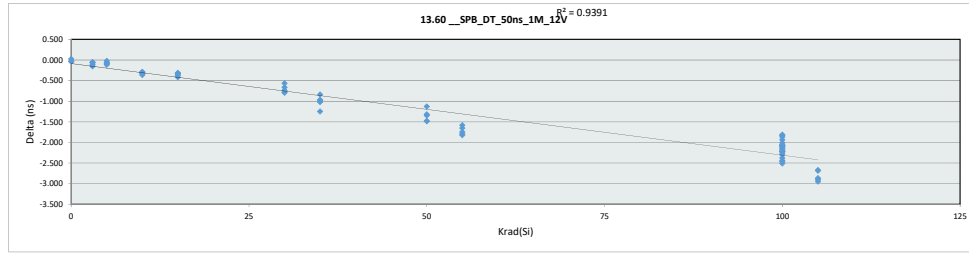


13.59 SPA DT 50ns 1M 12V											
Test Site											
Tester											
Test Number											
Max Limit	55	ns									
Min Limit	43	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	47.327	46.672	47.014	47.821	46.723	45.787	46.030	45.715	45.164	44.849	44.535
Average	47.459	47.877	47.563	48.248	47.963	46.255	47.273	46.432	46.230	45.815	45.081
Max	47.540	48.594	48.159	48.710	48.455	47.374	47.950	47.592	46.810	46.721	45.484
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

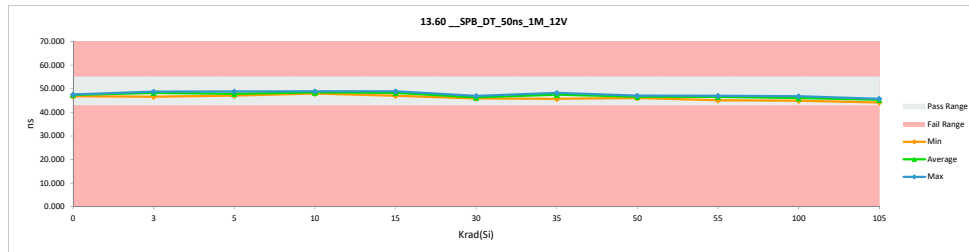


HDR TID Report
TPS7H5001-SP

13.60 SPB DT 50ns 1M 12V				
Test Site				
Tester				
Test Number				
Unit	ns		ns	
Max Limit	55		55	
Min Limit	43		43	
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	47.540	47.568	0.028
0	118	46.838	46.810	-0.028
0	122	47.605	47.567	-0.038
3	301	48.554	48.504	-0.050
3	306	48.834	48.783	-0.051
3	307	48.699	48.593	-0.106
3	308	46.746	46.596	-0.150
3	309	48.552	48.487	-0.065
5	310	47.343	47.234	-0.109
5	311	48.810	48.730	-0.080
5	312	48.875	48.858	-0.017
5	314	47.242	47.185	-0.057
5	316	47.174	47.087	-0.087
10	317	49.105	48.778	-0.327
10	318	48.522	48.219	-0.303
10	319	49.244	48.886	-0.358
10	321	48.656	48.366	-0.290
10	322	48.265	47.979	-0.286
15	323	48.472	48.110	-0.362
15	325	47.293	46.973	-0.320
15	326	48.831	48.417	-0.414
15	328	49.053	48.745	-0.308
15	333	49.219	48.888	-0.331
30	334	46.655	45.885	-0.790
30	335	47.160	46.427	-0.733
30	337	46.989	46.331	-0.658
30	339	46.443	45.884	-0.559
30	341	47.712	46.942	-0.770
35	342	46.640	45.680	-0.960
35	345	49.242	47.998	-1.244
35	346	48.871	47.855	-1.016
35	347	49.018	48.184	-0.834
35	348	48.768	47.783	-0.985
50	349	47.368	46.026	-1.342
50	350	48.275	46.800	-1.475
50	351	47.231	46.109	-1.122
50	353	48.228	46.751	-1.477
50	354	48.407	47.093	-1.314
55	355	48.831	47.011	-1.820
55	357	48.742	47.000	-1.742
55	359	48.327	46.539	-1.788
55	362	48.590	46.944	-1.646
55	363	46.667	45.088	-1.579
100	1	48.591	46.216	-2.375
100	3	47.347	45.520	-1.827
100	4	47.156	45.151	-2.005
100	5	48.775	46.681	-2.094
100	6	48.999	46.794	-2.205
100	7	47.157	45.109	-2.048
100	8	48.304	46.055	-2.249
100	9	46.789	44.982	-1.807
100	10	48.561	46.057	-2.504
100	11	48.219	46.003	-2.216
100	12	48.340	46.265	-2.075
100	13	48.870	46.736	-2.134
100	14	48.903	46.440	-2.463
100	15	49.162	46.727	-2.435
100	16	48.697	46.558	-2.139
100	17	47.445	45.516	-1.929
100	18	48.962	46.666	-2.296
100	19	46.763	44.901	-1.862
100	20	48.530	46.352	-2.178
100	21	47.763	45.695	-2.068
100	22	48.156	45.955	-2.201
100	24	47.989	45.902	-2.087
105	25	48.698	45.756	-2.942
105	26	46.859	44.192	-2.667
105	27	48.562	45.663	-2.899
105	28	48.184	45.504	-2.680
105	29	48.423	45.561	-2.862
	Max	49.244	48.888	0.028
	Average	48.112	46.838	-1.274
	Min	46.443	44.192	-2.942
	Std Dev	0.818	1.178	0.937

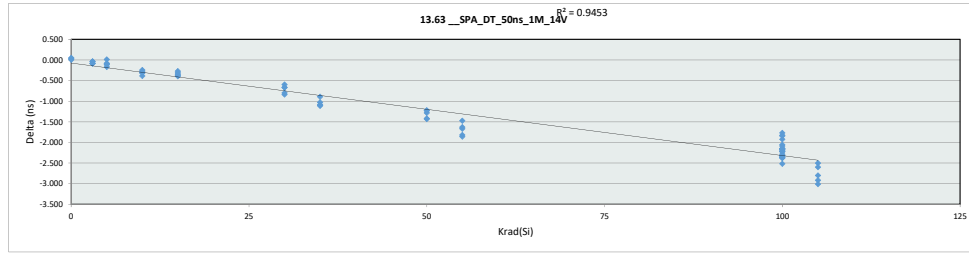


13.60 SPB DT 50ns 1M 12V											
Test Site											
Tester											
Test Number											
Max Limit	55		ns								
Min Limit	43		ns								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	46.810	46.596	47.087	47.979	46.973	45.865	45.680	46.026	45.088	44.901	44.192
Average	47.315	48.193	47.819	48.446	48.227	46.290	47.500	46.556	46.516	46.013	45.335
Max	47.568	48.783	48.858	48.886	48.888	46.942	48.184	47.093	47.011	46.794	45.756
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

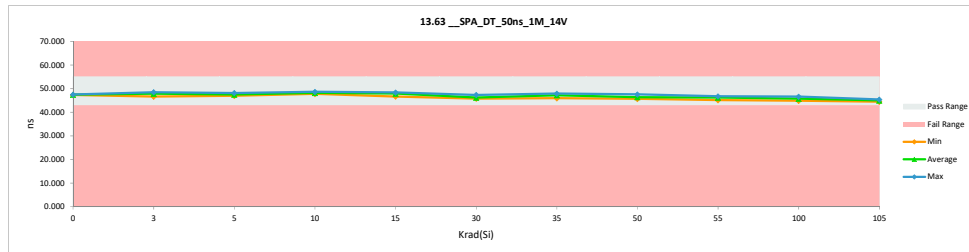


HDR TID Report
TPS7H5001-SP

13.63 SPA_DT_50ns_1M_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	55	55		
Min Limit	43	43		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	47.510	47.565	0.055
0	118	47.329	47.337	0.008
0	122	47.464	47.500	0.036
3	301	47.966	47.937	-0.029
3	306	48.349	48.273	-0.076
3	307	48.565	48.533	-0.032
3	308	46.751	46.666	-0.085
3	309	47.968	47.891	-0.077
5	310	47.614	47.537	-0.077
5	311	48.192	48.081	-0.111
5	312	48.166	48.185	0.019
5	314	47.118	47.027	-0.091
5	316	47.153	46.987	-0.166
10	317	48.528	48.288	-0.240
10	318	48.401	48.089	-0.312
10	319	49.017	48.718	-0.299
10	321	48.695	48.314	-0.381
10	322	48.071	47.806	-0.265
15	323	48.658	48.269	-0.389
15	325	47.003	46.694	-0.309
15	326	48.390	48.025	-0.365
15	328	48.781	48.440	-0.341
15	333	48.629	48.363	-0.266
30	334	46.473	45.812	-0.661
30	335	46.931	46.097	-0.834
30	337	46.729	46.067	-0.662
30	339	46.560	45.970	-0.590
30	341	48.163	47.371	-0.792
35	342	47.107	46.003	-1.104
35	345	48.422	47.342	-1.080
35	346	48.434	47.332	-1.102
35	347	48.839	47.951	-0.888
35	348	48.594	47.572	-1.022
50	349	47.351	46.073	-1.278
50	350	47.782	46.355	-1.427
50	351	46.948	45.730	-1.218
50	353	47.785	46.369	-1.416
50	354	48.835	47.610	-1.225
55	355	48.100	46.248	-1.852
55	357	48.450	46.826	-1.624
55	359	48.484	46.669	-1.815
55	362	47.946	46.282	-1.664
55	363	46.631	45.162	-1.469
100	1	47.801	45.585	-2.216
100	3	47.205	45.369	-1.836
100	4	46.731	44.907	-1.824
100	5	48.245	46.041	-2.204
100	6	48.281	46.131	-2.150
100	7	47.035	45.114	-1.921
100	8	48.210	45.845	-2.365
100	9	46.828	45.063	-1.765
100	10	48.238	45.726	-2.512
100	11	48.395	46.085	-2.310
100	12	48.766	46.684	-2.082
100	13	48.604	46.462	-2.142
100	14	48.624	46.259	-2.365
100	15	48.364	45.989	-2.375
100	16	48.807	46.566	-2.321
100	17	47.885	45.663	-2.222
100	18	48.417	46.074	-2.343
100	19	47.189	45.139	-2.050
100	20	48.318	46.141	-2.177
100	21	47.549	45.388	-2.161
100	22	48.034	45.755	-2.279
100	24	47.996	45.845	-2.151
105	25	48.369	45.455	-2.914
105	26	47.011	44.513	-2.498
105	27	48.113	45.106	-3.007
105	28	47.728	45.135	-2.593
105	29	47.881	45.083	-2.798
	Max	49.017	48.718	0.055
	Average	47.908	46.636	-1.273
	Min	46.473	44.513	-3.007
	Std Dev	0.684	1.113	0.944

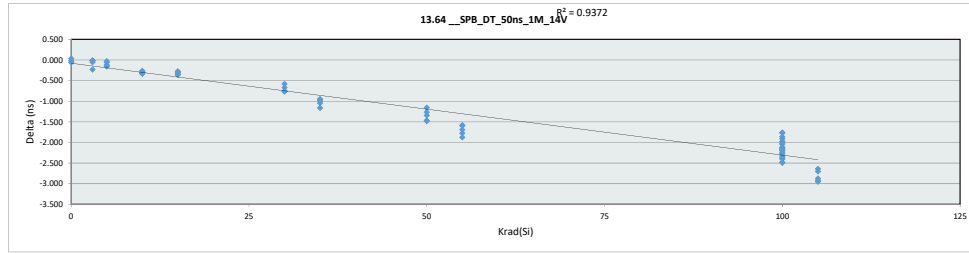


13.63 SPA_DT_50ns_1M_14V											
Test Site											
Tester											
Test Number											
Max Limit	55	ns									
Min Limit	43	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	47.337	46.666	46.987	47.806	46.694	45.812	46.003	45.730	45.162	44.907	44.513
Average	47.467	47.860	47.563	48.243	47.958	46.263	47.240	46.427	46.237	45.811	45.058
Max	47.565	48.533	48.185	48.718	48.440	47.371	47.951	47.610	46.826	46.684	45.455
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

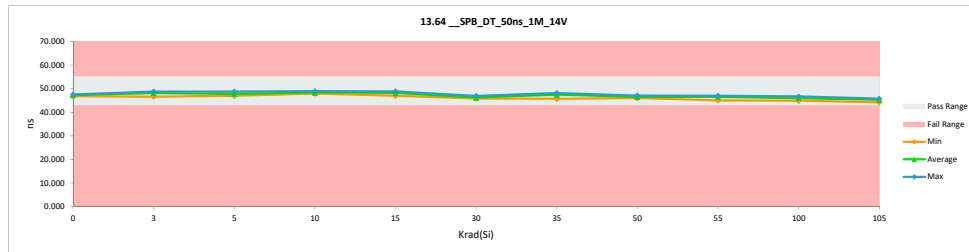


HDR TID Report
TPS7H5001-SP

13.64 SPB DT 50ns 1M 14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	55	55		
Min Limit	43	43		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	47.541	47.580	0.039
0	118	46.820	46.801	-0.019
0	122	47.630	47.572	-0.058
3	301	48.547	48.540	-0.007
3	306	48.830	48.818	-0.012
3	307	48.648	48.600	-0.048
3	308	46.659	46.644	-0.015
3	309	48.617	48.389	-0.228
5	310	47.327	47.274	-0.053
5	311	48.834	48.810	-0.024
5	312	48.894	48.755	-0.139
5	314	47.256	47.139	-0.117
5	316	47.192	47.039	-0.153
10	317	49.051	48.722	-0.329
10	318	48.499	48.241	-0.258
10	319	49.255	48.949	-0.306
10	321	48.662	48.339	-0.323
10	322	48.260	47.980	-0.280
15	323	48.411	48.056	-0.355
15	325	47.272	47.002	-0.270
15	326	48.771	48.428	-0.343
15	328	49.066	48.757	-0.309
15	333	49.156	48.869	-0.287
30	334	46.656	45.909	-0.747
30	335	47.193	46.433	-0.760
30	337	47.077	46.323	-0.754
30	339	46.460	45.885	-0.575
30	341	47.656	46.990	-0.666
35	342	46.676	45.722	-0.954
35	345	49.210	48.051	-1.159
35	346	48.926	47.884	-1.042
35	347	49.126	48.180	-0.946
35	348	48.786	47.796	-0.990
50	349	47.312	46.053	-1.259
50	350	48.301	46.823	-1.478
50	351	47.233	46.081	-1.152
50	353	48.209	46.749	-1.460
50	354	48.468	47.126	-1.342
55	355	48.834	46.962	-1.872
55	357	48.737	47.049	-1.688
55	359	48.263	46.491	-1.772
55	362	48.602	47.023	-1.579
55	363	46.659	45.071	-1.588
100	1	48.615	46.234	-2.381
100	3	47.261	45.497	-1.764
100	4	47.145	45.166	-1.979
100	5	48.786	46.759	-2.027
100	6	49.021	46.755	-2.266
100	7	47.098	45.192	-1.906
100	8	48.322	46.003	-2.319
100	9	46.747	44.990	-1.757
100	10	48.537	46.048	-2.489
100	11	48.216	45.975	-2.241
100	12	48.346	46.216	-2.130
100	13	48.840	46.668	-2.172
100	14	48.917	46.521	-2.396
100	15	49.210	46.734	-2.476
100	16	48.718	46.529	-2.189
100	17	47.483	45.504	-1.979
100	18	48.994	46.645	-2.349
100	19	46.755	44.899	-1.856
100	20	48.541	46.389	-2.152
100	21	47.784	45.750	-2.034
100	22	48.162	45.964	-2.198
100	24	48.021	45.912	-2.109
105	25	48.747	45.798	-2.949
105	26	46.917	44.216	-2.701
105	27	48.599	45.681	-2.918
105	28	48.168	45.531	-2.637
105	29	48.416	45.545	-2.871
	Max	49.255	48.949	0.039
	Average	48.114	46.843	-1.270
	Min	46.460	44.216	-2.949
	Std Dev	0.822	1.174	0.941

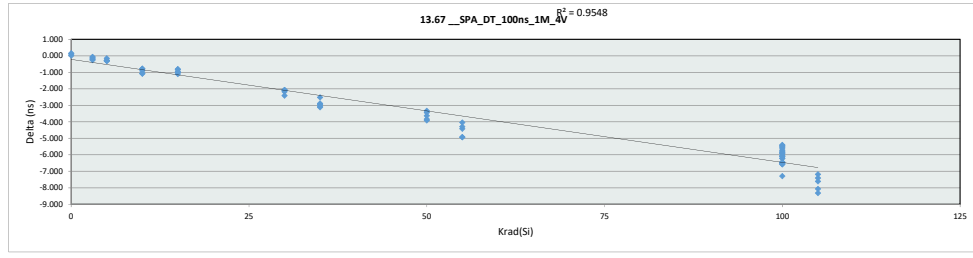


13.64 SPB DT 50ns 1M 14V											
Test Site											
Tester											
Test Number											
Max Limit	55	ns									
Min Limit	43	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	46.801	46.644	47.039	47.980	47.002	45.885	45.722	46.053	45.071	44.899	44.216
Average	47.318	48.198	47.803	48.446	48.222	46.308	47.527	46.566	46.519	46.016	45.354
Max	47.580	48.818	48.810	48.949	48.869	46.990	48.180	47.126	47.049	46.759	45.798
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

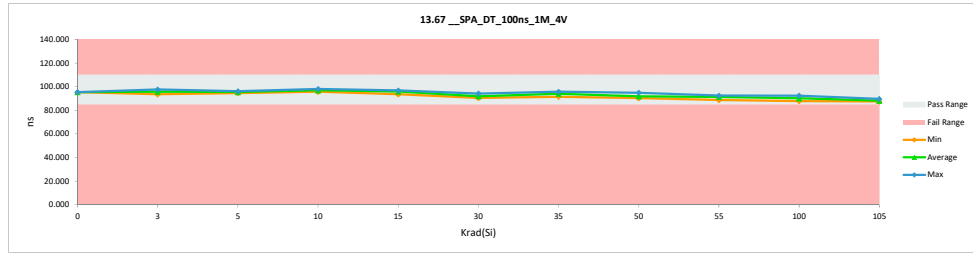


HDR TID Report
TPS7H5001-SP

13.67 SPA_DT_100ns_1M_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	110	110		
Min Limit	85	85		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	95.344	95.470	0.126
0	118	95.358	95.382	0.024
0	122	95.279	95.449	0.170
3	301	95.221	95.065	-0.156
3	306	96.625	96.576	-0.049
3	307	97.780	97.713	-0.067
3	308	93.850	93.652	-0.198
3	309	96.233	95.985	-0.248
5	310	96.492	96.222	-0.270
5	311	96.393	96.128	-0.265
5	312	95.932	95.794	-0.138
5	314	95.161	94.859	-0.302
5	316	94.899	94.613	-0.286
10	317	97.890	96.998	-0.892
10	318	97.628	96.849	-0.779
10	319	99.121	98.050	-1.071
10	321	98.317	97.269	-1.048
10	322	96.556	95.794	-0.762
15	323	97.648	96.657	-0.991
15	325	94.434	93.648	-0.786
15	326	97.034	96.090	-0.944
15	328	97.993	96.905	-1.088
15	333	97.706	96.874	-0.832
30	334	92.618	90.558	-2.060
30	335	94.425	92.024	-2.401
30	337	93.628	91.464	-2.164
30	339	93.560	91.501	-2.059
30	341	96.343	94.272	-2.071
35	342	94.589	91.552	-3.037
35	345	97.018	94.075	-2.943
35	346	97.497	94.391	-3.106
35	347	98.296	95.794	-2.502
35	348	97.614	94.744	-2.870
50	349	94.858	91.435	-3.423
50	350	95.197	91.578	-3.619
50	351	94.195	90.365	-3.830
50	353	95.242	91.337	-3.905
50	354	98.222	94.903	-3.319
55	355	96.654	91.731	-4.923
55	357	96.545	92.513	-4.032
55	359	97.457	92.545	-4.912
55	362	95.775	91.493	-4.282
55	363	93.144	88.737	-4.407
100	1	95.214	89.125	-6.089
100	3	94.838	89.452	-5.386
100	4	93.465	87.835	-5.630
100	5	96.613	90.868	-5.745
100	6	96.581	90.815	-5.766
100	7	94.963	89.053	-5.910
100	8	95.677	89.228	-6.449
100	9	94.176	88.699	-5.477
100	10	97.170	89.887	-7.283
100	11	97.133	90.595	-6.538
100	12	97.892	92.468	-5.424
100	13	97.174	91.636	-5.538
100	14	98.646	92.076	-6.570
100	15	96.706	90.284	-6.422
100	16	98.374	92.159	-6.215
100	17	96.949	90.759	-6.190
100	18	97.149	90.615	-6.534
100	19	95.258	89.384	-5.874
100	20	97.195	91.108	-6.087
100	21	95.058	89.191	-5.867
100	22	96.206	89.705	-6.501
100	24	96.569	90.553	-6.016
105	25	97.772	89.713	-8.059
105	26	94.675	87.504	-7.171
105	27	96.423	88.113	-8.310
105	28	94.999	87.599	-7.400
105	29	95.641	88.038	-7.603
Max		99.121	98.050	0.170
Average		96.147	92.593	-3.554
Min		92.618	87.504	-8.310
Std Dev		1.486	2.974	2.613

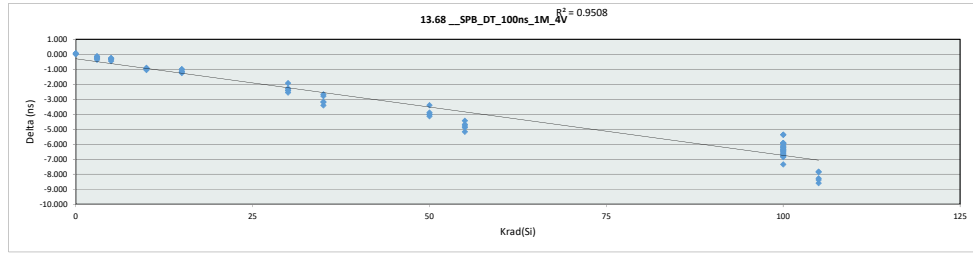


13.67 SPA_DT_100ns_1M_4V											
Test Site											
Tester											
Test Number											
Max Limit	110	ns									
Min Limit	85	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	95.382	93.652	94.613	95.794	93.648	90.558	91.552	90.365	88.737	87.835	87.504
Average	95.434	95.798	95.523	96.992	96.035	91.964	94.111	91.924	91.404	90.250	88.193
Max	95.470	97.713	96.222	98.050	96.905	94.272	95.794	94.903	92.545	92.468	89.713
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

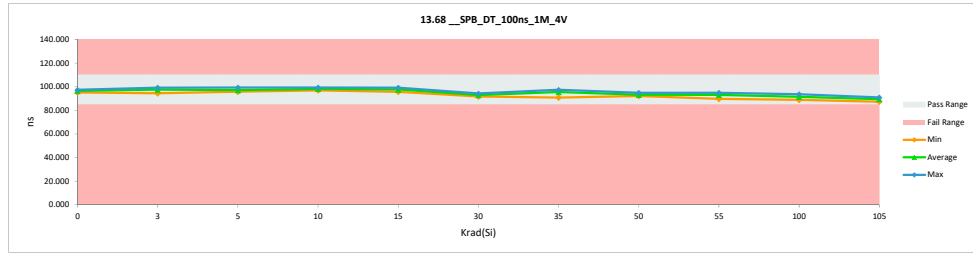


HDR TID Report
TPS7H5001-SP

13.68 SPB_DT_100ns_1M_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	110	110		
Min Limit	85	85		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	96.884	96.969	0.085
0	118	95.034	95.049	0.015
0	122	97.233	97.315	0.082
3	301	98.007	97.871	-0.136
3	306	99.200	99.082	-0.118
3	307	98.713	98.495	-0.218
3	308	94.580	94.335	-0.245
3	309	98.346	98.001	-0.345
5	310	96.339	96.054	-0.285
5	311	98.827	98.579	-0.248
5	312	99.461	99.230	-0.231
5	314	96.046	95.705	-0.341
5	316	96.242	95.804	-0.438
10	317	99.959	99.009	-0.950
10	318	98.763	97.808	-0.955
10	319	100.292	99.257	-1.035
10	321	98.759	97.816	-0.943
10	322	97.729	96.843	-0.886
15	323	98.093	96.928	-1.165
15	325	96.615	95.627	-0.988
15	326	99.255	98.018	-1.237
15	328	99.907	98.803	-1.104
15	333	100.066	99.087	-0.979
30	334	94.744	92.207	-2.537
30	335	95.784	93.405	-2.379
30	337	95.491	93.083	-2.408
30	339	93.590	91.688	-1.902
30	341	96.458	94.195	-2.263
35	342	93.855	90.700	-3.155
35	345	100.346	96.957	-3.389
35	346	99.727	96.550	-3.177
35	347	100.082	97.313	-2.769
35	348	98.719	96.060	-2.659
50	349	96.101	92.121	-3.980
50	350	97.257	93.247	-4.010
50	351	96.260	92.386	-3.874
50	353	97.499	93.382	-4.117
50	354	98.171	94.792	-3.379
55	355	99.954	94.799	-5.155
55	357	98.770	94.109	-4.661
55	359	97.756	92.879	-4.877
55	362	98.408	93.996	-4.412
55	363	94.449	89.685	-4.764
100	1	98.487	91.650	-6.837
100	3	95.482	90.144	-5.338
100	4	94.943	88.887	-6.056
100	5	99.241	93.324	-5.917
100	6	99.733	93.535	-6.198
100	7	95.851	89.538	-6.313
100	8	97.423	90.831	-6.592
100	9	94.793	89.441	-5.352
100	10	98.544	91.220	-7.324
100	11	97.305	90.636	-6.669
100	12	97.753	91.860	-5.893
100	13	99.027	92.857	-6.170
100	14	100.078	93.284	-6.794
100	15	99.764	93.293	-6.471
100	16	99.149	92.992	-6.157
100	17	96.603	90.698	-5.905
100	18	99.675	92.911	-6.764
100	19	95.165	89.255	-5.910
100	20	98.347	91.984	-6.363
100	21	96.678	90.383	-6.295
100	22	97.427	90.917	-6.510
100	24	97.259	90.868	-6.391
105	25	99.127	90.868	-8.259
105	26	95.141	87.299	-7.842
105	27	98.699	90.120	-8.579
105	28	97.003	89.190	-7.813
105	29	98.184	89.820	-8.364
Max		100.346	99.257	0.085
Average		97.638	93.901	-3.737
Min		93.590	87.299	-6.295
Std Dev		1.790	3.203	2.701

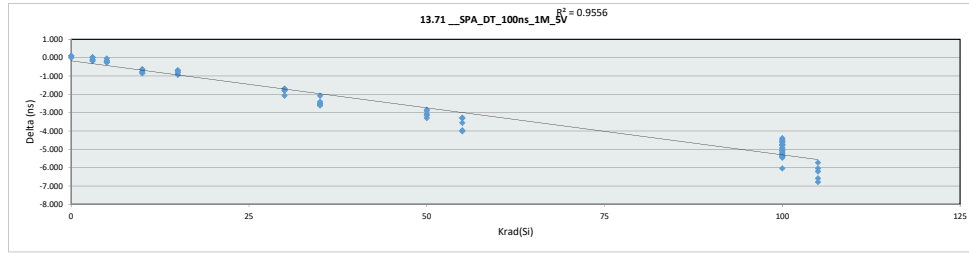


13.68 SPB_DT_100ns_1M_4V											
Test Site											
Tester											
Test Number											
Max Limit	110	ns									
Min Limit	85	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	95.049	94.335	95.705	96.843	95.627	91.688	90.700	92.121	89.685	88.887	87.299
Average	96.444	97.557	97.074	98.147	97.693	92.916	95.516	93.186	93.094	91.387	89.459
Max	97.315	99.082	99.230	99.257	99.087	94.195	97.313	94.792	94.799	93.535	90.868
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

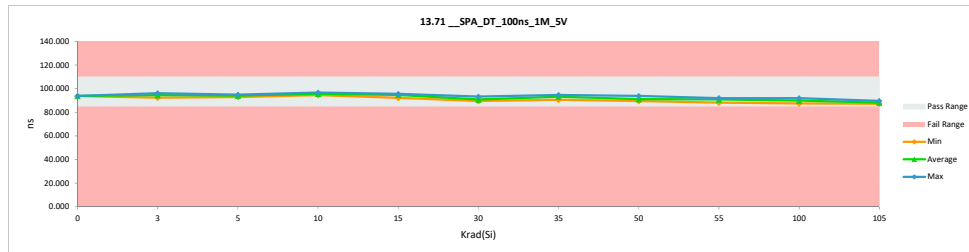


HDR TID Report
TPS7H5001-SP

13.71 SPA_DT_100ns_1M_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	110	110		
Min Limit	85	85		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	93.790	93.894	0.104
0	118	93.898	93.912	0.014
0	122	93.693	93.806	0.113
3	301	94.004	93.887	-0.117
3	306	95.413	95.444	0.031
3	307	96.154	96.194	0.040
3	308	92.474	92.370	-0.104
3	309	94.908	94.739	-0.169
5	310	94.780	94.598	-0.182
5	311	95.139	94.982	-0.157
5	312	94.693	94.656	-0.037
5	314	93.449	93.208	-0.241
5	316	93.246	92.991	-0.255
10	317	96.388	95.623	-0.765
10	318	96.171	95.554	-0.617
10	319	97.554	96.707	-0.847
10	321	96.758	95.980	-0.778
10	322	95.198	94.552	-0.646
15	323	96.237	95.425	-0.812
15	325	92.904	92.224	-0.680
15	326	95.572	94.859	-0.713
15	328	96.591	95.663	-0.928
15	333	96.232	95.543	-0.689
30	334	91.754	89.572	-2.182
30	335	92.993	90.935	-2.058
30	337	92.188	90.405	-1.783
30	339	92.194	90.414	-1.780
30	341	94.974	93.283	-1.691
35	342	93.185	90.586	-2.599
35	345	95.582	93.099	-2.483
35	346	95.911	93.364	-2.547
35	347	96.804	94.737	-2.067
35	348	96.342	93.940	-2.402
50	349	93.440	90.602	-2.838
50	350	94.055	90.990	-3.065
50	351	92.654	89.514	-3.140
50	353	93.976	90.690	-3.286
50	354	96.810	93.925	-2.885
55	355	95.094	91.143	-3.951
55	357	95.201	91.935	-3.266
55	359	96.048	92.038	-4.010
55	362	94.515	91.226	-3.289
55	363	91.742	88.201	-3.541
100	1	93.989	88.944	-5.045
100	3	93.300	88.917	-4.383
100	4	92.074	87.469	-4.605
100	5	95.460	90.736	-4.724
100	6	95.243	90.489	-4.754
100	7	93.470	88.572	-4.898
100	8	94.559	89.262	-5.297
100	9	92.729	88.188	-4.541
100	10	95.689	89.660	-6.029
100	11	95.845	90.434	-5.411
100	12	96.462	91.995	-4.467
100	13	95.792	91.226	-4.566
100	14	96.907	91.549	-5.358
100	15	95.242	90.033	-5.209
100	16	96.736	91.556	-5.180
100	17	95.265	90.185	-5.080
100	18	95.779	90.424	-5.355
100	19	93.491	88.736	-4.755
100	20	95.678	90.722	-4.956
100	21	93.741	89.010	-4.731
100	22	94.896	89.445	-5.451
100	24	95.195	90.129	-5.066
105	25	96.257	89.685	-6.572
105	26	93.032	87.318	-5.714
105	27	95.097	88.323	-6.774
105	28	93.795	87.760	-6.035
105	29	94.378	88.184	-6.194
Max		97.554	96.707	0.113
Average		94.719	91.805	-2.914
Min		91.254	87.318	-6.774
Std Dev		1.467	2.555	2.147

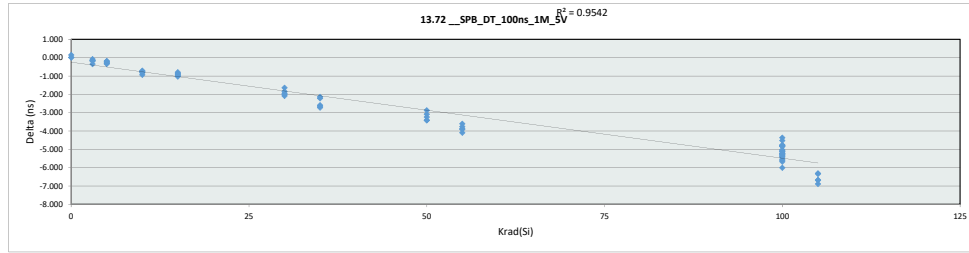


13.71 SPA_DT_100ns_1M_5V											
Test Site											
Tester											
Test Number											
Max Limit	110	ns									
Min Limit	85	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	93.806	92.370	92.991	94.552	92.224	89.572	90.586	89.514	88.201	87.469	87.318
Average	93.871	94.527	94.087	95.683	94.743	90.922	93.145	91.144	90.909	89.895	88.254
Max	93.912	96.194	94.982	96.707	95.663	93.283	94.737	93.925	92.038	91.995	89.685
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

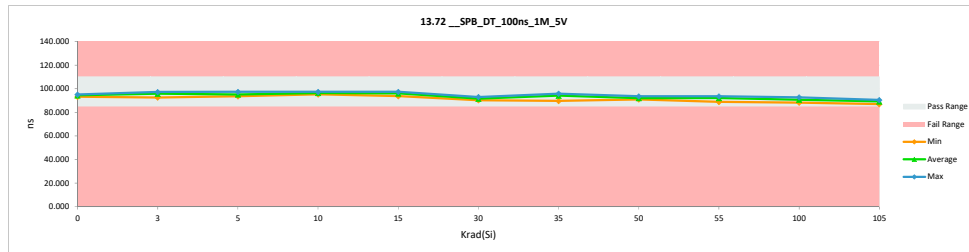


HDR TID Report
TPS7H5001-SP

13.72 SPB_DT_100ns_1M_5V				
Test Site				
Tester				
Test Number				
Unit	ns		ns	
Max Limit	110		110	
Min Limit	85		85	
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	94.797	94.955	0.158
0	118	93.212	93.242	0.030
0	122	95.121	95.143	0.022
3	301	96.249	96.177	-0.072
3	306	97.454	97.293	-0.161
3	307	96.866	96.716	-0.150
3	308	92.751	92.590	-0.161
3	309	96.645	96.305	-0.340
5	310	94.474	94.202	-0.272
5	311	97.063	96.888	-0.175
5	312	97.575	97.394	-0.181
5	314	93.912	93.624	-0.288
5	316	94.138	93.811	-0.327
10	317	97.984	97.169	-0.815
10	318	96.877	96.131	-0.746
10	319	98.300	97.373	-0.927
10	321	96.817	96.056	-0.761
10	322	95.945	95.249	-0.696
15	323	96.420	95.399	-1.021
15	325	94.607	93.750	-0.857
15	326	97.389	96.427	-0.962
15	328	97.975	97.071	-0.904
15	333	98.203	97.415	-0.788
30	334	92.911	90.835	-2.076
30	335	93.926	91.923	-2.003
30	337	93.554	91.619	-1.935
30	339	91.947	90.317	-1.630
30	341	94.851	93.022	-1.829
35	342	92.237	89.653	-2.584
35	345	98.174	95.464	-2.710
35	346	97.734	95.097	-2.637
35	347	98.050	95.842	-2.208
35	348	96.911	94.778	-2.133
50	349	94.213	90.987	-3.226
50	350	95.775	92.375	-3.400
50	351	94.122	91.046	-3.076
50	353	95.774	92.366	-3.408
50	354	96.451	93.585	-2.866
55	355	97.736	93.654	-4.082
55	357	96.904	93.147	-3.757
55	359	95.981	92.113	-3.868
55	362	96.646	93.050	-3.596
55	363	92.750	88.848	-3.902
100	1	96.731	91.147	-5.584
100	3	93.703	89.188	-4.515
100	4	93.045	88.264	-4.781
100	5	97.477	92.686	-4.791
100	6	97.861	92.750	-5.111
100	7	93.920	88.685	-5.235
100	8	95.791	90.425	-5.366
100	9	93.056	88.699	-4.357
100	10	96.644	90.647	-5.997
100	11	95.744	90.329	-5.415
100	12	96.049	91.212	-4.837
100	13	97.140	92.112	-5.028
100	14	98.089	92.421	-5.668
100	15	97.759	92.517	-5.242
100	16	97.248	92.219	-5.029
100	17	94.679	89.862	-4.817
100	18	97.806	92.276	-5.530
100	19	93.231	88.480	-4.751
100	20	96.524	91.402	-5.122
100	21	95.028	89.774	-5.254
100	22	95.740	90.409	-5.331
100	24	95.532	90.331	-5.201
105	25	97.165	90.503	-6.662
105	26	93.234	86.932	-6.302
105	27	96.802	89.926	-6.876
105	28	95.402	89.069	-6.333
105	29	96.434	89.757	-6.677
Max		98.300	97.415	0.158
Average		95.789	92.745	-3.045
Min		91.947	86.932	-6.876
Std Dev		1.756	2.752	2.193

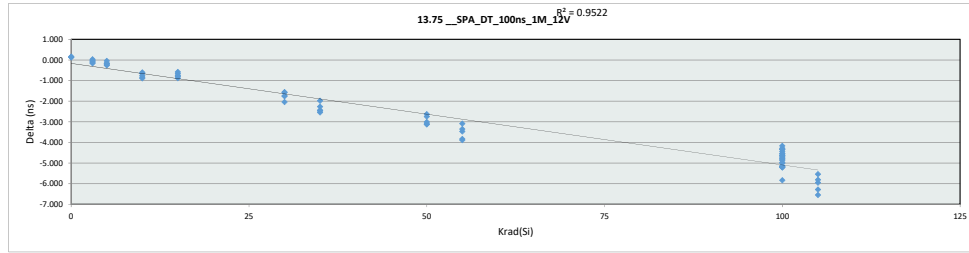


13.72 SPB_DT_100ns_1M_5V											
Test Site											
Tester											
Test Number											
Max Limit	110		ns								
Min Limit	85		ns								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	93.242	92.590	93.624	95.249	93.750	90.317	89.653	90.987	88.848	88.264	86.932
Average	94.447	95.816	95.184	96.396	96.012	91.543	94.167	92.072	92.162	90.720	89.237
Max	95.143	97.293	97.394	97.373	97.415	93.022	95.842	93.585	93.654	92.750	90.503
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

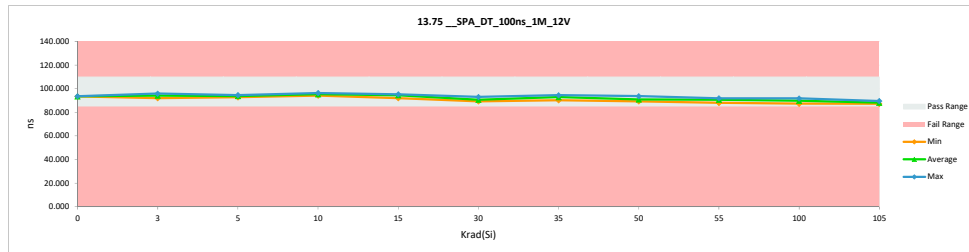


HDR TID Report
TPS7H5001-SP

13.75 SPA DT 100ns 1M 12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	110	110		
Min Limit	85	85		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	93.536	93.709	0.173
0	118	93.483	93.618	0.135
0	122	93.431	93.585	0.154
3	301	93.716	93.657	-0.059
3	306	95.043	95.089	0.046
3	307	95.934	95.916	-0.018
3	308	92.206	92.054	-0.152
3	309	94.558	94.439	-0.119
5	310	94.472	94.312	-0.160
5	311	94.751	94.611	-0.140
5	312	94.347	94.315	-0.032
5	314	93.175	92.956	-0.219
5	316	93.055	92.800	-0.255
10	317	95.990	95.242	-0.748
10	318	95.836	95.240	-0.596
10	319	97.273	96.389	-0.884
10	321	96.382	95.557	-0.825
10	322	94.857	94.212	-0.645
15	323	95.992	95.198	-0.794
15	325	92.621	92.051	-0.570
15	326	95.264	94.522	-0.742
15	328	96.237	95.360	-0.877
15	333	95.883	95.230	-0.653
30	334	91.058	89.330	-1.728
30	335	92.720	90.692	-2.028
30	337	91.954	90.208	-1.746
30	339	91.747	90.200	-1.547
30	341	94.636	93.066	-1.570
35	342	92.858	90.324	-2.534
35	345	95.319	92.895	-2.424
35	346	95.563	93.107	-2.456
35	347	96.525	94.560	-1.965
35	348	96.031	93.779	-2.252
50	349	93.127	90.505	-2.622
50	350	93.820	90.819	-3.001
50	351	92.438	89.347	-3.091
50	353	93.664	90.551	-3.113
50	354	96.491	93.756	-2.735
55	355	94.797	90.972	-3.825
55	357	94.891	91.810	-3.081
55	359	95.791	91.913	-3.878
55	362	94.212	90.873	-3.339
55	363	91.519	88.060	-3.459
100	1	93.682	88.871	-4.811
100	3	93.068	88.908	-4.160
100	4	91.833	87.408	-4.425
100	5	95.106	90.587	-4.519
100	6	94.977	90.366	-4.611
100	7	93.168	88.508	-4.660
100	8	94.272	89.144	-5.128
100	9	92.440	88.164	-4.276
100	10	95.407	89.581	-5.826
100	11	95.489	90.312	-5.177
100	12	96.206	91.890	-4.316
100	13	95.498	91.166	-4.332
100	14	96.634	91.441	-5.193
100	15	94.909	89.913	-4.996
100	16	96.438	91.472	-4.966
100	17	94.905	90.041	-4.864
100	18	95.477	90.271	-5.206
100	19	93.320	88.706	-4.614
100	20	95.440	90.703	-4.737
100	21	93.436	88.888	-4.548
100	22	94.538	89.377	-5.161
100	24	94.827	90.066	-4.761
105	25	95.897	89.622	-6.275
105	26	92.786	87.252	-5.534
105	27	94.755	88.216	-6.539
105	28	93.510	87.709	-5.801
105	29	94.064	88.123	-5.941
Max		97.273	96.389	0.173
Average		94.419	91.622	-2.797
Min		91.068	87.252	-6.539
Std Dev		1.450	2.472	2.066

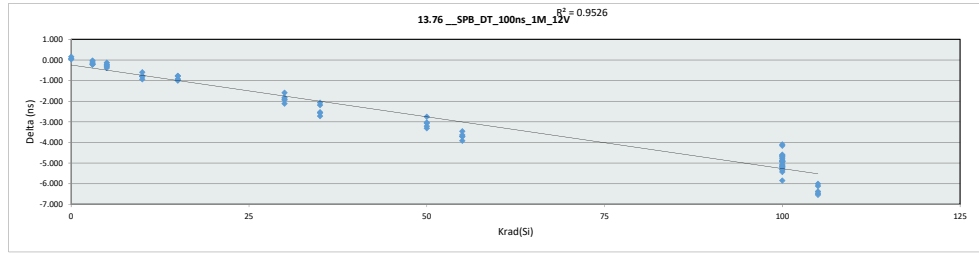


13.75 SPA DT 100ns 1M 12V											
Test Site											
Tester											
Test Number											
Max Limit	110	ns									
Min Limit	85	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	93.585	92.054	92.800	94.212	92.051	89.330	90.324	89.347	88.060	87.408	87.252
Average	93.637	94.231	93.799	95.328	94.472	90.699	92.933	90.996	90.726	89.808	88.184
Max	93.709	95.916	94.611	96.389	95.360	93.066	94.560	93.756	91.913	91.890	89.622
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

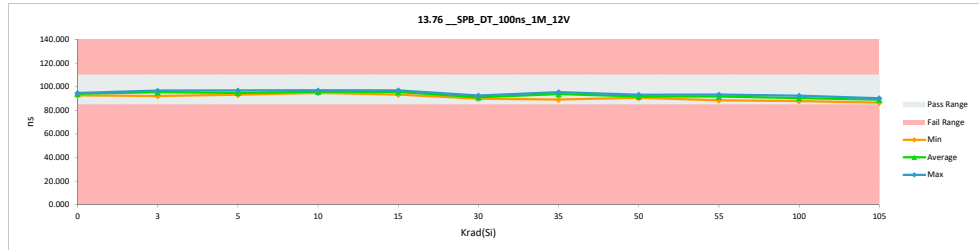


HDR TID Report
TPS7H5001-SP

13.76 SPB_DT_100ns_1M_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	110	110		
Min Limit	85	85		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	94.318	94.482	0.164
0	118	92.566	92.608	0.042
0	122	94.554	94.632	0.078
3	301	95.617	95.600	-0.017
3	306	96.845	96.634	-0.211
3	307	96.412	96.251	-0.161
3	308	92.141	92.055	-0.086
3	309	96.035	95.829	-0.206
5	310	93.900	93.658	-0.242
5	311	96.469	96.268	-0.201
5	312	96.920	96.802	-0.118
5	314	93.455	93.139	-0.316
5	316	93.665	93.287	-0.378
10	317	97.388	96.602	-0.786
10	318	96.277	95.488	-0.789
10	319	97.815	96.891	-0.924
10	321	96.112	95.344	-0.768
10	322	95.352	94.769	-0.583
15	323	95.818	94.909	-0.909
15	325	94.031	93.259	-0.772
15	326	96.887	95.900	-0.987
15	328	97.499	96.543	-0.956
15	333	97.698	96.942	-0.756
30	334	92.351	90.244	-2.107
30	335	93.381	91.451	-1.930
30	337	93.063	91.173	-1.890
30	339	91.373	89.792	-1.581
30	341	94.308	92.497	-1.811
35	342	91.713	89.159	-2.554
35	345	97.688	94.977	-2.711
35	346	97.190	94.662	-2.528
35	347	97.525	95.349	-2.176
35	348	96.365	94.304	-2.061
50	349	93.582	90.526	-3.056
50	350	95.152	91.950	-3.202
50	351	93.688	90.661	-3.027
50	353	95.236	91.933	-3.303
50	354	95.902	93.156	-2.746
55	355	97.218	93.310	-3.908
55	357	96.275	92.649	-3.626
55	359	95.380	91.682	-3.698
55	362	96.020	92.572	-3.448
55	363	92.157	88.451	-3.706
100	1	96.123	90.826	-5.297
100	3	93.103	88.955	-4.148
100	4	92.513	87.734	-4.779
100	5	96.885	92.263	-4.622
100	6	97.300	92.369	-4.931
100	7	93.300	88.405	-4.895
100	8	95.281	90.073	-5.208
100	9	92.498	88.414	-4.084
100	10	96.145	90.306	-5.839
100	11	95.132	89.969	-5.163
100	12	95.558	90.860	-4.698
100	13	96.597	91.743	-4.854
100	14	97.391	92.070	-5.321
100	15	97.204	92.111	-5.093
100	16	96.633	91.730	-4.903
100	17	94.088	89.429	-4.659
100	18	97.197	91.777	-5.420
100	19	92.779	88.197	-4.582
100	20	95.981	91.077	-4.904
100	21	94.380	89.451	-4.929
100	22	95.174	90.030	-5.144
100	24	95.006	89.959	-5.047
105	25	96.603	90.231	-6.372
105	26	92.694	86.590	-6.104
105	27	96.238	89.706	-6.532
105	28	94.793	88.800	-5.993
105	29	95.882	89.430	-6.452
Max		97.815	96.942	0.164
Average		95.226	92.298	-2.927
Min		91.373	86.590	-6.532
Std Dev		1.752	2.681	2.105

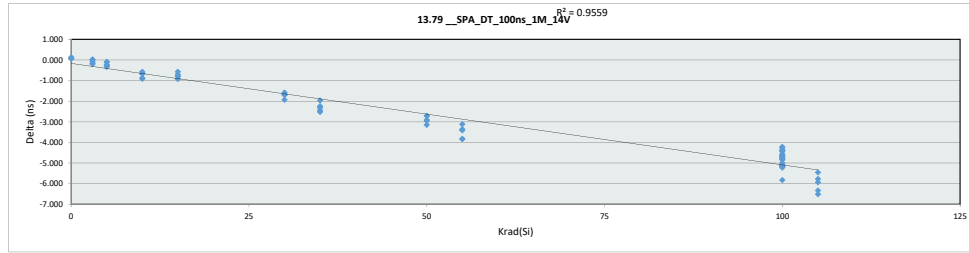


13.76 SPB_DT_100ns_1M_12V											
Test Site											
Tester											
Test Number											
Max Limit	110	ns									
Min Limit	85	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	92.608	92.055	93.139	94.769	93.259	89.792	89.159	90.526	88.451	87.734	86.590
Average	93.907	95.274	94.631	95.819	95.511	91.031	93.690	91.645	91.733	90.352	88.951
Max	94.632	96.634	96.802	96.891	96.942	92.497	95.349	93.156	93.310	92.369	90.231
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

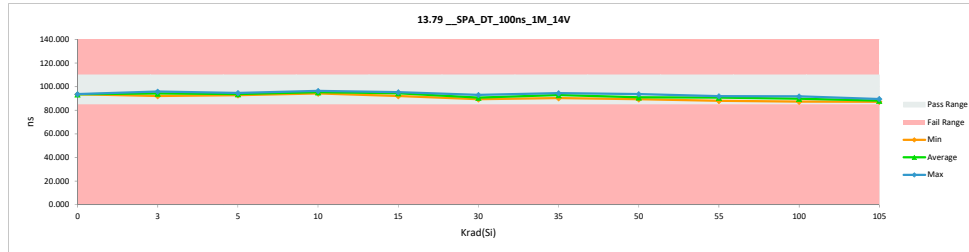


HDR TID Report
TPS7H5001-SP

13.79 SPA_DT_100ns_1M_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	110	110		
Min Limit	85	85		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	93.583	93.677	0.094
0	118	93.519	93.588	0.069
0	122	93.437	93.579	0.142
3	301	93.698	93.624	-0.074
3	306	95.056	95.074	0.018
3	307	95.909	95.940	0.031
3	308	92.223	92.045	-0.178
3	309	94.613	94.432	-0.181
5	310	94.510	94.290	-0.220
5	311	94.760	94.664	-0.096
5	312	94.372	94.289	-0.083
5	314	93.183	92.916	-0.267
5	316	93.046	92.722	-0.324
10	317	95.912	95.240	-0.672
10	318	95.811	95.245	-0.566
10	319	97.306	96.404	-0.902
10	321	96.421	95.553	-0.868
10	322	94.877	94.249	-0.628
15	323	95.957	95.192	-0.765
15	325	92.622	92.061	-0.561
15	326	95.317	94.528	-0.789
15	328	96.264	95.351	-0.913
15	333	95.930	95.237	-0.693
30	334	91.023	89.334	-1.689
30	335	92.641	90.716	-1.925
30	337	91.910	90.231	-1.679
30	339	91.773	90.131	-1.642
30	341	94.649	93.076	-1.573
35	342	92.887	90.375	-2.512
35	345	95.219	92.918	-2.301
35	346	95.534	93.100	-2.434
35	347	96.497	94.545	-1.952
35	348	96.044	93.815	-2.229
50	349	93.138	90.439	-2.699
50	350	93.737	90.834	-2.903
50	351	92.304	89.356	-2.948
50	353	93.677	90.536	-3.141
50	354	96.462	93.750	-2.712
55	355	94.828	91.003	-3.825
55	357	94.896	91.788	-3.108
55	359	95.756	91.932	-3.824
55	362	94.241	90.836	-3.405
55	363	91.435	88.086	-3.349
100	1	93.686	88.855	-4.831
100	3	93.009	88.808	-4.201
100	4	91.800	87.370	-4.430
100	5	95.134	90.530	-4.604
100	6	94.898	90.346	-4.552
100	7	93.216	88.498	-4.718
100	8	94.268	89.160	-5.108
100	9	92.449	88.197	-4.252
100	10	95.421	89.602	-5.819
100	11	95.528	90.319	-5.209
100	12	96.202	91.852	-4.350
100	13	95.524	91.159	-4.365
100	14	96.604	91.457	-5.147
100	15	94.955	89.881	-5.074
100	16	96.405	91.499	-4.906
100	17	94.881	90.079	-4.802
100	18	95.434	90.320	-5.114
100	19	93.326	88.676	-4.650
100	20	95.424	90.641	-4.783
100	21	93.463	88.825	-4.638
100	22	94.496	89.352	-5.144
100	24	94.833	90.098	-4.735
105	25	95.932	89.612	-6.320
105	26	92.740	87.293	-5.447
105	27	94.784	88.281	-6.503
105	28	93.486	87.722	-5.764
105	29	94.036	88.112	-5.924
Max		97.306	96.404	0.142
Average		94.414	91.618	-2.796
Min		91.023	87.293	-6.503
Std Dev		1.458	2.473	2.057

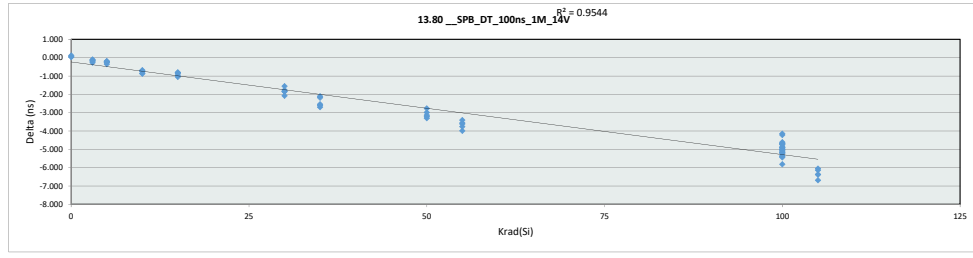


13.79 SPA_DT_100ns_1M_14V											
Test Site											
Tester											
Test Number											
Max Limit	110	ns									
Min Limit	85	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	93.579	92.045	92.722	94.249	92.061	89.334	90.375	89.356	88.086	87.370	87.293
Average	93.615	94.223	93.776	95.338	94.474	90.698	92.951	90.983	90.729	89.797	88.204
Max	93.677	95.940	94.664	96.404	95.351	93.076	94.545	93.750	91.932	91.852	89.612
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

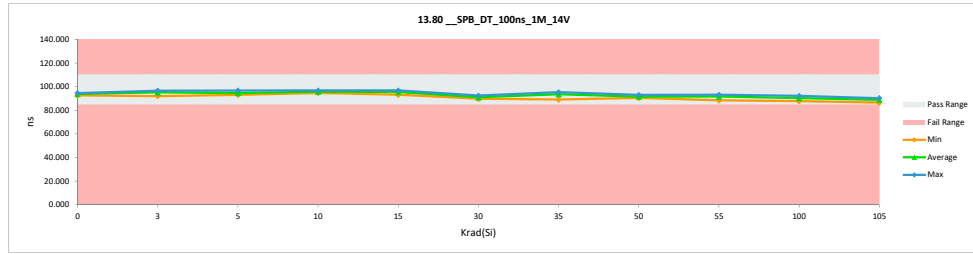


HDR TID Report
TPS7H5001-SP

13.80 SPB DT 100ns 1M 14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	110	110		
Min Limit	85	85		
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	94.361	94.411	0.050
0	118	92.579	92.631	0.052
0	122	94.502	94.633	0.131
3	301	95.656	95.572	-0.084
3	306	96.780	96.656	-0.124
3	307	96.353	96.176	-0.177
3	308	92.199	92.037	-0.162
3	309	96.052	95.790	-0.262
5	310	93.914	93.692	-0.222
5	311	96.426	96.226	-0.200
5	312	96.974	96.805	-0.169
5	314	93.404	93.150	-0.254
5	316	93.697	93.358	-0.339
10	317	97.449	96.614	-0.835
10	318	96.264	95.540	-0.724
10	319	97.770	96.904	-0.866
10	321	96.128	95.402	-0.726
10	322	95.379	94.705	-0.674
15	323	95.751	94.914	-0.837
15	325	94.027	93.234	-0.793
15	326	96.897	95.860	-1.037
15	328	97.452	96.507	-0.945
15	333	97.711	96.906	-0.805
30	334	92.355	90.789	-1.566
30	335	93.364	91.536	-1.828
30	337	93.053	91.225	-1.828
30	339	91.348	89.808	-1.540
30	341	94.287	92.537	-1.750
35	342	91.698	89.104	-2.594
35	345	97.653	94.961	-2.692
35	346	97.177	94.646	-2.531
35	347	97.542	95.370	-2.172
35	348	96.382	94.290	-2.092
50	349	93.685	90.560	-3.125
50	350	95.117	91.911	-3.206
50	351	93.683	90.709	-2.974
50	353	95.231	91.940	-3.291
50	354	95.866	93.106	-2.760
55	355	97.220	93.239	-3.981
55	357	96.248	92.695	-3.553
55	359	95.305	91.703	-3.602
55	362	96.031	92.636	-3.395
55	363	92.212	88.459	-3.753
100	1	96.148	90.721	-5.427
100	3	93.082	88.871	-4.211
100	4	92.479	87.743	-4.736
100	5	96.877	92.202	-4.675
100	6	97.292	92.285	-5.007
100	7	93.376	88.383	-4.993
100	8	95.234	90.056	-5.178
100	9	92.574	88.435	-4.139
100	10	96.135	90.332	-5.803
100	11	95.159	89.939	-5.220
100	12	95.518	90.811	-4.707
100	13	96.579	91.695	-4.884
100	14	97.451	92.082	-5.369
100	15	97.237	92.083	-5.154
100	16	96.689	91.789	-4.900
100	17	94.040	89.444	-4.596
100	18	97.186	91.831	-5.355
100	19	92.789	88.136	-4.653
100	20	95.961	91.100	-4.861
100	21	94.381	89.416	-4.965
100	22	95.143	90.021	-5.122
100	24	95.013	89.960	-5.053
105	25	96.645	90.279	-6.366
105	26	92.640	86.596	-6.044
105	27	96.312	89.638	-6.674
105	28	94.864	88.734	-6.130
105	29	95.801	89.437	-6.364
	Max	97.770	96.906	0.131
	Average	95.226	92.292	-2.933
	Min	91.348	86.596	-6.674
	Std Dev	1.749	2.681	2.114

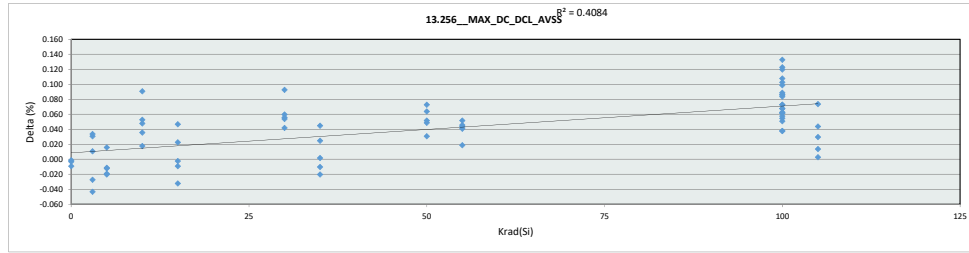


13.80 SPB DT 100ns 1M 14V											
Test Site											
Tester											
Test Number											
Max Limit	110	ns									
Min Limit	85	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	92.631	92.037	93.150	94.705	93.234	89.808	89.104	90.560	88.459	87.743	86.596
Average	93.892	95.246	94.646	95.833	95.484	91.077	93.674	91.645	91.746	90.333	88.937
Max	94.633	96.656	96.805	96.904	96.906	92.537	95.370	93.106	93.239	92.285	90.279
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

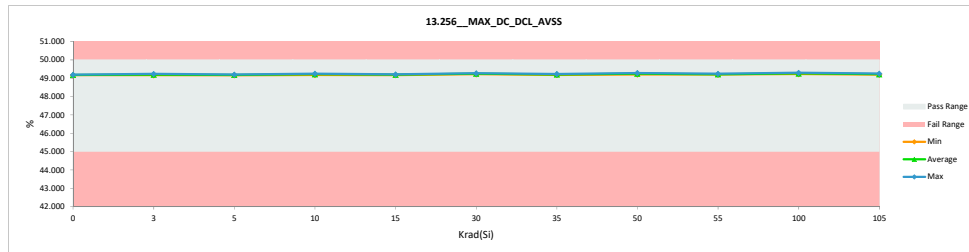


HDR TID Report
TPS7H5001-SP

		13.256 MAX DC DCL AVSS		
Test Site				0.034
Tester				
Test Number				
Unit		%	%	
Max Limit		50	50	
Min Limit		45	45	
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	49.195	49.192	-0.003
0	118	49.169	49.168	-0.001
0	122	49.199	49.190	-0.009
3	301	49.201	49.235	0.034
3	306	49.208	49.165	-0.043
3	307	49.166	49.177	0.011
3	308	49.209	49.240	0.031
3	309	49.212	49.185	-0.027
5	310	49.178	49.166	-0.012
5	311	49.193	49.174	-0.019
5	312	49.188	49.177	-0.011
5	314	49.206	49.186	-0.020
5	316	49.188	49.204	0.016
10	317	49.161	49.252	0.091
10	318	49.190	49.243	0.053
10	319	49.206	49.224	0.018
10	321	49.136	49.184	0.048
10	322	49.171	49.207	0.036
15	323	49.184	49.175	-0.009
15	325	49.207	49.205	-0.002
15	326	49.180	49.203	0.023
15	328	49.165	49.212	0.047
15	333	49.212	49.180	-0.032
30	334	49.194	49.236	0.042
30	335	49.213	49.273	0.060
30	337	49.213	49.267	0.054
30	339	49.170	49.263	0.093
30	341	49.164	49.220	0.056
35	342	49.180	49.225	0.045
35	345	49.210	49.235	0.025
35	346	49.215	49.205	-0.010
35	347	49.204	49.184	-0.020
35	348	49.192	49.194	0.002
50	349	49.206	49.255	0.049
50	350	49.179	49.243	0.064
50	351	49.256	49.287	0.031
50	353	49.214	49.266	0.052
50	354	49.133	49.206	0.073
55	355	49.174	49.226	0.052
55	357	49.158	49.204	0.046
55	359	49.161	49.202	0.041
55	362	49.202	49.246	0.044
55	363	49.195	49.214	0.019
100	1	49.190	49.298	0.108
100	3	49.194	49.232	0.038
100	4	49.211	49.262	0.051
100	5	49.187	49.273	0.086
100	6	49.178	49.277	0.099
100	7	49.197	49.252	0.055
100	8	49.246	49.284	0.038
100	9	49.177	49.264	0.087
100	10	49.155	49.275	0.120
100	11	49.193	49.282	0.089
100	12	49.170	49.254	0.084
100	13	49.162	49.230	0.068
100	14	49.158	49.226	0.068
100	15	49.231	49.293	0.062
100	16	49.122	49.245	0.123
100	17	49.200	49.273	0.073
100	18	49.208	49.270	0.062
100	19	49.144	49.247	0.103
100	20	49.150	49.283	0.133
100	21	49.197	49.270	0.073
100	22	49.205	49.263	0.058
100	24	49.186	49.249	0.063
105	25	49.200	49.203	0.003
105	26	49.186	49.200	0.014
105	27	49.178	49.208	0.030
105	28	49.192	49.236	0.044
105	29	49.181	49.255	0.074
Max		49.256	49.298	0.133
Average		49.188	49.230	0.042
Min		49.122	49.165	-0.043
Std Dev		0.024	0.036	0.040

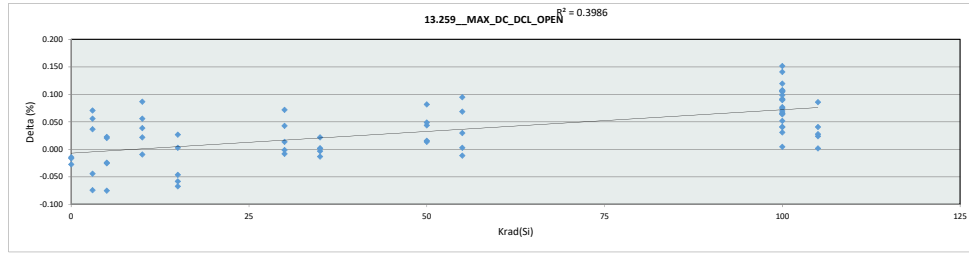


		13.256 MAX DC DCL AVSS									
Test Site											
Tester											
Test Number											
Max Limit		50	%								
Min Limit		45	%								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	45.000	45.000	45.000	45.000	45.000	45.000	45.000	45.000	45.000	45.000	45.000
Min	49.168	49.165	49.166	49.184	49.175	49.220	49.184	49.206	49.202	49.226	49.220
Average	49.183	49.200	49.181	49.222	49.195	49.252	49.209	49.251	49.218	49.264	49.220
Max	49.192	49.240	49.204	49.252	49.212	49.273	49.235	49.287	49.246	49.298	49.255
UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000

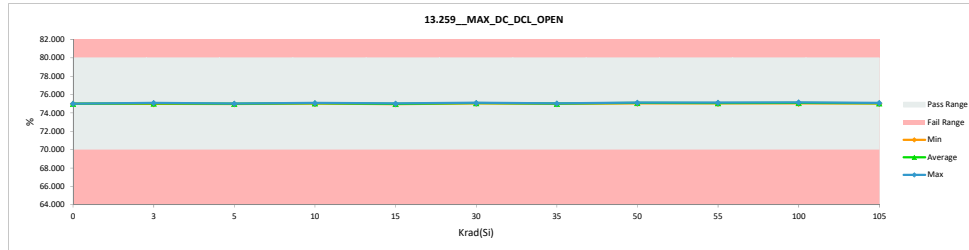


HDR TID Report
TPS7H5001-SP

		13.259 MAX DC DCL OPEN		
Test Site				
Tester				
Test Number				
Unit		%	%	
Max Limit		80	80	
Min Limit		70	70	
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	75.033	75.006	-0.027
0	118	75.020	75.006	-0.014
0	122	75.032	75.016	-0.016
3	301	75.060	75.016	-0.044
3	306	75.050	75.087	0.037
3	307	75.049	74.975	-0.074
3	308	74.988	75.059	0.071
3	309	75.001	75.057	0.056
5	310	74.969	74.992	0.023
5	311	75.045	74.970	-0.075
5	312	75.047	75.022	-0.025
5	314	74.981	75.002	0.021
5	316	75.045	75.021	-0.024
10	317	75.026	75.082	0.056
10	318	74.993	75.080	0.087
10	319	74.990	75.012	0.022
10	321	75.073	75.064	-0.009
10	322	75.000	75.039	0.039
15	323	74.967	74.994	0.027
15	325	75.020	74.962	-0.058
15	326	75.018	74.972	-0.046
15	328	75.026	75.029	0.003
15	333	75.039	74.972	-0.067
30	334	75.105	75.097	-0.008
30	335	75.054	75.097	0.043
30	337	74.985	75.057	0.072
30	339	75.079	75.078	-0.001
30	341	75.030	75.044	0.014
35	342	75.018	75.040	0.022
35	345	75.033	75.020	-0.013
35	346	74.985	74.982	-0.003
35	347	74.987	74.989	0.002
35	348	75.024	75.026	0.002
50	349	75.023	75.105	0.082
50	350	75.026	75.042	0.016
50	351	75.063	75.112	0.049
50	353	75.052	75.066	0.014
50	354	75.076	75.120	0.044
55	355	75.012	75.042	0.030
55	357	75.039	75.028	-0.011
55	359	75.051	75.054	0.003
55	362	75.051	75.120	0.069
55	363	75.001	75.096	0.095
100	1	75.024	75.101	0.077
100	3	75.048	75.155	0.107
100	4	75.066	75.097	0.031
100	5	75.022	75.027	0.005
100	6	75.059	75.123	0.064
100	7	74.981	75.051	0.070
100	8	74.980	75.100	0.120
100	9	74.938	75.044	0.106
100	10	74.963	75.071	0.108
100	11	74.987	75.128	0.141
100	12	74.993	75.145	0.152
100	13	75.005	75.071	0.066
100	14	75.021	75.087	0.066
100	15	75.062	75.114	0.052
100	16	74.993	75.090	0.097
100	17	75.063	75.138	0.075
100	18	75.002	75.093	0.091
100	19	75.075	75.116	0.041
100	20	75.020	75.119	0.099
100	21	74.982	75.074	0.092
100	22	75.047	75.152	0.105
100	24	75.039	75.080	0.041
105	25	75.015	75.056	0.041
105	26	75.068	75.096	0.028
105	27	74.960	75.046	0.086
105	28	75.023	75.025	0.002
105	29	75.018	75.042	0.024
	Max	75.105	75.155	0.152
	Average	75.023	75.058	0.035
	Min	74.938	74.962	-0.075
	Std Dev	0.034	0.049	0.051

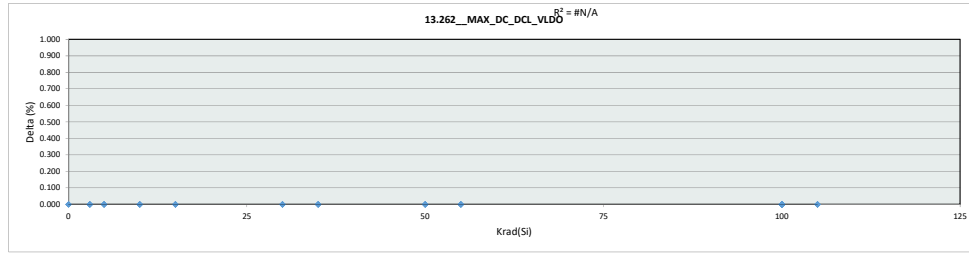


		13.259 MAX DC DCL OPEN										
Test Site												
Tester												
Test Number												
Max Limit		80	%									
Min Limit		70	%									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL	70.000	70.000	70.000	70.000	70.000	70.000	70.000	70.000	70.000	70.000	70.000	
Min	75.006	74.975	74.970	75.012	74.962	75.044	74.982	75.042	75.028	75.027	75.025	
Average	75.009	75.039	75.001	75.055	74.986	75.075	75.011	75.089	75.068	75.099	75.053	
Max	75.016	75.087	75.022	75.082	75.029	75.097	75.040	75.120	75.120	75.155	75.096	
UL	80.000	80.000	80.000	80.000	80.000	80.000	80.000	80.000	80.000	80.000	80.000	

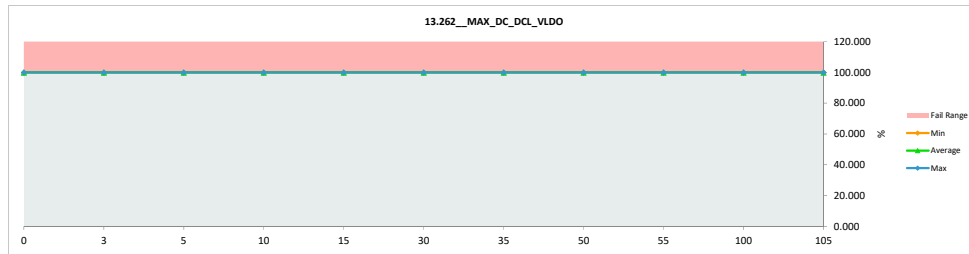


HDR TID Report
TPS7H5001-SP

		13.262_MAX_DC_DCL_VLDO		
Test Site				
Tester				
Test Number				
Unit		%	%	
Max Limit		100	100	
Min Limit				
Krad(Si)	Serial #	PRE_HDR	POST_HDR	Delta
0	117	100.000	100.000	0.000
0	118	100.000	100.000	0.000
0	122	100.000	100.000	0.000
3	301	100.000	100.000	0.000
3	306	100.000	100.000	0.000
3	307	100.000	100.000	0.000
3	308	100.000	100.000	0.000
3	309	100.000	100.000	0.000
5	310	100.000	100.000	0.000
5	311	100.000	100.000	0.000
5	312	100.000	100.000	0.000
5	314	100.000	100.000	0.000
5	316	100.000	100.000	0.000
10	317	100.000	100.000	0.000
10	318	100.000	100.000	0.000
10	319	100.000	100.000	0.000
10	321	100.000	100.000	0.000
10	322	100.000	100.000	0.000
15	323	100.000	100.000	0.000
15	325	100.000	100.000	0.000
15	326	100.000	100.000	0.000
15	328	100.000	100.000	0.000
15	333	100.000	100.000	0.000
30	334	100.000	100.000	0.000
30	335	100.000	100.000	0.000
30	337	100.000	100.000	0.000
30	339	100.000	100.000	0.000
30	341	100.000	100.000	0.000
35	342	100.000	100.000	0.000
35	345	100.000	100.000	0.000
35	346	100.000	100.000	0.000
35	347	100.000	100.000	0.000
35	348	100.000	100.000	0.000
50	349	100.000	100.000	0.000
50	350	100.000	100.000	0.000
50	351	100.000	100.000	0.000
50	353	100.000	100.000	0.000
50	354	100.000	100.000	0.000
55	355	100.000	100.000	0.000
55	357	100.000	100.000	0.000
55	359	100.000	100.000	0.000
55	362	100.000	100.000	0.000
55	363	100.000	100.000	0.000
100	1	100.000	100.000	0.000
100	3	100.000	100.000	0.000
100	4	100.000	100.000	0.000
100	5	100.000	100.000	0.000
100	6	100.000	100.000	0.000
100	7	100.000	100.000	0.000
100	8	100.000	100.000	0.000
100	9	100.000	100.000	0.000
100	10	100.000	100.000	0.000
100	11	100.000	100.000	0.000
100	12	100.000	100.000	0.000
100	13	100.000	100.000	0.000
100	14	100.000	100.000	0.000
100	15	100.000	100.000	0.000
100	16	100.000	100.000	0.000
100	17	100.000	100.000	0.000
100	18	100.000	100.000	0.000
100	19	100.000	100.000	0.000
100	20	100.000	100.000	0.000
100	21	100.000	100.000	0.000
100	22	100.000	100.000	0.000
100	24	100.000	100.000	0.000
105	25	100.000	100.000	0.000
105	26	100.000	100.000	0.000
105	27	100.000	100.000	0.000
105	28	100.000	100.000	0.000
105	29	100.000	100.000	0.000
Max		100.000	100.000	0.000
Average		100.000	100.000	0.000
Min		100.000	100.000	0.000
Std Dev		0.000	0.000	0.000



		13.262_MAX_DC_DCL_VLDO										
Test Site												
Tester												
Test Number												
Max Limit		100	%									
Min Limit		%										
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Average	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Max	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
UL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000



C Appendix: LDR TID Report Data

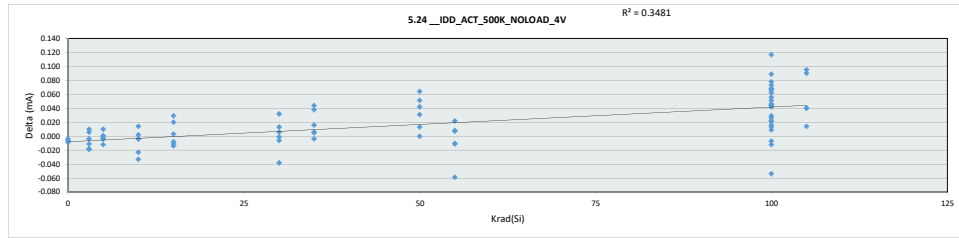
This appendix contains the LDR TID report data.

LDR TID REPORT
TPS7H5001-SP

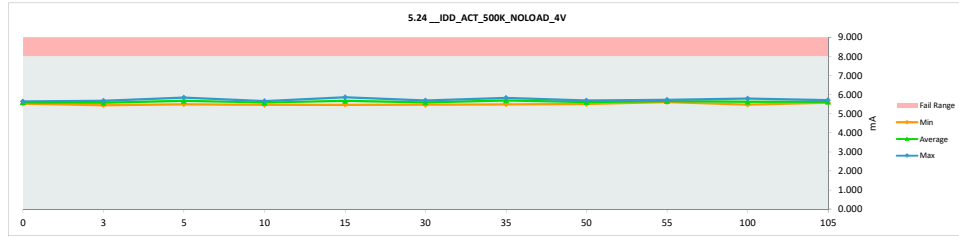
IDENTIFIER	DESCRIPTION
0	Control Unit
3	3 krad(Si) biased
5	3 krad(Si) unbiased
10	10 krad(Si) biased
15	10 krad(Si) unbiased
30	30 krad(Si) biased
35	30 krad(Si) unbiased
50	50 krad(Si) biased
55	50 krad(Si) unbiased
100	100 krad(Si) biased
105	100 krad(Si) unbiased

LDR TID Report
TPS7H5001-SP

5.24_IDD_ACT_500K_NOLOAD_4V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	8	8		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	5.527	5.521	-0.006
0	118	5.630	5.626	-0.004
0	122	5.659	5.650	-0.009
3	61	5.652	5.633	-0.019
3	62	5.685	5.681	-0.004
3	63	5.604	5.610	0.006
3	64	5.652	5.641	-0.011
3	65	5.460	5.442	-0.018
3	66	5.478	5.488	0.010
5	67	5.562	5.562	0.000
5	68	5.709	5.705	-0.004
5	69	5.487	5.488	0.001
5	70	5.834	5.844	0.010
5	71	5.678	5.666	-0.012
5	72	5.758	5.755	-0.003
10	73	5.652	5.619	-0.033
10	74	5.685	5.662	-0.023
10	75	5.604	5.618	0.014
10	76	5.652	5.648	-0.004
10	77	5.460	5.456	-0.004
10	78	5.478	5.480	0.002
15	79	5.562	5.591	0.029
15	80	5.709	5.701	-0.008
15	81	5.487	5.473	-0.014
15	82	5.834	5.854	0.020
15	83	5.678	5.667	-0.011
15	84	5.758	5.761	0.003
30	86	5.652	5.665	0.013
30	89	5.685	5.691	0.006
30	91	5.604	5.636	0.032
30	92	5.652	5.614	-0.038
30	94	5.460	5.459	-0.001
30	95	5.478	5.472	-0.006
35	100	5.562	5.606	0.044
35	101	5.709	5.747	0.038
35	102	5.487	5.493	0.006
35	103	5.834	5.830	-0.004
35	104	5.678	5.683	0.005
35	105	5.758	5.774	0.016
50	106	5.656	5.687	0.031
50	107	5.660	5.602	-0.042
50	108	5.451	5.515	0.064
50	109	5.608	5.659	0.051
50	110	5.628	5.641	0.013
50	111	5.510	5.510	0.000
55	113	5.675	5.682	0.007
55	114	5.611	5.600	-0.011
55	115	5.662	5.684	0.022
55	116	5.619	5.627	0.008
55	126	5.741	5.731	-0.010
55	128	5.668	5.609	-0.059
100	129	5.669	5.747	0.078
100	131	5.550	5.496	-0.054
100	132	5.672	5.665	-0.007
100	133	5.670	5.714	0.044
100	134	5.559	5.604	0.045
100	135	5.598	5.586	-0.012
100	138	5.614	5.680	0.066
100	139	5.476	5.544	0.068
100	140	5.587	5.633	0.046
100	141	5.500	5.521	0.021
100	142	5.650	5.723	0.073
100	143	5.625	5.693	0.068
100	144	5.556	5.598	0.042
100	220	5.664	5.677	0.013
100	221	5.540	5.556	0.016
100	224	5.578	5.634	0.056
100	225	5.453	5.480	0.027
100	226	5.736	5.787	0.051
100	228	5.544	5.661	0.117
100	232	5.603	5.692	0.089
100	233	5.611	5.634	0.023
100	236	5.469	5.498	0.029
100	238	5.659	5.721	0.062
100	243	5.520	5.529	0.009
105	244	5.594	5.608	0.014
105	246	5.494	5.589	0.095
105	248	5.642	5.682	0.040
105	374	5.614	5.704	0.090
105	376	5.532	5.572	0.040
Max		5.834	5.854	0.117
Average		5.611	5.629	0.018
Min		5.451	5.442	-0.009
Std Dev		0.094	0.095	0.034

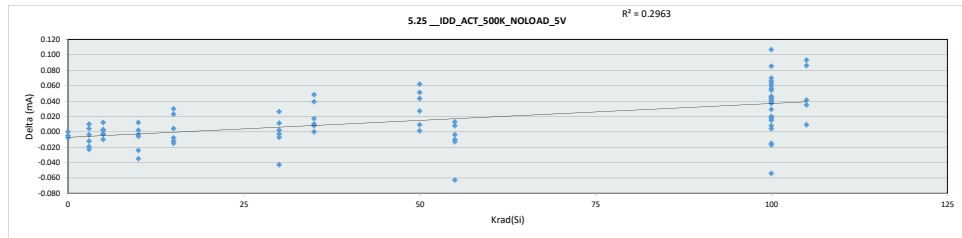


5.24_IDD_ACT_500K_NOLOAD											
Test Site											
Tester											
Test Number											
Max Limit	8	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	5.521	5.442	5.488	5.456	5.473	5.459	5.493	5.510	5.600	5.480	5.572
Average	5.599	5.583	5.670	5.581	5.675	5.590	5.689	5.602	5.656	5.628	5.631
Max	5.650	5.681	5.844	5.662	5.854	5.691	5.830	5.687	5.731	5.787	5.704
UL	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000

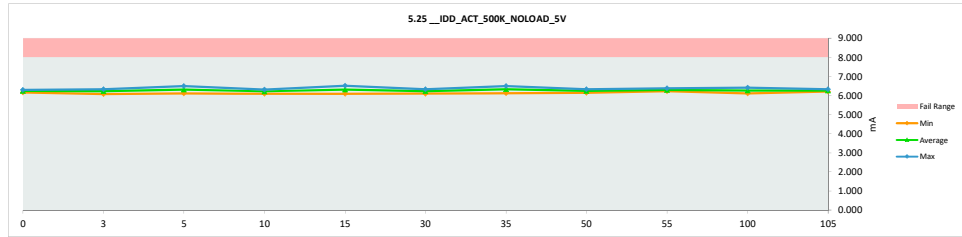


LDR TID Report
TPS7H5001-SP

5.25_IDD_ACT_500K_NOLOAD_5V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	8	8		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	6.164	6.159	-0.005
0	118	6.264	6.264	0.000
0	122	6.300	6.292	-0.008
3	61	6.297	6.274	-0.023
3	62	6.329	6.325	-0.004
3	63	6.238	6.242	0.004
3	64	6.295	6.283	-0.012
3	65	6.098	6.079	-0.019
3	66	6.117	6.127	0.010
5	67	6.196	6.199	0.003
5	68	6.344	6.341	-0.003
5	69	6.117	6.119	0.002
5	70	6.492	6.504	0.012
5	71	6.316	6.306	-0.010
5	72	6.394	6.391	-0.003
10	73	6.297	6.262	-0.035
10	74	6.329	6.305	-0.024
10	75	6.238	6.250	0.012
10	76	6.295	6.289	-0.006
10	77	6.098	6.094	-0.004
10	78	6.117	6.119	0.002
15	79	6.196	6.226	0.030
15	80	6.344	6.336	-0.008
15	81	6.117	6.102	-0.015
15	82	6.492	6.515	0.023
15	83	6.316	6.304	-0.012
15	84	6.394	6.398	0.004
30	86	6.297	6.308	0.011
30	89	6.329	6.331	0.002
30	91	6.238	6.264	0.026
30	92	6.295	6.252	-0.043
30	94	6.098	6.095	-0.003
30	95	6.117	6.110	-0.007
35	100	6.196	6.244	0.048
35	101	6.344	6.383	0.039
35	102	6.117	6.127	0.010
35	103	6.492	6.492	0.000
35	104	6.316	6.324	0.008
35	105	6.394	6.411	0.017
50	106	6.299	6.326	0.027
50	107	6.244	6.244	0.000
50	108	6.088	6.150	0.062
50	109	6.248	6.299	0.051
50	110	6.266	6.275	0.009
50	111	6.146	6.147	0.001
55	113	6.312	6.308	-0.004
55	114	6.244	6.231	-0.013
55	115	6.296	6.309	0.013
55	116	6.256	6.264	0.008
55	126	6.382	6.372	-0.010
55	128	6.312	6.249	-0.063
100	129	6.307	6.377	0.070
100	131	6.184	6.130	-0.054
100	132	6.312	6.297	-0.015
100	133	6.309	6.350	0.041
100	134	6.198	6.241	0.043
100	135	6.239	6.222	-0.017
100	138	6.249	6.315	0.066
100	139	6.107	6.167	0.060
100	140	6.221	6.266	0.045
100	141	6.138	6.153	0.015
100	142	6.285	6.341	0.056
100	143	6.260	6.323	0.063
100	144	6.190	6.227	0.037
100	220	6.301	6.309	0.008
100	221	6.177	6.194	0.017
100	224	6.213	6.259	0.046
100	225	6.093	6.112	0.019
100	226	6.382	6.420	0.038
100	228	6.179	6.286	0.107
100	232	6.242	6.327	0.085
100	233	6.267	6.287	0.020
100	236	6.105	6.134	0.029
100	238	6.299	6.353	0.054
100	243	6.154	6.158	0.004
105	244	6.229	6.238	0.009
105	246	6.127	6.220	0.093
105	248	6.279	6.314	0.035
105	374	6.248	6.334	0.086
105	376	6.172	6.213	0.041
Max		6.492	6.515	0.107
Average		6.249	6.265	0.016
Min		6.088	6.079	-0.063
Std Dev		0.097	0.097	0.032

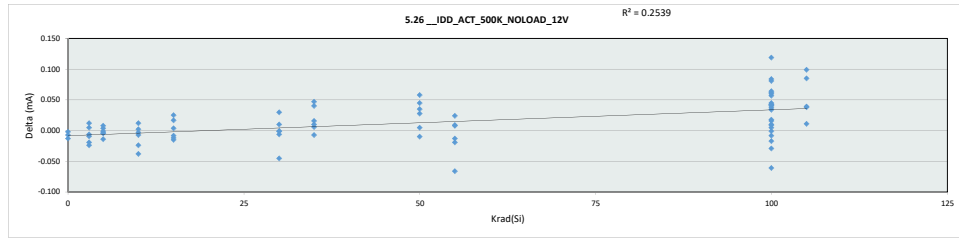


5.25_IDD_ACT_500K_NOLOAD_5V											
Test Site											
Tester											
Test Number											
Max Limit	8	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.159	6.079	6.119	6.094	6.102	6.095	6.127	6.147	6.231	6.112	6.213
Average	6.238	6.222	6.310	6.220	6.314	6.227	6.330	6.240	6.289	6.260	6.264
Max	6.292	6.325	6.504	6.305	6.515	6.331	6.492	6.326	6.372	6.420	6.334
UL	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000

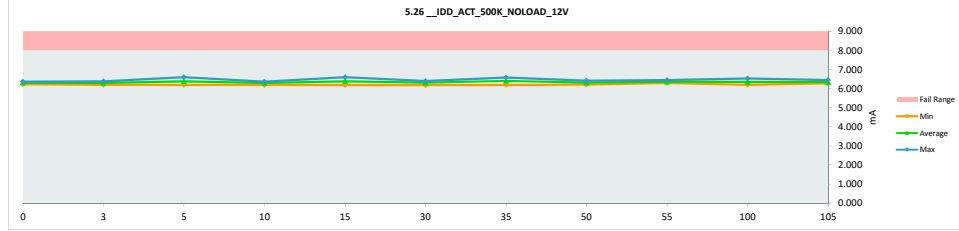


LDR TID Report
TPS7H5001-SP

5.26_IDD_ACT_500K_NOLOAD_12V				
Test Site				
Tester				
Test Number				
Unit		mA	mA	
Max Limit		8	8	
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	6.237	6.230	-0.007
0	118	6.359	6.357	-0.002
0	122	6.374	6.361	-0.013
3	61	6.383	6.359	-0.024
3	62	6.393	6.387	-0.006
3	63	6.298	6.303	0.005
3	64	6.376	6.367	-0.009
3	65	6.206	6.187	-0.019
3	66	6.188	6.200	0.012
5	67	6.270	6.269	-0.001
5	68	6.415	6.411	-0.004
5	69	6.185	6.189	0.004
5	70	6.582	6.590	0.008
5	71	6.394	6.380	-0.014
5	72	6.469	6.465	-0.004
10	73	6.383	6.345	-0.038
10	74	6.393	6.369	-0.024
10	75	6.298	6.310	0.012
10	76	6.376	6.369	-0.007
10	77	6.206	6.203	-0.003
10	78	6.188	6.190	0.002
15	79	6.270	6.295	0.025
15	80	6.415	6.407	-0.008
15	81	6.185	6.173	-0.012
15	82	6.582	6.599	0.017
15	83	6.394	6.379	-0.015
15	84	6.469	6.473	0.004
30	86	6.383	6.393	0.010
30	89	6.393	6.392	-0.001
30	91	6.298	6.328	0.030
30	92	6.376	6.331	-0.045
30	94	6.206	6.205	-0.001
30	95	6.188	6.182	-0.006
35	100	6.270	6.317	0.047
35	101	6.415	6.455	0.040
35	102	6.185	6.195	0.010
35	103	6.582	6.575	-0.007
35	104	6.394	6.400	0.006
35	105	6.469	6.485	0.016
50	106	6.381	6.409	0.028
50	107	6.290	6.325	0.035
50	108	6.179	6.237	0.058
50	109	6.309	6.354	0.045
50	110	6.361	6.366	0.005
50	111	6.222	6.212	-0.010
55	113	6.408	6.416	0.008
55	114	6.308	6.289	-0.019
55	115	6.357	6.381	0.024
55	116	6.323	6.332	0.009
55	126	6.462	6.449	-0.013
55	128	6.367	6.301	-0.066
100	129	6.404	6.488	0.084
100	131	6.255	6.194	-0.061
100	132	6.384	6.367	-0.017
100	133	6.411	6.448	0.037
100	134	6.274	6.314	0.040
100	135	6.308	6.279	-0.029
100	138	6.326	6.383	0.057
100	139	6.186	6.246	0.060
100	140	6.305	6.347	0.042
100	141	6.207	6.216	0.009
100	142	6.360	6.422	0.062
100	143	6.332	6.396	0.064
100	144	6.266	6.300	0.034
100	220	6.376	6.375	-0.001
100	221	6.245	6.251	0.005
100	224	6.294	6.339	0.045
100	225	6.205	6.215	0.010
100	226	6.481	6.524	0.043
100	228	6.263	6.382	0.119
100	232	6.324	6.405	0.081
100	233	6.358	6.374	0.016
100	236	6.192	6.210	0.018
100	238	6.367	6.409	0.042
100	243	6.241	6.233	-0.008
105	244	6.313	6.324	0.011
105	246	6.193	6.278	0.085
105	248	6.370	6.408	0.038
105	374	6.345	6.444	0.099
105	376	6.248	6.287	0.039
	Max	6.582	6.599	0.119
	Average	6.328	6.342	0.014
	Min	6.179	6.173	-0.006
	Std Dev	0.097	0.099	0.034

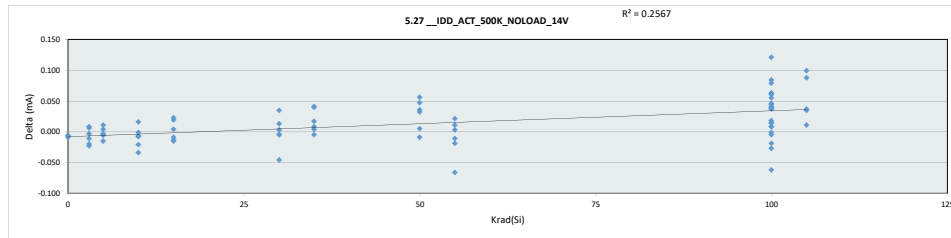


5.26_IDD_ACT_500K_NOLOAD											
Test Site											
Tester											
Test Number											
Max Limit		8	mA								
Min Limit			mA								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.230	6.187	6.189	6.190	6.173	6.182	6.195	6.212	6.289	6.194	6.278
Average	6.316	6.301	6.384	6.298	6.388	6.305	6.405	6.317	6.361	6.338	6.348
Max	6.361	6.387	6.590	6.369	6.599	6.393	6.575	6.409	6.449	6.524	6.444
UL	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000

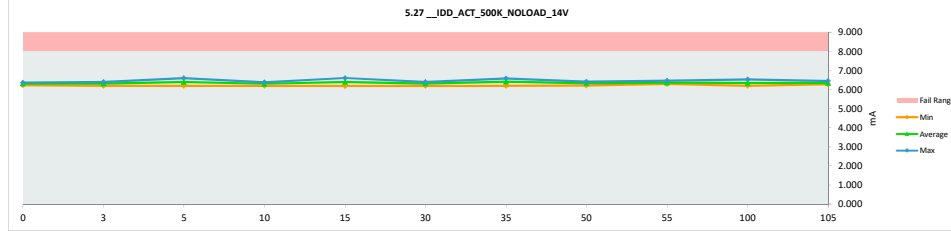


LDR TID Report
TPS7H5001-SP

5.27_IDD_ACT_500K_NOLOAD_14V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	8	8		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	6.241	6.234	-0.007
0	118	6.365	6.359	-0.006
0	122	6.377	6.368	-0.009
3	61	6.385	6.362	-0.023
3	62	6.395	6.392	-0.003
3	63	6.301	6.307	0.006
3	64	6.381	6.370	-0.011
3	65	6.212	6.192	-0.020
3	66	6.194	6.202	0.008
5	67	6.276	6.272	-0.004
5	68	6.420	6.414	-0.006
5	69	6.191	6.195	0.004
5	70	6.585	6.596	0.011
5	71	6.396	6.381	-0.015
5	72	6.472	6.469	-0.003
10	73	6.385	6.351	-0.034
10	74	6.395	6.374	-0.021
10	75	6.301	6.317	0.016
10	76	6.381	6.373	-0.008
10	77	6.212	6.205	-0.007
10	78	6.194	6.193	-0.001
15	79	6.276	6.299	0.023
15	80	6.420	6.411	-0.009
15	81	6.191	6.178	-0.013
15	82	6.585	6.604	0.019
15	83	6.396	6.381	-0.015
15	84	6.472	6.476	0.004
30	86	6.385	6.398	0.013
30	89	6.395	6.398	0.003
30	91	6.301	6.336	0.035
30	92	6.381	6.335	-0.046
30	94	6.212	6.208	-0.004
30	95	6.194	6.189	-0.005
35	100	6.276	6.316	0.040
35	101	6.420	6.461	0.041
35	102	6.191	6.199	0.008
35	103	6.585	6.580	-0.005
35	104	6.396	6.400	0.004
35	105	6.472	6.489	0.017
50	106	6.384	6.416	0.032
50	107	6.294	6.330	0.036
50	108	6.186	6.242	0.056
50	109	6.313	6.360	0.047
50	110	6.365	6.370	0.005
50	111	6.226	6.217	-0.009
55	113	6.416	6.419	0.003
55	114	6.312	6.293	-0.019
55	115	6.364	6.385	0.021
55	116	6.326	6.337	0.011
55	126	6.466	6.455	-0.011
55	128	6.373	6.307	-0.066
100	129	6.408	6.492	0.084
100	131	6.257	6.195	-0.062
100	132	6.389	6.370	-0.019
100	133	6.413	6.452	0.039
100	134	6.279	6.317	0.038
100	135	6.310	6.283	-0.027
100	138	6.330	6.390	0.060
100	139	6.194	6.249	0.055
100	140	6.309	6.349	0.040
100	141	6.211	6.219	0.008
100	142	6.366	6.429	0.063
100	143	6.337	6.400	0.063
100	144	6.267	6.304	0.037
100	220	6.380	6.379	-0.001
100	221	6.245	6.254	0.008
100	224	6.298	6.342	0.044
100	225	6.207	6.221	0.014
100	226	6.485	6.526	0.041
100	228	6.264	6.385	0.121
100	232	6.329	6.408	0.079
100	233	6.363	6.378	0.015
100	236	6.196	6.214	0.018
100	238	6.370	6.416	0.046
100	243	6.243	6.238	-0.005
105	244	6.318	6.329	0.011
105	246	6.196	6.284	0.088
105	248	6.374	6.409	0.035
105	374	6.350	6.449	0.099
105	376	6.253	6.290	0.037
	Max	6.585	6.604	0.121
	Average	6.332	6.346	0.014
	Min	6.186	6.178	-0.006
	Std Dev	0.097	0.099	0.034

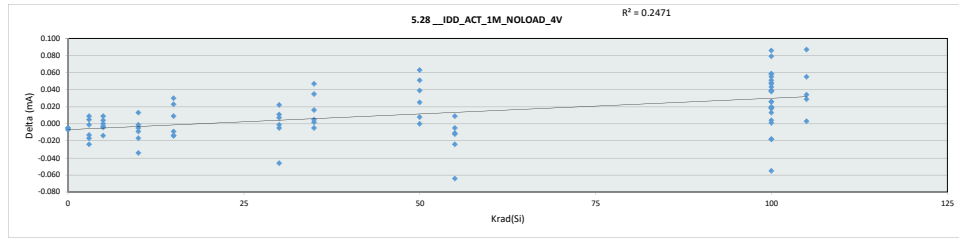


5.27_IDD_ACT_500K_NOLOAD_14V											
Test Site											
Tester											
Test Number											
Max Limit	8	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.234	6.192	6.195	6.193	6.178	6.189	6.199	6.217	6.293	6.195	6.284
Average	6.320	6.304	6.388	6.302	6.392	6.311	6.408	6.323	6.366	6.342	6.352
Max	6.368	6.392	6.596	6.374	6.604	6.398	6.580	6.416	6.455	6.526	6.449
UL	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000

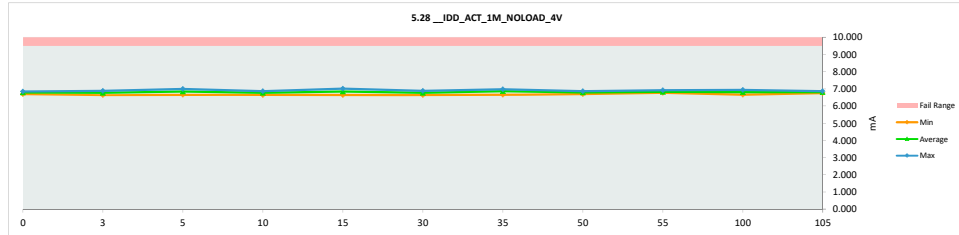


LDR TID Report
TPS7H5001-SP

5.28_IDD_ACT_1M_NOLOAD_4V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	9.5	9.5		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	6.713	6.706	-0.007
0	118	6.814	6.809	-0.005
0	122	6.853	6.848	-0.005
3	61	6.844	6.820	-0.024
3	62	6.980	6.879	-0.001
3	63	6.780	6.785	0.005
3	64	6.842	6.829	-0.013
3	65	6.650	6.633	-0.017
3	66	6.674	6.683	0.009
5	67	6.738	6.738	0.000
5	68	6.898	6.894	-0.004
5	69	6.662	6.666	0.004
5	70	6.985	6.994	0.009
5	71	6.861	6.847	-0.014
5	72	6.940	6.937	-0.003
10	73	6.844	6.810	-0.034
10	74	6.880	6.863	-0.017
10	75	6.780	6.793	0.013
10	76	6.842	6.833	-0.009
10	77	6.650	6.645	-0.005
10	78	6.674	6.673	-0.001
15	79	6.738	6.768	0.030
15	80	6.898	6.889	-0.009
15	81	6.662	6.648	-0.014
15	82	6.985	7.008	0.023
15	83	6.861	6.847	-0.014
15	84	6.940	6.949	0.009
30	86	6.844	6.855	0.011
30	89	6.880	6.887	0.007
30	91	6.780	6.802	0.022
30	92	6.842	6.796	-0.046
30	94	6.650	6.649	-0.001
30	95	6.674	6.669	-0.005
35	100	6.738	6.785	0.047
35	101	6.898	6.933	0.035
35	102	6.662	6.667	0.005
35	103	6.985	6.980	-0.005
35	104	6.861	6.863	0.002
35	105	6.940	6.956	0.016
50	106	6.842	6.867	0.025
50	107	6.747	6.786	0.039
50	108	6.636	6.699	0.063
50	109	6.796	6.847	0.051
50	110	6.814	6.822	0.008
50	111	6.692	6.692	0.000
55	113	6.858	6.834	-0.024
55	114	6.784	6.772	-0.012
55	115	6.842	6.837	-0.005
55	116	6.799	6.808	0.009
55	126	6.925	6.914	-0.011
55	128	6.864	6.800	-0.064
100	129	6.857	6.908	0.051
100	131	6.741	6.686	-0.055
100	132	6.865	6.847	-0.018
100	133	6.864	6.902	0.038
100	134	6.753	6.792	0.039
100	135	6.786	6.768	-0.018
100	138	6.801	6.860	0.059
100	139	6.662	6.717	0.055
100	140	6.771	6.810	0.039
100	141	6.685	6.698	0.013
100	142	6.840	6.887	0.047
100	143	6.803	6.861	0.058
100	144	6.747	6.773	0.026
100	220	6.847	6.848	0.001
100	221	6.720	6.738	0.018
100	224	6.757	6.800	0.043
100	225	6.649	6.668	0.019
100	226	6.923	6.942	0.019
100	228	6.730	6.816	0.086
100	232	6.791	6.870	0.079
100	233	6.795	6.815	0.020
100	236	6.646	6.672	0.026
100	238	6.851	6.899	0.048
100	243	6.700	6.704	0.004
105	244	6.779	6.782	0.003
105	246	6.681	6.768	0.087
105	248	6.831	6.860	0.029
105	374	6.789	6.844	0.055
105	376	6.720	6.754	0.034
Max		6.985	7.008	0.087
Average		6.795	6.808	0.012
Min		6.636	6.633	-0.064
Std Dev		0.091	0.091	0.030

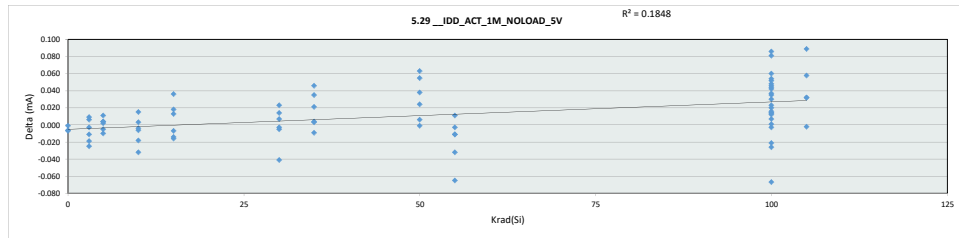


5.28_IDD_ACT_1M_NOLOAD											
Test Site											
Tester											
Test Number											
Max Limit	9.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.706	6.633	6.666	6.645	6.648	6.649	6.667	6.692	6.772	6.668	6.754
Average	6.788	6.772	6.846	6.770	6.852	6.776	6.864	6.786	6.828	6.803	6.802
Max	6.848	6.879	6.994	6.863	7.008	6.887	6.980	6.867	6.914	6.942	6.860
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

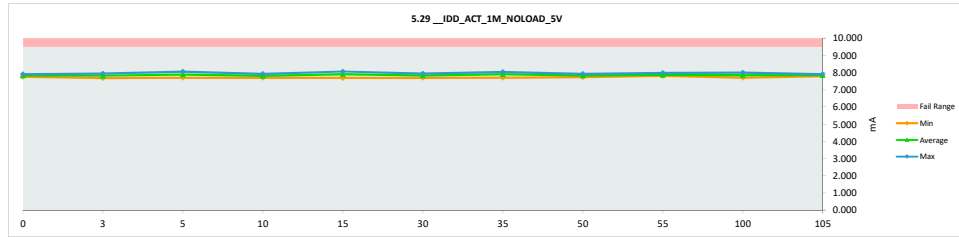


LDR TID Report
TPS7H5001-SP

5.29_IDD_ACT_1M_NOLOAD_5V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	9.5	9.5		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	7.759	7.752	-0.007
0	118	7.857	7.856	-0.001
0	122	7.910	7.904	-0.006
3	61	7.901	7.876	-0.025
3	62	7.943	7.940	-0.003
3	63	7.821	7.827	0.006
3	64	7.894	7.883	-0.011
3	65	7.698	7.679	-0.019
3	66	7.724	7.733	0.009
5	67	7.783	7.787	0.004
5	68	7.945	7.940	-0.005
5	69	7.699	7.703	0.004
5	70	8.045	8.056	0.011
5	71	7.908	7.898	-0.010
5	72	7.986	7.988	0.002
10	73	7.901	7.869	-0.032
10	74	7.943	7.925	-0.018
10	75	7.821	7.836	0.015
10	76	7.894	7.888	-0.006
10	77	7.698	7.694	-0.004
10	78	7.724	7.727	0.003
15	79	7.783	7.819	0.036
15	80	7.945	7.938	-0.007
15	81	7.699	7.683	-0.016
15	82	8.045	8.063	0.018
15	83	7.908	7.894	-0.014
15	84	7.986	7.999	0.013
30	86	7.901	7.915	0.014
30	89	7.943	7.950	0.007
30	91	7.821	7.844	0.023
30	92	7.894	7.853	-0.041
30	94	7.698	7.695	-0.003
30	95	7.724	7.719	-0.005
35	100	7.783	7.829	0.046
35	101	7.945	7.980	0.035
35	102	7.699	7.702	0.003
35	103	8.045	8.036	-0.009
35	104	7.908	7.912	0.004
35	105	7.986	8.007	0.021
50	106	7.899	7.923	0.024
50	107	7.804	7.842	0.038
50	108	7.681	7.744	0.063
50	109	7.846	7.901	0.055
50	110	7.865	7.871	0.006
50	111	7.739	7.738	-0.001
55	113	7.910	7.878	-0.032
55	114	7.822	7.811	-0.011
55	115	7.887	7.884	-0.003
55	116	7.845	7.856	0.011
55	126	7.986	7.975	-0.011
55	128	7.922	7.857	-0.065
100	129	7.906	7.954	0.048
100	131	7.791	7.724	-0.067
100	132	7.915	7.894	-0.021
100	133	7.915	7.945	0.030
100	134	7.806	7.843	0.037
100	135	7.839	7.813	-0.026
100	138	7.849	7.909	0.060
100	139	7.701	7.753	0.052
100	140	7.813	7.848	0.035
100	141	7.736	7.743	0.007
100	142	7.890	7.936	0.046
100	143	7.848	7.902	0.054
100	144	7.797	7.820	0.023
100	220	7.895	7.892	-0.003
100	221	7.772	7.784	0.012
100	224	7.796	7.838	0.042
100	225	7.699	7.713	0.014
100	226	7.981	7.995	0.014
100	228	7.775	7.861	0.086
100	232	7.840	7.921	0.081
100	233	7.864	7.880	0.016
100	236	7.688	7.708	0.020
100	238	7.906	7.950	0.044
100	243	7.744	7.745	0.001
105	244	7.824	7.822	-0.002
105	246	7.724	7.813	0.089
105	248	7.882	7.914	0.032
105	374	7.831	7.889	0.058
105	376	7.772	7.804	0.032
	Max	8.045	8.063	0.089
	Average	7.845	7.856	0.011
	Min	7.681	7.679	-0.067
	Std Dev	0.095	0.094	0.030

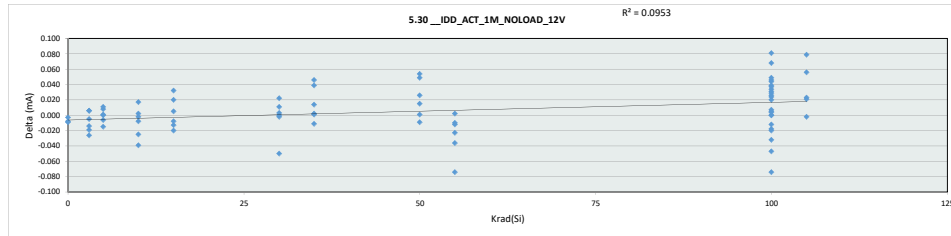


5.29_IDD_ACT_1M_NOLOAD											
Test Site											
Tester											
Test Number											
Max Limit	9.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.752	7.679	7.703	7.694	7.683	7.695	7.702	7.738	7.811	7.708	7.804
Average	7.837	7.823	7.895	7.823	7.899	7.829	7.911	7.837	7.877	7.849	7.848
Max	7.904	7.940	8.056	7.925	8.063	7.950	8.036	7.923	7.975	7.995	7.914
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

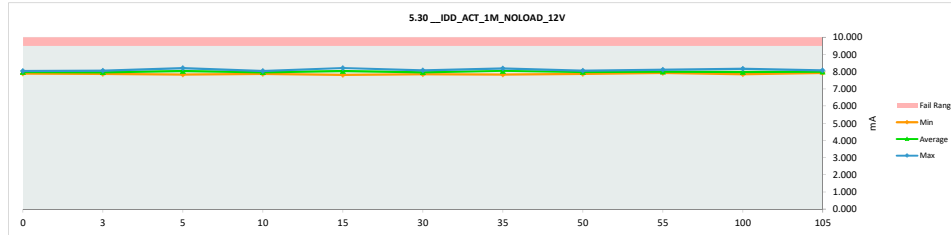


LDR TID Report
TPS7H5001-SP

5.30_IDD_ACT_1M_NOLOAD_12V				
Test Site				
Tester				
Test Number				
Unit		mA	mA	
Max Limit		9.5	9.5	
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	7.895	7.886	-0.009
0	118	8.028	8.025	-0.003
0	122	8.047	8.039	-0.008
3	61	8.061	8.035	-0.026
3	62	8.065	8.060	-0.005
3	63	7.930	7.936	0.006
3	64	8.045	8.031	-0.014
3	65	7.890	7.871	-0.019
3	66	7.860	7.866	0.006
5	67	7.915	7.915	0.000
5	68	8.078	8.072	-0.006
5	69	7.829	7.837	0.008
5	70	8.193	8.204	0.011
5	71	8.050	8.035	-0.015
5	72	8.125	8.126	0.001
10	73	8.061	8.022	-0.039
10	74	8.065	8.040	-0.025
10	75	7.930	7.947	0.017
10	76	8.045	8.037	-0.008
10	77	7.890	7.888	-0.002
10	78	7.860	7.862	0.002
15	79	7.915	7.947	0.032
15	80	8.078	8.070	-0.008
15	81	7.829	7.816	-0.013
15	82	8.193	8.213	0.020
15	83	8.050	8.030	-0.020
15	84	8.125	8.130	0.005
30	86	8.061	8.072	0.011
30	89	8.065	8.068	0.003
30	91	7.930	7.952	0.022
30	92	8.045	7.995	-0.050
30	94	7.890	7.890	0.000
30	95	7.860	7.858	-0.002
35	100	7.915	7.961	0.046
35	101	8.078	8.117	0.039
35	102	7.829	7.831	0.002
35	103	8.193	8.182	-0.011
35	104	8.050	8.051	0.001
35	105	8.125	8.139	0.014
50	106	8.051	8.066	0.015
50	107	7.965	7.991	0.026
50	108	7.846	7.900	0.054
50	109	7.961	8.010	0.049
50	110	8.039	8.040	0.001
50	111	7.876	7.867	-0.009
55	113	8.083	8.047	-0.036
55	114	7.943	7.920	-0.023
55	115	8.003	7.993	-0.010
55	116	7.974	7.976	0.002
55	126	8.130	8.118	-0.012
55	128	8.026	7.952	-0.074
100	129	8.083	8.122	0.039
100	131	7.918	7.844	-0.074
100	132	8.048	8.016	-0.032
100	133	8.100	8.124	0.024
100	134	7.943	7.974	0.031
100	135	7.967	7.920	-0.047
100	138	7.989	8.038	0.049
100	139	7.851	7.895	0.044
100	140	7.965	7.994	0.029
100	141	7.869	7.857	-0.012
100	142	8.029	8.066	0.037
100	143	7.986	8.032	0.046
100	144	7.929	7.949	0.020
100	220	8.031	8.011	-0.020
100	221	7.896	7.896	0.000
100	224	7.944	7.978	0.034
100	225	7.897	7.902	0.005
100	226	8.156	8.160	0.004
100	228	7.930	8.011	0.081
100	232	7.990	8.058	0.068
100	233	8.027	8.034	0.007
100	236	7.848	7.848	0.000
100	238	8.031	8.057	0.026
100	243	7.901	7.883	-0.018
105	244	7.976	7.974	-0.002
105	246	7.846	7.925	0.079
105	248	8.047	8.068	0.021
105	374	8.005	8.061	0.056
105	376	7.918	7.941	0.023
	Max	8.193	8.213	0.081
	Average	7.989	7.995	0.006
	Min	7.829	7.816	-0.014
	Std Dev	0.095	0.095	0.030

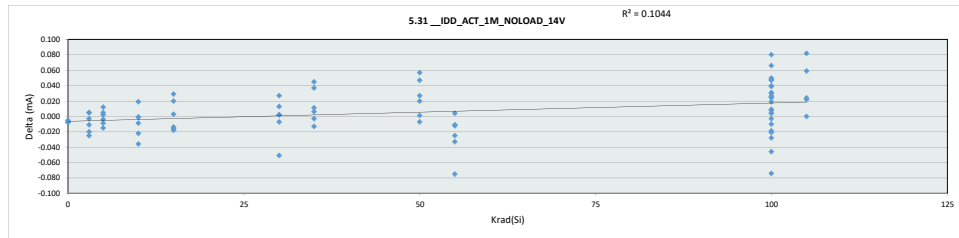


5.30_IDD_ACT_1M_NOLOAD											
Test Site											
Tester											
Test Number											
Max Limit		9.5	mA								
Min Limit			mA								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.886	7.866	7.837	7.862	7.816	7.858	7.831	7.867	7.920	7.844	7.925
Average	7.983	7.967	8.032	7.966	8.034	7.973	8.047	7.979	8.001	7.986	7.994
Max	8.039	8.060	8.204	8.040	8.213	8.072	8.182	8.066	8.118	8.160	8.068
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

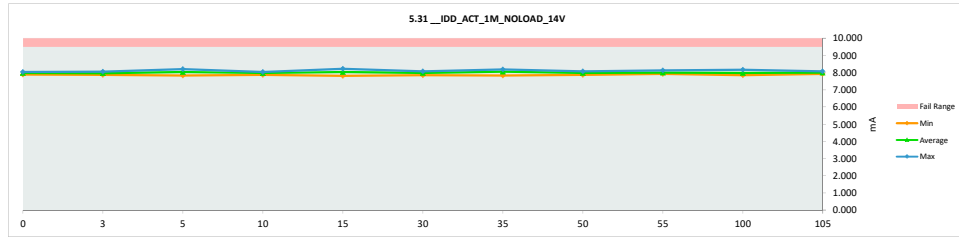


LDR TID Report
TPS7H5001-SP

5.31_IDD_ACT_1M_NOLOAD_14V					
Test Site					
Tester					
Test Number					
Unit		mA	mA		
Max Limit		9.5	9.5		
Min Limit					
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta	
0	117	7.899	7.892	-0.007	
0	118	8.031	8.026	-0.005	
0	122	8.050	8.042	-0.008	
3	61	8.064	8.039	-0.025	
3	62	8.068	8.065	-0.003	
3	63	7.933	7.938	0.005	
3	64	8.048	8.037	-0.011	
3	65	7.892	7.872	-0.020	
3	66	7.867	7.872	0.005	
5	67	7.922	7.918	-0.004	
5	68	8.084	8.075	-0.009	
5	69	7.833	7.838	0.005	
5	70	8.198	8.210	0.012	
5	71	8.054	8.039	-0.015	
5	72	8.130	8.132	0.002	
10	73	8.064	8.028	-0.036	
10	74	8.068	8.046	-0.022	
10	75	7.933	7.952	0.019	
10	76	8.048	8.039	-0.009	
10	77	7.892	7.891	-0.001	
10	78	7.867	7.866	-0.001	
15	79	7.922	7.951	0.029	
15	80	8.084	8.070	-0.014	
15	81	7.833	7.817	-0.016	
15	82	8.198	8.218	0.020	
15	83	8.054	8.036	-0.018	
15	84	8.130	8.133	0.003	
30	86	8.064	8.077	0.013	
30	89	8.068	8.070	0.002	
30	91	7.933	7.960	0.027	
30	92	8.048	7.997	-0.051	
30	94	7.892	7.894	0.002	
30	95	7.867	7.860	-0.007	
35	100	7.922	7.967	0.045	
35	101	8.084	8.121	0.037	
35	102	7.833	7.839	0.006	
35	103	8.198	8.185	-0.013	
35	104	8.054	8.051	-0.003	
35	105	8.130	8.141	0.011	
50	106	8.052	8.072	0.020	
50	107	7.968	7.957	-0.011	
50	108	7.846	7.903	0.057	
50	109	7.969	8.016	0.047	
50	110	8.044	8.045	0.001	
50	111	7.879	7.872	-0.007	
55	113	8.085	8.052	-0.033	
55	114	7.950	7.925	-0.025	
55	115	8.008	7.996	-0.012	
55	116	7.979	7.983	0.004	
55	126	8.134	8.123	-0.011	
55	128	8.031	7.956	-0.075	
100	129	8.085	8.124	0.039	
100	131	7.921	7.847	-0.074	
100	132	8.050	8.022	-0.028	
100	133	8.103	8.129	0.026	
100	134	7.951	7.977	0.026	
100	135	7.971	7.925	-0.046	
100	138	7.993	8.043	0.050	
100	139	7.853	7.900	0.047	
100	140	7.966	7.996	0.030	
100	141	7.870	7.860	-0.010	
100	142	8.030	8.070	0.040	
100	143	7.990	8.038	0.048	
100	144	7.934	7.953	0.019	
100	220	8.033	8.014	-0.019	
100	221	7.901	7.898	-0.003	
100	224	7.949	7.980	0.031	
100	225	7.900	7.908	0.008	
100	226	8.159	8.163	0.004	
100	228	7.933	8.013	0.080	
100	232	7.996	8.062	0.066	
100	233	8.030	8.039	0.009	
100	236	7.852	7.856	0.004	
100	238	8.036	8.060	0.024	
100	243	7.908	7.887	-0.021	
105	244	7.978	7.978	0.000	
105	246	7.848	7.930	0.082	
105	248	8.050	8.072	0.022	
105	374	8.006	8.065	0.059	
105	376	7.921	7.945	0.024	
Max		8.198	8.218	0.082	
Average		7.993	7.999	-0.006	
Min		7.833	7.817	-0.016	
Std Dev		0.095	0.095	0.030	

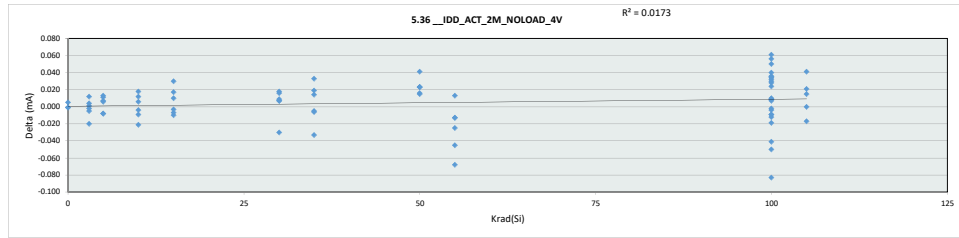


5.31_IDD_ACT_1M_NOLOAD											
Test Site											
Tester											
Test Number											
Max Limit		9.5	mA								
Min Limit			mA								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.892	7.872	7.838	7.866	7.817	7.860	7.839	7.872	7.925	7.847	7.930
Average	7.987	7.971	8.035	7.970	8.038	7.976	8.051	7.984	8.006	7.990	7.998
Max	8.042	8.065	8.210	8.046	8.218	8.077	8.185	8.072	8.123	8.163	8.072
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

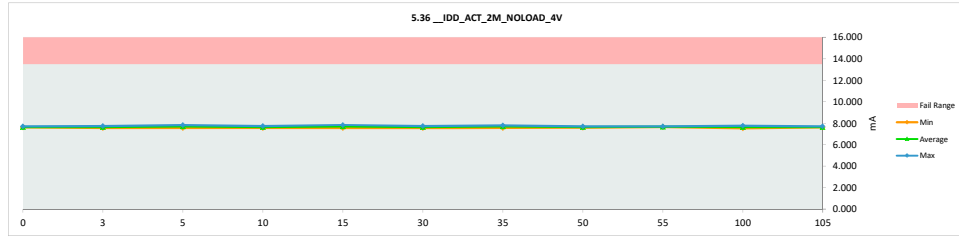


LDR TID Report
TPS7H5001-SP

5.36_IDD_ACT_2M_NOLOAD_4V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	13.5	13.5		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	7.611	7.610	-0.001
0	118	7.639	7.638	-0.001
0	122	7.699	7.704	0.005
3	61	7.675	7.655	-0.020
3	62	7.741	7.745	0.004
3	63	7.672	7.684	0.012
3	64	7.717	7.715	-0.002
3	65	7.547	7.542	-0.005
3	66	7.570	7.571	0.001
5	67	7.560	7.566	0.006
5	68	7.738	7.730	-0.008
5	69	7.561	7.568	0.007
5	70	7.820	7.833	0.013
5	71	7.711	7.703	-0.008
5	72	7.761	7.772	0.011
10	73	7.675	7.654	-0.021
10	74	7.741	7.732	-0.009
10	75	7.672	7.690	0.018
10	76	7.717	7.713	-0.004
10	77	7.547	7.553	0.006
10	78	7.570	7.582	0.012
15	79	7.560	7.590	0.030
15	80	7.738	7.735	-0.003
15	81	7.561	7.551	-0.010
15	82	7.820	7.837	0.017
15	83	7.711	7.704	-0.007
15	84	7.761	7.771	0.010
30	86	7.675	7.691	0.016
30	89	7.741	7.748	0.007
30	91	7.672	7.690	0.018
30	92	7.717	7.687	-0.030
30	94	7.547	7.556	0.009
30	95	7.570	7.577	0.007
35	100	7.560	7.593	0.033
35	101	7.738	7.757	0.019
35	102	7.561	7.556	-0.005
35	103	7.820	7.787	-0.033
35	104	7.711	7.705	-0.006
35	105	7.761	7.775	0.014
50	106	7.674	7.697	0.023
50	107	7.574	7.597	0.023
50	108	7.599	7.614	0.015
50	109	7.612	7.653	0.041
50	110	7.628	7.651	0.023
50	111	7.608	7.624	0.016
55	113	7.748	7.703	-0.045
55	114	7.650	7.637	-0.013
55	115	7.663	7.650	-0.013
55	116	7.620	7.633	0.013
55	126	7.735	7.710	-0.025
55	128	7.701	7.633	-0.068
100	129	7.675	7.715	0.040
100	131	7.618	7.568	-0.050
100	132	7.725	7.684	-0.041
100	133	7.688	7.718	0.030
100	134	7.594	7.628	0.034
100	135	7.779	7.696	-0.083
100	138	7.630	7.665	0.035
100	139	7.573	7.605	0.032
100	140	7.622	7.672	0.050
100	141	7.571	7.562	-0.009
100	142	7.680	7.704	0.024
100	143	7.644	7.680	0.036
100	144	7.602	7.609	0.007
100	220	7.705	7.701	-0.004
100	221	7.583	7.581	-0.002
100	224	7.609	7.637	0.028
100	225	7.594	7.585	-0.009
100	226	7.764	7.773	0.009
100	228	7.589	7.650	0.061
100	232	7.657	7.713	0.056
100	233	7.687	7.697	0.010
100	236	7.581	7.569	-0.012
100	238	7.749	7.758	0.009
100	243	7.549	7.530	-0.019
105	244	7.622	7.605	-0.017
105	246	7.567	7.608	0.041
105	248	7.712	7.712	0.000
105	374	7.640	7.655	0.015
105	376	7.606	7.627	0.021
Max		7.820	7.837	0.061
Average		7.657	7.662	0.005
Min		7.547	7.530	-0.083
Std Dev		0.075	0.072	0.025

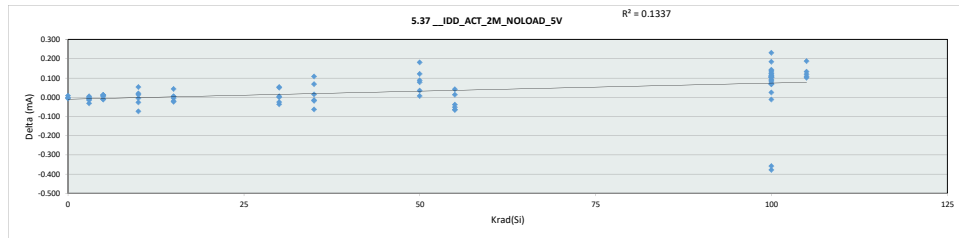


5.36_IDD_ACT_2M_NOLOAD											
Test Site											
Tester											
Test Number											
Max Limit	13.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.610	7.542	7.566	7.553	7.551	7.556	7.556	7.597	7.633	7.530	7.605
Average	7.651	7.652	7.695	7.654	7.698	7.658	7.696	7.639	7.661	7.654	7.641
Max	7.704	7.745	7.833	7.732	7.837	7.748	7.787	7.697	7.710	7.773	7.712
UL	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500

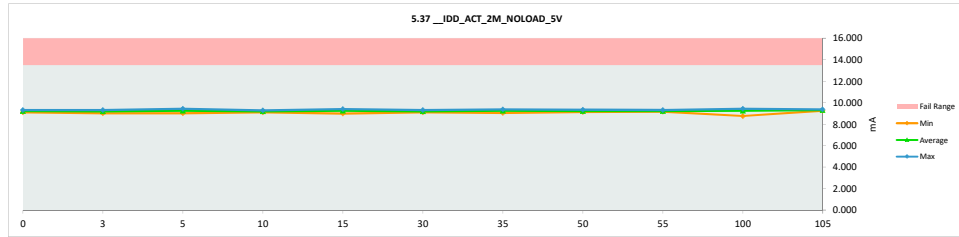


LDR TID Report
TPS7H5001-SP

5.37_IDD_ACT_2M_NOLOAD_5V					
Test Site					
Tester					
Test Number					
Unit		mA	mA		
Max Limit		13.5	13.5		
Min Limit					
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta	
0	117	9.103	9.096	-0.007	
0	118	9.228	9.226	-0.002	
0	122	9.320	9.328	0.008	
3	61	9.283	9.251	-0.032	
3	62	9.313	9.303	-0.010	
3	63	9.154	9.159	0.005	
3	64	9.265	9.260	-0.005	
3	65	9.044	9.029	-0.015	
3	66	9.104	9.104	0.000	
5	67	9.101	9.112	0.011	
5	68	9.314	9.303	-0.011	
5	69	8.992	9.003	0.011	
5	70	9.426	9.428	0.002	
5	71	9.278	9.266	-0.012	
5	72	9.372	9.383	0.011	
10	73	9.283	9.209	-0.074	
10	74	9.313	9.286	-0.027	
10	75	9.154	9.173	0.019	
10	76	9.265	9.260	-0.005	
10	77	9.044	9.096	0.052	
10	78	9.104	9.116	0.012	
15	79	9.101	9.143	0.042	
15	80	9.314	9.316	0.002	
15	81	8.992	8.987	-0.005	
15	82	9.426	9.406	-0.020	
15	83	9.278	9.254	-0.024	
15	84	9.372	9.377	0.005	
30	86	9.283	9.258	-0.025	
30	89	9.313	9.313	0.000	
30	91	9.154	9.204	0.050	
30	92	9.265	9.228	-0.037	
30	94	9.044	9.097	0.053	
30	95	9.104	9.109	0.005	
35	100	9.101	9.209	0.108	
35	101	9.314	9.329	0.015	
35	102	8.992	9.060	0.068	
35	103	9.426	9.363	-0.063	
35	104	9.278	9.263	-0.015	
35	105	9.372	9.353	-0.019	
50	106	9.228	9.349	0.121	
50	107	9.108	9.143	0.035	
50	108	8.983	9.163	0.180	
50	109	9.204	9.210	0.006	
50	110	9.218	9.296	0.078	
50	111	9.049	9.137	0.088	
55	113	9.302	9.315	0.013	
55	114	9.183	9.224	0.041	
55	115	9.229	9.177	-0.052	
55	116	9.227	9.188	-0.039	
55	126	9.318	9.252	-0.066	
55	128	9.292	9.227	-0.065	
100	129	9.300	9.405	0.105	
100	131	9.204	9.191	-0.013	
100	132	9.325	9.349	0.024	
100	133	9.296	9.419	0.123	
100	134	9.216	9.323	0.107	
100	135	9.137	8.759	-0.378	
100	138	9.220	9.362	0.142	
100	139	9.058	9.179	0.121	
100	140	9.188	9.292	0.104	
100	141	9.084	9.154	0.070	
100	142	9.282	8.924	-0.358	
100	143	9.187	9.325	0.138	
100	144	9.160	9.272	0.112	
100	220	9.268	9.335	0.067	
100	221	9.121	9.212	0.091	
100	224	9.143	9.230	0.087	
100	225	9.060	9.162	0.102	
100	226	9.310	9.441	0.131	
100	228	9.128	9.358	0.230	
100	232	9.228	9.412	0.184	
100	233	9.251	9.318	0.067	
100	236	8.978	9.083	0.105	
100	238	9.312	9.423	0.111	
100	243	9.090	9.169	0.079	
105	244	9.160	9.261	0.101	
105	246	9.090	9.277	0.187	
105	248	9.259	9.378	0.119	
105	374	9.192	9.325	0.133	
105	376	9.151	9.258	0.107	
	Max	9.426	9.441	0.230	
	Average	9.204	9.237	0.033	
	Min	8.978	8.759	-0.378	
	Std Dev	0.113	0.124	0.093	

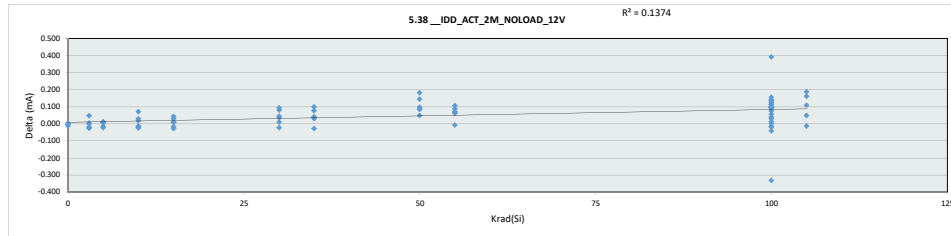


5.37_IDD_ACT_2M_NOLOAD											
Test Site											
Tester											
Test Number											
Max Limit		13.5	mA								
Min Limit			mA								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.096	9.029	9.003	9.096	8.987	9.097	9.060	9.137	9.177	8.759	9.258
Average	9.217	9.184	9.249	9.190	9.247	9.202	9.263	9.216	9.231	9.254	9.300
Max	9.328	9.303	9.428	9.286	9.406	9.313	9.363	9.349	9.315	9.441	9.378
UL	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500

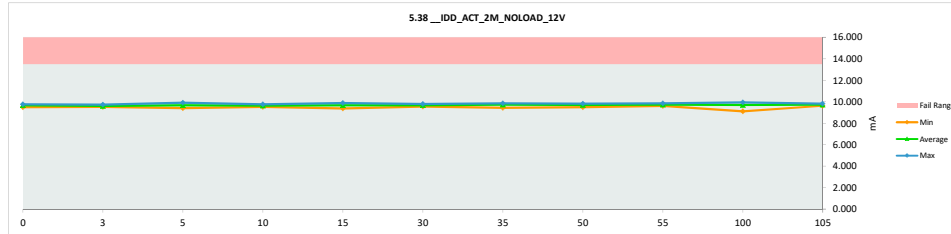


LDR TID Report
TPS7H5001-SP

5.38_IDD_ACT_2M_NOLOAD_12V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	13.5	13.5		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	9.515	9.503	-0.012
0	118	9.709	9.704	-0.005
0	122	9.765	9.769	0.004
3	61	9.753	9.728	-0.025
3	62	9.727	9.727	0.000
3	63	9.504	9.508	0.004
3	64	9.692	9.674	-0.018
3	65	9.573	9.549	-0.024
3	66	9.489	9.536	0.047
5	67	9.553	9.559	0.006
5	68	9.746	9.724	-0.022
5	69	9.388	9.394	0.006
5	70	9.885	9.897	0.012
5	71	9.701	9.686	-0.015
5	72	9.781	9.791	0.010
10	73	9.753	9.728	-0.025
10	74	9.727	9.709	-0.018
10	75	9.504	9.522	0.018
10	76	9.692	9.763	0.071
10	77	9.573	9.560	-0.013
10	78	9.489	9.519	0.030
15	79	9.553	9.596	0.043
15	80	9.746	9.729	-0.017
15	81	9.553	9.389	-0.164
15	82	9.885	9.893	0.008
15	83	9.701	9.730	0.029
15	84	9.781	9.792	0.011
30	86	9.753	9.798	0.045
30	89	9.727	9.736	0.009
30	91	9.504	9.598	0.094
30	92	9.692	9.727	0.035
30	94	9.573	9.551	-0.022
30	95	9.489	9.569	0.080
35	100	9.553	9.652	0.099
35	101	9.746	9.777	0.031
35	102	9.388	9.420	0.032
35	103	9.885	9.857	-0.028
35	104	9.701	9.743	0.042
35	105	9.781	9.857	0.076
50	106	9.731	9.819	0.088
50	107	9.553	9.731	0.178
50	108	9.450	9.631	0.181
50	109	9.563	9.708	0.145
50	110	9.731	9.816	0.085
50	111	9.456	9.505	0.049
55	113	9.775	9.837	0.062
55	114	9.544	9.611	0.067
55	115	9.600	9.671	0.071
55	116	9.617	9.724	0.107
55	126	9.760	9.848	0.088
55	128	9.632	9.624	-0.008
100	129	9.794	9.908	0.114
100	131	9.603	9.588	-0.015
100	132	9.745	9.758	0.013
100	133	9.836	9.937	0.101
100	134	9.644	9.743	0.099
100	135	9.521	9.513	-0.008
100	138	9.626	9.782	0.156
100	139	9.493	9.620	0.127
100	140	9.631	9.746	0.115
100	141	9.469	9.508	0.039
100	142	9.691	9.828	0.137
100	143	9.655	9.730	0.075
100	144	9.620	9.677	0.057
100	220	9.752	9.711	-0.041
100	221	9.561	9.563	0.002
100	224	9.580	9.677	0.097
100	225	9.610	9.699	0.089
100	226	9.873	9.903	0.030
100	228	9.655	9.796	0.141
100	232	9.744	9.842	0.098
100	233	9.676	9.777	0.101
100	236	9.431	9.098	-0.333
100	238	9.677	9.801	0.124
100	243	9.604	9.585	-0.019
105	244	9.654	9.702	0.048
105	246	9.456	9.643	0.187
105	248	9.723	9.831	0.108
105	374	9.665	9.826	0.161
105	376	9.638	9.624	-0.014
Max		9.885	9.937	0.392
Average		9.644	9.691	0.047
Min		9.388	9.098	-0.333
Std Dev		0.123	0.144	0.081

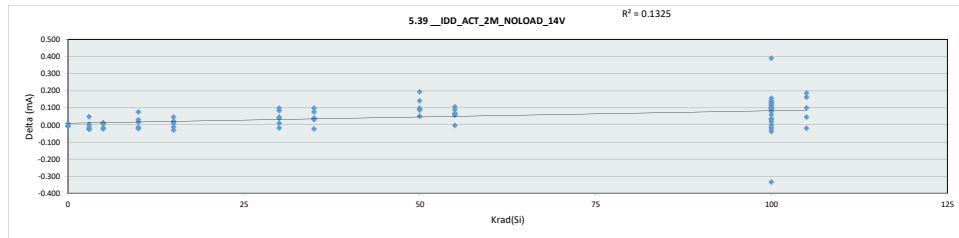


5.38_IDD_ACT_2M_NOLOAD											
Test Site											
Tester											
Test Number											
Max Limit	13.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.503	9.508	9.394	9.519	9.359	9.551	9.420	9.505	9.611	9.098	9.624
Average	9.659	9.620	9.675	9.634	9.683	9.663	9.718	9.702	9.719	9.716	9.725
Max	9.769	9.728	9.897	9.763	9.893	9.798	9.857	9.819	9.848	9.937	9.831
UL	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500

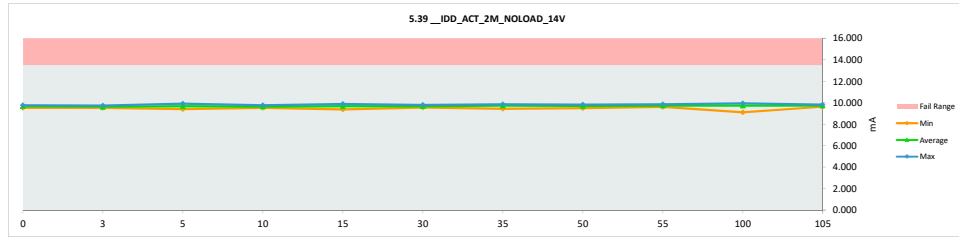


LDR TID Report
TPS7H5001-SP

5.39_IDD_ACT_2M_NOLOAD_14V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	13.5	13.5		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	9.518	9.509	-0.009
0	118	9.710	9.706	-0.004
0	122	9.766	9.774	0.008
3	61	9.754	9.731	-0.023
3	62	9.728	9.728	0.000
3	63	9.508	9.511	0.003
3	64	9.696	9.680	-0.016
3	65	9.578	9.552	-0.026
3	66	9.496	9.543	0.047
5	67	9.557	9.566	0.009
5	68	9.747	9.724	-0.023
5	69	9.393	9.399	0.006
5	70	9.885	9.897	0.012
5	71	9.708	9.691	-0.017
5	72	9.785	9.794	0.009
10	73	9.754	9.733	-0.021
10	74	9.728	9.710	-0.018
10	75	9.508	9.524	0.016
10	76	9.696	9.771	0.075
10	77	9.578	9.564	-0.014
10	78	9.496	9.526	0.030
15	79	9.557	9.602	0.045
15	80	9.747	9.734	-0.013
15	81	9.393	9.362	-0.031
15	82	9.885	9.895	0.010
15	83	9.708	9.730	0.022
15	84	9.785	9.795	0.010
30	86	9.754	9.799	0.045
30	89	9.728	9.738	0.010
30	91	9.508	9.607	0.099
30	92	9.696	9.735	0.039
30	94	9.578	9.561	-0.017
30	95	9.496	9.579	0.083
35	100	9.557	9.656	0.099
35	101	9.747	9.782	0.035
35	102	9.393	9.424	0.031
35	103	9.885	9.861	-0.024
35	104	9.708	9.748	0.040
35	105	9.785	9.861	0.076
50	106	9.732	9.823	0.091
50	107	9.638	9.737	0.099
50	108	9.451	9.644	0.193
50	109	9.569	9.711	0.142
50	110	9.734	9.821	0.087
50	111	9.456	9.507	0.051
55	113	9.783	9.840	0.057
55	114	9.550	9.612	0.062
55	115	9.606	9.675	0.069
55	116	9.624	9.729	0.105
55	126	9.766	9.855	0.089
55	128	9.635	9.633	-0.002
100	129	9.797	9.911	0.114
100	131	9.604	9.590	-0.014
100	132	9.747	9.764	0.017
100	133	9.838	9.939	0.101
100	134	9.645	9.746	0.101
100	135	9.528	9.519	0.391
100	138	9.629	9.786	0.157
100	139	9.493	9.624	0.131
100	140	9.634	9.748	0.114
100	141	9.476	9.512	0.036
100	142	9.693	9.827	0.134
100	143	9.658	9.735	0.077
100	144	9.621	9.680	0.059
100	220	9.751	9.713	-0.038
100	221	9.564	9.565	0.001
100	224	9.583	9.682	0.099
100	225	9.612	9.702	0.090
100	226	9.875	9.905	0.030
100	228	9.658	9.800	0.142
100	232	9.745	9.845	0.100
100	233	9.680	9.780	0.100
100	236	9.434	9.100	-0.334
100	238	9.681	9.803	0.122
100	243	9.614	9.589	-0.025
105	244	9.659	9.705	0.046
105	246	9.461	9.648	0.187
105	248	9.732	9.832	0.100
105	374	9.668	9.830	0.162
105	376	9.643	9.623	-0.020
Max		9.885	9.939	0.391
Average		9.647	9.695	0.048
Min		9.393	9.100	-0.334
Std Dev		0.122	0.143	0.081

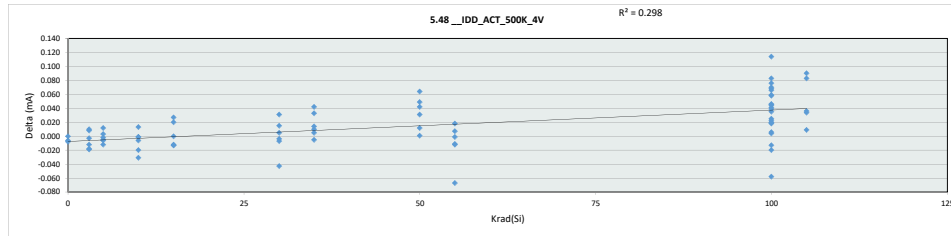


5.39_IDD_ACT_2M_NOLOAD											
Test Site											
Tester											
Test Number											
Max Limit	13.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.509	9.511	9.399	9.524	9.362	9.561	9.424	9.507	9.612	9.100	9.623
Average	9.663	9.624	9.679	9.638	9.686	9.670	9.722	9.707	9.724	9.719	9.728
Max	9.774	9.731	9.897	9.771	9.895	9.799	9.861	9.823	9.855	9.939	9.832
UL	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500

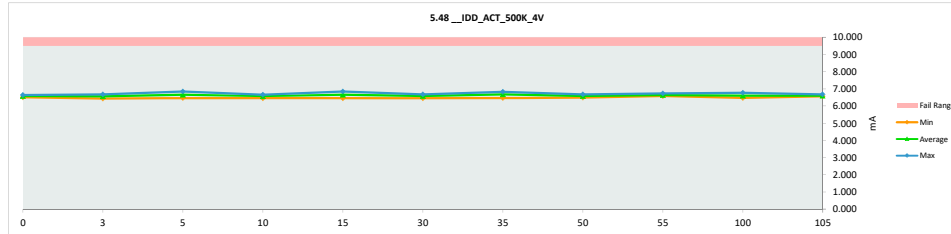


LDR TID Report
TPS7H5001-SP

5.48_IDD_ACT_500K_4V				
Test Site				
Tester				
Test Number				
Unit		mA	mA	
Max Limit		9.5	9.5	
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	6.519	6.513	-0.006
0	118	6.617	6.617	0.000
0	122	6.649	6.641	-0.008
3	61	6.643	6.624	-0.019
3	62	6.681	6.678	-0.003
3	63	6.584	6.592	0.008
3	64	6.646	6.634	-0.012
3	65	6.455	6.437	-0.018
3	66	6.472	6.482	0.010
5	67	6.550	6.548	-0.002
5	68	6.696	6.691	-0.005
5	69	6.467	6.470	0.003
5	70	6.831	6.843	0.012
5	71	6.666	6.654	-0.012
5	72	6.739	6.733	-0.006
10	73	6.643	6.612	-0.031
10	74	6.681	6.661	-0.020
10	75	6.584	6.597	0.013
10	76	6.646	6.640	-0.006
10	77	6.455	6.454	-0.001
10	78	6.472	6.471	-0.001
15	79	6.550	6.577	0.027
15	80	6.696	6.683	-0.013
15	81	6.467	6.455	-0.012
15	82	6.831	6.851	0.020
15	83	6.666	6.653	-0.013
15	84	6.739	6.739	0.000
30	86	6.643	6.658	0.015
30	89	6.681	6.686	0.005
30	91	6.584	6.615	0.031
30	92	6.646	6.603	-0.043
30	94	6.455	6.451	-0.004
30	95	6.472	6.465	-0.007
35	100	6.550	6.592	0.042
35	101	6.696	6.729	0.033
35	102	6.467	6.477	0.010
35	103	6.831	6.826	-0.005
35	104	6.666	6.671	0.005
35	105	6.739	6.753	0.014
50	106	6.650	6.681	0.031
50	107	6.550	6.592	0.042
50	108	6.447	6.511	0.064
50	109	6.606	6.655	0.049
50	110	6.614	6.626	0.012
50	111	6.495	6.496	0.001
55	113	6.656	6.655	-0.001
55	114	6.590	6.579	-0.011
55	115	6.647	6.665	0.018
55	116	6.594	6.601	0.007
55	126	6.737	6.725	-0.012
55	128	6.662	6.595	-0.067
100	129	6.653	6.729	0.076
100	131	6.543	6.485	-0.058
100	132	6.670	6.657	-0.013
100	133	6.654	6.695	0.041
100	134	6.549	6.594	0.045
100	135	6.588	6.568	-0.020
100	138	6.598	6.664	0.066
100	139	6.464	6.522	0.058
100	140	6.577	6.616	0.039
100	141	6.491	6.509	0.018
100	142	6.639	6.709	0.070
100	143	6.605	6.673	0.068
100	144	6.547	6.582	0.035
100	220	6.654	6.658	0.004
100	221	6.530	6.549	0.019
100	224	6.567	6.613	0.046
100	225	6.449	6.469	0.020
100	226	6.731	6.776	0.045
100	228	6.529	6.643	0.114
100	232	6.589	6.672	0.083
100	233	6.595	6.618	0.023
100	236	6.455	6.480	0.025
100	238	6.651	6.710	0.059
100	243	6.505	6.511	0.006
105	244	6.574	6.583	0.009
105	246	6.479	6.569	0.090
105	248	6.634	6.668	0.034
105	374	6.593	6.676	0.083
105	376	6.522	6.558	0.036
Max		6.831	6.851	0.114
Average		6.599	6.615	0.016
Min		6.447	6.437	-0.067
Std Dev		0.094	0.095	0.033

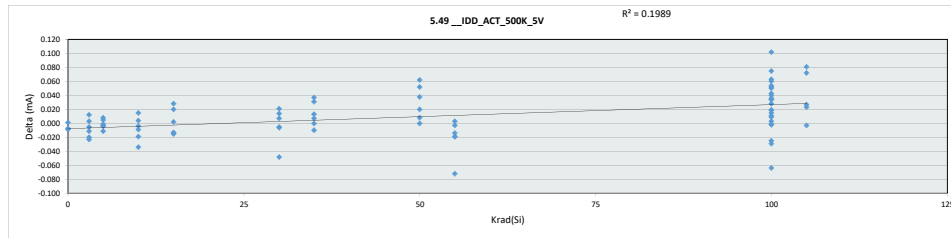


5.48_IDD_ACT_500K_4V											
Test Site											
Tester											
Test Number											
Max Limit		9.5	mA								
Min Limit			mA								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.513	6.437	6.470	6.454	6.455	6.451	6.477	6.496	6.579	6.469	6.558
Average	6.590	6.575	6.657	6.573	6.660	6.580	6.675	6.594	6.637	6.613	6.611
Max	6.641	6.678	6.843	6.661	6.851	6.686	6.826	6.681	6.725	6.776	6.676
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

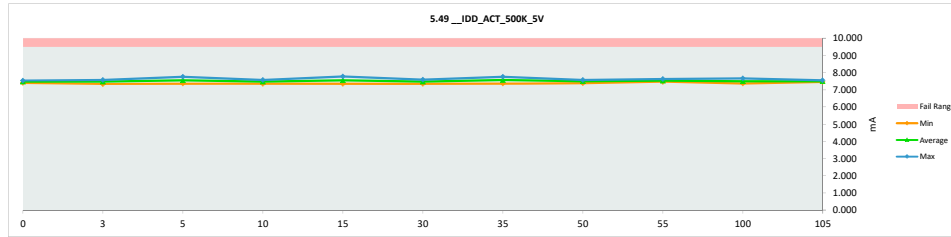


LDR TID Report
TPS7H5001-SP

5.49_IDD_ACT_500K_5V				
Test Site				
Tester				
Test Number				
Unit		mA	mA	
Max Limit		9.5	9.5	
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	7.420	7.411	-0.009
0	118	7.507	7.508	0.001
0	122	7.548	7.541	-0.007
3	61	7.548	7.525	-0.023
3	62	7.586	7.581	-0.005
3	63	7.479	7.482	0.003
3	64	7.553	7.542	-0.011
3	65	7.359	7.339	-0.020
3	66	7.368	7.380	0.012
5	67	7.450	7.449	-0.001
5	68	7.586	7.582	-0.004
5	69	7.366	7.371	0.005
5	70	7.760	7.768	0.008
5	71	7.568	7.557	-0.011
5	72	7.640	7.636	-0.004
10	73	7.548	7.514	-0.034
10	74	7.586	7.567	-0.019
10	75	7.479	7.494	0.015
10	76	7.553	7.544	-0.009
10	77	7.359	7.355	-0.004
10	78	7.368	7.372	0.004
15	79	7.450	7.478	0.028
15	80	7.586	7.573	-0.013
15	81	7.366	7.351	-0.015
15	82	7.760	7.780	0.020
15	83	7.568	7.553	-0.015
15	84	7.640	7.642	0.002
30	86	7.548	7.562	0.014
30	89	7.586	7.593	0.007
30	91	7.479	7.500	0.021
30	92	7.553	7.505	-0.048
30	94	7.359	7.354	-0.005
30	95	7.368	7.362	-0.006
35	100	7.450	7.487	0.037
35	101	7.586	7.617	0.031
35	102	7.366	7.373	0.007
35	103	7.760	7.750	-0.010
35	104	7.568	7.568	0.000
35	105	7.640	7.653	0.013
50	106	7.559	7.579	0.020
50	107	7.461	7.499	0.038
50	108	7.345	7.407	0.062
50	109	7.500	7.552	0.052
50	110	7.518	7.526	0.008
50	111	7.395	7.395	0.000
55	113	7.559	7.540	-0.019
55	114	7.486	7.467	-0.019
55	115	7.552	7.549	-0.003
55	116	7.484	7.487	0.003
55	126	7.636	7.622	-0.014
55	128	7.567	7.495	-0.072
100	129	7.556	7.619	0.063
100	131	7.437	7.373	-0.064
100	132	7.578	7.553	-0.025
100	133	7.552	7.586	0.034
100	134	7.462	7.498	0.036
100	135	7.491	7.462	-0.029
100	138	7.502	7.562	0.060
100	139	7.352	7.402	0.050
100	140	7.472	7.506	0.034
100	141	7.396	7.399	0.003
100	142	7.537	7.589	0.052
100	143	7.497	7.551	0.054
100	144	7.445	7.464	0.019
100	220	7.549	7.548	-0.001
100	221	7.428	7.437	0.009
100	224	7.462	7.502	0.040
100	225	7.355	7.366	0.011
100	226	7.633	7.661	0.028
100	228	7.420	7.522	0.102
100	232	7.497	7.572	0.075
100	233	7.510	7.525	0.015
100	236	7.342	7.361	0.019
100	238	7.551	7.594	0.043
100	243	7.397	7.395	-0.002
105	244	7.468	7.465	-0.003
105	246	7.372	7.453	0.081
105	248	7.534	7.557	0.023
105	374	7.477	7.549	0.072
105	376	7.428	7.455	0.027
Max		7.760	7.780	0.102
Average		7.500	7.511	0.010
Min		7.342	7.339	-0.072
Std Dev		0.098	0.097	0.031

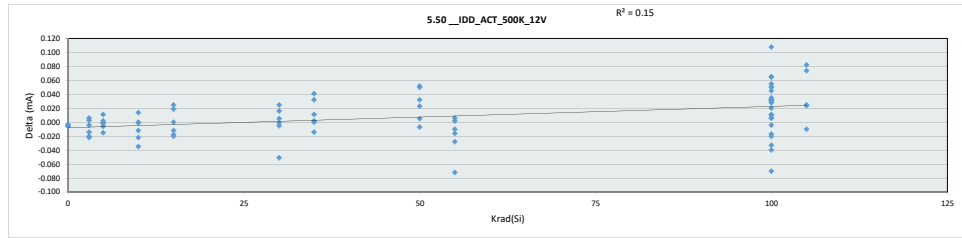


5.49_IDD_ACT_500K_5V											
Test Site											
Tester											
Test Number											
Max Limit		9.5	mA								
Min Limit			mA								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.411	7.339	7.371	7.355	7.351	7.354	7.373	7.395	7.467	7.361	7.453
Average	7.487	7.475	7.561	7.474	7.563	7.479	7.575	7.493	7.527	7.502	7.496
Max	7.541	7.581	7.768	7.567	7.780	7.593	7.750	7.579	7.622	7.661	7.557
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

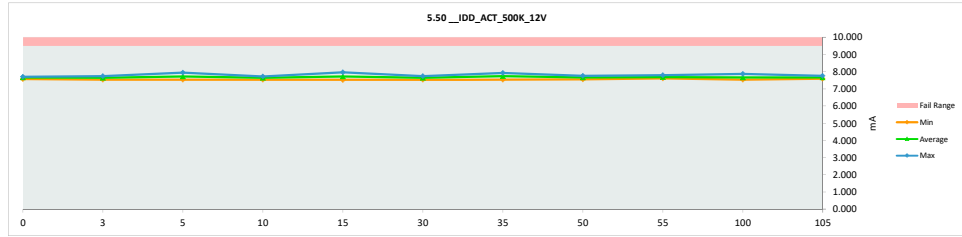


LDR TID Report
TPS7H5001-SP

5.50_IDD_ACT_500K_12V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	9.5	9.5		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	7.579	7.574	-0.005
0	118	7.698	7.695	-0.003
0	122	7.709	7.703	-0.006
3	61	7.730	7.708	-0.022
3	62	7.742	7.738	-0.004
3	63	7.620	7.623	0.003
3	64	7.732	7.718	-0.014
3	65	7.572	7.552	-0.020
3	66	7.535	7.541	0.006
5	67	7.611	7.610	-0.001
5	68	7.745	7.739	-0.006
5	69	7.535	7.537	0.002
5	70	7.941	7.952	0.011
5	71	7.739	7.724	-0.015
5	72	7.802	7.800	-0.002
10	73	7.730	7.695	-0.035
10	74	7.742	7.720	-0.022
10	75	7.620	7.634	0.014
10	76	7.732	7.720	-0.012
10	77	7.572	7.571	-0.001
10	78	7.535	7.535	0.000
15	79	7.611	7.636	0.025
15	80	7.745	7.733	-0.012
15	81	7.535	7.515	-0.020
15	82	7.941	7.960	0.019
15	83	7.739	7.721	-0.018
15	84	7.802	7.802	0.000
30	86	7.730	7.746	0.016
30	89	7.742	7.747	0.005
30	91	7.620	7.645	0.025
30	92	7.732	7.681	-0.051
30	94	7.572	7.571	-0.001
30	95	7.535	7.530	-0.005
35	100	7.611	7.652	0.041
35	101	7.745	7.777	0.032
35	102	7.535	7.537	0.002
35	103	7.941	7.927	-0.014
35	104	7.739	7.739	0.000
35	105	7.802	7.813	0.011
50	106	7.742	7.765	0.023
50	107	7.645	7.677	0.032
50	108	7.532	7.584	0.052
50	109	7.643	7.693	0.050
50	110	7.722	7.727	0.005
50	111	7.561	7.554	-0.007
55	113	7.757	7.747	-0.010
55	114	7.638	7.610	-0.028
55	115	7.695	7.701	0.006
55	116	7.639	7.641	0.002
55	126	7.809	7.793	-0.016
55	128	7.709	7.637	-0.072
100	129	7.764	7.829	0.065
100	131	7.599	7.529	-0.070
100	132	7.743	7.710	-0.033
100	133	7.765	7.785	0.020
100	134	7.629	7.661	0.032
100	135	7.643	7.603	-0.040
100	138	7.668	7.723	0.055
100	139	7.520	7.565	0.045
100	140	7.648	7.683	0.035
100	141	7.557	7.553	-0.004
100	142	7.704	7.754	0.050
100	143	7.661	7.712	0.051
100	144	7.610	7.638	0.028
100	220	7.722	7.705	-0.017
100	221	7.577	7.583	0.006
100	224	7.636	7.667	0.031
100	225	7.569	7.575	0.006
100	226	7.835	7.864	0.029
100	228	7.601	7.709	0.108
100	232	7.674	7.739	0.065
100	233	7.694	7.705	0.011
100	236	7.516	7.526	0.010
100	238	7.706	7.739	0.033
100	243	7.580	7.560	-0.020
105	244	7.646	7.636	-0.010
105	246	7.519	7.593	0.074
105	248	7.722	7.746	0.024
105	374	7.670	7.752	0.082
105	376	7.607	7.632	0.025
Max		7.941	7.960	0.108
Average		7.672	7.680	0.008
Min		7.515	7.515	-0.072
Std Dev		0.099	0.100	0.032

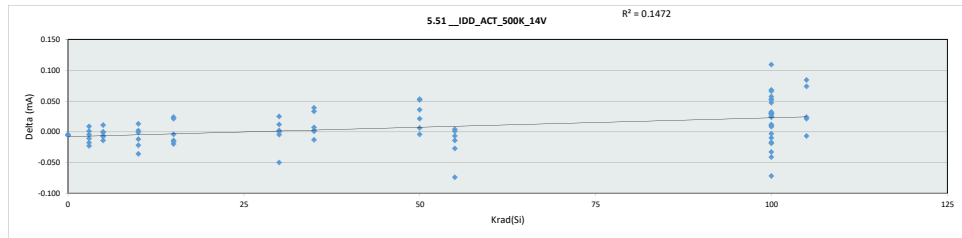


5.50_IDD_ACT_500K_12V											
Test Site											
Tester											
Test Number											
Max Limit	9.5	mA									
Min Limit	mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.574	7.541	7.537	7.535	7.515	7.530	7.537	7.554	7.610	7.526	7.593
Average	7.657	7.647	7.727	7.646	7.728	7.653	7.741	7.667	7.688	7.672	7.672
Max	7.703	7.738	7.952	7.720	7.720	7.960	7.747	7.927	7.765	7.793	7.864
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

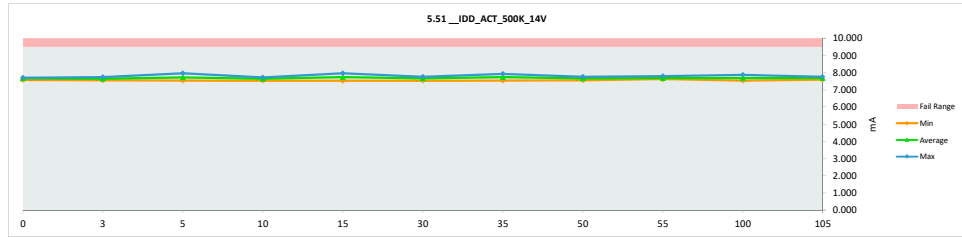


LDR TID Report
TPS7H5001-SP

5.51_IDD_ACT_500K_14V				
Test Site				
Tester				
Test Number				
Unit	mA		mA	
Max Limit	9.5		9.5	
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	7.582	7.576	-0.006
0	118	7.703	7.699	-0.004
0	122	7.714	7.708	-0.006
3	61	7.737	7.714	-0.023
3	62	7.748	7.743	-0.005
3	63	7.625	7.626	0.001
3	64	7.734	7.723	-0.011
3	65	7.576	7.558	-0.018
3	66	7.540	7.549	0.009
5	67	7.614	7.614	0.000
5	68	7.749	7.742	-0.007
5	69	7.540	7.539	-0.001
5	70	7.944	7.955	0.011
5	71	7.742	7.728	-0.014
5	72	7.810	7.802	-0.008
10	73	7.737	7.701	-0.036
10	74	7.748	7.726	-0.022
10	75	7.625	7.638	0.013
10	76	7.734	7.722	-0.012
10	77	7.576	7.578	0.002
10	78	7.540	7.539	-0.001
15	79	7.614	7.638	0.024
15	80	7.749	7.735	-0.014
15	81	7.540	7.520	-0.020
15	82	7.944	7.965	0.021
15	83	7.742	7.726	-0.016
15	84	7.810	7.806	-0.004
30	86	7.737	7.749	0.012
30	89	7.748	7.750	0.002
30	91	7.625	7.650	0.025
30	92	7.734	7.684	-0.050
30	94	7.576	7.576	0.000
30	95	7.540	7.535	-0.005
35	100	7.614	7.653	0.039
35	101	7.749	7.782	0.033
35	102	7.540	7.542	0.002
35	103	7.944	7.931	-0.013
35	104	7.742	7.743	0.001
35	105	7.810	7.817	0.007
50	106	7.747	7.768	0.021
50	107	7.647	7.684	0.036
50	108	7.536	7.588	0.052
50	109	7.647	7.700	0.053
50	110	7.726	7.732	0.006
50	111	7.566	7.562	-0.004
55	113	7.762	7.755	-0.007
55	114	7.645	7.618	-0.027
55	115	7.698	7.702	0.004
55	116	7.646	7.647	0.001
55	126	7.812	7.798	-0.014
55	128	7.715	7.641	-0.074
100	129	7.766	7.834	0.068
100	131	7.606	7.534	-0.072
100	132	7.748	7.715	-0.033
100	133	7.768	7.792	0.024
100	134	7.636	7.666	0.030
100	135	7.647	7.606	-0.041
100	138	7.673	7.730	0.057
100	139	7.526	7.573	0.047
100	140	7.654	7.684	0.030
100	141	7.565	7.555	-0.010
100	142	7.708	7.760	0.052
100	143	7.665	7.718	0.053
100	144	7.614	7.639	0.025
100	220	7.725	7.708	-0.017
100	221	7.586	7.583	-0.003
100	224	7.641	7.671	0.030
100	225	7.573	7.583	0.010
100	226	7.840	7.868	0.028
100	228	7.607	7.716	0.109
100	232	7.677	7.743	0.066
100	233	7.698	7.710	0.012
100	236	7.520	7.528	0.008
100	238	7.713	7.745	0.032
100	243	7.583	7.564	-0.019
105	244	7.649	7.642	-0.007
105	246	7.523	7.597	0.074
105	248	7.727	7.751	0.024
105	374	7.674	7.758	0.084
105	376	7.611	7.632	0.021
	Max	7.944	7.965	0.109
	Average	7.677	7.685	0.008
	Min	7.520	7.520	-0.074
	Std Dev	0.098	0.100	0.032

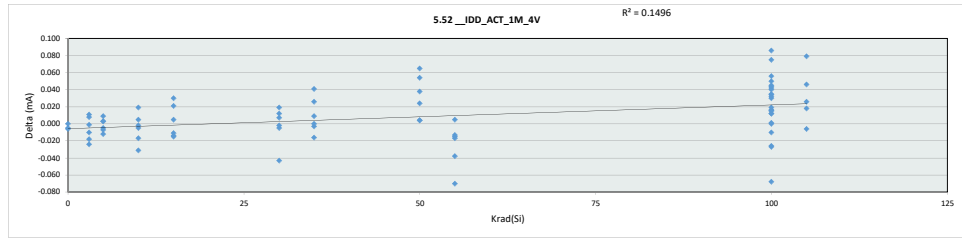


5.51_IDD_ACT_500K_14V											
Test Site											
Tester											
Test Number											
Max Limit	9.5										
Min Limit											
Unit											
Max Limit											
Min Limit											
Unit											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.576	7.549	7.539	7.539	7.520	7.535	7.542	7.562	7.618	7.528	7.597
Average	7.661	7.652	7.730	7.651	7.732	7.657	7.745	7.672	7.694	7.676	7.676
Max	7.708	7.743	7.955	7.726	7.965	7.750	7.931	7.768	7.798	7.868	7.758
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

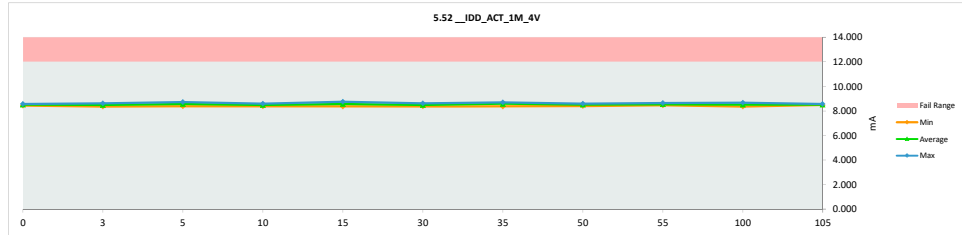


LDR TID Report
TPS7H5001-SP

5.52_IDD_ACT_1M_4V				
Test Site				
Tester				
Test Number				
Unit		mA	mA	
Max Limit		12	12	
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.436	8.431	-0.005
0	118	8.532	8.532	0.000
0	122	8.576	8.570	-0.006
3	61	8.566	8.542	-0.024
3	62	8.614	8.613	-0.001
3	63	8.486	8.494	0.008
3	64	8.566	8.556	-0.010
3	65	8.374	8.356	-0.018
3	66	8.399	8.410	0.011
5	67	8.455	8.458	0.003
5	68	8.617	8.610	-0.007
5	69	8.370	8.373	0.003
5	70	8.720	8.729	0.009
5	71	8.579	8.567	-0.012
5	72	8.654	8.649	-0.005
10	73	8.566	8.535	-0.031
10	74	8.614	8.597	-0.017
10	75	8.486	8.505	0.019
10	76	8.566	8.561	-0.005
10	77	8.374	8.372	-0.002
10	78	8.399	8.404	0.005
15	79	8.455	8.485	0.030
15	80	8.617	8.606	-0.011
15	81	8.370	8.356	-0.014
15	82	8.720	8.741	0.021
15	83	8.579	8.564	-0.015
15	84	8.654	8.659	0.005
30	86	8.566	8.578	0.012
30	89	8.614	8.621	0.007
30	91	8.486	8.505	0.019
30	92	8.566	8.523	-0.043
30	94	8.374	8.372	-0.002
30	95	8.399	8.394	-0.005
35	100	8.455	8.496	0.041
35	101	8.617	8.643	0.026
35	102	8.370	8.370	0.000
35	103	8.720	8.704	-0.016
35	104	8.579	8.576	-0.003
35	105	8.654	8.663	0.009
50	106	8.567	8.591	0.024
50	107	8.505	8.467	-0.038
50	108	8.359	8.424	0.065
50	109	8.525	8.579	0.054
50	110	8.539	8.543	0.004
50	111	8.405	8.409	0.004
55	113	8.572	8.534	-0.038
55	114	8.488	8.471	-0.017
55	115	8.561	8.548	-0.013
55	116	8.502	8.507	0.005
55	126	8.655	8.640	-0.015
55	128	8.588	8.518	-0.070
100	129	8.575	8.619	0.044
100	131	8.470	8.402	-0.068
100	132	8.597	8.571	-0.026
100	133	8.579	8.609	0.030
100	134	8.476	8.510	0.034
100	135	8.504	8.477	-0.027
100	138	8.522	8.578	0.056
100	139	8.381	8.426	0.045
100	140	8.489	8.521	0.032
100	141	8.408	8.409	0.001
100	142	8.562	8.604	0.042
100	143	8.512	8.562	0.050
100	144	8.475	8.487	0.012
100	220	8.569	8.559	-0.010
100	221	8.442	8.457	0.015
100	224	8.473	8.508	0.035
100	225	8.376	8.388	0.012
100	226	8.643	8.659	0.016
100	228	8.447	8.533	0.086
100	232	8.508	8.583	0.075
100	233	8.512	8.531	0.019
100	236	8.355	8.371	0.016
100	238	8.576	8.616	0.040
100	243	8.411	8.411	0.000
105	244	8.484	8.478	-0.006
105	246	8.398	8.477	0.079
105	248	8.558	8.576	0.018
105	374	8.493	8.539	0.046
105	376	8.447	8.473	0.026
	Max	8.720	8.741	0.086
	Average	8.515	8.524	0.009
	Min	8.355	8.356	-0.070
	Std Dev	0.094	0.092	0.029

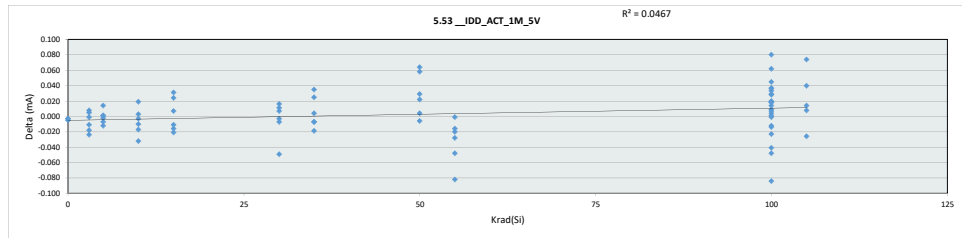


5.52_IDD_ACT_1M_4V											
Test Site											
Tester											
Test Number											
Max Limit	12	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.431	8.356	8.373	8.372	8.356	8.372	8.370	8.409	8.471	8.371	8.473
Average	8.511	8.495	8.564	8.496	8.569	8.499	8.575	8.509	8.536	8.516	8.509
Max	8.570	8.613	8.729	8.597	8.569	8.621	8.704	8.591	8.640	8.659	8.576
UL	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000

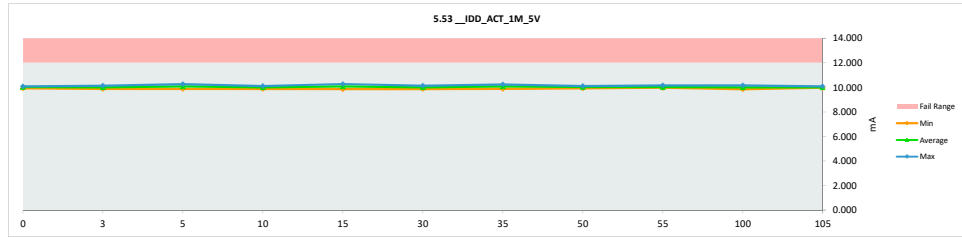


LDR TID Report
TPS7H5001-SP

5.53_IDD_ACT_1M_5V				
Test Site				
Tester				
Test Number				
Unit	mA		mA	
Max Limit	12		12	
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	9.941	9.936	-0.005
0	118	10.029	10.026	-0.003
0	122	10.090	10.087	-0.003
3	61	10.084	10.060	-0.024
3	62	10.134	10.133	-0.001
3	63	9.981	9.986	0.005
3	64	10.084	10.073	-0.011
3	65	9.885	9.867	-0.018
3	66	9.908	9.916	0.008
5	67	9.967	9.968	0.001
5	68	10.116	10.109	-0.007
5	69	9.875	9.876	0.001
5	70	10.248	10.262	0.014
5	71	10.089	10.077	-0.012
5	72	10.170	10.168	-0.002
10	73	10.084	10.052	-0.032
10	74	10.134	10.117	-0.017
10	75	9.981	10.000	0.019
10	76	10.084	10.074	-0.010
10	77	9.885	9.882	-0.003
10	78	9.908	9.911	0.003
15	79	9.967	9.998	0.031
15	80	10.116	10.105	-0.011
15	81	9.875	9.854	-0.021
15	82	10.248	10.272	0.024
15	83	10.089	10.073	-0.016
15	84	10.170	10.177	0.007
30	86	10.084	10.095	0.011
30	89	10.134	10.141	0.007
30	91	9.981	9.997	0.016
30	92	10.084	10.035	-0.049
30	94	9.885	9.882	-0.003
30	95	9.908	9.901	-0.007
35	100	9.967	10.002	0.035
35	101	10.116	10.141	0.025
35	102	9.875	9.868	-0.007
35	103	10.248	10.229	-0.019
35	104	10.089	10.082	-0.007
35	105	10.170	10.174	0.004
50	106	10.090	10.112	0.022
50	107	10.006	10.035	0.029
50	108	9.861	9.925	0.064
50	109	10.032	10.090	0.058
50	110	10.059	10.053	-0.006
50	111	9.915	9.919	0.004
55	113	10.083	10.035	-0.048
55	114	9.988	9.960	-0.028
55	115	10.074	10.054	-0.020
55	116	9.998	9.997	-0.001
55	126	10.169	10.153	-0.016
55	128	10.111	10.029	-0.082
100	129	10.091	10.120	0.029
100	131	9.976	9.892	-0.084
100	132	10.126	10.085	-0.041
100	133	10.087	10.107	0.020
100	134	10.017	10.035	0.018
100	135	10.016	9.968	-0.048
100	138	10.036	10.081	0.045
100	139	9.875	9.911	0.036
100	140	9.997	10.016	0.019
100	141	9.923	9.909	-0.014
100	142	10.076	10.113	0.037
100	143	10.012	10.045	0.033
100	144	9.983	9.987	0.004
100	220	10.072	10.049	-0.023
100	221	9.950	9.951	0.001
100	224	9.980	9.998	0.018
100	225	9.891	9.890	-0.001
100	226	10.154	10.168	0.014
100	228	9.948	10.028	0.080
100	232	10.031	10.093	0.062
100	233	10.035	10.042	0.007
100	236	9.838	9.847	0.009
100	238	10.086	10.114	0.028
100	243	9.907	9.895	-0.012
105	244	9.986	9.960	-0.026
105	246	9.894	9.968	0.074
105	248	10.071	10.085	0.014
105	374	9.980	10.020	0.040
105	376	9.967	9.975	0.008
	Max	10.248	10.272	0.080
	Average	10.026	10.029	0.003
	Min	9.838	9.847	-0.084
	Std Dev	0.099	0.098	0.030

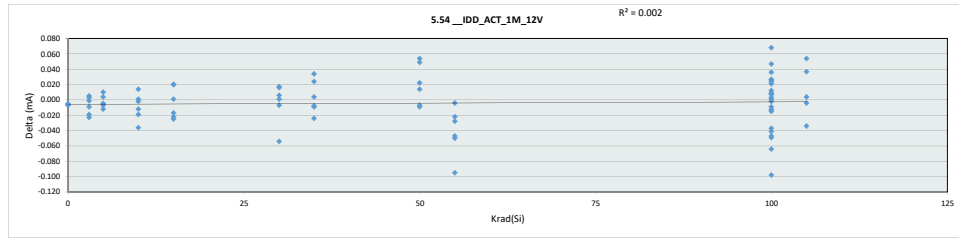


5.53_IDD_ACT_1M_5V											
Test Site											
Tester											
Test Number											
Max Limit	12										
Min Limit											
Unit	mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.936	9.867	9.876	9.882	9.854	9.882	9.868	9.919	9.960	9.847	9.960
Average	10.016	10.006	10.077	10.006	10.080	10.009	10.083	10.022	10.038	10.014	10.002
Max	10.087	10.133	10.262	10.117	10.272	10.141	10.229	10.112	10.153	10.168	10.085
UL	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000

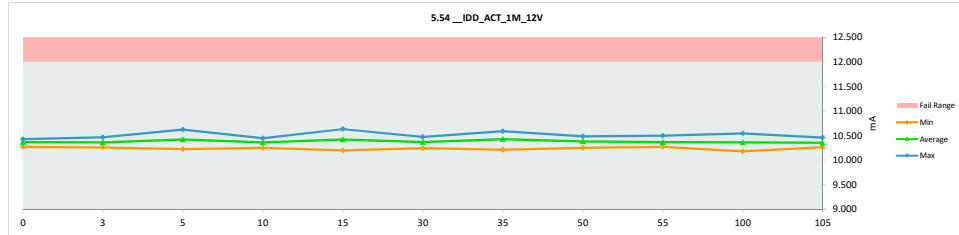


LDR TID Report
TPS7H5001-SP

5.54_IDD_ACT_1M_12V				
Test Site				
Tester				
Test Number				
Unit		mA	mA	
Max Limit		12	12	
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	10.275	10.268	-0.007
0	118	10.413	10.407	-0.006
0	122	10.428	10.423	-0.005
3	61	10.454	10.431	-0.023
3	62	10.463	10.462	-0.001
3	63	10.279	10.282	0.003
3	64	10.449	10.440	-0.009
3	65	10.311	10.292	-0.019
3	66	10.249	10.254	0.005
5	67	10.305	10.300	-0.005
5	68	10.447	10.440	-0.007
5	69	10.217	10.221	0.004
5	70	10.610	10.620	0.010
5	71	10.439	10.427	-0.012
5	72	10.509	10.504	-0.005
10	73	10.454	10.418	-0.036
10	74	10.463	10.444	-0.019
10	75	10.279	10.293	0.014
10	76	10.449	10.437	-0.012
10	77	10.311	10.312	0.001
10	78	10.249	10.247	-0.002
15	79	10.305	10.325	0.020
15	80	10.447	10.430	-0.017
15	81	10.217	10.195	-0.022
15	82	10.610	10.630	0.020
15	83	10.439	10.414	-0.025
15	84	10.509	10.510	0.001
30	86	10.454	10.471	0.017
30	89	10.463	10.469	0.006
30	91	10.279	10.295	0.016
30	92	10.449	10.395	-0.054
30	94	10.311	10.312	0.001
30	95	10.249	10.242	-0.007
35	100	10.305	10.339	0.034
35	101	10.447	10.471	0.024
35	102	10.217	10.209	-0.008
35	103	10.610	10.586	-0.024
35	104	10.439	10.430	-0.009
35	105	10.509	10.513	0.004
50	106	10.470	10.484	0.014
50	107	10.380	10.402	0.022
50	108	10.235	10.289	0.054
50	109	10.339	10.388	0.049
50	110	10.472	10.463	-0.009
50	111	10.256	10.249	-0.007
55	113	10.484	10.434	-0.050
55	114	10.312	10.265	-0.047
55	115	10.381	10.353	-0.028
55	116	10.322	10.318	-0.004
55	125	10.521	10.499	-0.022
55	128	10.414	10.319	-0.095
100	129	10.505	10.529	0.024
100	131	10.316	10.218	-0.098
100	132	10.461	10.412	-0.049
100	133	10.513	10.514	0.001
100	134	10.369	10.378	0.009
100	135	10.327	10.263	-0.064
100	138	10.383	10.419	0.036
100	139	10.223	10.244	0.021
100	140	10.363	10.375	0.012
100	141	10.262	10.225	-0.037
100	142	10.422	10.448	0.026
100	143	10.349	10.376	0.027
100	144	10.334	10.337	0.003
100	220	10.421	10.374	-0.047
100	221	10.265	10.252	-0.013
100	224	10.337	10.345	0.008
100	225	10.323	10.310	-0.013
100	226	10.552	10.543	-0.009
100	228	10.314	10.382	0.068
100	232	10.394	10.441	0.047
100	233	10.410	10.408	-0.002
100	236	10.190	10.175	-0.015
100	238	10.410	10.417	0.007
100	243	10.276	10.235	-0.041
105	244	10.347	10.313	-0.034
105	246	10.205	10.259	0.054
105	248	10.455	10.459	0.004
105	374	10.365	10.402	0.037
105	376	10.338	10.334	-0.004
	Max	10.610	10.630	0.068
	Average	10.379	10.375	-0.004
	Min	10.190	10.175	-0.098
	Std Dev	0.102	0.103	0.030

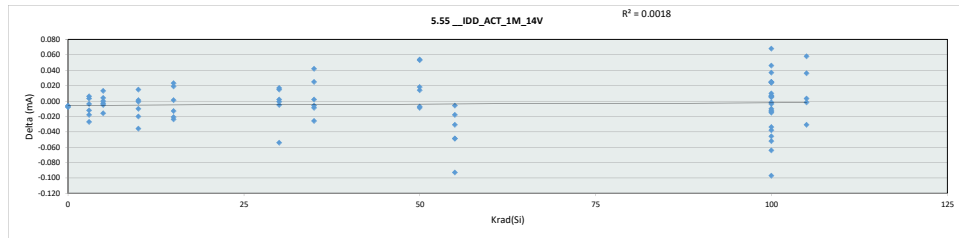


5.54_IDD_ACT_1M_12V											
Test Site											
Tester											
Test Number											
Max Limit	12	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	10.268	10.254	10.221	10.247	10.195	10.242	10.209	10.249	10.265	10.175	10.259
Average	10.366	10.360	10.419	10.359	10.417	10.364	10.425	10.379	10.365	10.359	10.353
Max	10.423	10.462	10.620	10.444	10.630	10.471	10.586	10.484	10.499	10.543	10.459
UL	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000

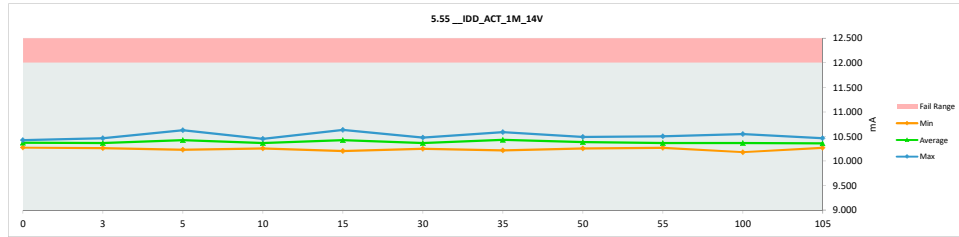


LDR TID Report
TPS7H5001-SP

5.55_IDD_ACT_1M_14V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	12	12		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	10.279	10.272	-0.007
0	118	10.417	10.411	-0.006
0	122	10.433	10.425	-0.008
3	61	10.460	10.433	-0.027
3	62	10.469	10.465	-0.004
3	63	10.283	10.286	0.003
3	64	10.454	10.442	-0.012
3	65	10.316	10.298	-0.018
3	66	10.254	10.260	0.006
5	67	10.308	10.305	-0.003
5	68	10.451	10.446	-0.005
5	69	10.223	10.227	0.004
5	70	10.615	10.628	0.013
5	71	10.442	10.426	-0.016
5	72	10.513	10.513	0.000
10	73	10.460	10.424	-0.036
10	74	10.469	10.449	-0.020
10	75	10.283	10.298	0.015
10	76	10.454	10.444	-0.010
10	77	10.316	10.317	0.001
10	78	10.254	10.253	-0.001
15	79	10.308	10.331	0.023
15	80	10.451	10.438	-0.013
15	81	10.223	10.202	-0.021
15	82	10.615	10.634	0.019
15	83	10.442	10.418	-0.024
15	84	10.513	10.514	0.001
30	86	10.460	10.475	0.015
30	89	10.469	10.471	0.002
30	91	10.283	10.300	0.017
30	92	10.454	10.400	-0.054
30	94	10.316	10.315	-0.001
30	95	10.254	10.249	-0.005
35	100	10.308	10.350	0.042
35	101	10.451	10.476	0.025
35	102	10.223	10.217	-0.006
35	103	10.615	10.589	-0.026
35	104	10.442	10.433	-0.009
35	105	10.513	10.515	0.002
50	106	10.476	10.490	0.014
50	107	10.385	10.403	0.018
50	108	10.239	10.293	0.054
50	109	10.342	10.395	0.053
50	110	10.476	10.467	-0.009
50	111	10.259	10.252	-0.007
55	113	10.488	10.439	-0.049
55	114	10.317	10.268	-0.049
55	115	10.387	10.356	-0.031
55	116	10.327	10.321	-0.006
55	126	10.523	10.505	-0.018
55	128	10.419	10.326	-0.093
100	129	10.509	10.533	0.024
100	131	10.321	10.224	-0.097
100	132	10.468	10.416	-0.052
100	133	10.519	10.517	-0.002
100	134	10.375	10.381	0.006
100	135	10.332	10.268	-0.064
100	138	10.386	10.423	0.037
100	139	10.227	10.251	0.024
100	140	10.367	10.377	0.010
100	141	10.266	10.232	-0.034
100	142	10.427	10.452	0.025
100	143	10.353	10.378	0.025
100	144	10.338	10.343	0.005
100	220	10.425	10.379	-0.046
100	221	10.269	10.257	-0.012
100	224	10.342	10.349	0.007
100	225	10.327	10.313	-0.014
100	226	10.556	10.546	-0.010
100	228	10.318	10.386	0.068
100	232	10.398	10.444	0.046
100	233	10.413	10.409	-0.004
100	236	10.195	10.180	-0.015
100	238	10.414	10.420	0.006
100	243	10.278	10.240	-0.038
105	244	10.349	10.318	-0.031
105	246	10.207	10.265	0.058
105	248	10.459	10.462	0.003
105	374	10.368	10.404	0.036
105	376	10.341	10.339	-0.002
	Max	10.615	10.634	0.068
	Average	10.383	10.380	-0.004
	Min	10.195	10.180	-0.097
	Std Dev	0.102	0.102	0.030

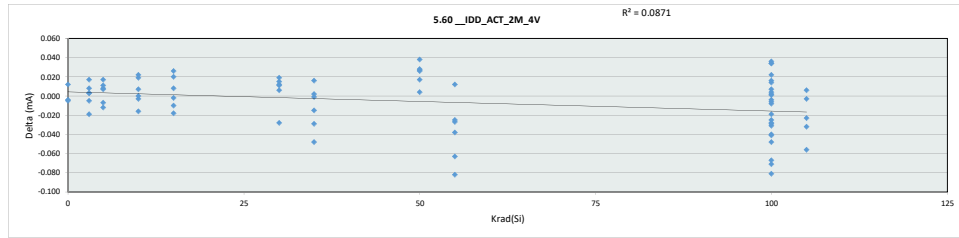


5.55_IDD_ACT_1M_14V											
Test Site											
Tester											
Test Number											
Max Limit	12	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	10.272	10.260	10.227	10.253	10.202	10.249	10.217	10.252	10.268	10.180	10.265
Average	10.369	10.364	10.424	10.364	10.423	10.368	10.430	10.383	10.369	10.363	10.358
Max	10.425	10.465	10.628	10.634	10.449	10.634	10.475	10.589	10.490	10.505	10.546
UL	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000

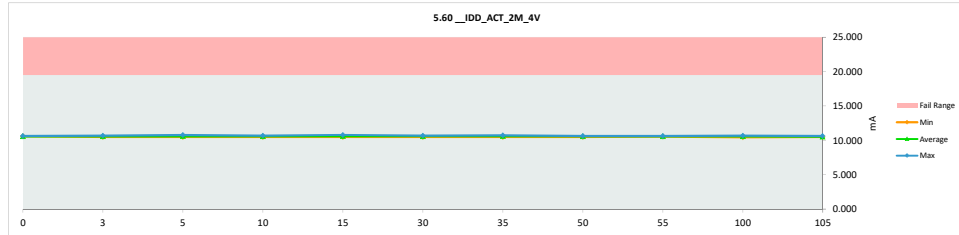


LDR TID Report
TPS7H5001-SP

5.60_IDD_ACT_2M_4V				
Test Site				
Tester				
Test Number				
Unit	mA		mA	
Max Limit	19.5		19.5	
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	10.578	10.574	-0.004
0	118	10.578	10.573	-0.005
0	122	10.638	10.650	0.012
3	61	10.601	10.582	-0.019
3	62	10.692	10.700	0.008
3	63	10.614	10.631	0.017
3	64	10.679	10.682	0.003
3	65	10.512	10.507	-0.005
3	66	10.530	10.533	0.003
5	67	10.495	10.503	0.008
5	68	10.673	10.661	-0.012
5	69	10.515	10.522	0.007
5	70	10.777	10.794	0.017
5	71	10.649	10.642	-0.007
5	72	10.690	10.701	0.011
10	73	10.601	10.585	-0.016
10	74	10.692	10.689	-0.003
10	75	10.614	10.636	0.022
10	76	10.679	10.679	0.000
10	77	10.512	10.519	0.007
10	78	10.530	10.549	0.019
15	79	10.495	10.521	0.026
15	80	10.673	10.671	-0.002
15	81	10.515	10.497	-0.018
15	82	10.777	10.797	0.020
15	83	10.649	10.639	-0.010
15	84	10.690	10.698	0.008
30	86	10.601	10.620	0.019
30	89	10.692	10.703	0.011
30	91	10.614	10.626	0.012
30	92	10.679	10.651	-0.028
30	94	10.512	10.518	0.006
30	95	10.530	10.545	0.015
35	100	10.495	10.511	0.016
35	101	10.673	10.675	0.002
35	102	10.515	10.486	-0.029
35	103	10.777	10.729	-0.048
35	104	10.649	10.634	-0.015
35	105	10.690	10.689	-0.001
50	106	10.615	10.641	0.026
50	107	10.504	10.521	0.017
50	108	10.583	10.587	0.004
50	109	10.561	10.599	0.038
50	110	10.561	10.589	0.028
50	111	10.558	10.585	0.027
55	113	10.688	10.625	-0.063
55	114	10.576	10.549	-0.027
55	115	10.587	10.562	-0.025
55	116	10.523	10.535	0.012
55	126	10.673	10.635	-0.038
55	128	10.639	10.557	-0.082
100	129	10.605	10.627	0.022
100	131	10.576	10.509	-0.067
100	132	10.703	10.632	-0.071
100	133	10.617	10.624	0.007
100	134	10.543	10.559	0.016
100	135	10.750	10.669	-0.081
100	138	10.560	10.574	0.014
100	139	10.548	10.540	-0.008
100	140	10.569	10.603	0.034
100	141	10.521	10.481	-0.040
100	142	10.621	10.622	0.001
100	143	10.565	10.567	0.002
100	144	10.562	10.531	-0.031
100	220	10.657	10.629	-0.028
100	221	10.531	10.512	-0.019
100	224	10.555	10.559	0.004
100	225	10.571	10.542	-0.029
100	226	10.696	10.690	-0.006
100	228	10.529	10.565	0.036
100	232	10.606	10.640	0.034
100	233	10.646	10.642	-0.004
100	236	10.535	10.494	-0.041
100	238	10.711	10.686	-0.025
100	243	10.481	10.433	-0.048
105	244	10.538	10.482	-0.056
105	246	10.515	10.521	0.006
105	248	10.673	10.641	-0.032
105	374	10.556	10.533	-0.023
105	376	10.564	10.561	-0.003
Max	10.777	10.797	0.038	
Average	10.603	10.597	-0.006	
Min	10.481	10.433	-0.082	
Std Dev	0.075	0.075	0.027	

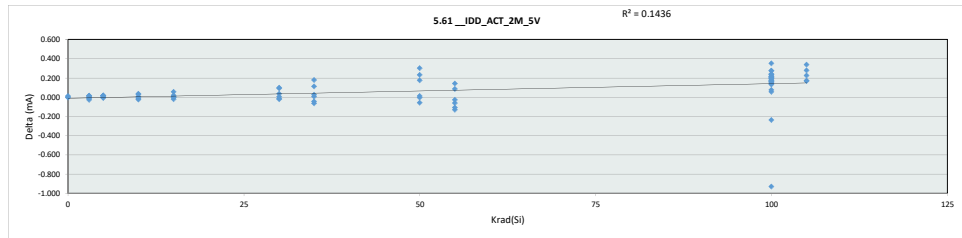


5.60_IDD_ACT_2M_4V											
Test Site											
Tester											
Test Number											
Max Limit	19.5		mA								
Min Limit			mA								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	10.573	10.507	10.503	10.519	10.497	10.518	10.486	10.521	10.535	10.433	10.482
Average	10.599	10.606	10.637	10.610	10.637	10.611	10.621	10.587	10.577	10.580	10.548
Max	10.650	10.700	10.794	10.689	10.797	10.703	10.729	10.641	10.635	10.690	10.641
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500

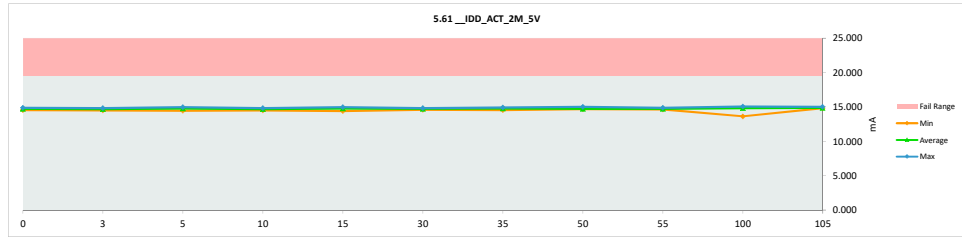


LDR TID Report
TPS7H5001-SP

5.61_IDDD_ACT_2M_5V					
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit
			mA	19.5	19.5
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta	
0	117	14.561	14.557	-0.004	
0	118	14.725	14.727	0.002	
0	122	14.863	14.875	0.012	
3	61	14.778	14.750	-0.028	
3	62	14.838	14.845	0.007	
3	63	14.592	14.606	0.014	
3	64	14.787	14.783	-0.004	
3	65	14.525	14.509	-0.016	
3	66	14.591	14.605	0.014	
5	67	14.628	14.648	0.020	
5	68	14.824	14.818	-0.006	
5	69	14.447	14.463	0.016	
5	70	14.975	14.992	0.017	
5	71	14.768	14.759	-0.009	
5	72	14.905	14.909	0.004	
10	73	14.778	14.753	-0.025	
10	74	14.838	14.819	-0.019	
10	75	14.592	14.626	0.034	
10	76	14.787	14.792	0.005	
10	77	14.525	14.522	-0.003	
10	78	14.591	14.621	0.030	
15	79	14.628	14.683	0.055	
15	80	14.824	14.825	0.001	
15	81	14.447	14.425	-0.022	
15	82	14.975	14.995	0.020	
15	83	14.768	14.770	0.002	
15	84	14.905	14.900	-0.005	
30	86	14.778	14.782	0.004	
30	89	14.838	14.824	-0.014	
30	91	14.592	14.691	0.099	
30	92	14.787	14.766	-0.021	
30	94	14.525	14.616	0.091	
30	95	14.591	14.627	0.036	
35	100	14.628	14.805	0.177	
35	101	14.824	14.830	0.006	
35	102	14.447	14.560	0.113	
35	103	14.975	14.911	-0.064	
35	104	14.768	14.794	0.026	
35	105	14.905	14.862	-0.043	
50	106	14.766	14.999	0.233	
50	107	14.657	14.670	0.013	
50	108	14.447	14.749	0.302	
50	109	14.712	14.706	-0.006	
50	110	14.750	14.691	-0.059	
50	111	14.509	14.683	0.174	
55	113	14.798	14.882	0.084	
55	114	14.633	14.775	0.142	
55	115	14.749	14.688	-0.061	
55	116	14.703	14.674	-0.029	
55	126	14.839	14.731	-0.108	
55	128	14.799	14.667	-0.132	
100	129	14.832	14.886	0.054	
100	131	14.721	14.798	0.077	
100	132	14.904	15.037	0.133	
100	133	14.818	15.051	0.233	
100	134	14.816	14.993	0.177	
100	135	14.569	13.641	-0.928	
100	138	14.725	14.886	0.161	
100	139	14.525	14.740	0.215	
100	140	14.699	14.909	0.210	
100	141	14.583	14.730	0.147	
100	142	14.795	14.556	-0.239	
100	143	14.678	14.912	0.234	
100	144	14.707	14.921	0.214	
100	220	14.794	14.934	0.140	
100	221	14.623	14.819	0.196	
100	224	14.641	14.834	0.193	
100	225	14.534	14.740	0.206	
100	226	14.819	15.060	0.241	
100	228	14.637	14.988	0.351	
100	232	14.793	15.069	0.276	
100	233	14.749	14.906	0.157	
100	236	14.381	14.548	0.167	
100	238	14.779	15.053	0.274	
100	243	14.574	14.746	0.172	
105	244	14.662	14.833	0.171	
105	246	14.539	14.878	0.339	
105	248	14.776	15.001	0.225	
105	374	14.618	14.896	0.278	
105	376	14.678	14.846	0.168	
	Max	14.975	15.069	0.351	
	Average	14.706	14.772	0.066	
	Min	14.381	13.641	-0.928	
	Std Dev	0.137	0.196	0.163	

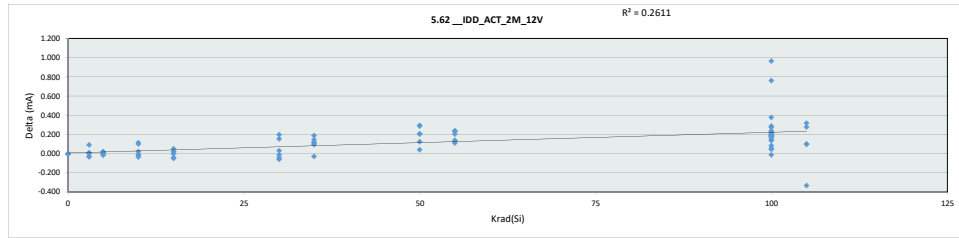


5.61_IDDD_ACT_2M_5V											
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit						
			mA	19.5	19.5						
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	14.557	14.509	14.463	14.522	14.425	14.616	14.560	14.670	14.667	13.641	14.833
Average	14.720	14.683	14.765	14.689	14.766	14.718	14.794	14.750	14.736	14.823	14.891
Max	14.875	14.845	14.992	14.819	14.995	14.824	14.911	14.999	14.882	15.069	15.001
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500

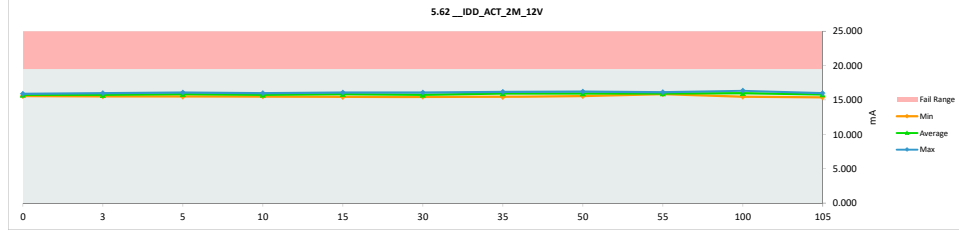


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5.62_IDD_ACT_2M_12V					
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit
			mA	19.5	19.5
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta	
0	117	15.575	15.568	-0.007	
0	118	15.846	15.845	-0.001	
0	122	15.906	15.909	0.003	
3	61	15.887	15.979	0.092	
3	62	15.851	15.858	0.007	
3	63	15.516	15.520	0.004	
3	64	15.837	15.805	-0.032	
3	65	15.735	15.705	-0.030	
3	66	15.553	15.561	0.008	
5	67	15.829	15.841	0.012	
5	68	15.844	15.837	-0.007	
5	69	15.487	15.493	0.006	
5	70	16.069	16.087	0.018	
5	71	15.834	15.819	-0.015	
5	72	16.042	16.063	0.021	
10	73	15.887	16.000	0.113	
10	74	15.851	15.952	0.101	
10	75	15.516	15.537	0.021	
10	76	15.837	15.800	-0.037	
10	77	15.735	15.718	-0.017	
10	78	15.553	15.543	-0.010	
15	79	15.829	15.881	0.052	
15	80	15.844	15.840	-0.004	
15	81	15.487	15.437	-0.050	
15	82	16.069	16.091	0.022	
15	83	15.834	15.791	-0.043	
15	84	16.042	16.063	0.021	
30	86	15.887	16.085	0.198	
30	89	15.851	16.004	0.153	
30	91	15.516	15.503	-0.013	
30	92	15.837	15.777	-0.060	
30	94	15.735	15.694	-0.041	
30	95	15.553	15.585	0.032	
35	100	15.829	15.940	0.111	
35	101	15.844	15.987	0.143	
35	102	15.487	15.459	-0.028	
35	103	16.069	16.185	0.116	
35	104	15.834	16.022	0.188	
35	105	16.042	16.135	0.093	
50	106	15.903	16.192	0.289	
50	107	15.772	16.067	0.295	
50	108	15.517	15.725	0.208	
50	109	15.778	15.982	0.204	
50	110	16.086	16.210	0.124	
50	111	15.515	15.557	0.042	
55	113	15.920	16.155	0.235	
55	114	15.612	15.850	0.238	
55	115	15.711	15.914	0.203	
55	116	15.808	15.942	0.134	
55	126	15.984	16.123	0.139	
55	128	15.742	15.854	0.112	
100	129	16.064	16.255	0.191	
100	131	15.770	15.829	0.059	
100	132	15.962	16.103	0.141	
100	133	16.079	16.240	0.161	
100	134	15.909	16.096	0.187	
100	135	15.512	16.274	0.762	
100	138	15.762	16.049	0.287	
100	139	15.571	15.750	0.179	
100	140	15.812	16.035	0.223	
100	141	15.505	15.493	-0.012	
100	142	15.850	16.123	0.273	
100	143	15.674	15.893	0.219	
100	144	15.763	15.956	0.193	
100	220	15.839	15.883	0.044	
100	221	15.774	16.000	0.226	
100	243	15.593	15.730	0.137	
105	244	15.704	15.800	0.096	
105	246	15.493	15.810	0.317	
105	248	15.861	15.962	0.101	
105	374	15.737	16.014	0.277	
105	376	15.728	15.395	-0.333	
	Max	16.086	16.347	0.963	
	Average	15.772	15.891	0.119	
	Min	15.384	15.395	-0.333	
	Std Dev	0.176	0.223	0.168	

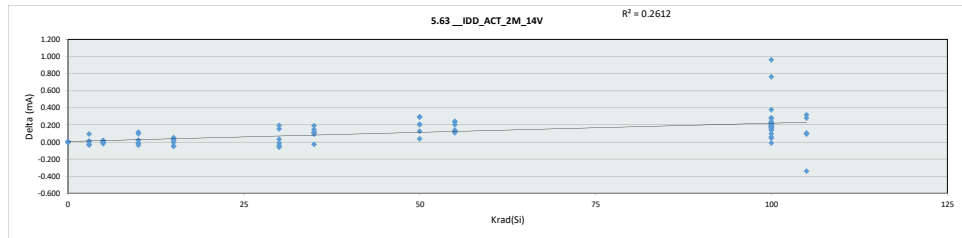


5.62_IDD_ACT_2M_12V											
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit						
			mA	19.5	19.5						
			mA								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	15.568	15.520	15.493	15.537	15.437	15.503	15.459	15.557	15.850	15.493	15.395
Average	15.774	15.738	15.857	15.758	15.851	15.775	15.955	15.956	15.973	15.993	15.796
Max	15.909	15.979	16.087	16.000	16.091	16.085	16.185	16.210	16.155	16.347	16.014
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500

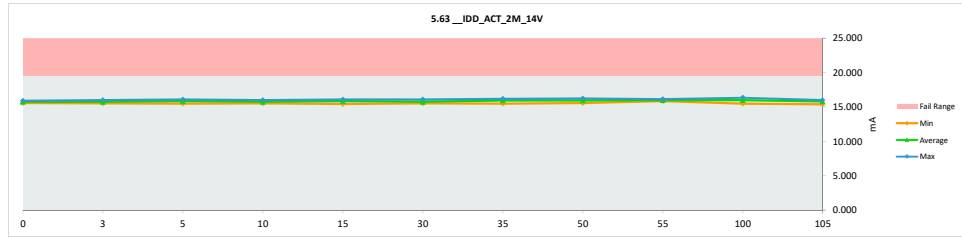


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5.63_IDD_ACT_2M_14V				
Test Site	Tester	Unit	Max Limit	Min Limit
Test Site	Tester	Unit	Max Limit	Min Limit
Test Number		mA	mA	
19.5		19.5		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	15.580	15.574	-0.006
0	118	15.854	15.856	0.002
0	122	15.908	15.916	0.008
3	61	15.893	15.986	0.093
3	62	15.857	15.861	0.004
3	63	15.521	15.527	0.006
3	64	15.843	15.809	-0.034
3	65	15.742	15.710	-0.032
3	66	15.560	15.570	0.010
5	67	15.836	15.845	0.009
5	68	15.848	15.842	-0.006
5	69	15.491	15.498	0.007
5	70	16.076	16.090	0.014
5	71	15.841	15.822	-0.019
5	72	16.047	16.065	0.018
10	73	15.893	16.007	0.114
10	74	15.857	15.955	0.098
10	75	15.521	15.542	0.021
10	76	15.843	15.806	-0.037
10	77	15.742	15.725	-0.017
10	78	15.560	15.549	-0.011
15	79	15.836	15.887	0.051
15	80	15.848	15.844	-0.004
15	81	15.491	15.441	-0.050
15	82	16.076	16.102	0.026
15	83	15.841	15.794	-0.047
15	84	16.047	16.069	0.022
30	86	15.893	16.086	0.193
30	89	15.857	16.010	0.153
30	91	15.521	15.510	-0.011
30	92	15.843	15.784	-0.059
30	94	15.742	15.699	-0.043
30	95	15.560	15.592	0.032
35	100	15.836	15.947	0.111
35	101	15.848	15.992	0.144
35	102	15.491	15.465	-0.026
35	103	16.076	16.191	0.115
35	104	15.841	16.030	0.189
35	105	16.047	16.141	0.094
50	106	15.909	16.200	0.291
50	107	15.778	16.072	0.294
50	108	15.525	15.729	0.204
50	109	15.783	15.990	0.207
50	110	16.092	16.219	0.127
50	111	15.521	15.559	0.038
55	113	15.926	16.160	0.234
55	114	15.616	15.858	0.242
55	115	15.717	15.920	0.203
55	116	15.815	15.951	0.136
55	126	15.991	16.128	0.137
55	128	15.748	15.858	0.110
100	129	16.072	16.259	0.187
100	131	15.776	15.834	0.058
100	132	15.968	16.110	0.142
100	133	16.082	16.246	0.164
100	134	15.915	16.105	0.190
100	135	15.520	16.282	0.762
100	138	15.768	16.052	0.284
100	139	15.577	15.752	0.175
100	140	15.818	16.042	0.224
100	141	15.512	15.500	-0.012
100	142	15.855	16.131	0.276
100	143	15.677	15.898	0.221
100	144	15.772	15.965	0.193
100	220	15.845	15.889	0.044
100	221	15.517	15.692	0.175
100	224	15.722	15.942	0.220
100	225	15.777	15.874	0.097
100	226	15.945	16.144	0.199
100	228	15.671	16.048	0.377
100	232	15.930	16.171	0.241
100	233	15.779	15.948	0.169
100	236	15.391	16.352	0.961
100	238	15.776	16.005	0.229
100	243	15.597	15.740	0.143
105	244	15.712	15.807	0.095
105	246	15.496	15.815	0.319
105	248	15.868	15.971	0.103
105	374	15.739	16.020	0.281
105	376	15.733	15.392	-0.341
Max		16.092	16.352	0.961
Average		15.778	15.897	0.119
Min		15.391	15.392	-0.341
Std Dev		0.176	0.224	0.169

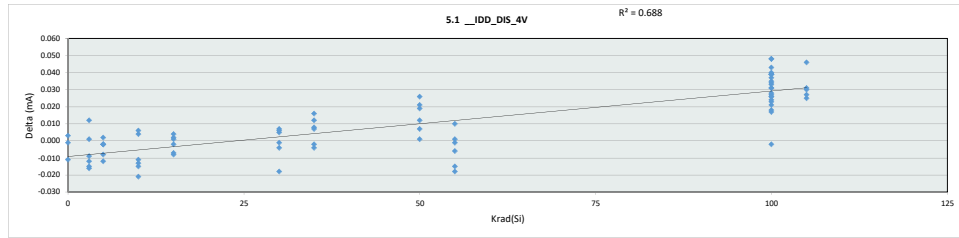


5.63_IDD_ACT_2M_14V											
Test Site	Tester	Unit	Max Limit	Min Limit							
Test Site	Tester	Unit	Max Limit	Min Limit							
19.5		mA									
19.5		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	15.574	15.527	15.498	15.542	15.441	15.510	15.465	15.559	15.858	15.500	15.392
Min	15.782	15.744	15.860	15.764	15.856	15.780	15.961	15.962	15.979	15.999	15.801
Average	15.916	15.986	16.090	16.007	16.102	16.086	16.191	16.219	16.160	16.352	16.020
Max	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500

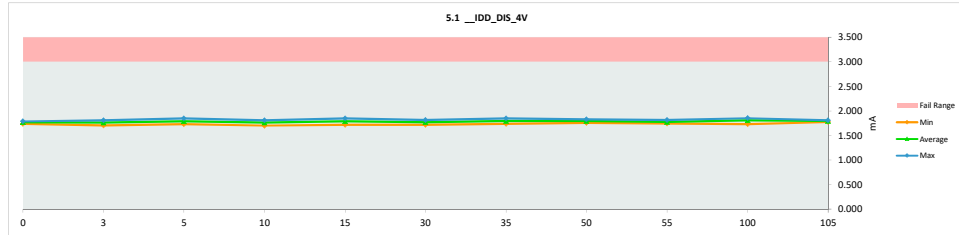


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5.1 IDD_DIS_4V					
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit
			mA	3	3
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta	
0	117	1.750	1.739	-0.011	
0	118	1.782	1.781	-0.001	
0	122	1.784	1.787	0.003	
3	61	1.776	1.761	-0.015	
3	62	1.819	1.807	-0.012	
3	63	1.769	1.770	0.001	
3	64	1.818	1.802	-0.016	
3	65	1.714	1.705	-0.009	
3	66	1.733	1.745	0.012	
5	67	1.763	1.761	-0.002	
5	68	1.822	1.820	-0.002	
5	69	1.728	1.730	0.002	
5	70	1.849	1.847	-0.002	
5	71	1.791	1.783	-0.008	
5	72	1.802	1.790	-0.012	
10	73	1.776	1.755	-0.021	
10	74	1.819	1.804	-0.015	
10	75	1.769	1.773	0.004	
10	76	1.818	1.807	-0.011	
10	77	1.714	1.701	-0.013	
10	78	1.733	1.739	0.006	
15	79	1.763	1.767	0.004	
15	80	1.822	1.820	-0.002	
15	81	1.728	1.720	-0.008	
15	82	1.849	1.851	0.002	
15	83	1.791	1.792	0.001	
15	84	1.802	1.795	-0.007	
30	86	1.776	1.775	-0.001	
30	89	1.819	1.815	-0.004	
30	91	1.769	1.776	0.007	
30	92	1.818	1.800	-0.018	
30	94	1.714	1.720	0.006	
30	95	1.733	1.738	0.005	
35	100	1.763	1.775	0.012	
35	101	1.822	1.838	0.016	
35	102	1.728	1.736	0.008	
35	103	1.849	1.847	-0.002	
35	104	1.791	1.798	0.007	
35	105	1.802	1.798	-0.004	
50	106	1.816	1.823	0.007	
50	107	1.806	1.826	0.020	
50	108	1.750	1.771	0.021	
50	109	1.796	1.815	0.019	
50	110	1.784	1.785	0.001	
50	111	1.748	1.760	0.012	
55	113	1.788	1.773	-0.015	
55	114	1.773	1.772	-0.001	
55	115	1.765	1.775	0.010	
55	116	1.790	1.791	0.001	
55	126	1.822	1.816	-0.006	
55	128	1.764	1.746	-0.018	
100	129	1.795	1.834	0.039	
100	131	1.762	1.760	-0.002	
100	132	1.807	1.831	0.024	
100	133	1.794	1.825	0.031	
100	134	1.761	1.796	0.035	
100	135	1.777	1.794	0.017	
100	138	1.781	1.829	0.048	
100	139	1.758	1.791	0.033	
100	140	1.786	1.823	0.037	
100	141	1.746	1.767	0.021	
100	142	1.782	1.810	0.028	
100	143	1.792	1.840	0.048	
100	144	1.777	1.811	0.034	
100	220	1.772	1.815	0.043	
100	221	1.782	1.805	0.023	
100	224	1.807	1.846	0.039	
100	225	1.708	1.734	0.026	
100	226	1.790	1.829	0.039	
100	228	1.767	1.806	0.039	
100	232	1.812	1.852	0.040	
100	233	1.816	1.843	0.027	
100	236	1.737	1.763	0.026	
100	238	1.788	1.819	0.031	
100	243	1.737	1.755	0.018	
105	244	1.746	1.776	0.030	
105	246	1.728	1.774	0.046	
105	248	1.785	1.810	0.025	
105	374	1.774	1.805	0.031	
105	376	1.784	1.811	0.027	
	Max	1.849	1.852	0.048	
	Average	1.779	1.790	0.011	
	Min	1.708	1.701	-0.021	
	Std Dev	0.033	0.036	0.019	

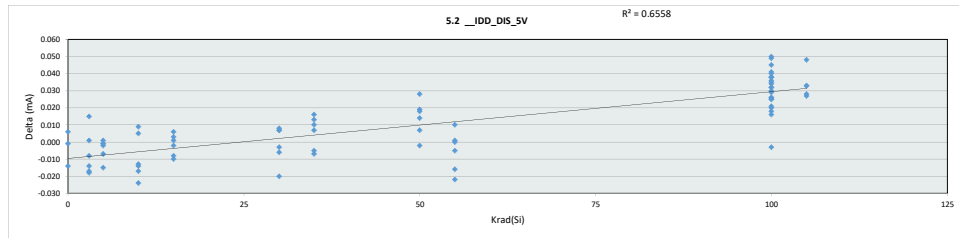


5.1 __IDD_DIS_4V											
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit						
			mA	3	3						
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.739	1.705	1.730	1.701	1.720	1.720	1.736	1.760	1.746	1.734	1.774
Average	1.769	1.765	1.789	1.763	1.791	1.771	1.799	1.797	1.779	1.807	1.795
Max	1.787	1.807	1.847	1.807	1.851	1.815	1.847	1.826	1.816	1.852	1.811
UL	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000

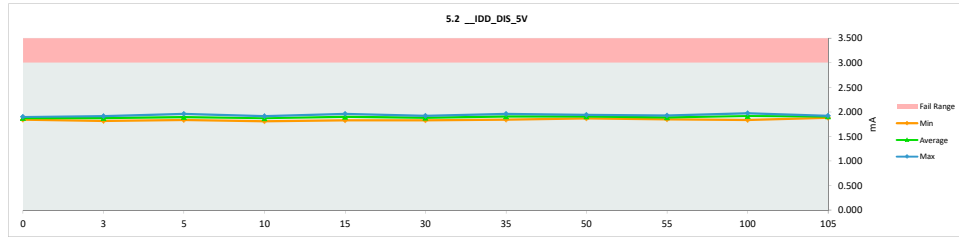


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5.2 _IDD_DIS_5V					
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit
			mA	3	3
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta	
0	117	1.858	1.844	-0.014	
0	118	1.889	1.888	-0.001	
0	122	1.892	1.898	0.006	
3	61	1.886	1.869	-0.017	
3	62	1.928	1.914	-0.014	
3	63	1.876	1.877	0.001	
3	64	1.928	1.910	-0.018	
3	65	1.821	1.813	-0.008	
3	66	1.838	1.853	0.015	
5	67	1.870	1.869	-0.001	
5	68	1.929	1.928	-0.001	
5	69	1.834	1.835	0.001	
5	70	1.961	1.959	-0.002	
5	71	1.898	1.891	-0.007	
5	72	1.912	1.897	-0.015	
10	73	1.886	1.862	-0.024	
10	74	1.928	1.911	-0.017	
10	75	1.876	1.881	0.005	
10	76	1.928	1.915	-0.013	
10	77	1.821	1.807	-0.014	
10	78	1.838	1.847	0.009	
15	79	1.870	1.876	0.006	
15	80	1.929	1.927	-0.002	
15	81	1.834	1.826	-0.008	
15	82	1.961	1.962	0.001	
15	83	1.898	1.901	0.003	
15	84	1.912	1.902	-0.010	
30	86	1.886	1.883	-0.003	
30	89	1.928	1.922	-0.006	
30	91	1.876	1.883	0.007	
30	92	1.928	1.908	-0.020	
30	94	1.821	1.828	0.007	
30	95	1.838	1.846	0.008	
35	100	1.870	1.883	0.013	
35	101	1.929	1.945	0.016	
35	102	1.834	1.841	0.007	
35	103	1.961	1.956	-0.005	
35	104	1.898	1.908	0.010	
35	105	1.912	1.905	-0.007	
50	106	1.924	1.931	0.007	
50	107	1.909	1.937	0.028	
50	108	1.859	1.878	0.019	
50	109	1.907	1.925	0.018	
50	110	1.894	1.892	-0.002	
50	111	1.854	1.868	0.014	
55	113	1.897	1.881	-0.016	
55	114	1.879	1.879	0.000	
55	115	1.871	1.881	0.010	
55	116	1.898	1.899	0.001	
55	126	1.931	1.926	-0.005	
55	128	1.873	1.851	-0.022	
100	129	1.902	1.940	0.038	
100	131	1.871	1.868	-0.003	
100	132	1.915	1.941	0.026	
100	133	1.901	1.933	0.032	
100	134	1.868	1.902	0.034	
100	135	1.886	1.902	0.016	
100	138	1.887	1.936	0.049	
100	139	1.866	1.896	0.030	
100	140	1.896	1.932	0.036	
100	141	1.852	1.872	0.020	
100	142	1.889	1.918	0.029	
100	143	1.900	1.950	0.050	
100	144	1.885	1.920	0.035	
100	220	1.879	1.924	0.045	
100	221	1.892	1.913	0.021	
100	224	1.916	1.954	0.038	
100	225	1.813	1.838	0.025	
100	226	1.899	1.939	0.040	
100	228	1.873	1.914	0.041	
100	232	1.923	1.961	0.038	
100	233	1.945	1.970	0.025	
100	236	1.846	1.872	0.026	
100	238	1.896	1.928	0.032	
100	243	1.844	1.862	0.018	
105	244	1.854	1.887	0.033	
105	246	1.834	1.882	0.048	
105	248	1.892	1.920	0.028	
105	374	1.882	1.915	0.033	
105	376	1.894	1.921	0.027	
	Max	1.961	1.970	0.050	
	Average	1.887	1.898	0.011	
	Min	1.813	1.807	-0.024	
	Std Dev	0.035	0.037	0.019	

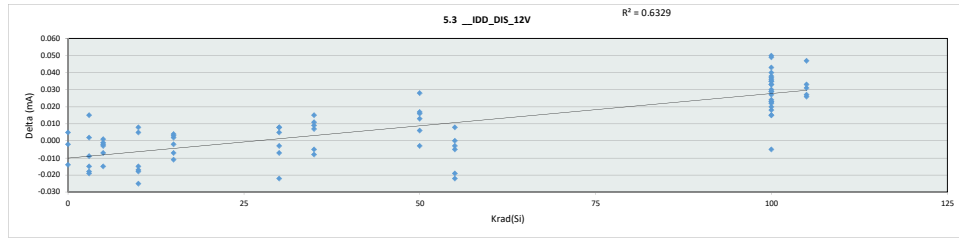


5.2 _IDD_DIS_5V											
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit						
		3	mA	3	3						
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.844	1.813	1.835	1.807	1.826	1.828	1.841	1.868	1.851	1.838	1.882
Average	1.877	1.873	1.897	1.871	1.899	1.878	1.906	1.905	1.886	1.916	1.905
Max	1.898	1.914	1.959	1.915	1.962	1.922	1.956	1.937	1.926	1.970	1.921
UL	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000

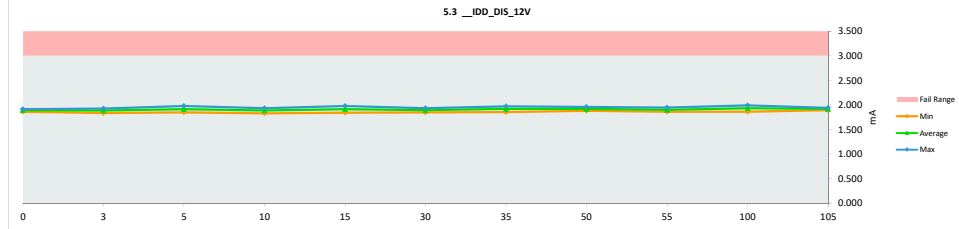


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5.3 IDD_DIS_12V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	3	3		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.874	1.860	-0.014
0	118	1.910	1.908	-0.002
0	122	1.909	1.914	0.005
3	61	1.906	1.888	-0.018
3	62	1.943	1.928	-0.015
3	63	1.889	1.891	0.002
3	64	1.947	1.928	-0.019
3	65	1.845	1.836	-0.009
3	66	1.855	1.870	0.015
5	67	1.888	1.885	-0.003
5	68	1.945	1.944	-0.001
5	69	1.850	1.851	0.001
5	70	1.978	1.976	-0.002
5	71	1.915	1.908	-0.007
5	72	1.928	1.913	-0.015
10	73	1.906	1.881	-0.025
10	74	1.943	1.925	-0.018
10	75	1.889	1.894	0.005
10	76	1.947	1.932	-0.015
10	77	1.845	1.828	-0.017
10	78	1.855	1.863	0.008
15	79	1.888	1.892	0.004
15	80	1.945	1.943	-0.002
15	81	1.850	1.843	-0.007
15	82	1.978	1.980	0.002
15	83	1.915	1.918	0.003
15	84	1.928	1.917	-0.011
30	86	1.906	1.903	-0.003
30	89	1.943	1.936	-0.007
30	91	1.889	1.897	0.008
30	92	1.947	1.925	-0.022
30	94	1.845	1.850	0.005
30	95	1.855	1.863	0.008
35	100	1.888	1.899	0.011
35	101	1.945	1.960	0.015
35	102	1.850	1.857	0.007
35	103	1.978	1.973	-0.005
35	104	1.915	1.924	0.009
35	105	1.928	1.920	-0.008
50	106	1.943	1.949	0.006
50	107	1.928	1.956	0.028
50	108	1.879	1.896	0.017
50	109	1.922	1.938	0.016
50	110	1.914	1.911	-0.003
50	111	1.871	1.884	0.013
55	113	1.919	1.900	-0.019
55	114	1.895	1.892	-0.003
55	115	1.886	1.894	0.008
55	116	1.914	1.914	0.000
55	126	1.948	1.943	-0.005
55	128	1.886	1.864	-0.022
100	129	1.923	1.959	0.036
100	131	1.887	1.882	-0.005
100	132	1.932	1.955	0.023
100	133	1.923	1.952	0.029
100	134	1.885	1.918	0.033
100	135	1.901	1.916	0.015
100	138	1.904	1.953	0.049
100	139	1.883	1.913	0.030
100	140	1.915	1.950	0.035
100	141	1.869	1.887	0.018
100	142	1.907	1.934	0.027
100	143	1.917	1.967	0.050
100	144	1.902	1.935	0.033
100	220	1.896	1.939	0.043
100	221	1.907	1.927	0.020
100	224	1.935	1.972	0.037
100	225	1.837	1.859	0.022
100	226	1.920	1.958	0.038
100	228	1.892	1.932	0.040
100	232	1.942	1.977	0.035
100	233	1.967	1.990	0.023
100	236	1.866	1.890	0.024
100	238	1.912	1.941	0.029
100	243	1.863	1.878	0.015
105	244	1.873	1.904	0.031
105	246	1.849	1.896	0.047
105	248	1.912	1.938	0.026
105	374	1.902	1.935	0.033
105	376	1.912	1.939	0.027
Max		1.978	1.990	0.050
Average		1.905	1.914	0.010
Min		1.837	1.828	-0.025
Std Dev		0.034	0.037	0.019

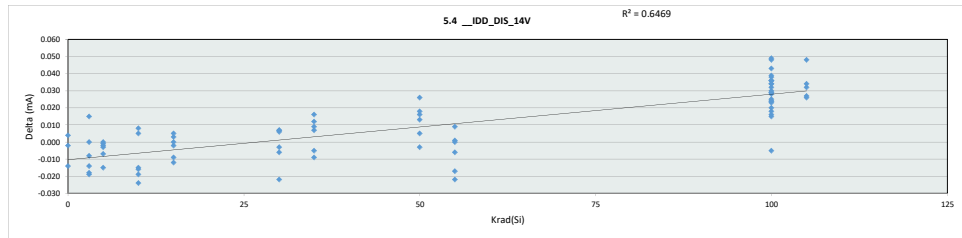


5.3 _IDD_DIS_12V											
Test Site											
Tester											
Test Number											
Max Limit	3	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.860	1.836	1.851	1.828	1.843	1.850	1.857	1.884	1.864	1.859	1.896
Average	1.894	1.890	1.913	1.887	1.916	1.896	1.922	1.922	1.901	1.933	1.922
Max	1.914	1.928	1.976	1.932	1.980	1.936	1.973	1.956	1.943	1.990	1.939
UL	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000

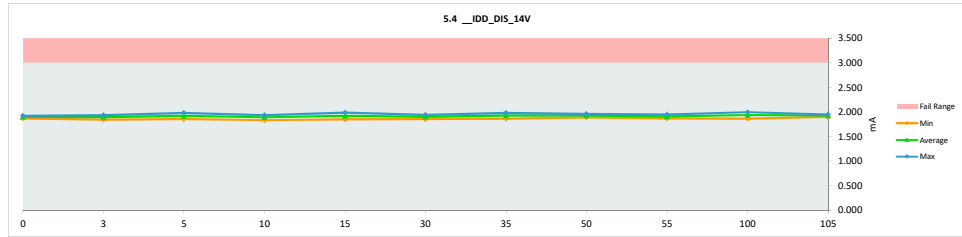


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5.4 IDD_DIS_14V				
Test Site	Tester	Test Number	Unit	Max Limit
			mA	3
			mA	3
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.879	1.865	-0.014
0	118	1.914	1.912	-0.002
0	122	1.914	1.918	0.004
3	61	1.910	1.892	-0.018
3	62	1.947	1.933	-0.014
3	63	1.894	1.894	0.000
3	64	1.951	1.932	-0.019
3	65	1.848	1.840	-0.008
3	66	1.859	1.874	0.015
5	67	1.891	1.889	-0.002
5	68	1.949	1.948	-0.001
5	69	1.855	1.855	0.000
5	70	1.983	1.980	-0.003
5	71	1.919	1.912	-0.007
5	72	1.933	1.918	-0.015
10	73	1.910	1.886	-0.024
10	74	1.947	1.928	-0.019
10	75	1.894	1.899	0.005
10	76	1.951	1.936	-0.015
10	77	1.848	1.832	-0.016
10	78	1.859	1.867	0.008
15	79	1.891	1.896	0.005
15	80	1.949	1.947	-0.002
15	81	1.865	1.846	-0.009
15	82	1.983	1.983	0.000
15	83	1.919	1.922	0.003
15	84	1.933	1.921	-0.012
30	86	1.910	1.907	-0.003
30	89	1.947	1.941	-0.006
30	91	1.894	1.901	0.007
30	92	1.951	1.929	-0.022
30	94	1.848	1.854	0.006
30	95	1.859	1.866	0.007
35	100	1.891	1.903	0.012
35	101	1.949	1.965	0.016
35	102	1.855	1.862	0.007
35	103	1.983	1.978	-0.005
35	104	1.919	1.928	0.009
35	105	1.933	1.924	-0.009
50	106	1.948	1.953	0.005
50	107	1.943	1.959	0.016
50	108	1.883	1.901	0.018
50	109	1.926	1.942	0.016
50	110	1.919	1.916	-0.003
50	111	1.875	1.888	0.013
55	113	1.922	1.905	-0.017
55	114	1.898	1.898	0.000
55	115	1.889	1.898	0.009
55	116	1.918	1.919	0.001
55	126	1.953	1.947	-0.006
55	128	1.890	1.868	-0.022
100	129	1.927	1.963	0.036
100	131	1.891	1.886	-0.005
100	132	1.936	1.961	0.025
100	133	1.927	1.957	0.030
100	134	1.889	1.921	0.032
100	135	1.905	1.920	0.015
100	138	1.909	1.957	0.048
100	139	1.888	1.917	0.029
100	140	1.919	1.953	0.034
100	141	1.872	1.890	0.018
100	142	1.910	1.938	0.028
100	143	1.921	1.970	0.049
100	144	1.906	1.940	0.034
100	220	1.900	1.943	0.043
100	221	1.912	1.932	0.020
100	224	1.940	1.976	0.036
100	225	1.841	1.864	0.023
100	226	1.924	1.962	0.038
100	228	1.897	1.936	0.039
100	232	1.946	1.982	0.036
100	233	1.971	1.995	0.024
100	236	1.870	1.894	0.024
100	238	1.916	1.945	0.029
100	243	1.867	1.883	0.016
105	244	1.877	1.909	0.032
105	246	1.853	1.901	0.048
105	248	1.916	1.943	0.027
105	374	1.907	1.941	0.034
105	376	1.916	1.942	0.026
Max		1.983	1.995	0.049
Average		1.909	1.919	0.010
Min		1.841	1.832	-0.024
Std Dev		0.034	0.037	0.019

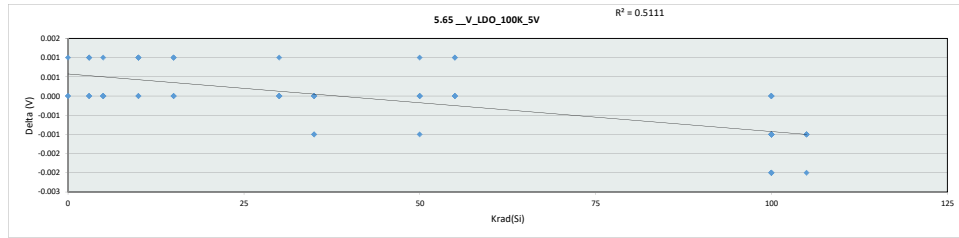


5.4 __IDD_DIS_14V											
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit						
			mA	3	3						
			mA	3	3						
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.865	1.840	1.855	1.832	1.846	1.854	1.862	1.888	1.868	1.864	1.901
Average	1.898	1.894	1.917	1.891	1.919	1.900	1.927	1.927	1.906	1.937	1.927
Max	1.918	1.933	1.980	1.936	1.983	1.941	1.978	1.959	1.947	1.995	1.943
UL	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000

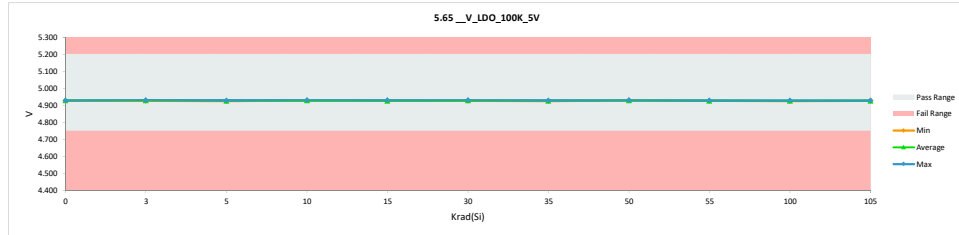


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5.65_V_LDO_100K_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	4.929	4.929	0.000
0	118	4.927	4.927	0.000
0	122	4.927	4.928	0.001
3	61	4.927	4.927	0.000
3	62	4.927	4.928	0.001
3	63	4.927	4.928	0.001
3	64	4.928	4.928	0.000
3	65	4.930	4.931	0.001
3	66	4.930	4.930	0.000
5	67	4.929	4.929	0.000
5	68	4.926	4.926	0.000
5	69	4.929	4.929	0.000
5	70	4.925	4.925	0.000
5	71	4.926	4.927	0.001
5	72	4.926	4.926	0.000
10	73	4.927	4.928	0.001
10	74	4.927	4.928	0.001
10	75	4.927	4.928	0.001
10	76	4.928	4.928	0.000
10	77	4.930	4.930	0.000
10	78	4.930	4.931	0.001
15	79	4.929	4.929	0.000
15	80	4.926	4.926	0.000
15	81	4.929	4.930	0.001
15	82	4.925	4.926	0.001
15	83	4.926	4.927	0.001
15	84	4.926	4.926	0.000
30	86	4.927	4.927	0.000
30	89	4.927	4.927	0.000
30	91	4.927	4.927	0.000
30	92	4.928	4.928	0.000
30	94	4.930	4.930	0.000
30	95	4.930	4.931	0.001
35	100	4.929	4.928	-0.001
35	101	4.926	4.925	-0.001
35	102	4.929	4.929	0.000
35	103	4.925	4.925	0.000
35	104	4.926	4.926	0.000
35	105	4.926	4.926	0.000
50	106	4.927	4.928	0.001
50	107	4.929	4.929	0.000
50	108	4.930	4.929	-0.001
50	109	4.928	4.928	0.000
50	110	4.928	4.928	0.000
50	111	4.930	4.930	0.000
55	113	4.927	4.927	0.000
55	114	4.928	4.928	0.000
55	115	4.927	4.927	0.000
55	116	4.927	4.928	0.001
55	126	4.926	4.926	0.000
55	128	4.927	4.928	0.001
100	129	4.927	4.926	-0.001
100	131	4.929	4.929	0.000
100	132	4.927	4.927	0.000
100	133	4.927	4.926	-0.001
100	134	4.929	4.928	-0.001
100	135	4.929	4.929	0.000
100	138	4.928	4.927	-0.001
100	139	4.930	4.929	-0.001
100	140	4.928	4.927	-0.001
100	141	4.930	4.929	-0.001
100	142	4.927	4.926	-0.001
100	143	4.928	4.926	-0.002
100	144	4.929	4.928	-0.001
100	220	4.927	4.927	0.000
100	221	4.929	4.929	0.000
100	224	4.929	4.927	-0.002
100	225	4.931	4.929	-0.002
100	226	4.926	4.925	-0.001
100	228	4.929	4.927	-0.002
100	232	4.929	4.927	-0.002
100	233	4.928	4.927	-0.001
100	236	4.930	4.929	-0.001
100	238	4.927	4.926	-0.001
100	243	4.929	4.929	0.000
105	244	4.928	4.927	-0.001
105	246	4.930	4.928	-0.002
105	248	4.928	4.927	-0.001
105	374	4.927	4.926	-0.001
105	376	4.929	4.928	-0.001
	Max	4.931	4.931	0.001
	Average	4.928	4.928	0.000
	Min	4.925	4.925	-0.002
	Std Dev	0.001	0.001	0.001

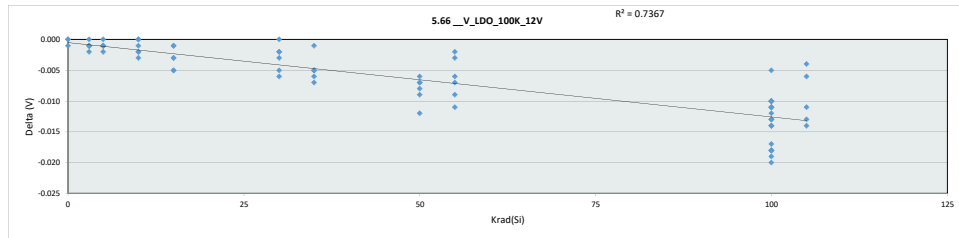


5.65_V_LDO_100K_5V											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	4.927	4.927	4.925	4.928	4.926	4.927	4.925	4.928	4.926	4.925	4.926
Average	4.928	4.929	4.927	4.929	4.927	4.928	4.927	4.929	4.927	4.927	4.927
Max	4.929	4.931	4.929	4.931	4.930	4.931	4.929	4.930	4.928	4.929	4.928
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

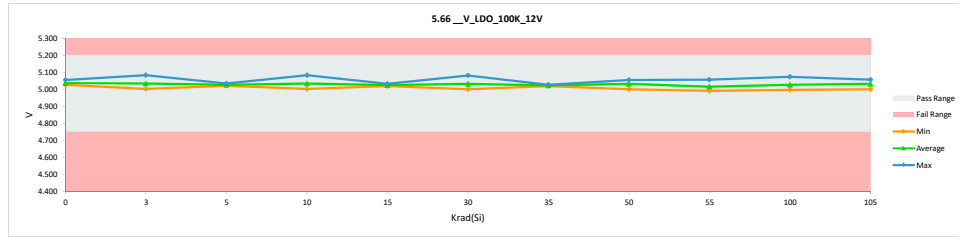


LDR TID Report
TPS7H5001-SP

5.66_V_LDO_100K_12V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	5.029	5.028	-0.001
0	118	5.055	5.055	0.000
0	122	5.026	5.026	0.000
3	61	5.047	5.046	-0.001
3	62	5.010	5.009	-0.001
3	63	5.003	5.002	-0.001
3	64	5.040	5.040	0.000
3	65	5.083	5.082	-0.001
3	66	5.029	5.027	-0.002
5	67	5.024	5.022	-0.002
5	68	5.021	5.020	-0.001
5	69	5.026	5.025	-0.001
5	70	5.033	5.033	0.000
5	71	5.029	5.028	-0.001
5	72	5.026	5.025	-0.001
10	73	5.047	5.045	-0.002
10	74	5.010	5.007	-0.003
10	75	5.003	5.002	-0.001
10	76	5.040	5.040	0.000
10	77	5.083	5.083	0.000
10	78	5.029	5.027	-0.002
15	79	5.024	5.019	-0.005
15	80	5.021	5.020	-0.001
15	81	5.026	5.023	-0.003
15	82	5.033	5.032	-0.001
15	83	5.029	5.026	-0.003
15	84	5.026	5.021	-0.005
30	86	5.047	5.047	0.000
30	89	5.010	5.005	-0.005
30	91	5.003	5.000	-0.003
30	92	5.040	5.034	-0.006
30	94	5.083	5.081	-0.002
30	95	5.029	5.027	-0.002
35	100	5.024	5.019	-0.005
35	101	5.021	5.020	-0.001
35	102	5.026	5.021	-0.005
35	103	5.033	5.026	-0.007
35	104	5.029	5.024	-0.005
35	105	5.026	5.020	-0.006
50	106	5.038	5.031	-0.007
50	107	5.053	5.041	-0.012
50	108	5.056	5.047	-0.009
50	109	5.008	5.000	-0.008
50	110	5.061	5.055	-0.006
50	111	5.029	5.022	-0.007
55	113	5.058	5.056	-0.002
55	114	5.013	5.002	-0.011
55	115	5.005	4.998	-0.007
55	116	5.019	5.013	-0.006
55	126	5.033	5.030	-0.003
55	128	4.999	4.990	-0.009
100	129	5.062	5.051	-0.011
100	131	5.021	5.011	-0.010
100	132	5.025	5.012	-0.013
100	133	5.074	5.061	-0.013
100	134	5.032	5.022	-0.010
100	135	5.019	5.000	-0.019
100	138	5.032	5.019	-0.013
100	139	5.040	5.030	-0.010
100	140	5.041	5.031	-0.010
100	141	5.025	5.007	-0.018
100	142	5.028	5.018	-0.010
100	143	5.029	5.018	-0.011
100	144	5.023	5.018	-0.005
100	220	5.025	5.008	-0.017
100	221	5.016	5.002	-0.014
100	224	5.039	5.027	-0.012
100	225	5.087	5.073	-0.014
100	226	5.059	5.045	-0.014
100	228	5.047	5.036	-0.011
100	232	5.043	5.025	-0.018
100	233	5.051	5.040	-0.011
100	236	5.051	5.033	-0.018
100	238	5.014	4.996	-0.018
100	243	5.048	5.028	-0.020
105	244	5.042	5.036	-0.006
105	246	5.014	5.000	-0.014
105	248	5.051	5.040	-0.011
105	374	5.061	5.057	-0.004
105	376	5.036	5.023	-0.013
	Max	5.087	5.083	0.000
	Average	5.034	5.027	-0.007
	Min	4.999	4.990	-0.009
	Std Dev	0.019	0.020	0.006



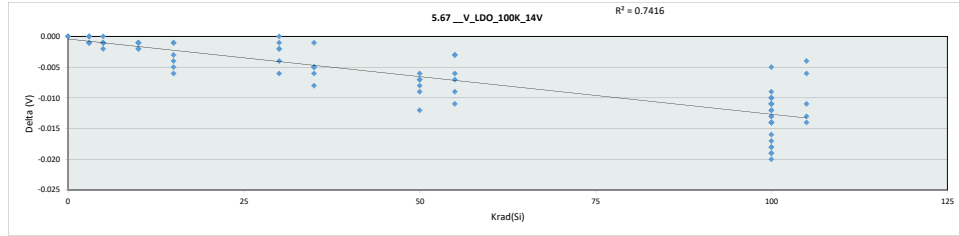
5.66_V_LDO_100K_12V											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.026	5.002	5.020	5.002	5.019	5.000	5.019	5.000	4.990	4.996	5.000
Average	5.036	5.034	5.026	5.034	5.024	5.032	5.022	5.033	5.015	5.025	5.031
Max	5.055	5.082	5.033	5.083	5.032	5.081	5.026	5.055	5.056	5.073	5.057
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



LDR TID Report
TPS7H5001-SP

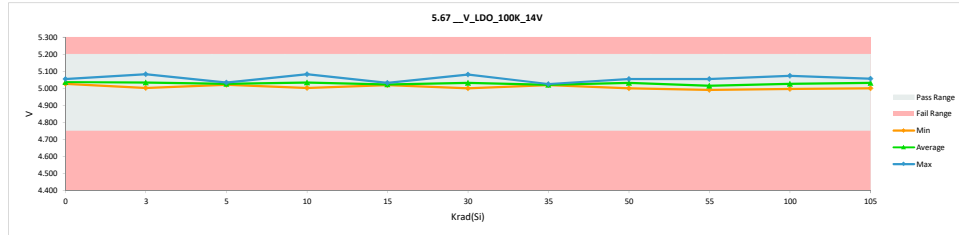
5.67_V_LDO_100K_14V		
Test Site		
Tester		
Test Number		
Unit	V	V
Max Limit	5.2	5.2
Min Limit	4.75	4.75

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	5.028	5.028	0.000
0	118	5.055	5.055	0.000
0	122	5.026	5.026	0.000
3	61	5.047	5.046	-0.001
3	62	5.009	5.009	0.000
3	63	5.002	5.002	0.000
3	64	5.040	5.040	0.000
3	65	5.083	5.082	-0.001
3	66	5.028	5.027	-0.001
5	67	5.024	5.022	-0.002
5	68	5.021	5.020	-0.001
5	69	5.026	5.025	-0.001
5	70	5.033	5.033	0.000
5	71	5.029	5.028	-0.001
5	72	5.026	5.025	-0.001
10	73	5.047	5.045	-0.002
10	74	5.009	5.007	-0.002
10	75	5.002	5.001	-0.001
10	76	5.040	5.039	-0.001
10	77	5.083	5.082	-0.001
10	78	5.028	5.027	-0.001
15	79	5.024	5.018	-0.006
15	80	5.021	5.020	-0.001
15	81	5.026	5.023	-0.003
15	82	5.033	5.032	-0.001
15	83	5.029	5.025	-0.004
15	84	5.026	5.021	-0.005
30	86	5.047	5.046	-0.001
30	89	5.009	5.005	-0.004
30	91	5.002	5.000	-0.002
30	92	5.040	5.034	-0.006
30	94	5.083	5.081	-0.002
30	95	5.028	5.028	0.000
35	100	5.024	5.019	-0.005
35	101	5.021	5.020	-0.001
35	102	5.026	5.021	-0.005
35	103	5.033	5.025	-0.008
35	104	5.029	5.024	-0.005
35	105	5.026	5.020	-0.006
50	106	5.038	5.031	-0.007
50	107	5.053	5.041	-0.012
50	108	5.056	5.047	-0.009
50	109	5.008	5.000	-0.008
50	110	5.061	5.055	-0.006
50	111	5.029	5.022	-0.007
55	113	5.058	5.055	-0.003
55	114	5.013	5.002	-0.011
55	115	5.005	4.998	-0.007
55	116	5.019	5.013	-0.006
55	126	5.033	5.030	-0.003
55	128	4.999	4.990	-0.009
100	129	5.062	5.051	-0.011
100	131	5.021	5.010	-0.011
100	132	5.024	5.012	-0.012
100	133	5.075	5.061	-0.014
100	134	5.032	5.022	-0.010
100	135	5.019	5.000	-0.019
100	138	5.032	5.019	-0.013
100	139	5.040	5.030	-0.010
100	140	5.041	5.032	-0.009
100	141	5.025	5.007	-0.018
100	142	5.028	5.018	-0.010
100	143	5.029	5.018	-0.011
100	144	5.023	5.018	-0.005
100	220	5.024	5.008	-0.016
100	221	5.016	5.002	-0.014
100	224	5.040	5.027	-0.013
100	225	5.087	5.073	-0.014
100	226	5.059	5.045	-0.014
100	228	5.047	5.036	-0.011
100	232	5.042	5.025	-0.017
100	233	5.051	5.039	-0.012
100	236	5.051	5.032	-0.019
100	238	5.014	4.996	-0.018
100	243	5.048	5.028	-0.020
105	244	5.042	5.036	-0.006
105	246	5.014	5.000	-0.014
105	248	5.051	5.040	-0.011
105	374	5.061	5.057	-0.004
105	376	5.036	5.023	-0.013
	Max	5.087	5.082	0.000
	Average	5.034	5.027	-0.007
	Min	4.999	4.990	-0.010
	Std Dev	0.020	0.020	0.006



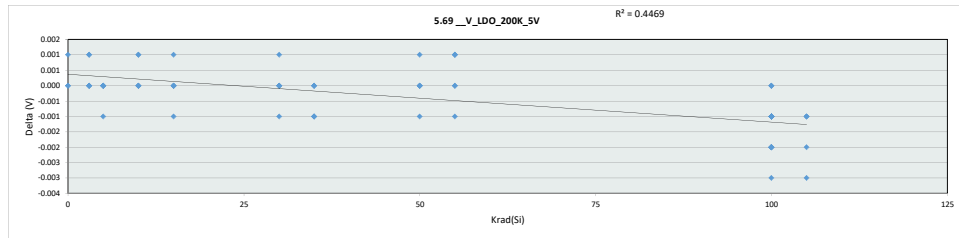
5.67_V_LDO_100K_14V		
Test Site		
Tester		
Test Number		
Max Limit	5.2	V
Min Limit	4.75	V

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.026	5.002	5.020	5.001	5.018	5.000	5.019	5.000	4.990	4.996	5.000
Average	5.036	5.034	5.026	5.034	5.023	5.032	5.022	5.033	5.015	5.025	5.031
Max	5.055	5.082	5.033	5.082	5.032	5.081	5.025	5.055	5.055	5.073	5.057
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

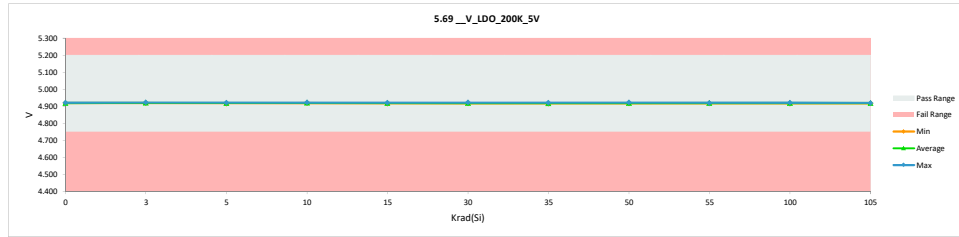


LDR TID Report
TPS7H5001-SP

5.69_V_LDO_200K_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	4.921	4.921	0.000
0	118	4.919	4.919	0.000
0	122	4.918	4.919	0.001
3	61	4.918	4.919	0.001
3	62	4.918	4.919	0.001
3	63	4.919	4.919	0.000
3	64	4.920	4.920	0.000
3	65	4.922	4.922	0.000
3	66	4.922	4.922	0.000
5	67	4.921	4.920	-0.001
5	68	4.917	4.917	0.000
5	69	4.921	4.921	0.000
5	70	4.916	4.916	0.000
5	71	4.918	4.918	0.000
5	72	4.917	4.917	0.000
10	73	4.918	4.919	0.001
10	74	4.918	4.919	0.001
10	75	4.919	4.919	0.000
10	76	4.920	4.920	0.000
10	77	4.922	4.922	0.000
10	78	4.922	4.922	0.000
15	79	4.921	4.920	-0.001
15	80	4.917	4.918	0.001
15	81	4.921	4.921	0.000
15	82	4.916	4.916	0.000
15	83	4.918	4.918	0.000
15	84	4.917	4.917	0.000
30	86	4.918	4.918	0.000
30	89	4.918	4.919	0.001
30	91	4.919	4.918	-0.001
30	92	4.920	4.920	0.000
30	94	4.922	4.922	0.000
30	95	4.922	4.922	0.000
35	100	4.921	4.920	-0.001
35	101	4.917	4.916	-0.001
35	102	4.921	4.921	0.000
35	103	4.916	4.916	0.000
35	104	4.918	4.917	-0.001
35	105	4.917	4.917	0.000
50	106	4.918	4.918	0.000
50	107	4.921	4.920	-0.001
50	108	4.921	4.921	0.000
50	109	4.920	4.920	0.000
50	110	4.919	4.919	0.000
50	111	4.921	4.922	0.001
55	113	4.918	4.919	0.001
55	114	4.919	4.920	0.001
55	115	4.918	4.918	0.000
55	116	4.919	4.918	-0.001
55	126	4.918	4.918	0.000
55	128	4.918	4.919	0.001
100	129	4.919	4.917	-0.002
100	131	4.921	4.921	0.000
100	132	4.919	4.918	-0.001
100	133	4.919	4.918	-0.001
100	134	4.921	4.920	-0.001
100	135	4.920	4.920	0.000
100	138	4.920	4.918	-0.002
100	139	4.922	4.920	-0.002
100	140	4.920	4.918	-0.002
100	141	4.921	4.921	0.000
100	142	4.918	4.917	-0.001
100	143	4.919	4.917	-0.002
100	144	4.921	4.920	-0.001
100	220	4.919	4.918	-0.001
100	221	4.921	4.920	-0.001
100	224	4.921	4.919	-0.002
100	225	4.922	4.921	-0.001
100	226	4.917	4.916	-0.001
100	228	4.920	4.918	-0.002
100	232	4.921	4.918	-0.003
100	233	4.920	4.919	-0.001
100	236	4.922	4.921	-0.001
100	238	4.918	4.917	-0.001
100	243	4.921	4.920	-0.001
105	244	4.919	4.918	-0.001
105	246	4.922	4.919	-0.003
105	248	4.919	4.918	-0.001
105	374	4.919	4.917	-0.002
105	376	4.921	4.920	-0.001
	Max	4.922	4.922	0.001
	Average	4.919	4.919	0.000
	Min	4.916	4.916	-0.003
	Std Dev	0.002	0.002	0.001

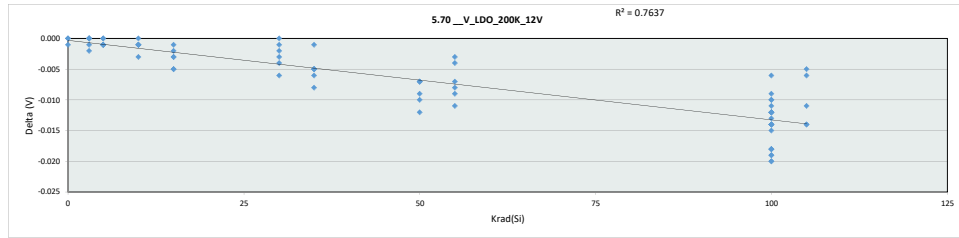


5.69_V_LDO_200K_5V											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	4.919	4.919	4.916	4.919	4.916	4.918	4.916	4.918	4.918	4.918	4.916
Average	4.920	4.920	4.918	4.920	4.918	4.920	4.918	4.920	4.919	4.919	4.918
Max	4.921	4.922	4.921	4.922	4.921	4.922	4.921	4.922	4.920	4.921	4.920
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

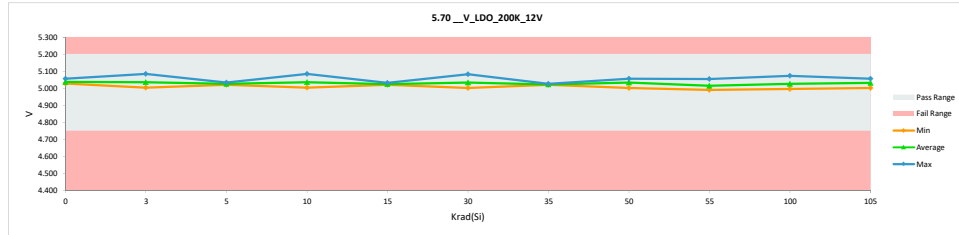


LDR TID Report
TPS7H5001-SP

5.70_V_LDO_200K_12V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	5.030	5.030	0.000
0	118	5.056	5.056	0.000
0	122	5.029	5.028	-0.001
3	61	5.048	5.047	-0.001
3	62	5.011	5.011	0.000
3	63	5.004	5.004	0.000
3	64	5.042	5.042	0.000
3	65	5.084	5.084	0.000
3	66	5.030	5.028	-0.002
5	67	5.025	5.024	-0.001
5	68	5.022	5.021	-0.001
5	69	5.027	5.026	-0.001
5	70	5.034	5.033	-0.001
5	71	5.030	5.030	0.000
5	72	5.026	5.026	0.000
10	73	5.048	5.047	-0.001
10	74	5.011	5.008	-0.003
10	75	5.004	5.003	-0.001
10	76	5.042	5.041	-0.001
10	77	5.084	5.084	0.000
10	78	5.030	5.029	-0.001
15	79	5.025	5.020	-0.005
15	80	5.022	5.021	-0.001
15	81	5.027	5.024	-0.003
15	82	5.034	5.032	-0.002
15	83	5.030	5.027	-0.003
15	84	5.026	5.021	-0.005
30	86	5.048	5.048	0.000
30	89	5.011	5.007	-0.004
30	91	5.004	5.001	-0.003
30	92	5.042	5.036	-0.006
30	94	5.084	5.082	-0.002
30	95	5.030	5.029	-0.001
35	100	5.025	5.020	-0.005
35	101	5.022	5.021	-0.001
35	102	5.027	5.022	-0.005
35	103	5.034	5.026	-0.008
35	104	5.030	5.025	-0.005
35	105	5.026	5.020	-0.006
50	106	5.040	5.031	-0.009
50	107	5.055	5.043	-0.012
50	108	5.057	5.047	-0.010
50	109	5.009	5.002	-0.007
50	110	5.063	5.056	-0.007
50	111	5.030	5.023	-0.007
55	113	5.059	5.055	-0.004
55	114	5.014	5.003	-0.011
55	115	5.006	4.998	-0.008
55	116	5.021	5.014	-0.007
55	126	5.034	5.031	-0.003
55	128	5.000	4.991	-0.009
100	129	5.063	5.051	-0.012
100	131	5.023	5.011	-0.012
100	132	5.026	5.012	-0.014
100	133	5.076	5.062	-0.014
100	134	5.033	5.023	-0.010
100	135	5.019	5.001	-0.018
100	138	5.033	5.019	-0.014
100	139	5.041	5.031	-0.010
100	140	5.042	5.033	-0.009
100	141	5.027	5.007	-0.020
100	142	5.029	5.018	-0.011
100	143	5.031	5.019	-0.012
100	144	5.024	5.018	-0.006
100	220	5.026	5.008	-0.018
100	221	5.017	5.016	-0.001
100	224	5.041	5.028	-0.013
100	225	5.088	5.074	-0.014
100	226	5.059	5.045	-0.014
100	228	5.048	5.036	-0.012
100	232	5.044	5.026	-0.018
100	233	5.052	5.040	-0.012
100	236	5.052	5.033	-0.019
100	238	5.016	4.997	-0.019
100	243	5.049	5.029	-0.020
105	244	5.043	5.037	-0.006
105	246	5.015	5.001	-0.014
105	248	5.052	5.041	-0.011
105	374	5.062	5.057	-0.005
105	376	5.038	5.024	-0.014
	Max	5.088	5.084	0.000
	Average	5.035	5.028	-0.007
	Min	5.000	4.991	-0.009
	Std Dev	0.019	0.020	0.006

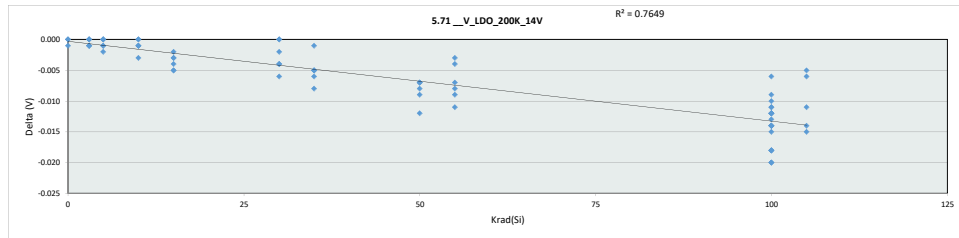


5.70_V_LDO_200K_12V											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.028	5.004	5.021	5.003	5.020	5.001	5.020	5.002	4.991	4.997	5.001
Average	5.038	5.036	5.027	5.035	5.024	5.034	5.022	5.034	5.015	5.026	5.032
Max	5.056	5.084	5.033	5.084	5.032	5.082	5.026	5.056	5.055	5.074	5.057
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

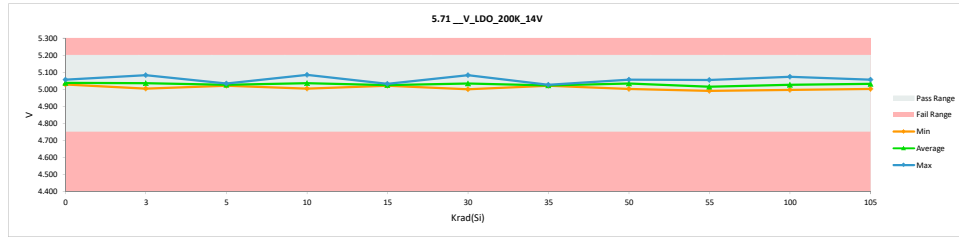


LDR TID Report
TPS7H5001-SP

5.71_V_LDO_200K_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	5.030	5.029	-0.001
0	118	5.056	5.056	0.000
0	122	5.028	5.028	0.000
3	61	5.048	5.047	-0.001
3	62	5.011	5.011	0.000
3	63	5.004	5.004	0.000
3	64	5.042	5.042	0.000
3	65	5.084	5.083	-0.001
3	66	5.029	5.028	-0.001
5	67	5.025	5.024	-0.001
5	68	5.022	5.020	-0.002
5	69	5.027	5.027	0.000
5	70	5.034	5.033	-0.001
5	71	5.030	5.030	0.000
5	72	5.026	5.026	0.000
10	73	5.048	5.047	-0.001
10	74	5.011	5.008	-0.003
10	75	5.004	5.003	-0.001
10	76	5.042	5.041	-0.001
10	77	5.084	5.084	0.000
10	78	5.029	5.029	0.000
15	79	5.025	5.020	-0.005
15	80	5.022	5.020	-0.002
15	81	5.027	5.024	-0.003
15	82	5.034	5.031	-0.003
15	83	5.030	5.026	-0.004
15	84	5.026	5.021	-0.005
30	86	5.048	5.048	0.000
30	89	5.011	5.007	-0.004
30	91	5.004	5.000	-0.004
30	92	5.042	5.036	-0.006
30	94	5.084	5.082	-0.002
30	95	5.029	5.029	0.000
35	100	5.025	5.020	-0.005
35	101	5.022	5.021	-0.001
35	102	5.027	5.022	-0.005
35	103	5.034	5.026	-0.008
35	104	5.030	5.025	-0.005
35	105	5.026	5.020	-0.006
50	106	5.039	5.031	-0.008
50	107	5.055	5.043	-0.012
50	108	5.057	5.048	-0.009
50	109	5.009	5.002	-0.007
50	110	5.063	5.056	-0.007
50	111	5.030	5.023	-0.007
55	113	5.059	5.055	-0.004
55	114	5.014	5.003	-0.011
55	115	5.006	4.998	-0.008
55	116	5.021	5.014	-0.007
55	126	5.034	5.031	-0.003
55	128	5.000	4.991	-0.009
100	129	5.063	5.051	-0.012
100	131	5.023	5.011	-0.012
100	132	5.026	5.012	-0.014
100	133	5.076	5.062	-0.014
100	134	5.033	5.023	-0.010
100	135	5.019	5.001	-0.018
100	138	5.033	5.019	-0.014
100	139	5.041	5.030	-0.011
100	140	5.042	5.033	-0.009
100	141	5.027	5.007	-0.020
100	142	5.029	5.018	-0.011
100	143	5.030	5.018	-0.012
100	144	5.024	5.018	-0.006
100	220	5.026	5.008	-0.018
100	221	5.017	5.002	-0.015
100	224	5.041	5.028	-0.013
100	225	5.088	5.074	-0.014
100	226	5.059	5.045	-0.014
100	228	5.048	5.036	-0.012
100	232	5.044	5.026	-0.018
100	233	5.052	5.040	-0.012
100	236	5.051	5.033	-0.018
100	238	5.016	4.996	-0.020
100	243	5.049	5.029	-0.020
105	244	5.043	5.037	-0.006
105	246	5.016	5.001	-0.015
105	248	5.052	5.041	-0.011
105	374	5.062	5.057	-0.005
105	376	5.038	5.024	-0.014
	Max	5.088	5.084	0.000
	Average	5.035	5.028	-0.007
	Min	5.000	4.991	-0.009
	Std Dev	0.019	0.020	0.006

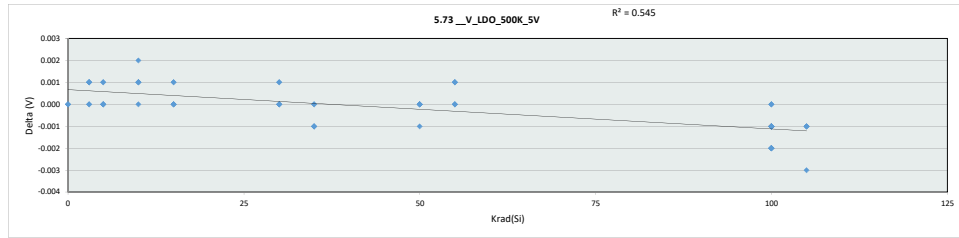


5.71_V_LDO_200K_14V											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.028	5.004	5.020	5.003	5.020	5.000	5.020	5.002	4.991	4.996	5.001
Average	5.038	5.036	5.027	5.035	5.024	5.034	5.022	5.034	5.015	5.026	5.032
Max	5.056	5.083	5.033	5.084	5.031	5.082	5.026	5.056	5.055	5.074	5.057
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

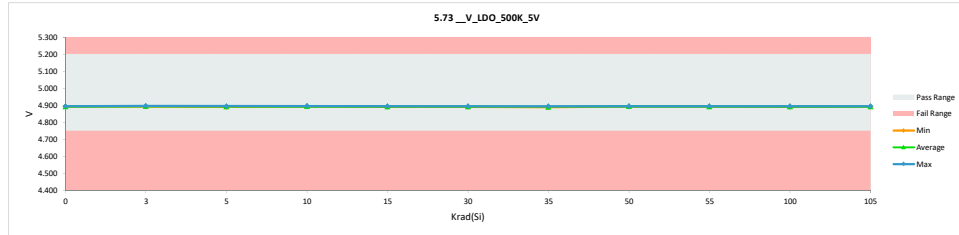


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5.73_V_LDO_500K_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	4.894	4.894	0.000
0	118	4.893	4.893	0.000
0	122	4.893	4.893	0.000
3	61	4.891	4.892	0.001
3	62	4.892	4.893	0.001
3	63	4.893	4.893	0.000
3	64	4.893	4.894	0.001
3	65	4.895	4.896	0.001
3	66	4.895	4.895	0.000
5	67	4.894	4.895	0.001
5	68	4.891	4.891	0.000
5	69	4.895	4.895	0.000
5	70	4.890	4.890	0.000
5	71	4.891	4.892	0.001
5	72	4.892	4.892	0.000
10	73	4.891	4.893	0.002
10	74	4.892	4.893	0.001
10	75	4.893	4.893	0.000
10	76	4.893	4.894	0.001
10	77	4.895	4.896	0.001
10	78	4.895	4.896	0.001
15	79	4.894	4.894	0.000
15	80	4.891	4.892	0.001
15	81	4.895	4.895	0.000
15	82	4.890	4.890	0.000
15	83	4.891	4.892	0.001
15	84	4.892	4.892	0.000
30	86	4.891	4.891	0.000
30	89	4.892	4.893	0.001
30	91	4.893	4.893	0.000
30	92	4.893	4.894	0.001
30	94	4.895	4.895	0.000
30	95	4.895	4.895	0.000
35	100	4.894	4.894	0.000
35	101	4.891	4.890	-0.001
35	102	4.895	4.894	-0.001
35	103	4.890	4.889	-0.001
35	104	4.891	4.891	0.000
35	105	4.892	4.891	-0.001
50	106	4.893	4.893	0.000
50	107	4.894	4.894	-0.001
50	108	4.895	4.895	0.000
50	109	4.894	4.894	0.000
50	110	4.893	4.893	0.000
50	111	4.895	4.895	0.000
55	113	4.893	4.893	0.000
55	114	4.893	4.894	0.001
55	115	4.892	4.893	0.001
55	116	4.893	4.893	0.000
55	126	4.892	4.892	0.000
55	128	4.892	4.893	0.001
100	129	4.893	4.891	-0.002
100	131	4.894	4.894	0.000
100	132	4.893	4.892	-0.001
100	133	4.893	4.892	-0.001
100	134	4.894	4.893	-0.001
100	135	4.894	4.894	0.000
100	138	4.894	4.892	-0.002
100	139	4.895	4.894	-0.001
100	140	4.893	4.892	-0.001
100	141	4.895	4.894	-0.001
100	142	4.892	4.891	-0.001
100	143	4.893	4.891	-0.002
100	144	4.895	4.894	-0.001
100	220	4.893	4.892	-0.001
100	221	4.895	4.894	-0.001
100	224	4.895	4.893	-0.002
100	225	4.895	4.894	-0.001
100	226	4.891	4.890	-0.001
100	228	4.894	4.892	-0.002
100	232	4.894	4.892	-0.002
100	233	4.894	4.893	-0.001
100	236	4.895	4.895	0.000
100	238	4.893	4.891	-0.002
100	243	4.895	4.894	-0.001
105	244	4.893	4.892	-0.001
105	246	4.896	4.893	-0.003
105	248	4.893	4.892	-0.001
105	374	4.893	4.892	-0.001
105	376	4.895	4.894	-0.001
Max		4.896	4.896	0.002
Average		4.893	4.893	0.000
Min		4.890	4.889	-0.003
Std Dev		0.002	0.002	0.001

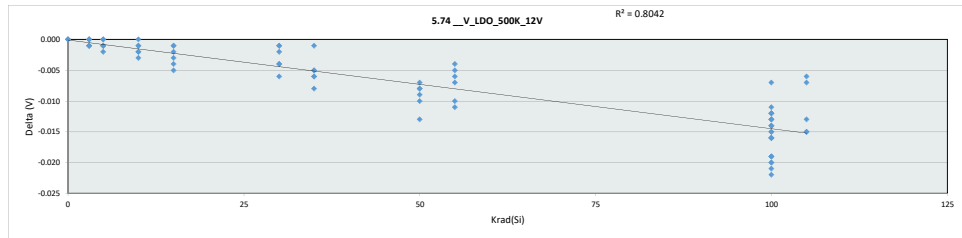


5.73_V_LDO_500K_5V											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	4.893	4.892	4.890	4.893	4.890	4.891	4.889	4.893	4.892	4.890	4.892
Average	4.893	4.894	4.893	4.894	4.893	4.894	4.892	4.894	4.893	4.893	4.893
Max	4.894	4.896	4.895	4.896	4.895	4.895	4.894	4.895	4.894	4.895	4.894
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

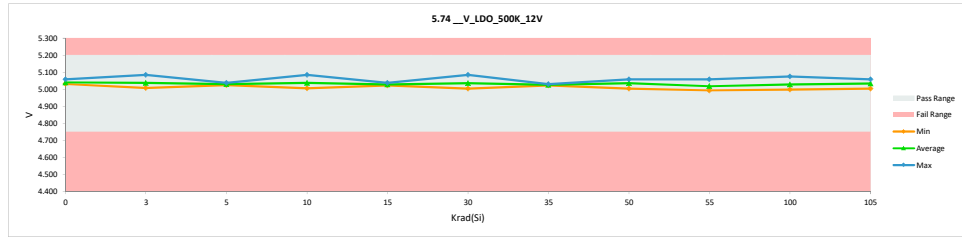


LDR TID Report
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5.74_V_LDO_500K_12V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	5.032	5.032	0.000
0	118	5.058	5.058	0.000
0	122	5.031	5.031	0.000
3	61	5.051	5.050	-0.001
3	62	5.013	5.013	0.000
3	63	5.007	5.007	0.000
3	64	5.044	5.044	0.000
3	65	5.086	5.085	-0.001
3	66	5.032	5.031	-0.001
5	67	5.028	5.026	-0.002
5	68	5.025	5.024	-0.001
5	69	5.029	5.029	0.000
5	70	5.038	5.038	0.000
5	71	5.033	5.032	-0.001
5	72	5.030	5.029	-0.001
10	73	5.051	5.049	-0.002
10	74	5.013	5.010	-0.003
10	75	5.007	5.005	-0.002
10	76	5.044	5.044	0.000
10	77	5.086	5.085	-0.001
10	78	5.032	5.031	-0.001
15	79	5.028	5.023	-0.005
15	80	5.025	5.024	-0.001
15	81	5.029	5.027	-0.002
15	82	5.038	5.037	-0.001
15	83	5.033	5.030	-0.003
15	84	5.030	5.026	-0.004
30	86	5.051	5.050	-0.001
30	89	5.013	5.009	-0.004
30	91	5.007	5.003	-0.004
30	92	5.044	5.038	-0.006
30	94	5.086	5.084	-0.002
30	95	5.032	5.031	-0.001
35	100	5.028	5.022	-0.006
35	101	5.025	5.024	-0.001
35	102	5.029	5.024	-0.005
35	103	5.038	5.030	-0.008
35	104	5.033	5.028	-0.005
35	105	5.030	5.024	-0.006
50	106	5.043	5.035	-0.008
50	107	5.058	5.045	-0.013
50	108	5.059	5.049	-0.010
50	109	5.011	5.003	-0.008
50	110	5.065	5.058	-0.007
50	111	5.032	5.023	-0.009
55	113	5.064	5.059	-0.005
55	114	5.016	5.005	-0.011
55	115	5.008	5.001	-0.007
55	116	5.023	5.017	-0.006
55	126	5.037	5.033	-0.004
55	128	5.003	4.993	-0.010
100	129	5.068	5.054	-0.014
100	131	5.025	5.012	-0.013
100	132	5.029	5.013	-0.016
100	133	5.079	5.063	-0.016
100	134	5.036	5.024	-0.012
100	135	5.021	5.001	-0.020
100	138	5.034	5.020	-0.014
100	139	5.044	5.032	-0.012
100	140	5.045	5.034	-0.011
100	141	5.030	5.009	-0.021
100	142	5.032	5.020	-0.012
100	143	5.033	5.020	-0.013
100	144	5.026	5.019	-0.007
100	220	5.029	5.010	-0.019
100	221	5.019	5.003	-0.016
100	224	5.044	5.029	-0.015
100	225	5.090	5.075	-0.015
100	226	5.063	5.047	-0.016
100	228	5.051	5.038	-0.013
100	232	5.047	5.028	-0.019
100	233	5.055	5.041	-0.014
100	236	5.053	5.034	-0.019
100	238	5.018	4.998	-0.020
100	243	5.052	5.030	-0.022
105	244	5.045	5.038	-0.007
105	246	5.018	5.003	-0.015
105	248	5.055	5.042	-0.013
105	374	5.065	5.059	-0.006
105	376	5.040	5.025	-0.015
	Max	5.090	5.085	0.000
	Average	5.038	5.030	-0.008
	Min	5.003	4.993	-0.022
	Std Dev	0.020	0.020	0.006



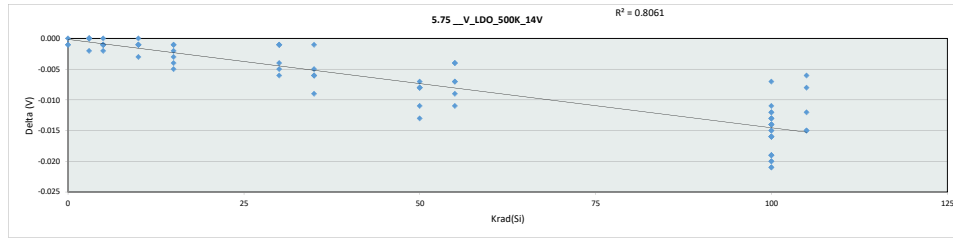
5.74_V_LDO_500K_12V											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.031	5.007	5.024	5.005	5.023	5.003	5.022	5.003	4.993	4.998	5.003
Average	5.040	5.038	5.030	5.037	5.028	5.036	5.025	5.036	5.018	5.027	5.033
Max	5.058	5.085	5.038	5.085	5.037	5.084	5.030	5.058	5.059	5.075	5.059
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



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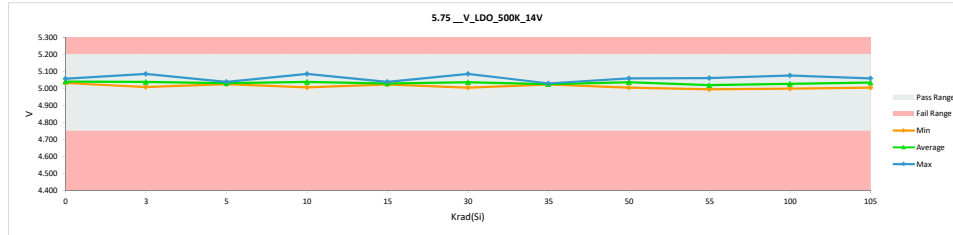
5.75_V_LDO_500K_14V		
Test Site		
Tester		
Test Number		
Unit	V	V
Max Limit	5.2	5.2
Min Limit	4.75	4.75

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	5.032	5.031	-0.001
0	118	5.058	5.057	-0.001
0	122	5.031	5.031	0.000
3	61	5.050	5.050	0.000
3	62	5.013	5.013	0.000
3	63	5.007	5.007	0.000
3	64	5.044	5.044	0.000
3	65	5.085	5.085	0.000
3	66	5.032	5.030	-0.002
5	67	5.028	5.026	-0.002
5	68	5.025	5.024	-0.001
5	69	5.029	5.029	0.000
5	70	5.038	5.037	-0.001
5	71	5.033	5.032	-0.001
5	72	5.030	5.029	-0.001
10	73	5.050	5.049	-0.001
10	74	5.013	5.010	-0.003
10	75	5.007	5.006	-0.001
10	76	5.044	5.043	-0.001
10	77	5.085	5.085	0.000
10	78	5.032	5.031	-0.001
15	79	5.028	5.023	-0.005
15	80	5.025	5.024	-0.001
15	81	5.029	5.027	-0.002
15	82	5.038	5.037	-0.001
15	83	5.033	5.030	-0.003
15	84	5.030	5.026	-0.004
30	86	5.050	5.049	-0.001
30	89	5.013	5.008	-0.005
30	91	5.007	5.003	-0.004
30	92	5.044	5.038	-0.006
30	94	5.085	5.084	-0.001
30	95	5.032	5.031	-0.001
35	100	5.028	5.022	-0.006
35	101	5.025	5.024	-0.001
35	102	5.029	5.023	-0.006
35	103	5.038	5.029	-0.009
35	104	5.033	5.028	-0.005
35	105	5.030	5.024	-0.006
50	106	5.043	5.035	-0.008
50	107	5.088	5.085	-0.003
50	108	5.060	5.049	-0.011
50	109	5.011	5.003	-0.008
50	110	5.065	5.058	-0.007
50	111	5.032	5.024	-0.008
55	113	5.064	5.060	-0.004
55	114	5.016	5.005	-0.011
55	115	5.008	5.001	-0.007
55	116	5.023	5.016	-0.007
55	126	5.037	5.033	-0.004
55	128	5.003	4.994	-0.009
100	129	5.068	5.053	-0.015
100	131	5.025	5.012	-0.013
100	132	5.029	5.013	-0.016
100	133	5.079	5.063	-0.016
100	134	5.036	5.024	-0.012
100	135	5.021	5.001	-0.020
100	138	5.034	5.020	-0.014
100	139	5.044	5.032	-0.012
100	140	5.045	5.034	-0.011
100	141	5.030	5.009	-0.021
100	142	5.032	5.019	-0.013
100	143	5.033	5.020	-0.013
100	144	5.026	5.019	-0.007
100	220	5.029	5.010	-0.019
100	221	5.019	5.003	-0.016
100	224	5.043	5.029	-0.014
100	225	5.090	5.075	-0.015
100	226	5.063	5.047	-0.016
100	228	5.051	5.037	-0.014
100	232	5.047	5.028	-0.019
100	233	5.055	5.041	-0.014
100	236	5.053	5.034	-0.019
100	238	5.018	4.998	-0.020
100	243	5.051	5.030	-0.021
105	244	5.045	5.037	-0.008
105	246	5.018	5.003	-0.015
105	248	5.054	5.042	-0.012
105	374	5.065	5.059	-0.006
105	376	5.040	5.025	-0.015
	Max	5.090	5.085	0.000
	Average	5.038	5.030	-0.008
	Min	5.003	4.994	-0.021
	Std Dev	0.019	0.020	0.006



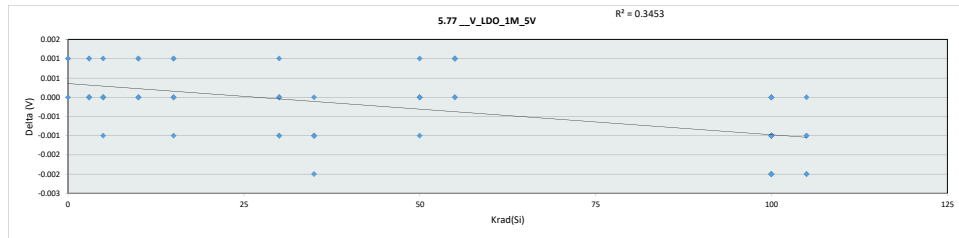
5.75_V_LDO_500K_14V		
Test Site		
Tester		
Test Number		
Max Limit	5.2	V
Min Limit	4.75	V

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.031	5.007	5.024	5.006	5.023	5.003	5.022	5.003	4.994	4.998	5.003
Average	5.040	5.038	5.030	5.037	5.028	5.036	5.025	5.036	5.018	5.027	5.033
Max	5.057	5.085	5.037	5.085	5.037	5.084	5.029	5.058	5.060	5.075	5.059
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

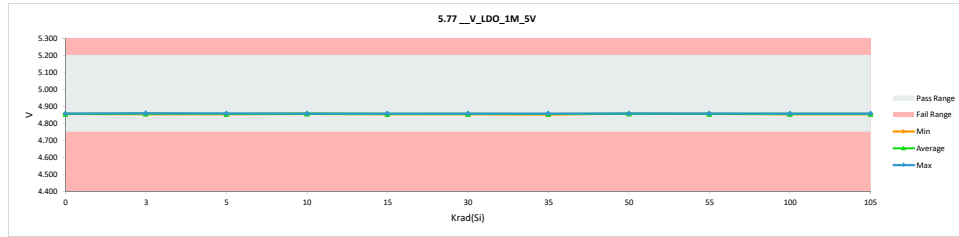


LDR TID Report
TPS7H5001-SP

5.77_V_LDO_1M_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	4.856	4.857	0.001
0	118	4.855	4.855	0.000
0	122	4.855	4.856	0.001
3	61	4.854	4.854	0.000
3	62	4.855	4.855	0.000
3	63	4.856	4.856	0.000
3	64	4.856	4.857	0.001
3	65	4.858	4.859	0.001
3	66	4.858	4.858	0.000
5	67	4.858	4.857	-0.001
5	68	4.854	4.854	0.000
5	69	4.858	4.858	0.000
5	70	4.853	4.853	0.000
5	71	4.854	4.855	0.001
5	72	4.855	4.855	0.000
10	73	4.854	4.855	0.001
10	74	4.855	4.855	0.000
10	75	4.856	4.856	0.000
10	76	4.856	4.857	0.001
10	77	4.858	4.858	0.000
10	78	4.858	4.858	0.000
15	79	4.858	4.857	-0.001
15	80	4.854	4.855	0.001
15	81	4.858	4.858	0.000
15	82	4.853	4.853	0.000
15	83	4.854	4.855	0.001
15	84	4.855	4.855	0.000
30	86	4.854	4.853	-0.001
30	89	4.855	4.855	0.000
30	91	4.856	4.855	-0.001
30	92	4.856	4.857	0.001
30	94	4.858	4.858	0.000
30	95	4.858	4.858	0.000
35	100	4.858	4.856	-0.002
35	101	4.854	4.853	-0.001
35	102	4.858	4.857	-0.001
35	103	4.853	4.852	-0.001
35	104	4.854	4.854	0.000
35	105	4.855	4.854	-0.001
50	106	4.856	4.856	0.000
50	107	4.857	4.857	0.000
50	108	4.858	4.857	-0.001
50	109	4.857	4.857	0.000
50	110	4.855	4.856	0.001
50	111	4.858	4.858	0.000
55	113	4.856	4.856	0.000
55	114	4.856	4.857	0.001
55	115	4.855	4.856	0.001
55	116	4.856	4.856	0.000
55	126	4.854	4.855	0.001
55	128	4.855	4.856	0.001
100	129	4.856	4.855	-0.001
100	131	4.857	4.857	0.000
100	132	4.855	4.855	0.000
100	133	4.855	4.854	-0.001
100	134	4.857	4.856	-0.001
100	135	4.857	4.857	0.000
100	138	4.857	4.855	-0.002
100	139	4.858	4.857	-0.001
100	140	4.856	4.855	-0.001
100	141	4.858	4.857	-0.001
100	142	4.855	4.854	-0.001
100	143	4.856	4.854	-0.002
100	144	4.858	4.856	-0.002
100	220	4.855	4.855	0.000
100	221	4.857	4.857	0.000
100	224	4.857	4.856	-0.001
100	225	4.858	4.857	-0.001
100	226	4.854	4.853	-0.001
100	228	4.857	4.855	-0.002
100	232	4.857	4.855	-0.002
100	233	4.856	4.855	-0.001
100	236	4.859	4.858	-0.001
100	238	4.855	4.853	-0.002
100	243	4.858	4.857	-0.001
105	244	4.855	4.855	0.000
105	246	4.858	4.856	-0.002
105	248	4.856	4.854	-0.002
105	374	4.856	4.855	-0.001
105	376	4.858	4.857	-0.001
Max		4.859	4.859	0.001
Average		4.856	4.856	0.000
Min		4.853	4.852	-0.002
Std Dev		0.002	0.002	0.001

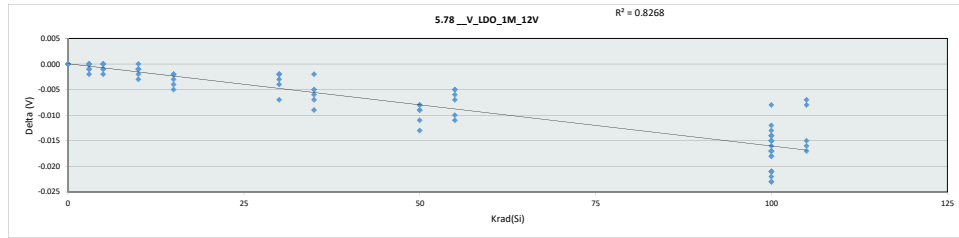


5.77_V_LDO_1M_5V											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	4.855	4.854	4.853	4.855	4.853	4.853	4.852	4.856	4.855	4.853	4.854
Average	4.856	4.857	4.855	4.857	4.856	4.856	4.854	4.857	4.856	4.856	4.855
Max	4.857	4.859	4.858	4.858	4.858	4.858	4.857	4.858	4.857	4.858	4.857
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

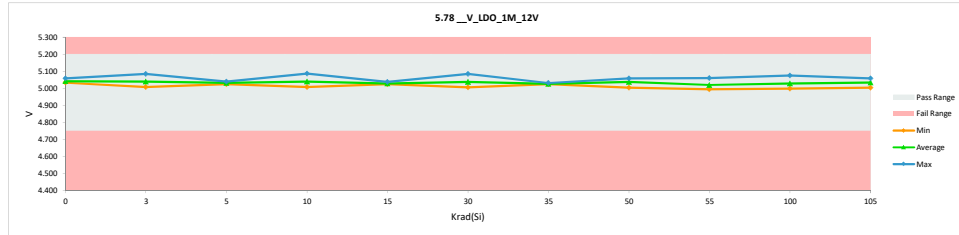


LDR TID Report
TPS7H5001-SP

5.78_V_LDO_1M_12V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	5.2	5.2			
Min Limit	4.75	4.75			
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta	
0	117	5.033	5.033	0.000	
0	118	5.059	5.059	0.000	
0	122	5.033	5.033	0.000	
3	61	5.052	5.051	-0.001	
3	62	5.014	5.014	0.000	
3	63	5.008	5.008	0.000	
3	64	5.046	5.046	0.000	
3	65	5.086	5.085	-0.001	
3	66	5.034	5.032	-0.002	
5	67	5.030	5.029	-0.001	
5	68	5.027	5.025	-0.002	
5	69	5.030	5.030	0.000	
5	70	5.040	5.040	0.000	
5	71	5.034	5.034	0.000	
5	72	5.031	5.030	-0.001	
10	73	5.052	5.050	-0.002	
10	74	5.014	5.011	-0.003	
10	75	5.008	5.007	-0.001	
10	76	5.046	5.045	-0.001	
10	77	5.086	5.086	0.000	
10	78	5.034	5.033	-0.001	
15	79	5.030	5.025	-0.005	
15	80	5.027	5.025	-0.002	
15	81	5.030	5.028	-0.002	
15	82	5.040	5.038	-0.002	
15	83	5.034	5.031	-0.003	
15	84	5.031	5.027	-0.004	
30	86	5.052	5.050	-0.002	
30	89	5.014	5.010	-0.004	
30	91	5.008	5.005	-0.003	
30	92	5.046	5.039	-0.007	
30	94	5.086	5.084	-0.002	
30	95	5.034	5.032	-0.002	
35	100	5.030	5.024	-0.006	
35	101	5.027	5.025	-0.002	
35	102	5.030	5.025	-0.005	
35	103	5.040	5.031	-0.009	
35	104	5.034	5.029	-0.005	
35	105	5.031	5.024	-0.007	
50	106	5.045	5.037	-0.008	
50	107	5.061	5.047	-0.013	
50	108	5.061	5.050	-0.011	
50	109	5.013	5.004	-0.009	
50	110	5.067	5.059	-0.008	
50	111	5.032	5.023	-0.009	
55	113	5.066	5.061	-0.005	
55	114	5.018	5.007	-0.011	
55	115	5.009	5.002	-0.007	
55	116	5.024	5.018	-0.006	
55	126	5.040	5.035	-0.005	
55	128	5.004	4.994	-0.010	
100	129	5.070	5.054	-0.016	
100	131	5.027	5.012	-0.015	
100	132	5.030	5.013	-0.017	
100	133	5.082	5.064	-0.018	
100	134	5.038	5.024	-0.014	
100	135	5.022	5.001	-0.021	
100	138	5.035	5.018	-0.017	
100	139	5.046	5.032	-0.014	
100	140	5.047	5.035	-0.012	
100	141	5.032	5.009	-0.023	
100	142	5.032	5.019	-0.013	
100	143	5.034	5.020	-0.014	
100	144	5.027	5.019	-0.008	
100	220	5.031	5.010	-0.021	
100	221	5.021	5.003	-0.018	
100	224	5.045	5.030	-0.015	
100	225	5.092	5.075	-0.017	
100	226	5.064	5.047	-0.017	
100	228	5.052	5.037	-0.015	
100	232	5.050	5.029	-0.021	
100	233	5.057	5.042	-0.015	
100	236	5.054	5.033	-0.021	
100	238	5.020	4.998	-0.022	
100	243	5.053	5.030	-0.023	
105	244	5.046	5.038	-0.008	
105	246	5.020	5.003	-0.017	
105	248	5.057	5.042	-0.015	
105	374	5.066	5.059	-0.007	
105	376	5.042	5.026	-0.016	
	Max	5.092	5.086	0.000	
	Average	5.039	5.031	-0.008	
	Min	5.004	4.994	-0.023	
	Std Dev	0.020	0.020	0.007	

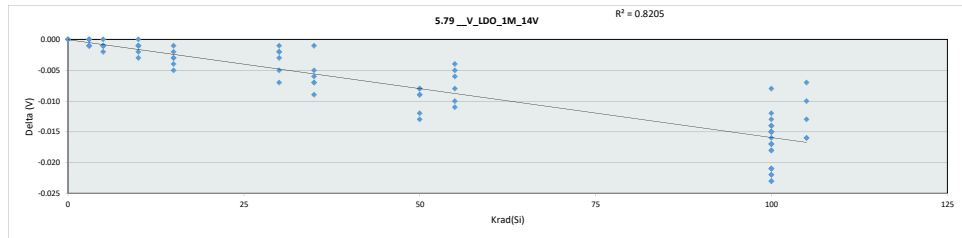


5.78_V_LDO_1M_12V											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.033	5.008	5.025	5.007	5.025	5.005	5.024	5.004	4.994	4.998	5.003
Average	5.042	5.039	5.031	5.039	5.029	5.037	5.026	5.037	5.020	5.027	5.034
Max	5.059	5.085	5.040	5.086	5.038	5.084	5.031	5.059	5.061	5.075	5.059
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

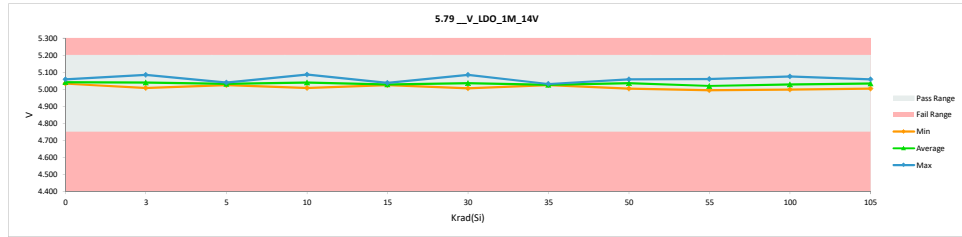


LDR TID Report
TPS7H5001-SP

5.79_V_LDO_1M_14V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	5.2	5.2			
Min Limit	4.75	4.75			
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta	
0	117	5.033	5.033	0.000	
0	118	5.059	5.059	0.000	
0	122	5.033	5.033	0.000	
3	61	5.052	5.051	-0.001	
3	62	5.014	5.014	0.000	
3	63	5.008	5.008	0.000	
3	64	5.046	5.046	0.000	
3	65	5.086	5.085	-0.001	
3	66	5.033	5.032	-0.001	
5	67	5.030	5.028	-0.002	
5	68	5.026	5.025	-0.001	
5	69	5.031	5.030	-0.001	
5	70	5.040	5.040	0.000	
5	71	5.034	5.033	-0.001	
5	72	5.031	5.030	-0.001	
10	73	5.052	5.050	-0.002	
10	74	5.014	5.011	-0.003	
10	75	5.008	5.007	-0.001	
10	76	5.046	5.045	-0.001	
10	77	5.086	5.086	0.000	
10	78	5.033	5.032	-0.001	
15	79	5.030	5.025	-0.005	
15	80	5.026	5.025	-0.001	
15	81	5.031	5.028	-0.003	
15	82	5.040	5.038	-0.002	
15	83	5.034	5.031	-0.003	
15	84	5.031	5.027	-0.004	
30	86	5.052	5.050	-0.002	
30	89	5.014	5.009	-0.005	
30	91	5.008	5.005	-0.003	
30	92	5.046	5.039	-0.007	
30	94	5.086	5.084	-0.002	
30	95	5.033	5.032	-0.001	
35	100	5.030	5.024	-0.006	
35	101	5.026	5.025	-0.001	
35	102	5.031	5.024	-0.007	
35	103	5.040	5.031	-0.009	
35	104	5.034	5.029	-0.005	
35	105	5.031	5.024	-0.007	
50	106	5.045	5.037	-0.008	
50	107	5.060	5.047	-0.013	
50	108	5.061	5.049	-0.012	
50	109	5.013	5.004	-0.009	
50	110	5.067	5.059	-0.008	
50	111	5.032	5.023	-0.009	
55	113	5.066	5.061	-0.005	
55	114	5.018	5.007	-0.011	
55	115	5.009	5.001	-0.008	
55	116	5.024	5.018	-0.006	
55	126	5.039	5.035	-0.004	
55	128	5.004	4.994	-0.010	
100	129	5.070	5.054	-0.016	
100	131	5.027	5.012	-0.015	
100	132	5.030	5.013	-0.017	
100	133	5.081	5.064	-0.017	
100	134	5.038	5.024	-0.014	
100	135	5.022	5.000	-0.022	
100	138	5.034	5.019	-0.015	
100	139	5.046	5.033	-0.013	
100	140	5.047	5.035	-0.012	
100	141	5.032	5.009	-0.023	
100	142	5.032	5.018	-0.014	
100	143	5.034	5.020	-0.014	
100	144	5.027	5.019	-0.008	
100	220	5.031	5.010	-0.021	
100	221	5.021	5.003	-0.018	
100	224	5.045	5.030	-0.015	
100	225	5.092	5.075	-0.017	
100	226	5.064	5.046	-0.018	
100	228	5.052	5.037	-0.015	
100	232	5.050	5.029	-0.021	
100	233	5.057	5.042	-0.015	
100	236	5.054	5.033	-0.021	
100	238	5.020	4.998	-0.022	
100	243	5.053	5.030	-0.023	
105	244	5.047	5.037	-0.010	
105	246	5.020	5.004	-0.016	
105	248	5.056	5.043	-0.013	
105	374	5.066	5.059	-0.007	
105	376	5.042	5.026	-0.016	
Max		5.092	5.086	0.000	
Average		5.039	5.031	-0.008	
Min		5.004	4.994	-0.023	
Std Dev		0.020	0.020	0.007	



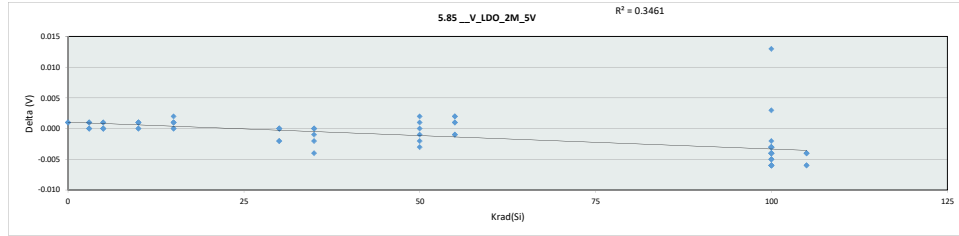
5.79_V_LDO_1M_14V											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.033	5.008	5.025	5.007	5.025	5.005	5.024	5.004	4.994	4.998	5.004
Average	5.042	5.039	5.031	5.039	5.029	5.037	5.026	5.037	5.019	5.027	5.034
Max	5.059	5.085	5.040	5.086	5.038	5.084	5.031	5.059	5.061	5.075	5.059
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



LDR TID Report
TPS7H5001-SP

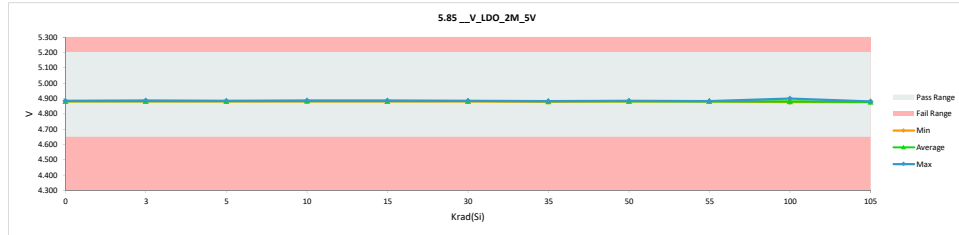
5.85_V_LDO_2M_5V		
Test Site		
Tester		
Test Number		
Unit	V	V
Max Limit	5.2	5.2
Min Limit	4.65	4.65

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	4.884	4.885	0.001
0	118	4.882	4.883	0.001
0	122	4.880	4.881	0.001
3	61	4.880	4.880	0.000
3	62	4.882	4.883	0.001
3	63	4.884	4.884	0.000
3	64	4.884	4.885	0.001
3	65	4.886	4.887	0.001
3	66	4.885	4.885	0.000
5	67	4.885	4.885	0.000
5	68	4.880	4.880	0.000
5	69	4.886	4.886	0.000
5	70	4.880	4.880	0.000
5	71	4.880	4.881	0.001
5	72	4.881	4.882	0.001
10	73	4.880	4.881	0.001
10	74	4.882	4.883	0.001
10	75	4.884	4.884	0.000
10	76	4.884	4.885	0.001
10	77	4.886	4.887	0.001
10	78	4.885	4.885	0.000
15	79	4.885	4.885	0.000
15	80	4.880	4.881	0.001
15	81	4.886	4.887	0.001
15	82	4.880	4.880	0.000
15	83	4.880	4.881	0.001
15	84	4.881	4.883	0.002
30	86	4.880	4.880	0.000
30	89	4.882	4.882	0.000
30	91	4.884	4.882	-0.002
30	92	4.884	4.884	0.000
30	94	4.886	4.884	-0.002
30	95	4.885	4.885	0.000
35	100	4.885	4.881	-0.004
35	101	4.880	4.880	0.000
35	102	4.886	4.884	-0.002
35	103	4.880	4.879	-0.001
35	104	4.880	4.880	0.000
35	105	4.881	4.881	0.000
50	106	4.883	4.881	-0.002
50	107	4.886	4.886	0.000
50	108	4.886	4.883	-0.003
50	109	4.885	4.885	0.000
50	110	4.882	4.884	0.002
50	111	4.886	4.885	-0.001
55	113	4.882	4.881	-0.001
55	114	4.883	4.882	-0.001
55	115	4.882	4.884	0.002
55	116	4.882	4.883	0.001
55	126	4.882	4.883	0.001
55	128	4.882	4.884	0.002
100	129	4.883	4.881	-0.002
100	131	4.883	4.880	-0.003
100	132	4.881	4.878	-0.003
100	133	4.882	4.877	-0.005
100	134	4.883	4.879	-0.004
100	135	4.886	4.899	0.013
100	138	4.884	4.880	-0.004
100	139	4.886	4.881	-0.005
100	140	4.882	4.878	-0.004
100	141	4.885	4.882	-0.003
100	142	4.881	4.884	0.003
100	143	4.883	4.877	-0.006
100	144	4.885	4.879	-0.006
100	220	4.882	4.879	-0.003
100	221	4.885	4.881	-0.004
100	224	4.886	4.880	-0.006
100	225	4.886	4.881	-0.005
100	226	4.881	4.877	-0.004
100	228	4.883	4.877	-0.006
100	232	4.884	4.878	-0.006
100	233	4.883	4.879	-0.004
100	236	4.888	4.885	-0.003
100	238	4.882	4.876	-0.006
100	243	4.885	4.882	-0.003
105	244	4.881	4.877	-0.004
105	246	4.886	4.880	-0.006
105	248	4.882	4.878	-0.004
105	374	4.884	4.878	-0.006
105	376	4.885	4.881	-0.004
	Max	4.888	4.899	0.013
	Average	4.883	4.882	-0.001
	Min	4.880	4.876	-0.006
	Std Dev	0.002	0.003	0.003



5.85_V_LDO_2M_5V		
Test Site		
Tester		
Test Number		
Max Limit	5.2	V
Min Limit	4.65	V

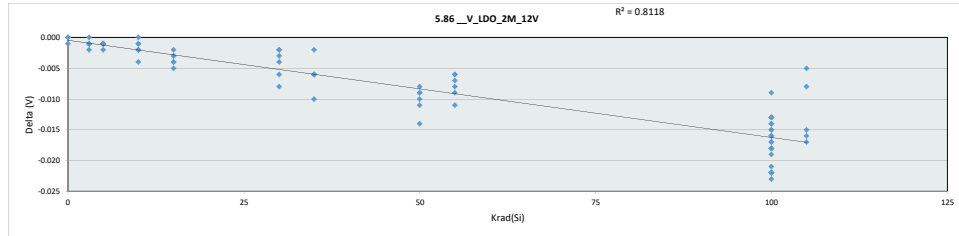
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650
Min	4.881	4.880	4.880	4.881	4.880	4.880	4.879	4.881	4.881	4.876	4.877
Average	4.883	4.884	4.882	4.884	4.883	4.883	4.881	4.884	4.883	4.880	4.879
Max	4.885	4.887	4.886	4.887	4.887	4.885	4.884	4.886	4.884	4.899	4.881
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



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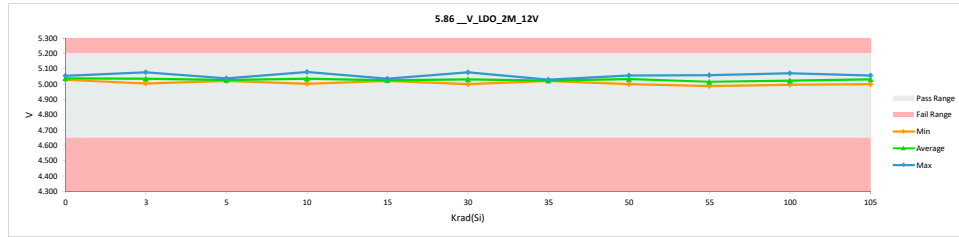
5.86_V_LDO_2M_12V		
Test Site		
Tester		
Test Number		
Unit	V	V
Max Limit	5.2	5.2
Min Limit	4.65	4.65

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	5.029	5.028	-0.001
0	118	5.055	5.055	0.000
0	122	5.030	5.030	0.000
3	61	5.048	5.047	-0.001
3	62	5.013	5.012	-0.001
3	63	5.004	5.004	0.000
3	64	5.043	5.042	-0.001
3	65	5.079	5.078	-0.001
3	66	5.032	5.030	-0.002
5	67	5.028	5.027	-0.001
5	68	5.023	5.021	-0.002
5	69	5.026	5.025	-0.001
5	70	5.039	5.038	-0.001
5	71	5.031	5.030	-0.001
5	72	5.027	5.026	-0.001
10	73	5.048	5.046	-0.002
10	74	5.013	5.009	-0.004
10	75	5.004	5.003	-0.001
10	76	5.043	5.042	-0.001
10	77	5.079	5.079	0.000
10	78	5.032	5.030	-0.002
15	79	5.028	5.023	-0.005
15	80	5.023	5.021	-0.002
15	81	5.026	5.023	-0.003
15	82	5.039	5.036	-0.003
15	83	5.031	5.027	-0.004
15	84	5.027	5.023	-0.004
30	86	5.048	5.046	-0.002
30	89	5.013	5.007	-0.006
30	91	5.004	5.000	-0.004
30	92	5.043	5.035	-0.008
30	94	5.079	5.077	-0.002
30	95	5.032	5.029	-0.003
35	100	5.028	5.022	-0.006
35	101	5.023	5.021	-0.002
35	102	5.026	5.020	-0.006
35	103	5.039	5.029	-0.010
35	104	5.031	5.025	-0.006
35	105	5.027	5.021	-0.006
50	106	5.044	5.035	-0.009
50	107	5.059	5.045	-0.014
50	108	5.054	5.043	-0.011
50	109	5.010	5.001	-0.009
50	110	5.065	5.057	-0.008
50	111	5.028	5.018	-0.010
55	113	5.064	5.058	-0.006
55	114	5.014	5.003	-0.011
55	115	5.006	4.998	-0.008
55	116	5.021	5.014	-0.007
55	126	5.037	5.031	-0.006
55	128	4.997	4.988	-0.009
100	129	5.069	5.051	-0.018
100	131	5.023	5.008	-0.015
100	132	5.025	5.008	-0.017
100	133	5.080	5.063	-0.017
100	134	5.036	5.021	-0.015
100	135	5.017	4.995	-0.022
100	138	5.030	5.014	-0.016
100	139	5.042	5.028	-0.014
100	140	5.045	5.032	-0.013
100	141	5.029	5.006	-0.023
100	142	5.026	5.013	-0.013
100	143	5.030	5.017	-0.013
100	144	5.024	5.015	-0.009
100	220	5.030	5.008	-0.022
100	221	5.018	4.999	-0.019
100	224	5.043	5.027	-0.016
100	225	5.088	5.070	-0.018
100	226	5.062	5.044	-0.018
100	228	5.046	5.032	-0.014
100	232	5.050	5.028	-0.022
100	233	5.055	5.039	-0.016
100	236	5.047	5.026	-0.021
100	238	5.017	4.995	-0.022
100	243	5.048	5.025	-0.022
105	244	5.042	5.034	-0.008
105	246	5.016	5.000	-0.016
105	248	5.055	5.040	-0.015
105	374	5.061	5.056	-0.005
105	376	5.041	5.024	-0.017
	Max	5.088	5.079	0.000
	Average	5.036	5.027	-0.009
	Min	4.997	4.988	-0.009
	Std Dev	0.019	0.020	0.007



5.86_V_LDO_2M_12V		
Test Site		
Tester		
Test Number		
Max Limit	5.2	V
Min Limit	4.65	V

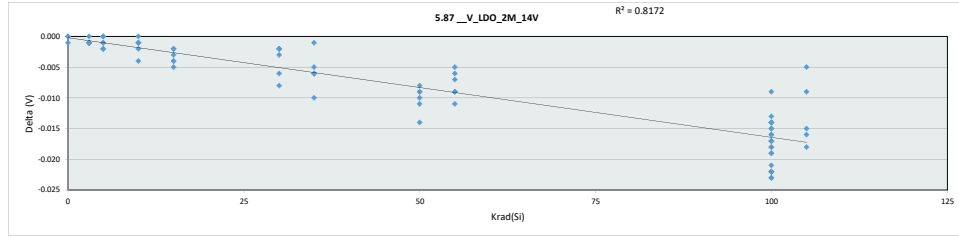
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650
Min	5.028	5.004	5.021	5.003	5.021	5.000	5.020	5.001	4.988	4.995	5.000
Average	5.038	5.036	5.028	5.035	5.026	5.032	5.023	5.033	5.015	5.024	5.031
Max	5.055	5.078	5.038	5.079	5.036	5.077	5.029	5.057	5.058	5.070	5.056
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



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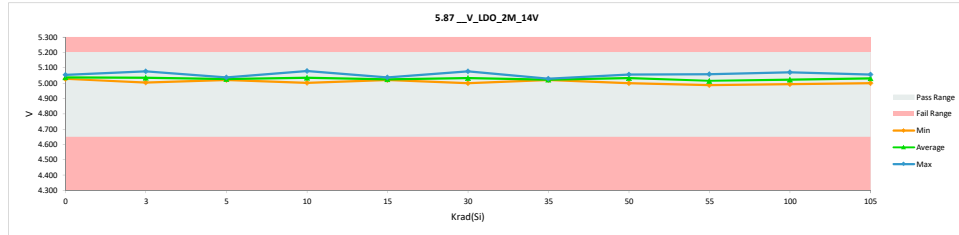
5.87_V_LDO_2M_14V		
Test Site		
Tester		
Test Number		
Unit	V	V
Max Limit	5.2	5.2
Min Limit	4.65	4.65

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	5.029	5.028	-0.001
0	118	5.055	5.055	0.000
0	122	5.030	5.030	0.000
3	61	5.048	5.047	-0.001
3	62	5.013	5.012	-0.001
3	63	5.004	5.004	0.000
3	64	5.043	5.042	-0.001
3	65	5.079	5.078	-0.001
3	66	5.031	5.030	-0.001
5	67	5.028	5.026	-0.002
5	68	5.023	5.021	-0.002
5	69	5.026	5.026	0.000
5	70	5.039	5.038	-0.001
5	71	5.030	5.030	0.000
5	72	5.027	5.026	-0.001
10	73	5.048	5.046	-0.002
10	74	5.013	5.009	-0.004
10	75	5.004	5.003	-0.001
10	76	5.043	5.042	-0.001
10	77	5.079	5.079	0.000
10	78	5.031	5.030	-0.001
15	79	5.028	5.023	-0.005
15	80	5.023	5.021	-0.002
15	81	5.026	5.022	-0.004
15	82	5.039	5.037	-0.002
15	83	5.030	5.027	-0.003
15	84	5.027	5.023	-0.004
30	86	5.048	5.046	-0.002
30	89	5.013	5.007	-0.006
30	91	5.004	5.001	-0.003
30	92	5.043	5.035	-0.008
30	94	5.079	5.077	-0.002
30	95	5.031	5.029	-0.002
35	100	5.028	5.022	-0.006
35	101	5.023	5.022	-0.001
35	102	5.026	5.020	-0.006
35	103	5.039	5.029	-0.010
35	104	5.030	5.025	-0.005
35	105	5.027	5.021	-0.006
50	106	5.044	5.035	-0.009
50	107	5.059	5.045	-0.014
50	108	5.054	5.043	-0.011
50	109	5.010	5.001	-0.009
50	110	5.065	5.057	-0.008
50	111	5.028	5.018	-0.010
55	113	5.063	5.058	-0.005
55	114	5.014	5.003	-0.011
55	115	5.007	4.998	-0.009
55	116	5.021	5.014	-0.007
55	126	5.037	5.031	-0.006
55	128	4.997	4.988	-0.009
100	129	5.069	5.052	-0.017
100	131	5.023	5.008	-0.015
100	132	5.025	5.007	-0.018
100	133	5.080	5.063	-0.017
100	134	5.036	5.020	-0.016
100	135	5.016	4.994	-0.022
100	138	5.030	5.013	-0.017
100	139	5.042	5.028	-0.014
100	140	5.045	5.032	-0.013
100	141	5.029	5.006	-0.023
100	142	5.027	5.013	-0.014
100	143	5.030	5.016	-0.014
100	144	5.024	5.015	-0.009
100	220	5.030	5.008	-0.022
100	221	5.018	4.999	-0.019
100	224	5.042	5.027	-0.015
100	225	5.088	5.070	-0.018
100	226	5.062	5.043	-0.019
100	228	5.046	5.032	-0.014
100	232	5.050	5.028	-0.022
100	233	5.055	5.039	-0.016
100	236	5.047	5.026	-0.021
100	238	5.017	4.995	-0.022
100	243	5.048	5.025	-0.023
105	244	5.042	5.033	-0.009
105	246	5.016	5.000	-0.016
105	248	5.055	5.040	-0.015
105	374	5.061	5.056	-0.005
105	376	5.041	5.023	-0.018
	Max	5.088	5.079	0.000
	Average	5.036	5.027	-0.009
	Min	4.997	4.988	-0.009
	Std Dev	0.019	0.020	0.007



5.87_V_LDO_2M_14V		
Test Site		
Tester		
Test Number		
Max Limit	5.2	V
Min Limit	4.65	V

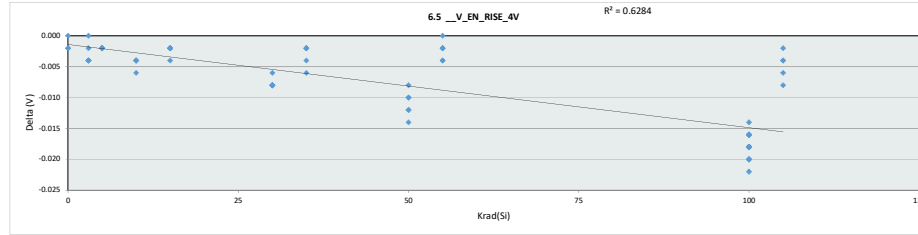
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650
Min	5.028	5.004	5.021	5.003	5.021	5.001	5.020	5.001	4.988	4.994	5.000
Average	5.038	5.036	5.028	5.035	5.026	5.033	5.023	5.033	5.015	5.023	5.030
Max	5.055	5.078	5.038	5.079	5.037	5.077	5.029	5.057	5.058	5.070	5.056
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



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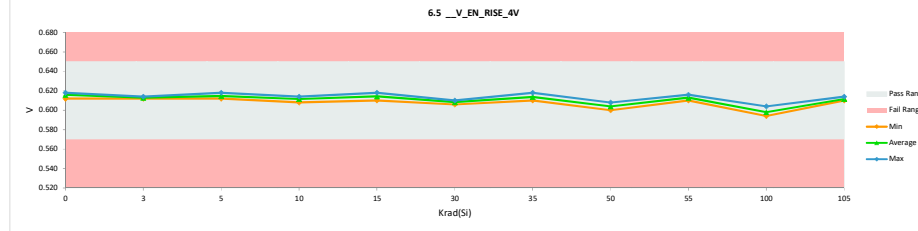
		6.5 V_EN_RISE_4V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	0.65	0.65	
Min Limit	0.57	0.57	

Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	0.618	0.618	0.000
0	118	0.614	0.612	-0.002
0	122	0.620	0.618	-0.002
3	61	0.618	0.614	-0.004
3	62	0.616	0.612	-0.004
3	63	0.612	0.612	0.000
3	64	0.618	0.614	-0.004
3	65	0.616	0.614	-0.002
3	66	0.616	0.612	-0.004
5	67	0.620	0.618	-0.002
5	68	0.616	0.614	-0.002
5	69	0.618	0.616	-0.002
5	70	0.618	0.616	-0.002
5	71	0.614	0.612	-0.002
5	72	0.614	0.612	-0.002
10	73	0.618	0.614	-0.004
10	74	0.616	0.612	-0.004
10	75	0.612	0.608	-0.004
10	76	0.618	0.612	-0.006
10	77	0.616	0.612	-0.004
10	78	0.616	0.612	-0.004
15	79	0.620	0.618	-0.002
15	80	0.616	0.614	-0.002
15	81	0.618	0.616	-0.002
15	82	0.618	0.616	-0.002
15	83	0.614	0.612	-0.002
15	84	0.614	0.610	-0.004
30	86	0.618	0.610	-0.008
30	89	0.616	0.608	-0.008
30	91	0.612	0.606	-0.006
30	92	0.618	0.610	-0.008
30	94	0.616	0.608	-0.008
30	95	0.616	0.608	-0.008
35	100	0.620	0.618	-0.002
35	101	0.616	0.614	-0.002
35	102	0.618	0.616	-0.002
35	103	0.618	0.612	-0.006
35	104	0.614	0.612	-0.002
35	105	0.614	0.610	-0.004
50	106	0.614	0.606	-0.008
50	107	0.614	0.602	-0.012
50	108	0.612	0.600	-0.012
50	109	0.616	0.606	-0.010
50	110	0.618	0.608	-0.010
50	111	0.616	0.602	-0.014
55	113	0.614	0.612	-0.002
55	114	0.612	0.612	0.000
55	115	0.618	0.616	-0.002
55	116	0.616	0.612	-0.004
55	126	0.618	0.616	-0.002
55	128	0.614	0.610	-0.004
100	129	0.616	0.596	-0.020
100	131	0.616	0.596	-0.020
100	132	0.620	0.600	-0.020
100	133	0.616	0.598	-0.018
100	134	0.612	0.596	-0.016
100	135	0.614	0.596	-0.018
100	138	0.618	0.604	-0.014
100	139	0.616	0.600	-0.016
100	140	0.612	0.594	-0.018
100	141	0.618	0.602	-0.016
100	142	0.612	0.594	-0.018
100	143	0.616	0.598	-0.018
100	144	0.622	0.600	-0.022
100	220	0.616	0.598	-0.018
100	221	0.616	0.598	-0.018
100	224	0.616	0.598	-0.018
100	225	0.614	0.596	-0.018
100	226	0.618	0.600	-0.018
100	228	0.618	0.602	-0.016
100	232	0.616	0.598	-0.018
100	233	0.616	0.596	-0.020
100	236	0.614	0.596	-0.018
100	238	0.614	0.598	-0.016
100	243	0.614	0.598	-0.016
105	244	0.616	0.612	-0.004
105	246	0.614	0.610	-0.004
105	248	0.616	0.614	-0.002
105	374	0.620	0.612	-0.008
105	376	0.616	0.610	-0.006
Max		0.622	0.618	0.000
Average		0.616	0.608	-0.008
Min		0.612	0.594	-0.022
Std Dev		0.002	0.007	0.007



		6.5 V_EN_RISE_4V	
Test Site			
Tester			
Test Number			
Max Limit	0.65	V	
Min Limit	0.57	V	

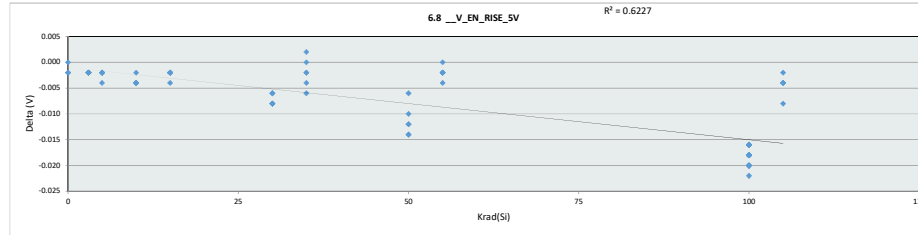
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.612	0.612	0.612	0.608	0.610	0.606	0.610	0.600	0.610	0.594	0.610
Average	0.616	0.613	0.615	0.612	0.614	0.608	0.614	0.604	0.613	0.598	0.612
Max	0.618	0.614	0.618	0.614	0.618	0.610	0.618	0.608	0.616	0.604	0.614
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



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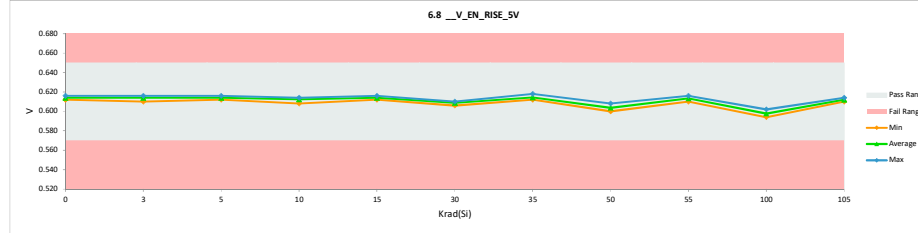
		6.8 V_EN_RISE_5V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	0.65	0.65	
Min Limit	0.57	0.57	

Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	0.616	0.616	0.000
0	118	0.614	0.612	-0.002
0	122	0.616	0.614	-0.002
3	61	0.618	0.616	-0.002
3	62	0.616	0.614	-0.002
3	63	0.612	0.610	-0.002
3	64	0.618	0.616	-0.002
3	65	0.616	0.614	-0.002
3	66	0.616	0.614	-0.002
5	67	0.616	0.614	-0.002
5	68	0.618	0.614	-0.004
5	69	0.618	0.616	-0.002
5	70	0.618	0.616	-0.002
5	71	0.614	0.612	-0.002
5	72	0.614	0.612	-0.002
10	73	0.618	0.614	-0.004
10	74	0.616	0.612	-0.004
10	75	0.612	0.608	-0.004
10	76	0.618	0.614	-0.004
10	77	0.616	0.614	-0.002
10	78	0.616	0.612	-0.004
15	79	0.616	0.614	-0.002
15	80	0.618	0.614	-0.004
15	81	0.618	0.616	-0.002
15	82	0.618	0.616	-0.002
15	83	0.614	0.612	-0.002
15	84	0.614	0.612	-0.002
30	86	0.618	0.610	-0.008
30	89	0.616	0.608	-0.008
30	91	0.612	0.606	-0.006
30	92	0.618	0.610	-0.008
30	94	0.616	0.610	-0.006
30	95	0.616	0.608	-0.008
35	100	0.616	0.618	0.002
35	101	0.618	0.614	-0.004
35	102	0.618	0.616	-0.002
35	103	0.618	0.612	-0.006
35	104	0.614	0.614	0.000
35	105	0.614	0.612	-0.002
50	106	0.614	0.602	-0.012
50	107	0.616	0.602	-0.014
50	108	0.612	0.600	-0.012
50	109	0.618	0.608	-0.010
50	110	0.614	0.608	-0.006
50	111	0.616	0.602	-0.014
55	113	0.614	0.612	-0.002
55	114	0.614	0.612	-0.002
55	115	0.616	0.616	0.000
55	116	0.616	0.614	-0.002
55	126	0.618	0.616	-0.002
55	128	0.614	0.610	-0.004
100	129	0.616	0.596	-0.020
100	131	0.616	0.596	-0.020
100	132	0.618	0.600	-0.018
100	133	0.616	0.598	-0.018
100	134	0.612	0.596	-0.016
100	135	0.614	0.596	-0.018
100	138	0.618	0.600	-0.018
100	139	0.616	0.594	-0.022
100	140	0.612	0.594	-0.018
100	141	0.618	0.600	-0.018
100	142	0.614	0.594	-0.020
100	143	0.616	0.598	-0.018
100	144	0.622	0.602	-0.020
100	220	0.616	0.600	-0.016
100	221	0.618	0.600	-0.018
100	224	0.614	0.596	-0.018
100	225	0.616	0.596	-0.020
100	226	0.618	0.600	-0.018
100	228	0.618	0.602	-0.016
100	232	0.616	0.598	-0.018
100	233	0.616	0.598	-0.018
100	236	0.614	0.596	-0.018
100	238	0.614	0.598	-0.016
100	243	0.614	0.598	-0.016
105	244	0.616	0.612	-0.004
105	246	0.614	0.610	-0.004
105	248	0.616	0.614	-0.002
105	374	0.620	0.612	-0.008
105	376	0.616	0.612	-0.004
Max		0.622	0.618	0.002
Average		0.616	0.608	-0.008
Min		0.612	0.594	-0.022
Std Dev		0.002	0.007	0.007



		6.8 V_EN_RISE_5V	
Test Site			
Tester			
Test Number			
Max Limit	0.65	V	
Min Limit	0.57	V	

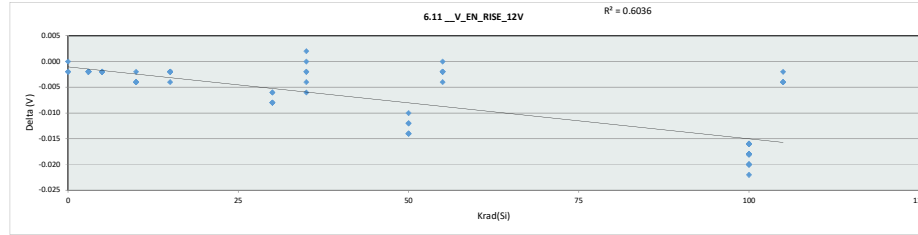
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.612	0.610	0.612	0.608	0.612	0.606	0.612	0.600	0.610	0.594	0.610
Average	0.614	0.614	0.614	0.612	0.614	0.609	0.614	0.604	0.613	0.598	0.612
Max	0.616	0.616	0.616	0.614	0.616	0.610	0.618	0.608	0.616	0.602	0.614
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



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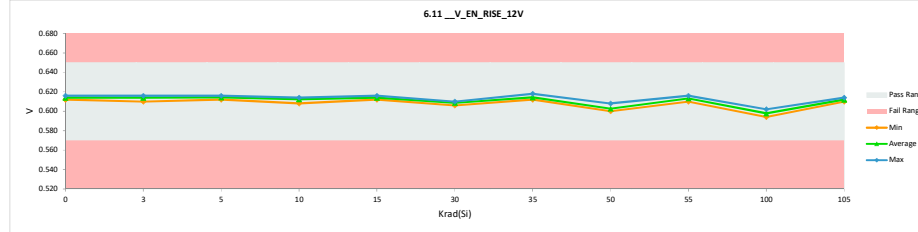
6.11 V_EN_RISE_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.65
Min Limit	0.57

Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	0.616	0.616	0.000
0	118	0.614	0.612	-0.002
0	122	0.616	0.614	-0.002
3	61	0.618	0.616	-0.002
3	62	0.616	0.614	-0.002
3	63	0.612	0.610	-0.002
3	64	0.618	0.616	-0.002
3	65	0.616	0.614	-0.002
3	66	0.616	0.614	-0.002
5	67	0.616	0.614	-0.002
5	68	0.618	0.616	-0.002
5	69	0.618	0.616	-0.002
5	70	0.618	0.616	-0.002
5	71	0.614	0.612	-0.002
5	72	0.614	0.612	-0.002
10	73	0.618	0.614	-0.004
10	74	0.616	0.612	-0.004
10	75	0.612	0.608	-0.004
10	76	0.618	0.614	-0.004
10	77	0.616	0.614	-0.002
10	78	0.616	0.612	-0.004
15	79	0.616	0.614	-0.002
15	80	0.618	0.614	-0.004
15	81	0.618	0.616	-0.002
15	82	0.618	0.616	-0.002
15	83	0.614	0.612	-0.002
15	84	0.614	0.612	-0.002
30	86	0.618	0.610	-0.008
30	89	0.616	0.608	-0.008
30	91	0.612	0.606	-0.006
30	92	0.618	0.610	-0.008
30	94	0.616	0.610	-0.006
30	95	0.616	0.608	-0.008
35	100	0.616	0.618	0.002
35	101	0.618	0.614	-0.004
35	102	0.618	0.616	-0.002
35	103	0.618	0.612	-0.006
35	104	0.614	0.614	0.000
35	105	0.614	0.612	-0.002
50	106	0.614	0.602	-0.012
50	107	0.616	0.602	-0.014
50	108	0.612	0.600	-0.012
50	109	0.618	0.608	-0.010
50	110	0.616	0.602	-0.014
50	111	0.616	0.602	-0.014
55	113	0.614	0.612	-0.002
55	114	0.614	0.612	-0.002
55	115	0.616	0.616	0.000
55	116	0.616	0.614	-0.002
55	126	0.618	0.616	-0.002
55	128	0.614	0.610	-0.004
100	129	0.616	0.596	-0.020
100	131	0.616	0.596	-0.020
100	132	0.618	0.600	-0.018
100	133	0.616	0.598	-0.018
100	134	0.612	0.596	-0.016
100	135	0.614	0.596	-0.018
100	138	0.618	0.600	-0.018
100	139	0.616	0.596	-0.020
100	140	0.612	0.594	-0.018
100	141	0.618	0.600	-0.018
100	142	0.614	0.594	-0.020
100	143	0.616	0.598	-0.018
100	144	0.624	0.622	-0.002
100	220	0.616	0.600	-0.016
100	221	0.618	0.600	-0.018
100	224	0.614	0.596	-0.018
100	225	0.616	0.596	-0.020
100	226	0.618	0.600	-0.018
100	228	0.618	0.602	-0.016
100	232	0.616	0.598	-0.018
100	233	0.616	0.598	-0.018
100	236	0.614	0.596	-0.018
100	238	0.614	0.598	-0.016
100	243	0.614	0.598	-0.016
105	244	0.616	0.612	-0.004
105	246	0.614	0.610	-0.004
105	248	0.616	0.614	-0.002
105	374	0.616	0.612	-0.004
105	376	0.616	0.612	-0.004
Max		0.624	0.618	0.002
Average		0.616	0.608	-0.008
Min		0.612	0.594	-0.022
Std Dev		0.002	0.007	0.007



6.11 V_EN_RISE_12V	
Test Site	
Tester	
Test Number	
Max Limit	0.65
Min Limit	0.57

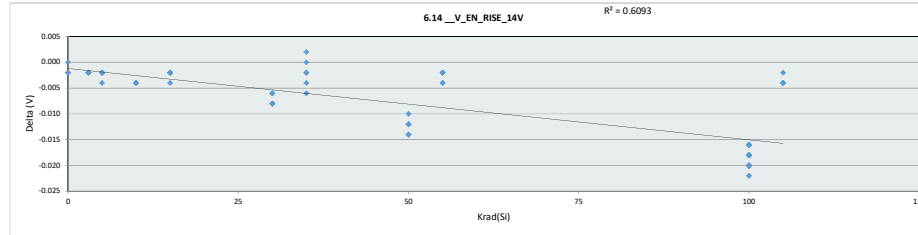
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.612	0.610	0.612	0.608	0.612	0.606	0.612	0.600	0.610	0.594	0.610
Average	0.614	0.614	0.614	0.612	0.614	0.609	0.614	0.603	0.613	0.598	0.612
Max	0.616	0.616	0.616	0.614	0.616	0.610	0.618	0.608	0.616	0.602	0.614
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



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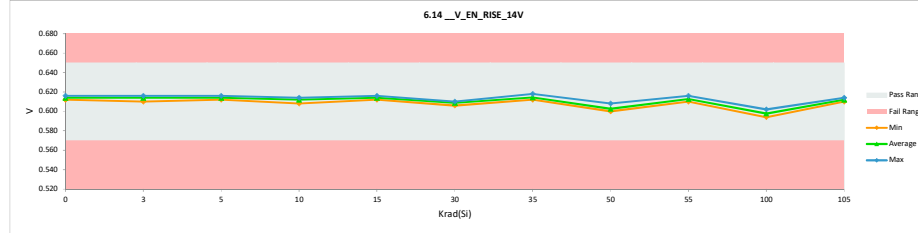
		6.14 V_EN_RISE_14V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	0.65	0.65	
Min Limit	0.57	0.57	

Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	0.616	0.616	0.000
0	118	0.614	0.612	-0.002
0	122	0.616	0.614	-0.002
3	61	0.618	0.616	-0.002
3	62	0.616	0.614	-0.002
3	63	0.612	0.610	-0.002
3	64	0.618	0.616	-0.002
3	65	0.616	0.614	-0.002
3	66	0.616	0.614	-0.002
5	67	0.616	0.614	-0.002
5	68	0.618	0.614	-0.004
5	69	0.618	0.616	-0.002
5	70	0.618	0.616	-0.002
5	71	0.614	0.612	-0.002
5	72	0.614	0.612	-0.002
10	73	0.618	0.614	-0.004
10	74	0.616	0.612	-0.004
10	75	0.612	0.608	-0.004
10	76	0.618	0.614	-0.004
10	77	0.616	0.612	-0.004
10	78	0.616	0.612	-0.004
15	79	0.616	0.614	-0.002
15	80	0.618	0.614	-0.004
15	81	0.618	0.616	-0.002
15	82	0.618	0.616	-0.002
15	83	0.614	0.612	-0.002
15	84	0.614	0.612	-0.002
30	86	0.618	0.610	-0.008
30	89	0.616	0.608	-0.008
30	91	0.612	0.606	-0.006
30	92	0.618	0.610	-0.008
30	94	0.616	0.610	-0.006
30	95	0.616	0.608	-0.008
35	100	0.616	0.618	0.002
35	101	0.618	0.614	-0.004
35	102	0.618	0.616	-0.002
35	103	0.618	0.612	-0.006
35	104	0.614	0.614	0.000
35	105	0.614	0.612	-0.002
50	106	0.614	0.602	-0.012
50	107	0.616	0.602	-0.014
50	108	0.612	0.600	-0.012
50	109	0.618	0.608	-0.010
50	110	0.614	0.602	-0.012
50	111	0.616	0.602	-0.014
55	113	0.614	0.612	-0.002
55	114	0.614	0.612	-0.002
55	115	0.616	0.612	-0.004
55	116	0.616	0.614	-0.002
55	126	0.618	0.616	-0.002
55	128	0.614	0.610	-0.004
100	129	0.616	0.596	-0.020
100	131	0.616	0.596	-0.020
100	132	0.618	0.600	-0.018
100	133	0.616	0.598	-0.018
100	134	0.612	0.596	-0.016
100	135	0.614	0.596	-0.018
100	138	0.618	0.600	-0.018
100	139	0.616	0.594	-0.022
100	140	0.612	0.594	-0.018
100	141	0.618	0.600	-0.018
100	142	0.614	0.594	-0.020
100	143	0.616	0.598	-0.018
100	144	0.622	0.602	-0.020
100	220	0.616	0.600	-0.016
100	221	0.618	0.600	-0.018
100	224	0.614	0.596	-0.018
100	225	0.616	0.596	-0.020
100	226	0.618	0.600	-0.018
100	228	0.618	0.602	-0.016
100	232	0.616	0.598	-0.018
100	233	0.616	0.598	-0.018
100	236	0.614	0.596	-0.018
100	238	0.614	0.598	-0.016
100	243	0.614	0.598	-0.016
105	244	0.616	0.612	-0.004
105	246	0.614	0.610	-0.004
105	248	0.616	0.614	-0.002
105	374	0.616	0.612	-0.004
105	376	0.616	0.612	-0.004
Max		0.622	0.618	0.002
Average		0.616	0.608	-0.008
Min		0.612	0.594	-0.022
Std Dev		0.002	0.007	0.007



		6.14 V_EN_RISE_14V	
Test Site			
Tester			
Test Number			
Max Limit	0.65	V	
Min Limit	0.57	V	

Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.612	0.610	0.612	0.608	0.612	0.606	0.612	0.600	0.610	0.594	0.610
Average	0.614	0.614	0.614	0.612	0.614	0.609	0.614	0.603	0.613	0.598	0.612
Max	0.616	0.616	0.616	0.614	0.616	0.610	0.618	0.608	0.616	0.602	0.614
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650

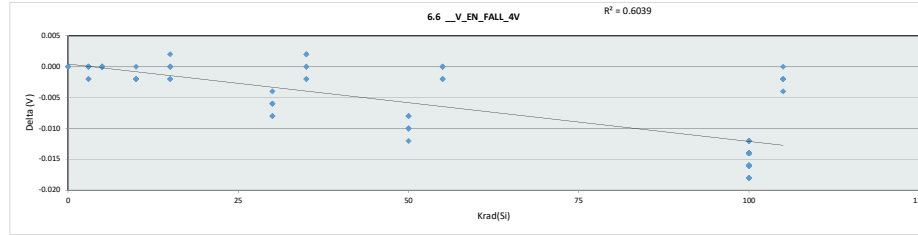


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		6.6 V_EN_FALL_4V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	0.55	0.55	
Min Limit	0.47	0.47	

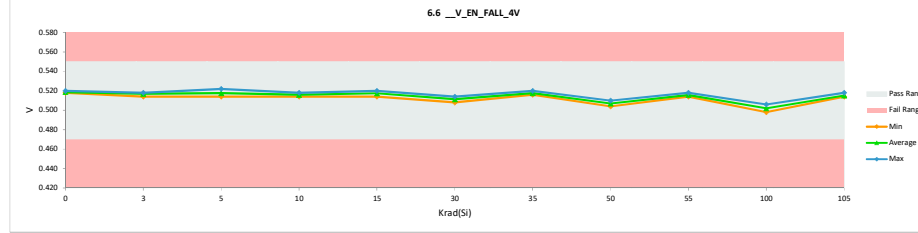
Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	0.518	0.518	0.000
0	118	0.518	0.518	0.000
0	122	0.520	0.520	0.000
3	61	0.518	0.518	0.000
3	62	0.518	0.516	-0.002
3	63	0.516	0.514	-0.002
3	64	0.518	0.518	0.000
3	65	0.518	0.518	0.000
3	66	0.518	0.518	0.000
5	67	0.522	0.522	0.000
5	68	0.516	0.516	0.000
5	69	0.520	0.520	0.000
5	70	0.518	0.518	0.000
5	71	0.516	0.516	0.000
5	72	0.514	0.514	0.000
10	73	0.518	0.516	-0.002
10	74	0.518	0.516	-0.002
10	75	0.516	0.514	-0.002
10	76	0.518	0.518	0.000
10	77	0.518	0.516	-0.002
10	78	0.518	0.516	-0.002
15	79	0.522	0.520	-0.002
15	80	0.516	0.516	0.000
15	81	0.520	0.518	-0.002
15	82	0.518	0.520	0.002
15	83	0.516	0.516	0.000
15	84	0.514	0.514	0.000
30	86	0.518	0.512	-0.006
30	89	0.518	0.510	-0.008
30	91	0.516	0.508	-0.008
30	92	0.518	0.512	-0.006
30	94	0.518	0.514	-0.004
30	95	0.518	0.512	-0.006
35	100	0.522	0.520	-0.002
35	101	0.516	0.518	0.002
35	102	0.520	0.520	0.000
35	103	0.518	0.516	-0.002
35	104	0.516	0.516	0.000
35	105	0.514	0.516	0.002
50	106	0.516	0.508	-0.008
50	107	0.516	0.504	-0.012
50	108	0.514	0.504	-0.010
50	109	0.520	0.510	-0.010
50	110	0.518	0.510	-0.008
50	111	0.516	0.506	-0.010
55	113	0.516	0.514	-0.002
55	114	0.514	0.514	0.000
55	115	0.520	0.518	-0.002
55	116	0.516	0.516	0.000
55	126	0.518	0.518	0.000
55	128	0.516	0.514	-0.002
100	129	0.516	0.498	-0.018
100	131	0.516	0.500	-0.016
100	132	0.520	0.506	-0.014
100	133	0.516	0.500	-0.016
100	134	0.514	0.502	-0.012
100	135	0.516	0.498	-0.018
100	138	0.520	0.506	-0.014
100	139	0.516	0.502	-0.014
100	140	0.512	0.498	-0.014
100	141	0.520	0.506	-0.014
100	142	0.512	0.498	-0.014
100	143	0.516	0.502	-0.014
100	144	0.524	0.506	-0.018
100	220	0.518	0.502	-0.016
100	221	0.516	0.502	-0.014
100	224	0.516	0.500	-0.016
100	225	0.516	0.502	-0.014
100	226	0.520	0.506	-0.014
100	228	0.518	0.504	-0.014
100	232	0.518	0.502	-0.016
100	233	0.518	0.504	-0.014
100	236	0.514	0.500	-0.014
100	238	0.516	0.500	-0.016
100	243	0.516	0.504	-0.012
105	244	0.516	0.514	-0.002
105	246	0.516	0.514	-0.002
105	248	0.516	0.516	0.000
105	374	0.522	0.518	-0.004
105	376	0.516	0.514	-0.002

Max	0.524	0.522	0.002
Average	0.517	0.511	-0.006
Min	0.512	0.498	-0.018
Std Dev	0.002	0.007	0.006



		6.6 V_EN_FALL_4V	
Test Site			
Tester			
Test Number			
Max Limit	0.55	V	
Min Limit	0.47	V	

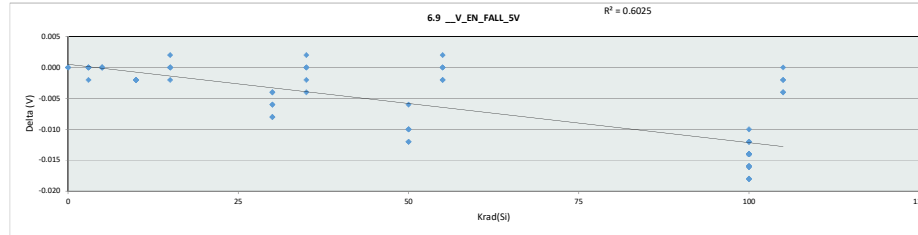
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.518	0.514	0.514	0.514	0.514	0.508	0.516	0.504	0.514	0.498	0.514
Average	0.519	0.517	0.518	0.516	0.517	0.514	0.518	0.507	0.516	0.502	0.515
Max	0.520	0.518	0.522	0.518	0.520	0.514	0.520	0.510	0.518	0.506	0.518
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



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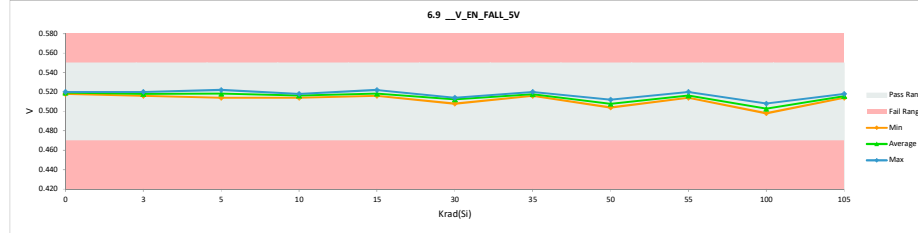
		6.9 V_EN_FALL_5V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	0.55	0.55	
Min Limit	0.47	0.47	

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	0.520	0.520	0.000
0	118	0.518	0.518	0.000
0	122	0.520	0.520	0.000
3	61	0.518	0.518	0.000
3	62	0.518	0.518	0.000
3	63	0.516	0.516	0.000
3	64	0.520	0.518	-0.002
3	65	0.520	0.520	0.000
3	66	0.518	0.518	0.000
5	67	0.522	0.522	0.000
5	68	0.518	0.518	0.000
5	69	0.520	0.520	0.000
5	70	0.520	0.520	0.000
5	71	0.516	0.516	0.000
5	72	0.514	0.514	0.000
10	73	0.518	0.516	-0.002
10	74	0.518	0.516	-0.002
10	75	0.516	0.514	-0.002
10	76	0.520	0.518	-0.002
10	77	0.520	0.518	-0.002
10	78	0.518	0.516	-0.002
15	79	0.522	0.522	0.000
15	80	0.518	0.516	-0.002
15	81	0.520	0.520	0.000
15	82	0.520	0.520	0.000
15	83	0.516	0.516	0.000
15	84	0.514	0.516	0.002
30	86	0.518	0.514	-0.004
30	89	0.518	0.514	-0.004
30	91	0.516	0.508	-0.008
30	92	0.520	0.512	-0.008
30	94	0.520	0.514	-0.006
30	95	0.518	0.512	-0.006
35	100	0.522	0.520	-0.002
35	101	0.518	0.518	0.000
35	102	0.520	0.520	0.000
35	103	0.520	0.516	-0.004
35	104	0.516	0.516	0.000
35	105	0.514	0.516	0.002
50	106	0.518	0.508	-0.010
50	107	0.516	0.504	-0.012
50	108	0.516	0.506	-0.010
50	109	0.520	0.510	-0.010
50	110	0.518	0.512	-0.006
50	111	0.518	0.506	-0.012
55	113	0.516	0.514	-0.002
55	114	0.514	0.514	0.000
55	115	0.520	0.518	-0.002
55	116	0.516	0.516	0.000
55	126	0.518	0.520	0.002
55	128	0.516	0.516	0.000
100	129	0.516	0.498	-0.018
100	131	0.516	0.502	-0.014
100	132	0.522	0.506	-0.016
100	133	0.516	0.504	-0.012
100	134	0.514	0.502	-0.012
100	135	0.516	0.498	-0.018
100	138	0.520	0.506	-0.014
100	139	0.518	0.504	-0.014
100	140	0.514	0.498	-0.016
100	141	0.520	0.506	-0.014
100	142	0.516	0.500	-0.016
100	143	0.518	0.502	-0.016
100	144	0.524	0.508	-0.016
100	220	0.518	0.504	-0.014
100	221	0.518	0.502	-0.016
100	224	0.516	0.502	-0.014
100	225	0.518	0.502	-0.016
100	226	0.520	0.506	-0.014
100	228	0.518	0.504	-0.014
100	232	0.518	0.502	-0.016
100	233	0.518	0.504	-0.014
100	236	0.514	0.500	-0.014
100	238	0.518	0.500	-0.018
100	243	0.516	0.506	-0.010
105	244	0.518	0.516	-0.002
105	246	0.518	0.514	-0.004
105	248	0.516	0.516	0.000
105	374	0.522	0.518	-0.004
105	376	0.516	0.514	-0.002
Max		0.524	0.522	0.002
Average		0.518	0.512	-0.006
Min		0.514	0.498	-0.018
Std Dev		0.002	0.007	0.007



		6.9 V_EN_FALL_5V	
Test Site			
Tester			
Test Number			
Max Limit	0.55	V	
Min Limit	0.47	V	

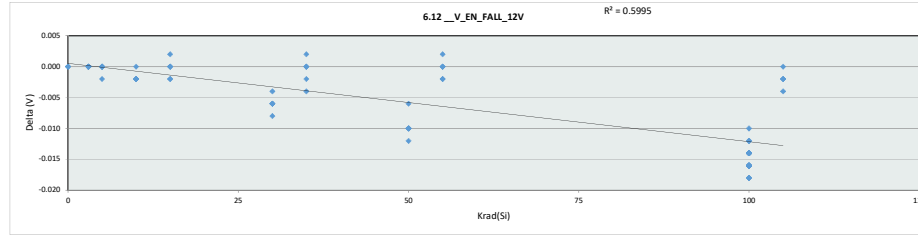
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.518	0.516	0.514	0.514	0.516	0.508	0.516	0.504	0.514	0.498	0.514
Average	0.519	0.518	0.518	0.516	0.518	0.512	0.518	0.508	0.516	0.503	0.516
Max	0.520	0.520	0.522	0.518	0.522	0.514	0.520	0.512	0.520	0.508	0.518
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



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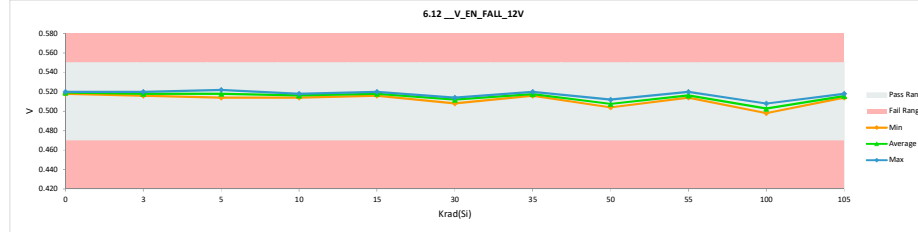
		6.12_V_EN_FALL_12V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	0.55	0.55	
Min Limit	0.47	0.47	

Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	0.520	0.520	0.000
0	118	0.518	0.518	0.000
0	122	0.520	0.520	0.000
3	61	0.518	0.518	0.000
3	62	0.518	0.518	0.000
3	63	0.516	0.516	0.000
3	64	0.518	0.518	0.000
3	65	0.520	0.520	0.000
3	66	0.518	0.518	0.000
5	67	0.522	0.522	0.000
5	68	0.518	0.516	-0.002
5	69	0.520	0.520	0.000
5	70	0.520	0.520	0.000
5	71	0.516	0.516	0.000
5	72	0.514	0.514	0.000
10	73	0.518	0.516	-0.002
10	74	0.518	0.516	-0.002
10	75	0.516	0.514	-0.002
10	76	0.518	0.518	0.000
10	77	0.520	0.518	-0.002
10	78	0.518	0.516	-0.002
15	79	0.522	0.520	-0.002
15	80	0.518	0.516	-0.002
15	81	0.520	0.520	0.000
15	82	0.520	0.520	0.000
15	83	0.516	0.516	0.000
15	84	0.514	0.516	0.002
30	86	0.518	0.512	-0.006
30	89	0.518	0.514	-0.004
30	91	0.516	0.508	-0.008
30	92	0.518	0.512	-0.006
30	94	0.520	0.514	-0.006
30	95	0.518	0.512	-0.006
35	100	0.522	0.520	-0.002
35	101	0.518	0.518	0.000
35	102	0.520	0.520	0.000
35	103	0.520	0.516	-0.004
35	104	0.516	0.516	0.000
35	105	0.514	0.516	0.002
50	106	0.518	0.508	-0.010
50	107	0.516	0.504	-0.012
50	108	0.516	0.506	-0.010
50	109	0.520	0.510	-0.010
50	110	0.518	0.512	-0.006
50	111	0.516	0.506	-0.010
55	113	0.516	0.514	-0.002
55	114	0.514	0.514	0.000
55	115	0.520	0.518	-0.002
55	116	0.516	0.516	0.000
55	126	0.518	0.520	0.002
55	128	0.516	0.516	0.000
100	129	0.516	0.498	-0.018
100	131	0.516	0.502	-0.014
100	132	0.522	0.506	-0.016
100	133	0.516	0.504	-0.012
100	134	0.514	0.502	-0.012
100	135	0.516	0.498	-0.018
100	138	0.520	0.506	-0.014
100	139	0.518	0.504	-0.014
100	140	0.514	0.498	-0.016
100	141	0.520	0.506	-0.014
100	142	0.516	0.500	-0.016
100	143	0.518	0.502	-0.016
100	144	0.524	0.508	-0.016
100	220	0.518	0.504	-0.014
100	221	0.518	0.502	-0.016
100	224	0.518	0.502	-0.016
100	225	0.518	0.502	-0.016
100	226	0.520	0.506	-0.014
100	228	0.518	0.504	-0.014
100	232	0.518	0.502	-0.016
100	233	0.518	0.504	-0.014
100	236	0.514	0.500	-0.014
100	238	0.518	0.500	-0.018
100	243	0.516	0.506	-0.010
105	244	0.518	0.516	-0.002
105	246	0.516	0.514	-0.002
105	248	0.516	0.516	0.000
105	374	0.522	0.518	-0.004
105	376	0.516	0.514	-0.002
Max		0.524	0.522	0.002
Average		0.518	0.512	-0.006
Min		0.514	0.498	-0.018
Std Dev		0.002	0.007	0.007



		6.12_V_EN_FALL_12V	
Test Site			
Tester			
Test Number			
Max Limit	0.55	V	
Min Limit	0.47	V	

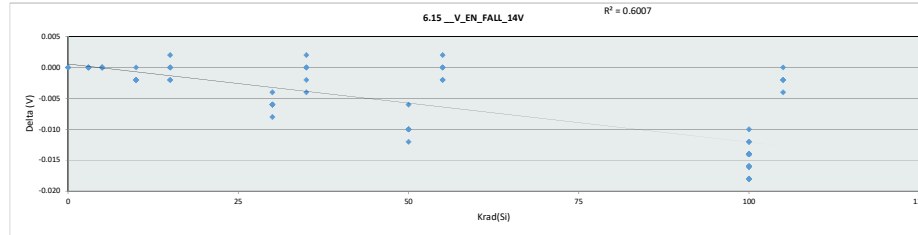
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.518	0.516	0.514	0.514	0.516	0.508	0.516	0.504	0.514	0.498	0.514
Average	0.519	0.518	0.518	0.516	0.518	0.512	0.518	0.508	0.516	0.503	0.516
Max	0.520	0.520	0.522	0.518	0.520	0.514	0.520	0.512	0.520	0.508	0.518
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



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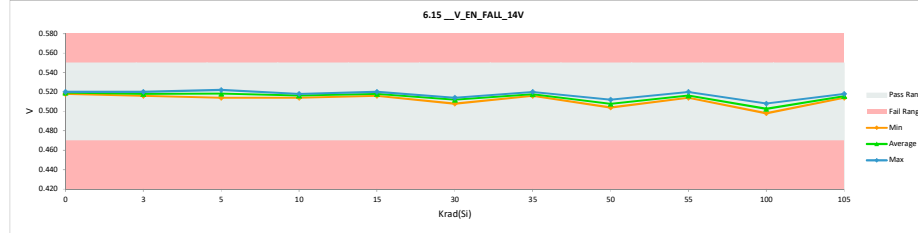
		6.15 V_EN_FALL_14V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	0.55	0.55	
Min Limit	0.47	0.47	

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	0.520	0.520	0.000
0	118	0.518	0.518	0.000
0	122	0.520	0.520	0.000
3	61	0.518	0.518	0.000
3	62	0.518	0.518	0.000
3	63	0.516	0.516	0.000
3	64	0.518	0.518	0.000
3	65	0.520	0.520	0.000
3	66	0.518	0.518	0.000
5	67	0.522	0.522	0.000
5	68	0.518	0.518	0.000
5	69	0.520	0.520	0.000
5	70	0.520	0.520	0.000
5	71	0.516	0.516	0.000
5	72	0.514	0.514	0.000
10	73	0.518	0.516	-0.002
10	74	0.518	0.516	-0.002
10	75	0.516	0.514	-0.002
10	76	0.518	0.518	0.000
10	77	0.520	0.518	-0.002
10	78	0.518	0.516	-0.002
15	79	0.522	0.520	-0.002
15	80	0.518	0.516	-0.002
15	81	0.520	0.520	0.000
15	82	0.520	0.520	0.000
15	83	0.516	0.516	0.000
15	84	0.514	0.516	0.002
30	86	0.518	0.512	-0.006
30	89	0.518	0.514	-0.004
30	91	0.516	0.508	-0.008
30	92	0.518	0.512	-0.006
30	94	0.520	0.514	-0.006
30	95	0.518	0.512	-0.006
35	100	0.522	0.520	-0.002
35	101	0.518	0.518	0.000
35	102	0.520	0.520	0.000
35	103	0.520	0.516	-0.004
35	104	0.516	0.516	0.000
35	105	0.514	0.516	0.002
50	106	0.518	0.508	-0.010
50	107	0.516	0.504	-0.012
50	108	0.516	0.506	-0.010
50	109	0.520	0.510	-0.010
50	110	0.518	0.512	-0.006
50	111	0.516	0.506	-0.010
55	113	0.516	0.514	-0.002
55	114	0.514	0.514	0.000
55	115	0.520	0.518	-0.002
55	116	0.516	0.516	0.000
55	126	0.518	0.520	0.002
55	128	0.516	0.516	0.000
100	129	0.516	0.498	-0.018
100	131	0.516	0.502	-0.014
100	132	0.522	0.506	-0.016
100	133	0.516	0.504	-0.012
100	134	0.514	0.502	-0.012
100	135	0.516	0.498	-0.018
100	138	0.520	0.506	-0.014
100	139	0.518	0.504	-0.014
100	140	0.512	0.498	-0.014
100	141	0.520	0.506	-0.014
100	142	0.516	0.498	-0.018
100	143	0.518	0.502	-0.016
100	144	0.524	0.508	-0.016
100	220	0.518	0.504	-0.014
100	221	0.518	0.502	-0.016
100	224	0.516	0.502	-0.014
100	225	0.518	0.502	-0.016
100	226	0.520	0.506	-0.014
100	228	0.518	0.504	-0.014
100	232	0.518	0.502	-0.016
100	233	0.518	0.504	-0.014
100	236	0.514	0.500	-0.014
100	238	0.518	0.500	-0.018
100	243	0.516	0.506	-0.010
105	244	0.518	0.516	-0.002
105	246	0.516	0.514	-0.002
105	248	0.516	0.516	0.000
105	374	0.522	0.518	-0.004
105	376	0.516	0.514	-0.002
Max		0.524	0.522	0.002
Average		0.518	0.512	-0.006
Min		0.512	0.498	-0.018
Std Dev		0.002	0.007	0.007



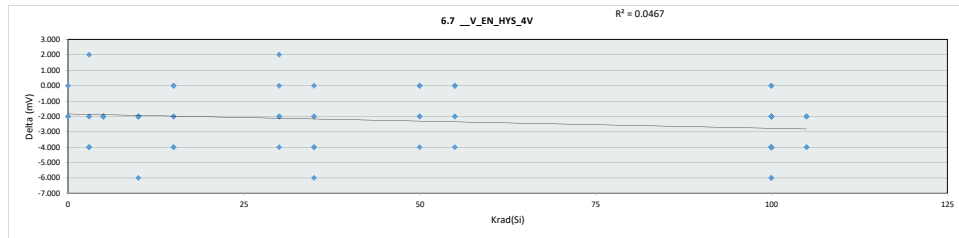
		6.15 __V_EN_FALL_14V	
Test Site			
Tester			
Test Number			
Max Limit	0.55	V	
Min Limit	0.47	V	

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.518	0.516	0.514	0.514	0.516	0.508	0.516	0.504	0.514	0.498	0.514
Average	0.519	0.518	0.518	0.516	0.518	0.512	0.518	0.508	0.516	0.503	0.516
Max	0.520	0.520	0.522	0.518	0.520	0.514	0.520	0.512	0.520	0.508	0.518
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550

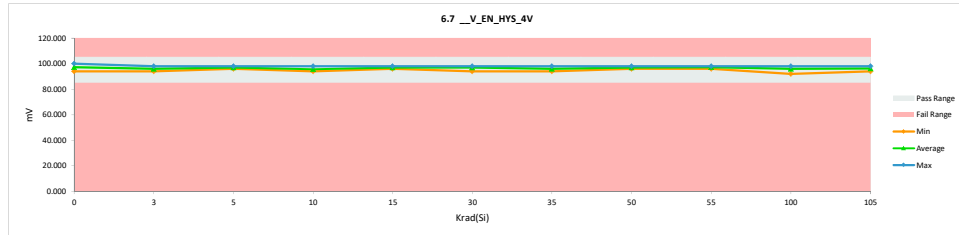


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6.7 _V_EN_HYS_4V				
Test Site				
Tester				
Test Number				
Unit	mV		mV	
Max Limit	105		105	
Min Limit	85		85	
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	100.000	100.000	0.000
0	118	96.000	94.000	-2.000
0	122	100.000	98.000	-2.000
3	61	100.000	96.000	-4.000
3	62	98.000	96.000	-2.000
3	63	96.000	98.000	2.000
3	64	100.000	96.000	-4.000
3	65	98.000	96.000	-2.000
3	66	98.000	94.000	-4.000
5	67	98.000	96.000	-2.000
5	68	100.000	98.000	-2.000
5	69	98.000	96.000	-2.000
5	70	100.000	98.000	-2.000
5	71	98.000	96.000	-2.000
5	72	100.000	98.000	-2.000
10	73	100.000	98.000	-2.000
10	74	98.000	96.000	-2.000
10	75	96.000	94.000	-2.000
10	76	100.000	94.000	-6.000
10	77	98.000	96.000	-2.000
10	78	98.000	96.000	-2.000
15	79	98.000	98.000	0.000
15	80	100.000	98.000	-2.000
15	81	98.000	98.000	0.000
15	82	100.000	96.000	-4.000
15	83	98.000	96.000	-2.000
15	84	100.000	96.000	-4.000
30	86	100.000	98.000	-2.000
30	89	98.000	98.000	0.000
30	91	96.000	98.000	2.000
30	92	100.000	98.000	-2.000
30	94	98.000	94.000	-4.000
30	95	98.000	96.000	-2.000
35	100	98.000	98.000	0.000
35	101	100.000	96.000	-4.000
35	102	98.000	96.000	-2.000
35	103	100.000	96.000	-4.000
35	104	98.000	96.000	-2.000
35	105	100.000	94.000	-6.000
50	106	98.000	98.000	0.000
50	107	98.000	98.000	0.000
50	108	98.000	96.000	-2.000
50	109	96.000	96.000	0.000
50	110	100.000	98.000	-2.000
50	111	100.000	96.000	-4.000
55	113	98.000	98.000	0.000
55	114	98.000	98.000	0.000
55	115	98.000	98.000	0.000
55	116	100.000	96.000	-4.000
55	126	100.000	98.000	-2.000
55	128	98.000	96.000	-2.000
100	129	100.000	98.000	-2.000
100	131	100.000	96.000	-4.000
100	132	100.000	94.000	-6.000
100	133	100.000	98.000	-2.000
100	134	98.000	94.000	-4.000
100	135	98.000	98.000	0.000
100	138	98.000	98.000	0.000
100	139	100.000	98.000	-2.000
100	140	100.000	96.000	-4.000
100	141	98.000	96.000	-2.000
100	142	100.000	96.000	-4.000
100	143	100.000	96.000	-4.000
100	144	98.000	94.000	-4.000
100	220	98.000	96.000	-2.000
100	221	100.000	96.000	-4.000
100	224	100.000	98.000	-2.000
100	225	98.000	94.000	-4.000
100	226	98.000	94.000	-4.000
100	228	100.000	98.000	-2.000
100	232	98.000	96.000	-2.000
100	233	98.000	92.000	-6.000
100	236	100.000	96.000	-4.000
100	238	98.000	98.000	0.000
100	243	98.000	94.000	-4.000
105	244	100.000	98.000	-2.000
105	246	98.000	96.000	-2.000
105	248	100.000	98.000	-2.000
105	374	98.000	94.000	-4.000
105	376	100.000	96.000	-4.000
	Max	100.000	100.000	2.000
	Average	98.775	96.450	-2.325
	Min	96.000	92.000	-6.000
	Std Dev	1.211	1.558	1.727

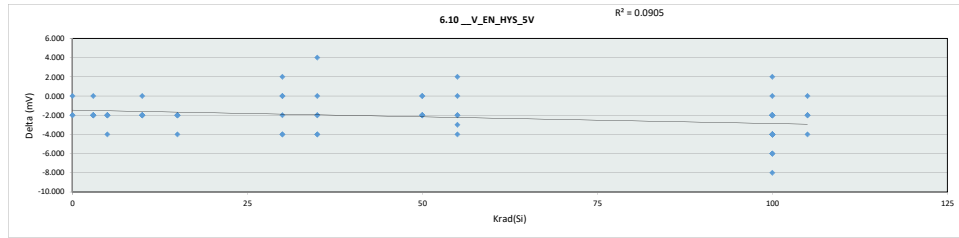


6.7 _V_EN_HYS_4V											
Test Site											
Tester											
Test Number											
Max Limit	105		mV								
Min Limit	85		mV								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.000	94.000	96.000	94.000	96.000	94.000	94.000	96.000	96.000	96.000	94.000
Average	97.333	96.000	97.000	95.667	97.000	97.000	96.000	97.000	97.333	96.000	96.400
Max	100.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000
UL	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000

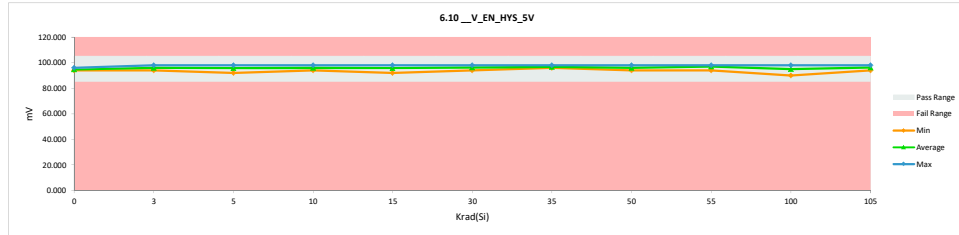


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6.10_V_EN_HYS_5V					
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit
			mV	105	85
			mV	105	85
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta	
0	117	96.000	96.000	0.000	
0	118	96.000	94.000	-2.000	
0	122	96.000	94.000	-2.000	
3	61	100.000	98.000	-2.000	
3	62	98.000	96.000	-2.000	
3	63	96.000	94.000	-2.000	
3	64	98.000	98.000	0.000	
3	65	96.000	94.000	-2.000	
3	66	98.000	96.000	-2.000	
5	67	94.000	92.000	-2.000	
5	68	100.000	96.000	-4.000	
5	69	98.000	96.000	-2.000	
5	70	98.000	96.000	-2.000	
5	71	98.000	96.000	-2.000	
5	72	100.000	98.000	-2.000	
10	73	100.000	98.000	-2.000	
10	74	98.000	96.000	-2.000	
10	75	96.000	94.000	-2.000	
10	76	98.000	96.000	-2.000	
10	77	96.000	96.000	0.000	
10	78	98.000	96.000	-2.000	
15	79	94.000	92.000	-2.000	
15	80	100.000	98.000	-2.000	
15	81	98.000	96.000	-2.000	
15	82	98.000	96.000	-2.000	
15	83	98.000	96.000	-2.000	
15	84	100.000	96.000	-4.000	
30	86	100.000	96.000	-4.000	
30	89	98.000	94.000	-4.000	
30	91	96.000	98.000	2.000	
30	92	98.000	98.000	0.000	
30	94	96.000	96.000	0.000	
30	95	98.000	96.000	-2.000	
35	100	94.000	98.000	4.000	
35	101	100.000	96.000	-4.000	
35	102	98.000	96.000	-2.000	
35	103	98.000	96.000	-2.000	
35	104	98.000	98.000	0.000	
35	105	100.000	96.000	-4.000	
50	106	96.000	94.000	-2.000	
50	107	100.000	98.000	-2.000	
50	108	96.000	94.000	-2.000	
50	109	98.000	98.000	0.000	
50	110	96.000	96.000	0.000	
50	111	98.000	96.000	-2.000	
55	113	98.000	98.000	0.000	
55	114	100.000	98.000	-2.000	
55	115	96.000	98.000	2.000	
55	116	100.000	98.000	-2.000	
55	125	100.000	96.000	-4.000	
55	128	97.000	94.000	-3.000	
100	129	100.000	98.000	-2.000	
100	131	100.000	94.000	-6.000	
100	132	96.000	94.000	-2.000	
100	133	100.000	94.000	-6.000	
100	134	98.000	94.000	-4.000	
100	135	98.000	98.000	0.000	
100	138	98.000	94.000	-4.000	
100	139	98.000	90.000	-8.000	
100	140	98.000	96.000	-2.000	
100	141	98.000	94.000	-4.000	
100	142	98.000	94.000	-4.000	
100	143	98.000	96.000	-2.000	
100	144	98.000	94.000	-4.000	
100	220	98.000	96.000	-2.000	
100	221	100.000	98.000	-2.000	
100	224	98.000	94.000	-4.000	
100	225	98.000	94.000	-4.000	
100	226	98.000	94.000	-4.000	
100	228	100.000	98.000	-2.000	
100	232	98.000	96.000	-2.000	
100	233	98.000	94.000	-4.000	
100	236	100.000	96.000	-4.000	
100	238	96.000	98.000	2.000	
100	243	98.000	92.000	-6.000	
105	244	98.000	96.000	-2.000	
105	246	96.000	96.000	0.000	
105	248	100.000	98.000	-2.000	
105	374	98.000	94.000	-4.000	
105	376	100.000	98.000	-2.000	
	Max	100.000	98.000	4.000	
	Average	97.963	95.775	-2.188	
	Min	94.000	90.000	-8.000	
	Std Dev	1.579	1.800	1.890	

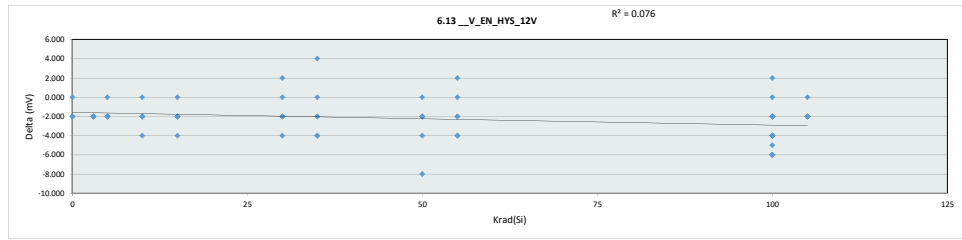


6.10_V_EN_HYS_5V											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			105	85							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.000	94.000	92.000	94.000	92.000	94.000	96.000	94.000	94.000	90.000	94.000
Average	94.667	96.000	95.667	96.000	95.667	96.333	96.667	96.000	97.000	95.000	96.400
Max	96.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000
UL	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000

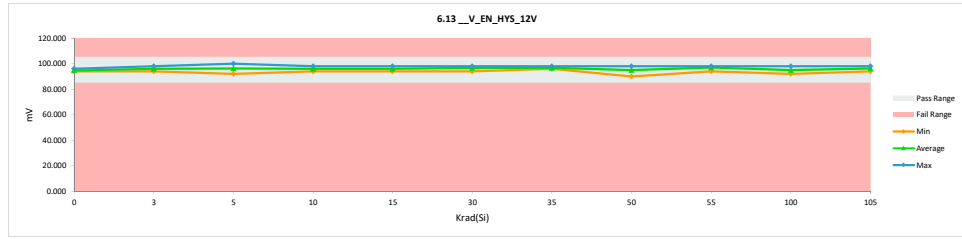


LDR TID Report
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6.13_V_EN_HYS_12V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	105	105		
Min Limit	85	85		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	96.000	96.000	0.000
0	118	96.000	94.000	-2.000
0	122	96.000	94.000	-2.000
3	61	100.000	98.000	-2.000
3	62	98.000	96.000	-2.000
3	63	96.000	94.000	-2.000
3	64	100.000	98.000	-2.000
3	65	96.000	94.000	-2.000
3	66	98.000	96.000	-2.000
5	67	94.000	92.000	-2.000
5	68	100.000	100.000	0.000
5	69	98.000	96.000	-2.000
5	70	98.000	96.000	-2.000
5	71	98.000	96.000	-2.000
5	72	100.000	98.000	-2.000
10	73	100.000	98.000	-2.000
10	74	98.000	96.000	-2.000
10	75	96.000	94.000	-2.000
10	76	100.000	96.000	-4.000
10	77	96.000	96.000	0.000
10	78	98.000	96.000	-2.000
15	79	94.000	94.000	0.000
15	80	100.000	98.000	-2.000
15	81	98.000	96.000	-2.000
15	82	98.000	96.000	-2.000
15	83	98.000	96.000	-2.000
15	84	100.000	96.000	-4.000
30	86	100.000	98.000	-2.000
30	89	98.000	94.000	-4.000
30	91	96.000	98.000	2.000
30	92	100.000	98.000	-2.000
30	94	96.000	96.000	0.000
30	95	98.000	96.000	-2.000
35	100	94.000	98.000	4.000
35	101	100.000	96.000	-4.000
35	102	98.000	96.000	-2.000
35	103	98.000	96.000	-2.000
35	104	98.000	98.000	0.000
35	105	100.000	96.000	-4.000
50	106	96.000	94.000	-2.000
50	107	100.000	98.000	-2.000
50	108	96.000	94.000	-2.000
50	109	98.000	98.000	0.000
50	110	98.000	90.000	-8.000
50	111	100.000	96.000	-4.000
55	113	98.000	98.000	0.000
55	114	100.000	98.000	-2.000
55	115	96.000	98.000	2.000
55	116	100.000	98.000	-2.000
55	126	100.000	96.000	-4.000
55	128	98.000	94.000	-4.000
100	129	100.000	98.000	-2.000
100	131	100.000	94.000	-6.000
100	132	96.000	94.000	-2.000
100	133	100.000	94.000	-6.000
100	134	98.000	94.000	-4.000
100	135	98.000	98.000	0.000
100	138	98.000	94.000	-4.000
100	139	98.000	92.000	-6.000
100	140	98.000	96.000	-2.000
100	141	98.000	93.000	-5.000
100	142	98.000	94.000	-4.000
100	143	98.000	96.000	-2.000
100	144	100.000	94.000	-6.000
100	220	98.000	96.000	-2.000
100	221	100.000	98.000	-2.000
100	224	96.000	94.000	-2.000
100	225	98.000	94.000	-4.000
100	226	98.000	94.000	-4.000
100	228	100.000	98.000	-2.000
100	232	98.000	96.000	-2.000
100	233	98.000	94.000	-4.000
100	236	100.000	96.000	-4.000
100	238	96.000	98.000	2.000
100	243	98.000	92.000	-6.000
105	244	98.000	96.000	-2.000
105	246	98.000	96.000	-2.000
105	248	100.000	98.000	-2.000
105	374	94.000	94.000	0.000
105	376	100.000	98.000	-2.000
Max	100.000	100.000	4.000	
Average	98.075	95.813	-2.263	
Min	94.000	90.000	-8.000	
Std Dev	1.697	1.903	1.960	

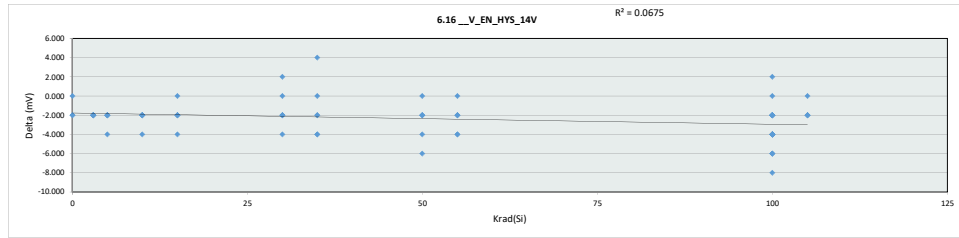


6.13_V_EN_HYS_12V											
Test Site											
Tester											
Test Number											
Max Limit	105	mV									
Min Limit	85	mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.000	94.000	92.000	94.000	94.000	94.000	96.000	90.000	94.000	92.000	94.000
Average	94.667	96.000	96.333	96.000	96.000	96.667	96.667	95.000	97.000	95.042	96.400
Max	96.000	98.000	100.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000
UL	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000

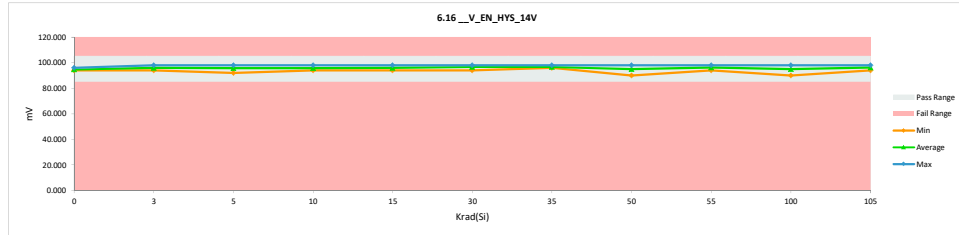


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6.16_V_EN_HYS_14V					
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit
			mV	105	85
			mV	105	85
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta	
0	117	96.000	96.000	0.000	
0	118	96.000	94.000	-2.000	
0	122	96.000	94.000	-2.000	
3	61	100.000	98.000	-2.000	
3	62	98.000	96.000	-2.000	
3	63	96.000	94.000	-2.000	
3	64	100.000	98.000	-2.000	
3	65	96.000	94.000	-2.000	
3	66	98.000	96.000	-2.000	
5	67	94.000	92.000	-2.000	
5	68	100.000	96.000	-4.000	
5	69	98.000	96.000	-2.000	
5	70	98.000	96.000	-2.000	
5	71	98.000	96.000	-2.000	
5	72	100.000	98.000	-2.000	
10	73	100.000	98.000	-2.000	
10	74	98.000	96.000	-2.000	
10	75	96.000	94.000	-2.000	
10	76	100.000	96.000	-4.000	
10	77	96.000	94.000	-2.000	
10	78	98.000	96.000	-2.000	
15	79	94.000	94.000	0.000	
15	80	100.000	98.000	-2.000	
15	81	98.000	96.000	-2.000	
15	82	98.000	96.000	-2.000	
15	83	98.000	96.000	-2.000	
15	84	100.000	96.000	-4.000	
30	86	100.000	98.000	-2.000	
30	89	98.000	94.000	-4.000	
30	91	96.000	98.000	2.000	
30	92	100.000	98.000	-2.000	
30	94	96.000	96.000	0.000	
30	95	98.000	96.000	-2.000	
35	100	94.000	98.000	4.000	
35	101	100.000	96.000	-4.000	
35	102	98.000	96.000	-2.000	
35	103	98.000	96.000	-2.000	
35	104	98.000	98.000	0.000	
35	105	100.000	96.000	-4.000	
50	106	96.000	94.000	-2.000	
50	107	100.000	98.000	-2.000	
50	108	96.000	94.000	-2.000	
50	109	98.000	98.000	0.000	
50	110	96.000	90.000	-6.000	
50	111	100.000	96.000	-4.000	
55	113	98.000	98.000	0.000	
55	114	100.000	98.000	-2.000	
55	115	96.000	94.000	-2.000	
55	116	100.000	98.000	-2.000	
55	125	100.000	96.000	-4.000	
55	128	98.000	94.000	-4.000	
100	129	100.000	98.000	-2.000	
100	131	100.000	94.000	-6.000	
100	132	96.000	94.000	-2.000	
100	133	100.000	94.000	-6.000	
100	134	98.000	94.000	-4.000	
100	135	98.000	98.000	0.000	
100	138	98.000	94.000	-4.000	
100	139	98.000	90.000	-8.000	
100	140	100.000	96.000	-4.000	
100	141	98.000	94.000	-4.000	
100	142	98.000	96.000	-2.000	
100	143	98.000	96.000	-2.000	
100	144	98.000	94.000	-4.000	
100	220	98.000	96.000	-2.000	
100	221	100.000	98.000	-2.000	
100	224	98.000	94.000	-4.000	
100	225	98.000	94.000	-4.000	
100	226	98.000	94.000	-4.000	
100	228	100.000	98.000	-2.000	
100	232	98.000	96.000	-2.000	
100	233	98.000	94.000	-4.000	
100	236	100.000	96.000	-4.000	
100	238	96.000	98.000	2.000	
100	243	98.000	92.000	-6.000	
105	244	98.000	96.000	-2.000	
105	246	98.000	96.000	-2.000	
105	248	100.000	98.000	-2.000	
105	374	94.000	94.000	0.000	
105	376	100.000	98.000	-2.000	
	Max	100.000	98.000	4.000	
	Average	98.075	95.700	-2.375	
	Min	94.000	90.000	-8.000	
	Std Dev	1.697	1.885	1.831	



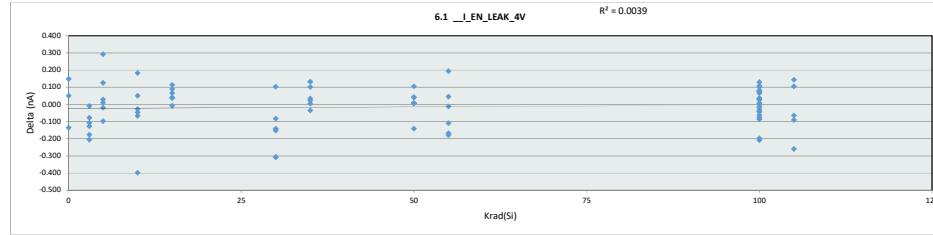
6.16_V_EN_HYS_14V											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			105	85							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.000	94.000	92.000	94.000	94.000	94.000	94.000	90.000	94.000	90.000	94.000
Average	94.667	96.000	95.667	95.667	96.000	96.667	96.667	95.000	96.333	95.083	96.400
Max	96.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000
UL	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000



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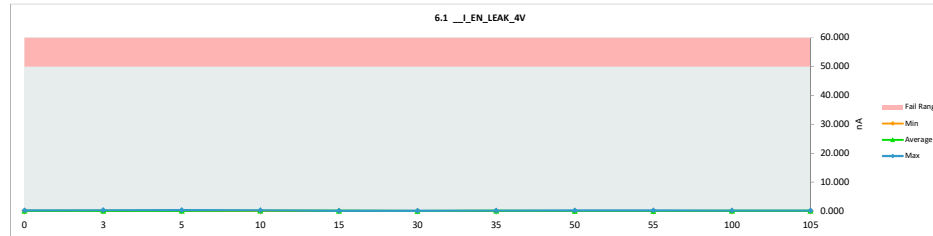
		6.1 I_EN_LEAK_4V	
Test Site			
Tester			
Test Number			
Unit	nA	nA	
Max Limit	50	50	
Min Limit			

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	0.095	0.244	0.149
0	118	0.017	0.068	0.051
0	122	0.203	0.068	-0.135
3	61	0.029	0.020	-0.009
3	62	0.156	0.029	-0.127
3	63	0.400	0.322	-0.078
3	64	0.361	0.156	-0.205
3	65	0.166	0.059	-0.107
3	66	0.264	0.088	-0.176
5	67	0.039	0.020	-0.019
5	68	0.020	0.312	0.292
5	69	0.039	0.049	0.010
5	70	0.039	0.068	0.029
5	71	0.117	0.020	-0.097
5	72	0.059	0.185	0.126
10	73	0.029	0.212	0.183
10	74	0.156	0.207	0.051
10	75	0.400	0.002	-0.398
10	76	0.361	0.295	-0.066
10	77	0.166	0.120	-0.046
10	78	0.264	0.237	-0.027
15	79	0.039	0.076	0.037
15	80	0.020	0.134	0.114
15	81	0.039	0.105	0.066
15	82	0.039	0.129	0.090
15	83	0.117	0.110	-0.007
15	84	0.059	0.100	0.041
30	86	0.029	0.132	0.103
30	89	0.156	0.073	-0.083
30	91	0.400	0.093	-0.307
30	92	0.361	0.054	-0.307
30	94	0.166	0.024	-0.142
30	95	0.264	0.112	-0.152
35	100	0.039	0.171	0.132
35	101	0.020	0.122	0.102
35	102	0.039	0.063	0.024
35	103	0.039	0.073	0.034
35	104	0.117	0.122	0.005
35	105	0.059	0.024	-0.035
50	106	0.146	0.005	-0.141
50	107	0.095	0.102	0.007
50	108	0.041	0.083	0.042
50	109	0.041	0.083	0.042
50	110	0.027	0.034	0.007
50	111	0.134	0.239	0.105
55	113	0.203	0.024	-0.179
55	114	0.222	0.054	-0.168
55	115	0.066	0.112	0.046
55	116	0.124	0.015	-0.109
55	126	0.066	0.259	0.193
55	128	0.017	0.005	-0.012
100	129	0.105	0.142	0.037
100	131	0.124	0.083	-0.041
100	132	0.183	0.210	0.027
100	133	0.154	0.112	-0.042
100	134	0.129	0.054	-0.075
100	135	0.002	0.132	0.130
100	138	0.056	0.024	-0.032
100	139	0.173	0.112	-0.061
100	140	0.027	0.102	0.075
100	141	0.203	0.005	-0.198
100	142	0.022	0.122	0.100
100	143	0.115	0.102	-0.013
100	144	0.066	0.073	0.007
100	220	0.037	0.073	0.036
100	221	0.271	0.063	-0.208
100	224	0.090	0.073	-0.017
100	225	0.090	0.093	0.003
100	226	0.105	0.181	0.076
100	228	0.012	0.122	0.110
100	232	0.149	0.063	-0.086
100	233	0.115	0.181	0.066
100	236	0.061	0.093	0.032
100	238	0.056	0.132	0.076
100	243	0.051	0.132	0.081
105	244	0.085	0.190	0.105
105	246	0.134	0.044	-0.090
105	248	0.027	0.171	0.144
105	374	0.176	0.112	-0.064
105	376	0.264	0.005	-0.259
Min		0.002	0.002	-0.398
Average		0.121	0.106	-0.014
Max		0.400	0.322	0.292
Std Dev		0.102	0.074	0.123



		6.1 I_EN_LEAK_4V	
Test Site			
Tester			
Test Number			
Max Limit	50	nA	
Min Limit		nA	

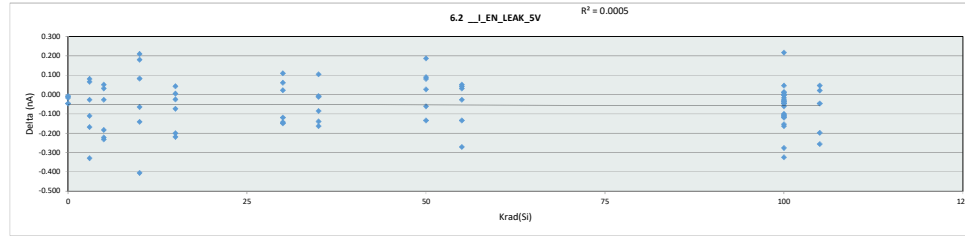
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	0.068	0.020	0.020	0.002	0.076	0.024	0.024	0.005	0.005	0.005	0.005
Average	0.127	0.112	0.109	0.179	0.109	0.081	0.096	0.091	0.078	0.103	0.104
Max	0.244	0.322	0.312	0.295	0.134	0.132	0.171	0.239	0.259	0.210	0.190
UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000



LDR TID Report
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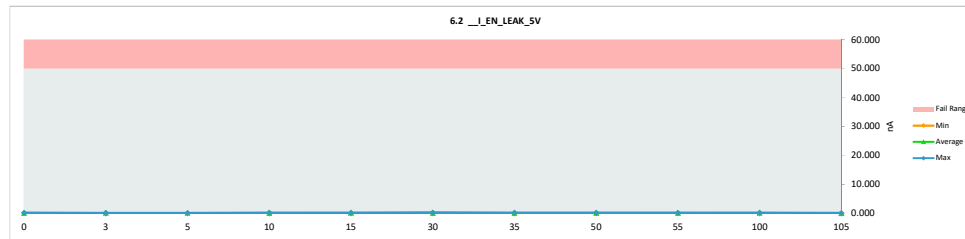
		6.2 I_EN_LEAK_SV	
Test Site			
Tester			
Test Number			
Unit	nA	nA	
Max Limit	50		
Min Limit			

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	0.098	0.090	-0.008
0	118	0.127	0.110	-0.017
0	122	0.234	0.188	-0.046
3	61	0.049	0.115	0.066
3	62	0.098	0.061	-0.027
3	63	0.185	0.017	-0.168
3	64	0.039	0.120	0.081
3	65	0.195	0.085	-0.110
3	66	0.429	0.100	-0.329
5	67	0.020	0.071	0.051
5	68	0.156	0.129	-0.027
5	69	0.224	0.002	-0.222
5	70	0.127	0.159	0.032
5	71	0.273	0.090	-0.183
5	72	0.234	0.002	-0.232
10	73	0.049	0.229	0.180
10	74	0.088	0.024	-0.064
10	75	0.185	0.268	0.083
10	76	0.039	0.249	0.210
10	77	0.195	0.054	-0.141
10	78	0.429	0.024	-0.405
15	79	0.020	0.063	0.043
15	80	0.156	0.083	-0.073
15	81	0.224	0.229	0.005
15	82	0.127	0.102	-0.025
15	83	0.273	0.073	-0.200
15	84	0.234	0.015	-0.219
30	86	0.049	0.159	0.110
30	89	0.088	0.110	0.022
30	91	0.185	0.066	-0.119
30	92	0.039	0.100	0.061
30	94	0.195	0.046	-0.149
30	95	0.429	0.286	-0.143
35	100	0.020	0.012	-0.008
35	101	0.156	0.144	-0.012
35	102	0.224	0.061	-0.163
35	103	0.127	0.232	0.105
35	104	0.273	0.134	-0.139
35	105	0.234	0.149	-0.085
50	106	0.312	0.178	-0.134
50	107	0.059	0.149	0.090
50	108	0.020	0.207	0.187
50	109	0.117	0.198	0.081
50	110	0.068	0.095	0.027
50	111	0.127	0.066	-0.061
55	113	0.020	0.071	0.051
55	114	0.273	0.139	-0.134
55	115	0.137	0.168	0.031
55	116	0.156	0.129	-0.027
55	126	0.381	0.110	-0.271
55	128	0.078	0.120	0.042
100	129	0.176	0.139	-0.037
100	131	0.224	0.115	-0.109
100	132	0.049	0.051	0.002
100	133	0.322	0.046	-0.276
100	134	0.088	0.027	-0.061
100	135	0.224	0.105	-0.119
100	138	0.137	0.100	-0.037
100	139	0.117	0.129	0.012
100	140	0.185	0.032	-0.153
100	141	0.049	0.017	-0.032
100	142	0.224	0.061	-0.163
100	143	0.010	0.056	0.046
100	144	0.068	0.022	-0.046
100	220	0.029	0.002	-0.027
100	221	0.195	0.090	-0.105
100	224	0.020	0.237	0.217
100	225	0.078	0.032	-0.046
100	226	0.078	0.061	-0.017
100	228	0.215	0.212	-0.003
100	232	0.098	0.110	0.012
100	233	0.117	0.017	-0.100
100	236	0.146	0.032	-0.114
100	238	0.049	0.046	-0.003
100	243	0.400	0.076	-0.324
105	244	0.127	0.081	-0.046
105	246	0.322	0.066	-0.256
105	248	0.029	0.076	0.047
105	374	0.020	0.041	0.021
105	376	0.254	0.056	-0.198
Max		0.429	0.286	0.217
Average		0.154	0.100	-0.054
Min		0.010	0.002	-0.405
Std Dev		0.108	0.068	0.124



		6.2 I_EN_LEAK_SV	
Test Site			
Tester			
Test Number			
Max Limit	50	nA	
Min Limit		nA	

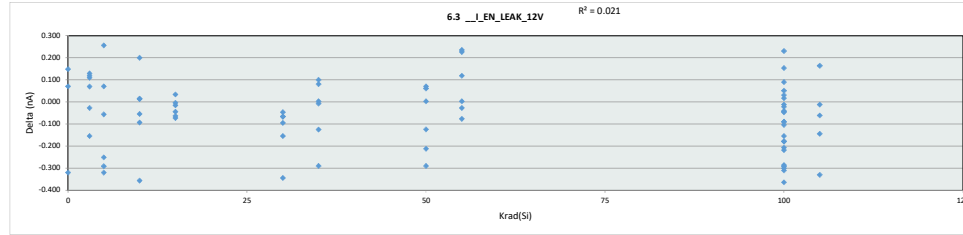
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	0.090	0.017	0.002	0.024	0.015	0.046	0.012	0.066	0.071	0.002	0.041
Average	0.129	0.083	0.076	0.141	0.094	0.128	0.122	0.149	0.123	0.076	0.064
Max	0.188	0.120	0.159	0.268	0.229	0.286	0.232	0.207	0.168	0.237	0.081
UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000



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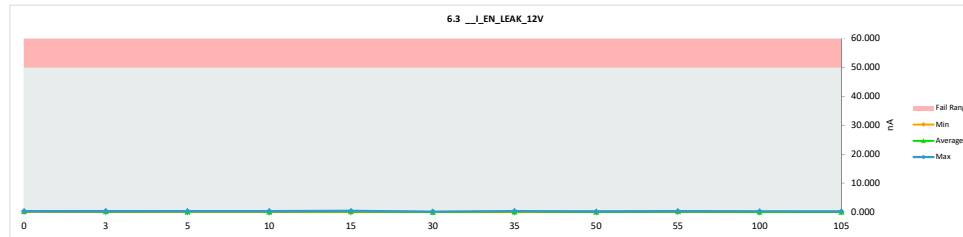
		6.3 I_EN_LEAK_12V	
Test Site			
Tester			
Test Number			
Unit	nA	nA	
Max Limit	50	50	
Min Limit			

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	0.205	0.276	0.071
0	118	0.664	0.344	-0.320
0	122	0.039	0.188	0.149
3	61	0.371	0.344	-0.027
3	62	0.166	0.295	0.129
3	63	0.127	0.246	0.119
3	64	0.137	0.207	0.070
3	65	0.342	0.188	-0.154
3	66	0.176	0.286	0.110
5	67	0.117	0.373	0.256
5	68	0.107	0.178	0.071
5	69	0.117	0.061	-0.056
5	70	0.429	0.178	-0.251
5	71	0.332	0.041	-0.291
5	72	0.449	0.129	-0.320
10	73	0.371	0.015	-0.356
10	74	0.166	0.366	0.200
10	75	0.127	0.142	0.015
10	76	0.137	0.151	0.014
10	77	0.342	0.249	-0.093
10	78	0.176	0.122	-0.054
15	79	0.117	0.044	-0.073
15	80	0.107	0.044	-0.063
15	81	0.117	0.073	-0.044
15	82	0.429	0.425	-0.004
15	83	0.332	0.366	0.034
15	84	0.449	0.434	-0.015
30	86	0.371	0.027	-0.344
30	89	0.166	0.100	-0.066
30	91	0.127	0.081	-0.046
30	92	0.137	0.071	-0.066
30	94	0.342	0.188	-0.154
30	95	0.176	0.081	-0.095
35	100	0.117	0.110	-0.007
35	101	0.107	0.207	0.100
35	102	0.117	0.198	0.081
35	103	0.429	0.432	0.003
35	104	0.332	0.207	-0.125
35	105	0.449	0.159	-0.290
50	106	0.273	0.061	-0.212
50	107	0.166	0.237	0.071
50	108	0.410	0.120	-0.290
50	109	0.234	0.110	-0.124
50	110	0.107	0.110	0.003
50	111	0.088	0.149	0.061
55	113	0.059	0.178	0.119
55	114	0.254	0.178	-0.076
55	115	0.254	0.227	-0.027
55	116	0.351	0.354	0.003
55	126	0.107	0.334	0.227
55	128	0.059	0.295	0.236
100	129	0.303	0.012	-0.291
100	131	0.303	0.124	-0.179
100	132	0.390	0.183	-0.207
100	133	0.244	0.232	-0.012
100	134	0.244	0.222	-0.022
100	135	0.166	0.076	-0.090
100	138	0.010	0.164	0.154
100	139	0.273	0.095	-0.178
100	140	0.107	0.124	0.017
100	141	0.215	0.173	-0.042
100	142	0.351	0.051	-0.300
100	143	0.469	0.251	-0.218
100	144	0.361	0.051	-0.310
100	220	0.459	0.095	-0.364
100	221	0.156	0.115	-0.041
100	224	0.117	0.027	-0.090
100	225	0.205	0.051	-0.154
100	226	0.224	0.120	-0.104
100	228	0.059	0.290	0.231
100	232	0.176	0.129	-0.047
100	233	0.371	0.085	-0.286
100	236	0.000	0.090	0.090
100	238	0.137	0.188	0.051
100	243	0.098	0.129	0.031
105	244	0.273	0.129	-0.144
105	246	0.127	0.066	-0.061
105	248	0.254	0.242	-0.012
105	374	0.342	0.012	-0.330
105	376	0.078	0.242	0.164
Max		0.664	0.434	0.256
Average		0.230	0.172	-0.058
Min		0.000	0.012	-0.364
Std Dev		0.134	0.107	0.157



		6.3 I_EN_LEAK_12V	
Test Site			
Tester			
Test Number			
Max Limit	50	nA	
Min Limit		nA	

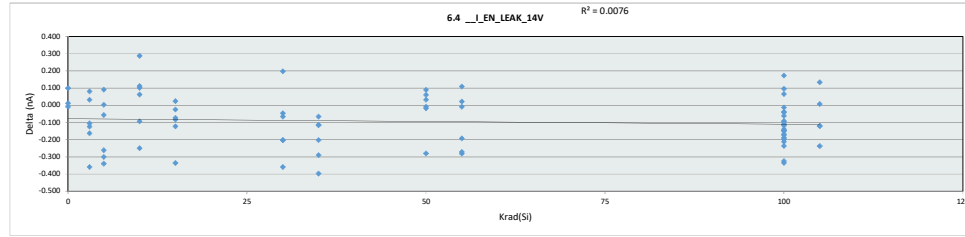
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	0.188	0.188	0.041	0.015	0.044	0.027	0.110	0.061	0.178	0.012	0.012
Average	0.269	0.261	0.160	0.174	0.231	0.091	0.219	0.131	0.261	0.128	0.138
Max	0.344	0.344	0.373	0.366	0.434	0.188	0.432	0.237	0.354	0.290	0.242
UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000



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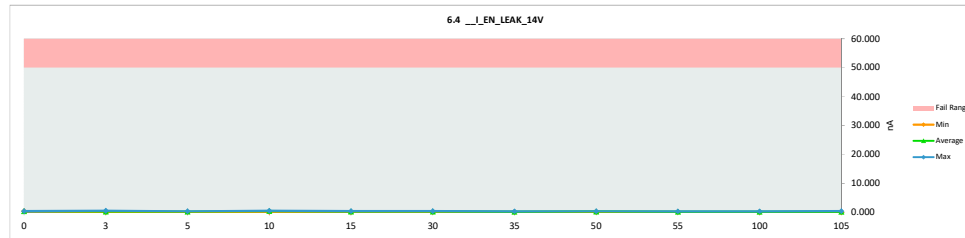
		6.4 I_EN_LEAK_14V	
Test Site			
Tester			
Test Number			
Unit	nA	nA	
Max Limit	50	50	
Min Limit			

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	0.371	0.383	0.012
0	118	0.234	0.334	0.100
0	122	0.381	0.373	-0.008
3	61	0.127	0.159	0.032
3	62	0.400	0.237	-0.163
3	63	0.449	0.530	0.081
3	64	0.459	0.100	-0.359
3	65	0.361	0.237	-0.124
3	66	0.420	0.315	-0.105
5	67	0.429	0.168	-0.261
5	68	0.595	0.256	-0.339
5	69	0.381	0.081	-0.300
5	70	0.234	0.237	0.003
5	71	0.390	0.334	-0.056
5	72	0.224	0.315	0.091
10	73	0.127	0.415	0.288
10	74	0.400	0.151	-0.249
10	75	0.449	0.356	-0.093
10	76	0.459	0.522	0.063
10	77	0.361	0.473	0.112
10	78	0.420	0.522	0.102
15	79	0.429	0.405	-0.024
15	80	0.595	0.259	-0.336
15	81	0.381	0.405	0.024
15	82	0.234	0.151	-0.083
15	83	0.390	0.268	-0.122
15	84	0.224	0.151	-0.073
30	86	0.127	0.325	0.198
30	89	0.400	0.198	-0.202
30	91	0.449	0.090	-0.359
30	92	0.459	0.393	-0.066
30	94	0.361	0.315	-0.046
30	95	0.420	0.217	-0.203
35	100	0.429	0.139	-0.290
35	101	0.595	0.198	-0.397
35	102	0.381	0.315	-0.066
35	103	0.234	0.032	-0.202
35	104	0.390	0.276	-0.114
35	105	0.224	0.110	-0.114
50	106	0.215	0.276	0.061
50	107	0.322	0.354	0.032
50	108	0.185	0.168	-0.017
50	109	0.244	0.334	0.090
50	110	0.351	0.071	-0.280
50	111	0.342	0.334	-0.008
55	113	0.303	0.325	0.022
55	114	0.332	0.139	-0.193
55	115	0.361	0.090	-0.271
55	116	0.137	0.246	0.109
55	126	0.410	0.129	-0.281
55	128	0.303	0.295	-0.008
100	129	0.137	0.203	0.066
100	131	0.303	0.290	-0.013
100	132	0.371	0.203	-0.168
100	133	0.107	0.203	0.096
100	134	0.156	0.095	-0.061
100	135	0.351	0.203	-0.148
100	138	0.410	0.300	-0.110
100	139	0.322	0.212	-0.110
100	140	0.342	0.007	-0.335
100	141	0.517	0.320	-0.197
100	142	0.234	0.022	-0.212
100	143	0.117	0.290	0.173
100	144	0.185	0.012	-0.173
100	220	0.176	0.134	-0.042
100	221	0.322	0.183	-0.139
100	224	0.439	0.203	-0.236
100	225	0.361	0.164	-0.197
100	226	0.205	0.110	-0.095
100	228	0.410	0.222	-0.188
100	232	0.098	0.061	-0.037
100	233	0.185	0.066	-0.119
100	236	0.342	0.251	-0.091
100	238	0.439	0.290	-0.149
100	243	0.429	0.105	-0.324
105	244	0.254	0.134	-0.120
105	246	0.166	0.300	0.134
105	248	0.293	0.173	-0.120
105	374	0.254	0.017	-0.237
105	376	0.390	0.398	0.008
Min		0.595	0.530	0.288
Average		0.328	0.233	-0.094
Max		0.098	0.007	-0.397
Std Dev		0.119	0.124	0.148



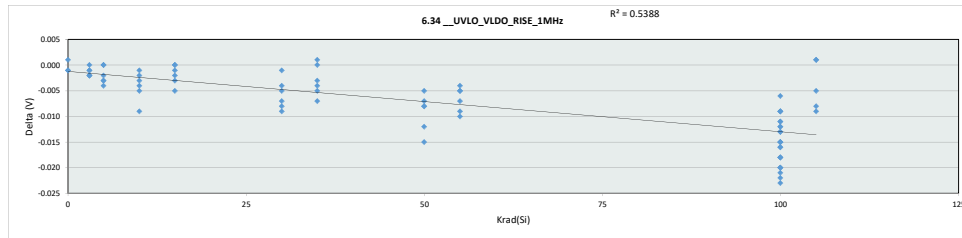
		6.4 I_EN_LEAK_14V	
Test Site			
Tester			
Test Number			
Max Limit	50	nA	
Min Limit		nA	

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	0.334	0.100	0.081	0.151	0.151	0.090	0.032	0.071	0.090	0.007	0.017
Average	0.363	0.263	0.232	0.407	0.273	0.256	0.178	0.256	0.204	0.173	0.204
Max	0.383	0.530	0.334	0.522	0.405	0.393	0.315	0.354	0.325	0.320	0.398
UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000

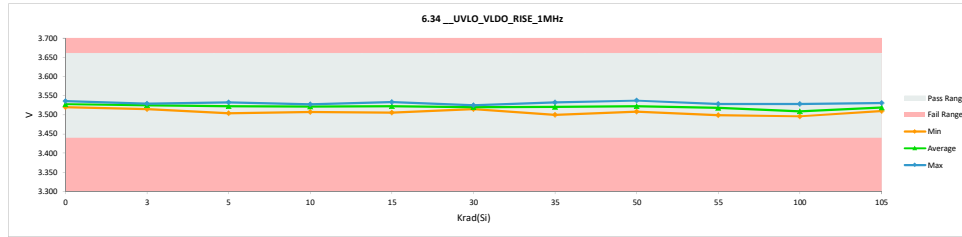


LDR TID Report
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6.34_UVLO_VLDO_RISE_1MHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.66	3.66		
Min Limit	3.44	3.44		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	3.519	3.520	0.001
0	118	3.528	3.527	-0.001
0	122	3.537	3.536	-0.001
3	61	3.525	3.523	-0.002
3	62	3.528	3.526	-0.002
3	63	3.516	3.515	-0.001
3	64	3.529	3.528	-0.001
3	65	3.528	3.526	-0.002
3	66	3.529	3.529	0.000
5	67	3.535	3.532	-0.003
5	68	3.507	3.504	-0.003
5	69	3.532	3.528	-0.004
5	70	3.524	3.524	0.000
5	71	3.523	3.523	0.000
5	72	3.523	3.521	-0.002
10	73	3.525	3.520	-0.005
10	74	3.528	3.525	-0.003
10	75	3.516	3.507	-0.009
10	76	3.529	3.525	-0.004
10	77	3.528	3.527	-0.001
10	78	3.529	3.527	-0.002
15	79	3.535	3.533	-0.002
15	80	3.507	3.506	-0.001
15	81	3.532	3.529	-0.003
15	82	3.524	3.524	0.000
15	83	3.523	3.518	-0.005
15	84	3.523	3.523	0.000
30	86	3.525	3.517	-0.008
30	89	3.528	3.521	-0.007
30	91	3.516	3.515	-0.001
30	92	3.529	3.520	-0.009
30	94	3.528	3.523	-0.005
30	95	3.529	3.525	-0.004
35	100	3.535	3.532	-0.003
35	101	3.507	3.500	-0.007
35	102	3.532	3.528	-0.004
35	103	3.524	3.525	0.001
35	104	3.523	3.518	-0.005
35	105	3.523	3.523	0.000
50	106	3.528	3.523	-0.005
50	107	3.534	3.519	-0.015
50	108	3.520	3.508	-0.012
50	109	3.539	3.531	-0.008
50	110	3.545	3.537	-0.008
50	111	3.525	3.518	-0.007
55	113	3.529	3.520	-0.009
55	114	3.504	3.499	-0.005
55	115	3.531	3.527	-0.004
55	116	3.526	3.516	-0.010
55	126	3.535	3.528	-0.007
55	128	3.524	3.519	-0.005
100	129	3.535	3.524	-0.011
100	131	3.518	3.509	-0.009
100	132	3.530	3.512	-0.018
100	133	3.521	3.509	-0.012
100	134	3.535	3.515	-0.020
100	135	3.520	3.502	-0.018
100	138	3.528	3.508	-0.020
100	139	3.527	3.512	-0.015
100	140	3.520	3.511	-0.009
100	141	3.528	3.506	-0.022
100	142	3.506	3.497	-0.009
100	143	3.526	3.511	-0.015
100	144	3.526	3.515	-0.011
100	220	3.529	3.506	-0.023
100	221	3.541	3.523	-0.018
100	224	3.513	3.497	-0.016
100	225	3.512	3.496	-0.016
100	226	3.541	3.528	-0.013
100	228	3.524	3.509	-0.015
100	232	3.525	3.504	-0.021
100	233	3.523	3.503	-0.020
100	236	3.516	3.503	-0.013
100	238	3.516	3.504	-0.012
100	243	3.513	3.507	-0.006
105	244	3.509	3.510	0.001
105	246	3.530	3.525	-0.005
105	248	3.527	3.519	-0.008
105	374	3.530	3.531	0.001
105	376	3.520	3.511	-0.009
Max		3.545	3.537	0.001
Average		3.525	3.518	-0.007
Min		3.504	3.496	-0.008
Std Dev		0.008	0.010	0.006

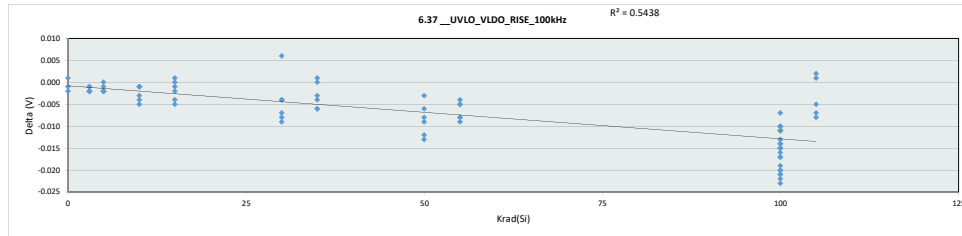


6.34_UVLO_VLDO_RISE_1MHz											
Test Site											
Tester											
Test Number											
Max Limit	3.66	V									
Min Limit	3.44	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.520	3.515	3.504	3.507	3.506	3.515	3.500	3.508	3.499	3.496	3.510
Average	3.528	3.525	3.522	3.522	3.522	3.520	3.521	3.523	3.518	3.509	3.519
Max	3.536	3.529	3.532	3.527	3.533	3.525	3.532	3.532	3.528	3.528	3.531
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660

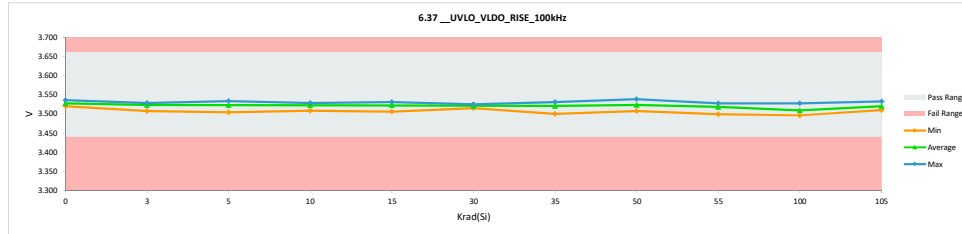


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6.37_UVLO_VLDO_RISE_100kHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.66	3.66		
Min Limit	3.44	3.44		
Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	3.519	3.520	0.001
0	118	3.527	3.526	-0.001
0	122	3.538	3.536	-0.002
3	61	3.525	3.523	-0.002
3	62	3.528	3.526	-0.002
3	63	3.509	3.507	-0.002
3	64	3.529	3.528	-0.001
3	65	3.528	3.526	-0.002
3	66	3.529	3.528	-0.001
5	67	3.535	3.533	-0.002
5	68	3.506	3.504	-0.002
5	69	3.531	3.529	-0.002
5	70	3.525	3.525	0.000
5	71	3.524	3.522	-0.002
5	72	3.522	3.521	-0.001
10	73	3.525	3.520	-0.005
10	74	3.528	3.525	-0.003
10	75	3.509	3.508	-0.001
10	76	3.529	3.525	-0.004
10	77	3.528	3.527	-0.001
10	78	3.529	3.528	-0.001
15	79	3.535	3.531	-0.004
15	80	3.506	3.506	0.000
15	81	3.531	3.529	-0.002
15	82	3.525	3.524	-0.001
15	83	3.524	3.519	-0.005
15	84	3.522	3.523	0.001
30	86	3.525	3.517	-0.008
30	89	3.528	3.521	-0.007
30	91	3.509	3.515	0.006
30	92	3.529	3.520	-0.009
30	94	3.528	3.524	-0.004
30	95	3.529	3.525	-0.004
35	100	3.535	3.531	-0.004
35	101	3.506	3.500	-0.006
35	102	3.531	3.528	-0.003
35	103	3.525	3.525	0.000
35	104	3.524	3.518	-0.006
35	105	3.522	3.523	0.001
50	106	3.527	3.524	-0.003
50	107	3.533	3.533	0.000
50	108	3.519	3.507	-0.012
50	109	3.540	3.531	-0.009
50	110	3.546	3.538	-0.008
50	111	3.524	3.518	-0.006
55	113	3.528	3.520	-0.008
55	114	3.504	3.499	-0.005
55	115	3.531	3.526	-0.005
55	116	3.525	3.516	-0.009
55	126	3.535	3.527	-0.008
55	128	3.524	3.520	-0.004
100	129	3.536	3.525	-0.011
100	131	3.519	3.509	-0.010
100	132	3.531	3.511	-0.020
100	133	3.520	3.509	-0.011
100	134	3.534	3.515	-0.019
100	135	3.520	3.503	-0.017
100	138	3.528	3.507	-0.021
100	139	3.527	3.512	-0.015
100	140	3.521	3.511	-0.010
100	141	3.528	3.505	-0.023
100	142	3.507	3.496	-0.011
100	143	3.526	3.512	-0.014
100	144	3.526	3.516	-0.010
100	220	3.529	3.507	-0.022
100	221	3.541	3.524	-0.017
100	224	3.513	3.497	-0.016
100	225	3.512	3.497	-0.015
100	226	3.542	3.527	-0.015
100	228	3.524	3.510	-0.014
100	232	3.525	3.504	-0.021
100	233	3.523	3.503	-0.020
100	236	3.516	3.503	-0.013
100	238	3.516	3.505	-0.011
100	243	3.514	3.507	-0.007
105	244	3.509	3.510	0.001
105	246	3.530	3.525	-0.005
105	248	3.527	3.519	-0.008
105	374	3.530	3.532	0.002
105	376	3.519	3.512	-0.007
Max		3.546	3.538	0.006
Average		3.525	3.518	-0.007
Min		3.504	3.496	-0.008
Std Dev		0.009	0.010	0.007

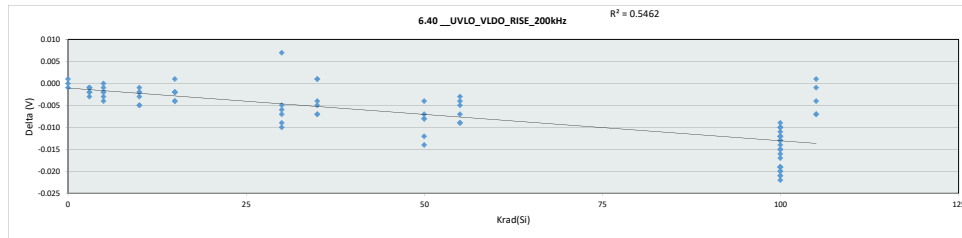


6.37_UVLO_VLDO_RISE_100kHz											
Test Site											
Tester											
Test Number											
Max Limit	3.66	V									
Min Limit	3.44	V									
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.520	3.507	3.504	3.508	3.506	3.515	3.500	3.507	3.499	3.496	3.510
Average	3.527	3.523	3.522	3.522	3.522	3.520	3.521	3.523	3.518	3.509	3.520
Max	3.536	3.528	3.533	3.528	3.531	3.525	3.531	3.531	3.538	3.527	3.532
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660

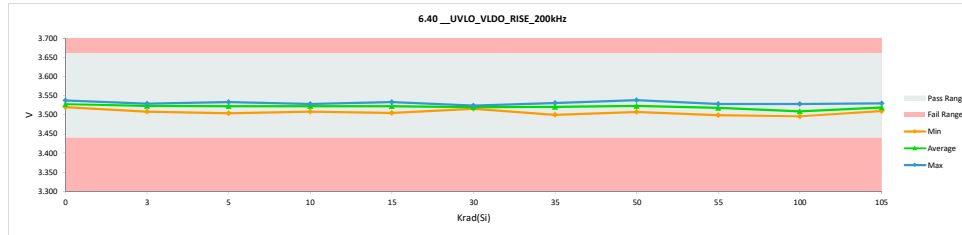


LDR TID Report
TPS7H5001-SP

6.40_UVLO_VLDO_RISE_200kHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.66	3.66		
Min Limit	3.44	3.44		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	3.519	3.520	0.001
0	118	3.527	3.526	-0.001
0	122	3.537	3.537	0.000
3	61	3.525	3.524	-0.001
3	62	3.528	3.526	-0.002
3	63	3.509	3.508	-0.001
3	64	3.530	3.527	-0.003
3	65	3.529	3.527	-0.002
3	66	3.530	3.529	-0.001
5	67	3.535	3.533	-0.002
5	68	3.507	3.504	-0.003
5	69	3.533	3.529	-0.004
5	70	3.525	3.525	0.000
5	71	3.524	3.522	-0.002
5	72	3.522	3.521	-0.001
10	73	3.525	3.520	-0.005
10	74	3.528	3.525	-0.003
10	75	3.509	3.508	-0.001
10	76	3.530	3.525	-0.005
10	77	3.529	3.527	-0.002
10	78	3.530	3.528	-0.002
15	79	3.535	3.533	-0.002
15	80	3.507	3.505	-0.002
15	81	3.533	3.529	-0.004
15	82	3.525	3.523	-0.002
15	83	3.524	3.520	-0.004
15	84	3.522	3.523	0.001
30	86	3.525	3.516	-0.009
30	89	3.528	3.521	-0.007
30	91	3.509	3.516	0.007
30	92	3.530	3.520	-0.010
30	94	3.529	3.524	-0.005
30	95	3.530	3.524	-0.006
35	100	3.535	3.531	-0.004
35	101	3.507	3.500	-0.007
35	102	3.533	3.528	-0.005
35	103	3.525	3.526	0.001
35	104	3.524	3.517	-0.007
35	105	3.522	3.523	0.001
50	106	3.528	3.524	-0.004
50	107	3.534	3.520	-0.014
50	108	3.519	3.507	-0.012
50	109	3.539	3.531	-0.008
50	110	3.546	3.538	-0.008
50	111	3.524	3.517	-0.007
55	113	3.529	3.520	-0.009
55	114	3.504	3.499	-0.005
55	115	3.531	3.527	-0.004
55	116	3.524	3.515	-0.009
55	126	3.535	3.528	-0.007
55	128	3.523	3.520	-0.003
100	129	3.536	3.524	-0.012
100	131	3.518	3.508	-0.010
100	132	3.530	3.511	-0.019
100	133	3.521	3.509	-0.012
100	134	3.534	3.515	-0.019
100	135	3.520	3.501	-0.019
100	138	3.528	3.508	-0.020
100	139	3.527	3.512	-0.015
100	140	3.521	3.512	-0.009
100	141	3.528	3.506	-0.022
100	142	3.506	3.496	-0.010
100	143	3.526	3.511	-0.015
100	144	3.526	3.516	-0.010
100	220	3.528	3.507	-0.021
100	221	3.542	3.523	-0.019
100	224	3.513	3.497	-0.016
100	225	3.513	3.496	-0.017
100	226	3.541	3.528	-0.013
100	228	3.524	3.510	-0.014
100	232	3.525	3.504	-0.021
100	233	3.523	3.503	-0.020
100	236	3.516	3.503	-0.013
100	238	3.516	3.504	-0.012
100	243	3.519	3.508	-0.011
105	244	3.509	3.510	0.001
105	246	3.530	3.526	-0.004
105	248	3.526	3.519	-0.007
105	374	3.531	3.530	-0.001
105	376	3.519	3.512	-0.007
Max		3.546	3.538	0.007
Average		3.525	3.518	-0.007
Min		3.504	3.496	-0.008
Std Dev		0.009	0.010	0.007

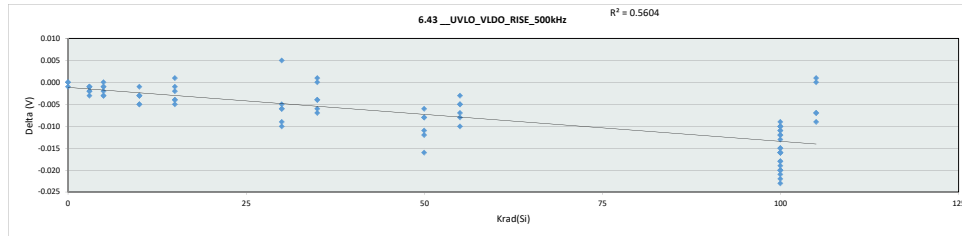


6.40_UVLO_VLDO_RISE_200k											
Test Site											
Tester											
Test Number											
Max Limit	3.66	V									
Min Limit	3.44	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.520	3.508	3.504	3.508	3.505	3.516	3.500	3.507	3.499	3.496	3.510
Average	3.528	3.524	3.522	3.522	3.522	3.520	3.521	3.523	3.518	3.509	3.519
Max	3.537	3.529	3.533	3.528	3.533	3.524	3.531	3.538	3.528	3.528	3.530
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660

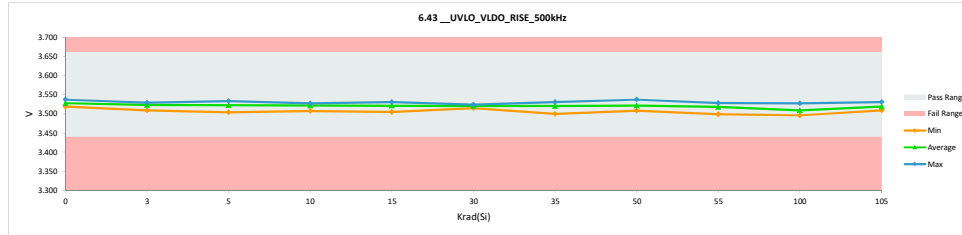


LDR TID Report
TPS7H5001-SP

6.43_UVLO_VLDO_RISE_500kHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.66	3.66		
Min Limit	3.44	3.44		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	3.519	3.519	0.000
0	118	3.528	3.527	-0.001
0	122	3.537	3.537	0.000
3	61	3.525	3.523	-0.002
3	62	3.528	3.525	-0.003
3	63	3.510	3.509	-0.001
3	64	3.530	3.529	-0.001
3	65	3.528	3.526	-0.002
3	66	3.530	3.529	-0.001
5	67	3.535	3.533	-0.002
5	68	3.507	3.504	-0.003
5	69	3.532	3.529	-0.003
5	70	3.525	3.525	0.000
5	71	3.524	3.523	-0.001
5	72	3.522	3.521	-0.001
10	73	3.525	3.520	-0.005
10	74	3.528	3.525	-0.003
10	75	3.510	3.507	-0.003
10	76	3.530	3.525	-0.005
10	77	3.528	3.527	-0.001
10	78	3.530	3.527	-0.003
15	79	3.535	3.531	-0.004
15	80	3.507	3.505	-0.002
15	81	3.532	3.528	-0.004
15	82	3.525	3.524	-0.001
15	83	3.524	3.519	-0.005
15	84	3.522	3.523	0.001
30	86	3.525	3.516	-0.009
30	89	3.528	3.522	-0.006
30	91	3.510	3.515	0.005
30	92	3.530	3.520	-0.010
30	94	3.528	3.523	-0.005
30	95	3.530	3.524	-0.006
35	100	3.535	3.531	-0.004
35	101	3.507	3.500	-0.007
35	102	3.532	3.528	-0.004
35	103	3.525	3.525	0.000
35	104	3.524	3.518	-0.006
35	105	3.522	3.523	0.001
50	106	3.528	3.516	-0.012
50	107	3.535	3.519	-0.016
50	108	3.519	3.508	-0.011
50	109	3.539	3.531	-0.008
50	110	3.545	3.537	-0.008
50	111	3.525	3.519	-0.006
55	113	3.528	3.520	-0.008
55	114	3.504	3.499	-0.005
55	115	3.530	3.527	-0.003
55	116	3.526	3.516	-0.010
55	126	3.535	3.528	-0.007
55	128	3.525	3.520	-0.005
100	129	3.536	3.524	-0.012
100	131	3.518	3.508	-0.010
100	132	3.531	3.512	-0.019
100	133	3.520	3.509	-0.011
100	134	3.535	3.515	-0.020
100	135	3.520	3.502	-0.018
100	138	3.528	3.507	-0.021
100	139	3.527	3.511	-0.016
100	140	3.521	3.512	-0.009
100	141	3.528	3.505	-0.023
100	142	3.506	3.496	-0.010
100	143	3.526	3.511	-0.015
100	144	3.527	3.516	-0.011
100	220	3.529	3.507	-0.022
100	221	3.541	3.523	-0.018
100	224	3.513	3.497	-0.016
100	225	3.512	3.496	-0.016
100	226	3.542	3.527	-0.015
100	228	3.525	3.509	-0.016
100	232	3.524	3.504	-0.020
100	233	3.524	3.504	-0.020
100	236	3.516	3.503	-0.013
100	238	3.517	3.505	-0.012
100	243	3.518	3.508	-0.010
105	244	3.509	3.509	0.000
105	246	3.532	3.525	-0.007
105	248	3.526	3.519	-0.007
105	374	3.530	3.531	0.001
105	376	3.520	3.511	-0.009
Max		3.545	3.537	0.005
Average		3.525	3.518	-0.008
Min		3.504	3.496	-0.008
Std Dev		0.009	0.010	0.007

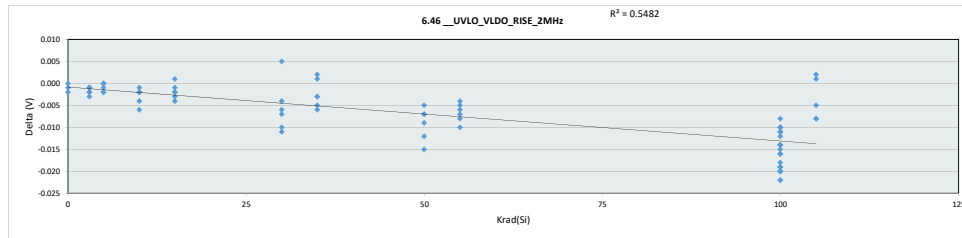


6.43_UVLO_VLDO_RISE_500kHz											
Test Site											
Tester											
Test Number											
Max Limit	3.66	V									
Min Limit	3.44	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.519	3.509	3.504	3.507	3.505	3.515	3.500	3.508	3.499	3.496	3.509
Average	3.528	3.524	3.523	3.522	3.522	3.520	3.521	3.522	3.518	3.509	3.531
Max	3.537	3.529	3.533	3.527	3.531	3.524	3.531	3.524	3.537	3.528	3.527
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660

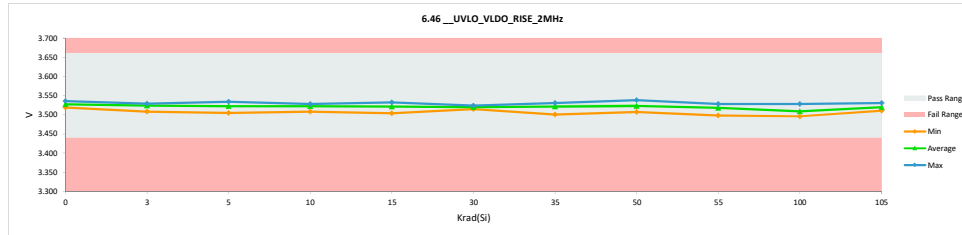


LDR TID Report
TPS7H5001-SP

6.46_UVLO_VLDO_RISE_2MHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.66	3.66		
Min Limit	3.44	3.44		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	3.520	3.519	-0.001
0	118	3.527	3.527	0.000
0	122	3.538	3.536	-0.002
3	61	3.526	3.523	-0.003
3	62	3.528	3.527	-0.001
3	63	3.510	3.508	-0.002
3	64	3.530	3.528	-0.002
3	65	3.528	3.527	-0.001
3	66	3.530	3.529	-0.001
5	67	3.534	3.534	0.000
5	68	3.507	3.505	-0.002
5	69	3.531	3.529	-0.002
5	70	3.524	3.524	0.000
5	71	3.523	3.522	-0.001
5	72	3.522	3.522	0.000
10	73	3.526	3.520	-0.006
10	74	3.528	3.526	-0.002
10	75	3.510	3.508	-0.002
10	76	3.530	3.526	-0.004
10	77	3.528	3.527	-0.001
10	78	3.530	3.528	-0.002
15	79	3.534	3.532	-0.002
15	80	3.507	3.504	-0.003
15	81	3.531	3.529	-0.002
15	82	3.524	3.523	-0.001
15	83	3.523	3.519	-0.004
15	84	3.522	3.523	0.001
30	86	3.526	3.516	-0.010
30	89	3.528	3.521	-0.007
30	91	3.510	3.515	0.005
30	92	3.530	3.519	-0.011
30	94	3.528	3.524	-0.004
30	95	3.530	3.524	-0.006
35	100	3.534	3.531	-0.003
35	101	3.507	3.501	-0.006
35	102	3.531	3.528	-0.003
35	103	3.524	3.525	0.001
35	104	3.523	3.518	-0.005
35	105	3.522	3.524	0.002
50	106	3.528	3.523	-0.005
50	107	3.534	3.534	0.000
50	108	3.519	3.507	-0.012
50	109	3.540	3.531	-0.009
50	110	3.545	3.538	-0.007
50	111	3.526	3.519	-0.007
55	113	3.529	3.521	-0.008
55	114	3.504	3.498	-0.006
55	115	3.531	3.527	-0.004
55	116	3.526	3.516	-0.010
55	126	3.535	3.528	-0.007
55	128	3.524	3.519	-0.005
100	129	3.535	3.524	-0.011
100	131	3.517	3.509	-0.008
100	132	3.531	3.511	-0.020
100	133	3.521	3.510	-0.011
100	134	3.534	3.515	-0.019
100	135	3.520	3.502	-0.018
100	138	3.527	3.507	-0.020
100	139	3.526	3.512	-0.014
100	140	3.522	3.511	-0.011
100	141	3.528	3.506	-0.022
100	142	3.507	3.497	-0.010
100	143	3.526	3.510	-0.016
100	144	3.526	3.516	-0.010
100	220	3.529	3.507	-0.022
100	221	3.541	3.522	-0.019
100	224	3.514	3.498	-0.016
100	225	3.512	3.496	-0.016
100	226	3.542	3.528	-0.014
100	228	3.524	3.509	-0.015
100	232	3.525	3.505	-0.020
100	233	3.523	3.503	-0.020
100	236	3.516	3.502	-0.014
100	238	3.515	3.505	-0.010
100	243	3.520	3.508	-0.012
105	244	3.509	3.511	0.002
105	246	3.531	3.526	-0.005
105	248	3.527	3.519	-0.008
105	374	3.530	3.531	0.001
105	376	3.519	3.511	-0.008
Max		3.545	3.538	0.005
Average		3.525	3.518	-0.007
Min		3.504	3.496	-0.008
Std Dev		0.009	0.010	0.007

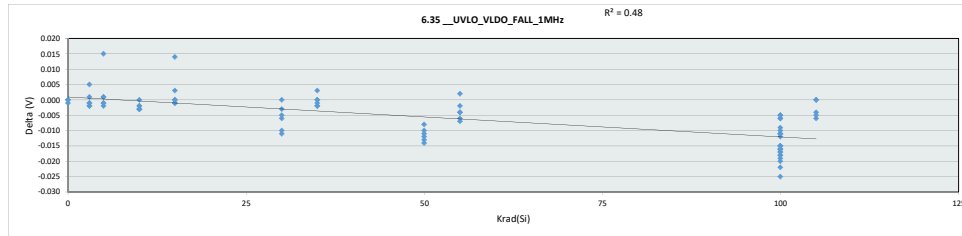


6.46_UVLO_VLDO_RISE_2MHz											
Test Site											
Tester											
Test Number											
Max Limit	3.66	V									
Min Limit	3.44	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.519	3.508	3.505	3.508	3.504	3.515	3.501	3.507	3.498	3.496	3.511
Average	3.527	3.524	3.523	3.523	3.522	3.520	3.521	3.523	3.518	3.509	3.520
Max	3.536	3.529	3.534	3.528	3.532	3.524	3.531	3.538	3.528	3.528	3.531
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660

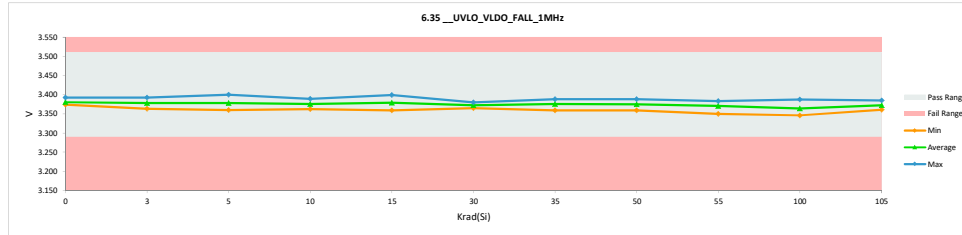


LDR TID Report
TPS7H5001-SP

6.35_UVLO_VLDO_FALL_1MHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.51	3.51		
Min Limit	3.29	3.29		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	3.374	3.374	0.000
0	118	3.375	3.375	0.000
0	122	3.393	3.392	-0.001
3	61	3.373	3.378	0.005
3	62	3.377	3.376	-0.001
3	63	3.365	3.363	-0.002
3	64	3.381	3.379	-0.002
3	65	3.391	3.392	0.001
3	66	3.380	3.379	-0.001
5	67	3.385	3.400	0.015
5	68	3.359	3.360	0.001
5	69	3.390	3.388	-0.002
5	70	3.375	3.376	0.001
5	71	3.378	3.377	-0.001
5	72	3.370	3.369	-0.001
10	73	3.373	3.373	0.000
10	74	3.377	3.374	-0.003
10	75	3.365	3.362	-0.003
10	76	3.381	3.378	-0.003
10	77	3.391	3.389	-0.002
10	78	3.380	3.378	-0.002
15	79	3.385	3.399	0.014
15	80	3.359	3.359	0.000
15	81	3.390	3.389	-0.001
15	82	3.375	3.375	0.000
15	83	3.378	3.377	-0.001
15	84	3.370	3.373	0.003
30	86	3.373	3.367	-0.006
30	89	3.377	3.372	-0.005
30	91	3.365	3.365	0.000
30	92	3.381	3.371	-0.010
30	94	3.391	3.380	-0.011
30	95	3.380	3.377	-0.003
35	100	3.385	3.383	-0.002
35	101	3.359	3.359	0.000
35	102	3.390	3.388	-0.002
35	103	3.375	3.375	0.000
35	104	3.378	3.377	-0.001
35	105	3.370	3.373	0.003
50	106	3.391	3.377	-0.014
50	107	3.389	3.379	-0.010
50	108	3.371	3.359	-0.012
50	109	3.390	3.379	-0.011
50	110	3.396	3.388	-0.008
50	111	3.382	3.369	-0.013
55	113	3.377	3.375	-0.002
55	114	3.354	3.350	-0.004
55	115	3.379	3.381	0.002
55	116	3.373	3.367	-0.006
55	126	3.390	3.383	-0.007
55	128	3.372	3.368	-0.004
100	129	3.383	3.374	-0.009
100	131	3.381	3.359	-0.022
100	132	3.384	3.367	-0.017
100	133	3.380	3.374	-0.006
100	134	3.381	3.364	-0.017
100	135	3.375	3.360	-0.015
100	138	3.378	3.363	-0.015
100	139	3.390	3.372	-0.018
100	140	3.370	3.365	-0.005
100	141	3.377	3.357	-0.020
100	142	3.371	3.346	-0.025
100	143	3.375	3.360	-0.015
100	144	3.374	3.368	-0.006
100	220	3.379	3.369	-0.010
100	221	3.389	3.373	-0.016
100	224	3.367	3.348	-0.019
100	225	3.363	3.352	-0.011
100	226	3.392	3.387	-0.005
100	228	3.383	3.367	-0.016
100	232	3.378	3.360	-0.018
100	233	3.373	3.362	-0.011
100	236	3.385	3.369	-0.016
100	238	3.370	3.358	-0.012
100	243	3.368	3.357	-0.011
105	244	3.361	3.361	0.000
105	246	3.380	3.376	-0.004
105	248	3.375	3.370	-0.005
105	374	3.385	3.385	0.000
105	376	3.376	3.370	-0.006
Max		3.396	3.400	0.015
Average		3.378	3.372	-0.006
Min		3.354	3.346	-0.008
Std Dev		0.009	0.011	0.008

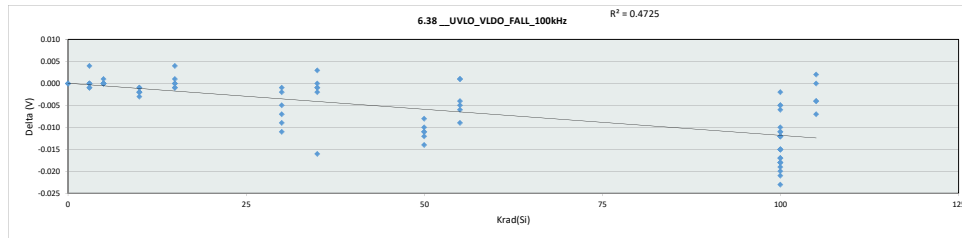


6.35_UVLO_VLDO_FALL_1MHz											
Test Site											
Tester											
Test Number											
Max Limit	3.51	V									
Min Limit	3.29	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.374	3.363	3.360	3.362	3.359	3.365	3.359	3.359	3.350	3.346	3.361
Average	3.380	3.378	3.378	3.376	3.379	3.372	3.376	3.375	3.371	3.364	3.372
Max	3.392	3.392	3.400	3.389	3.399	3.380	3.388	3.388	3.383	3.387	3.385
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510

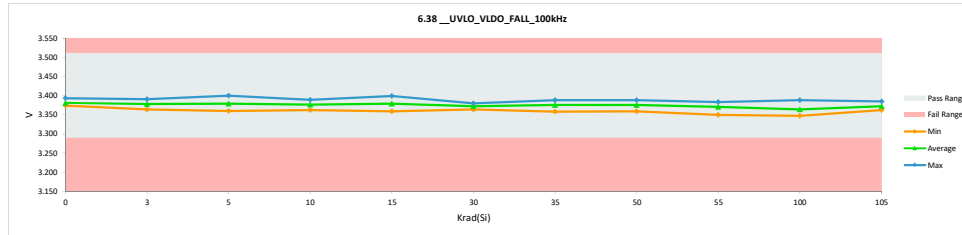


LDR TID Report
TPS7H5001-SP

6.38_UVLO_VLDO_FALL_100kHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.51	3.51		
Min Limit	3.29	3.29		
Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	3.374	3.374	0.000
0	118	3.376	3.376	0.000
0	122	3.393	3.393	0.000
3	61	3.374	3.378	0.004
3	62	3.377	3.377	0.000
3	63	3.365	3.364	-0.001
3	64	3.381	3.380	-0.001
3	65	3.391	3.391	0.000
3	66	3.380	3.380	0.000
5	67	3.400	3.400	0.000
5	68	3.359	3.360	0.001
5	69	3.389	3.389	0.000
5	70	3.375	3.375	0.000
5	71	3.378	3.378	0.000
5	72	3.370	3.370	0.000
10	73	3.374	3.373	-0.001
10	74	3.377	3.375	-0.002
10	75	3.365	3.362	-0.003
10	76	3.381	3.379	-0.002
10	77	3.391	3.389	-0.002
10	78	3.380	3.379	-0.001
15	79	3.400	3.399	-0.001
15	80	3.359	3.359	0.000
15	81	3.389	3.390	0.001
15	82	3.375	3.374	-0.001
15	83	3.378	3.378	0.000
15	84	3.370	3.374	0.004
30	86	3.374	3.367	-0.007
30	89	3.377	3.372	-0.005
30	91	3.365	3.364	-0.001
30	92	3.381	3.372	-0.009
30	94	3.391	3.380	-0.011
30	95	3.380	3.378	-0.002
35	100	3.400	3.384	-0.016
35	101	3.359	3.358	-0.001
35	102	3.389	3.388	-0.001
35	103	3.375	3.375	0.000
35	104	3.378	3.376	-0.002
35	105	3.370	3.373	0.003
50	106	3.391	3.377	-0.014
50	107	3.390	3.379	-0.011
50	108	3.370	3.359	-0.011
50	109	3.390	3.380	-0.010
50	110	3.396	3.388	-0.008
50	111	3.382	3.370	-0.012
55	113	3.376	3.377	0.001
55	114	3.355	3.350	-0.005
55	115	3.379	3.380	0.001
55	116	3.373	3.367	-0.006
55	126	3.392	3.383	-0.009
55	128	3.372	3.368	-0.004
100	129	3.384	3.373	-0.011
100	131	3.381	3.360	-0.021
100	132	3.385	3.368	-0.017
100	133	3.380	3.374	-0.006
100	134	3.382	3.364	-0.018
100	135	3.375	3.360	-0.015
100	138	3.378	3.363	-0.015
100	139	3.391	3.372	-0.019
100	140	3.369	3.367	-0.002
100	141	3.378	3.358	-0.020
100	142	3.370	3.347	-0.023
100	143	3.373	3.361	-0.012
100	144	3.373	3.368	-0.005
100	220	3.380	3.368	-0.012
100	221	3.389	3.374	-0.015
100	224	3.366	3.348	-0.018
100	225	3.364	3.352	-0.012
100	226	3.393	3.388	-0.005
100	228	3.384	3.367	-0.017
100	232	3.378	3.360	-0.018
100	233	3.373	3.363	-0.010
100	236	3.384	3.369	-0.015
100	238	3.370	3.358	-0.012
100	243	3.368	3.357	-0.011
105	244	3.360	3.362	0.002
105	246	3.380	3.376	-0.004
105	248	3.375	3.371	-0.004
105	374	3.385	3.385	0.000
105	376	3.376	3.369	-0.007
Max		3.400	3.400	0.004
Average		3.378	3.372	-0.006
Min		3.355	3.347	-0.008
Std Dev		0.010	0.011	0.007

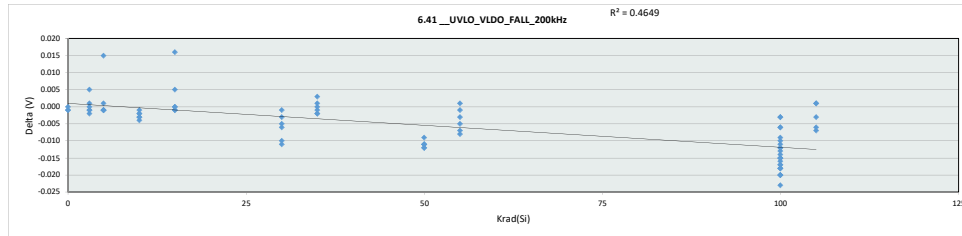


6.38_UVLO_VLDO_FALL_100k											
Test Site											
Tester											
Test Number											
Max Limit	3.51	V									
Min Limit	3.29	V									
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.374	3.364	3.360	3.362	3.359	3.364	3.358	3.359	3.350	3.347	3.362
Average	3.381	3.378	3.379	3.376	3.379	3.372	3.376	3.376	3.371	3.364	3.373
Max	3.393	3.391	3.400	3.389	3.399	3.380	3.388	3.388	3.383	3.388	3.385
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510

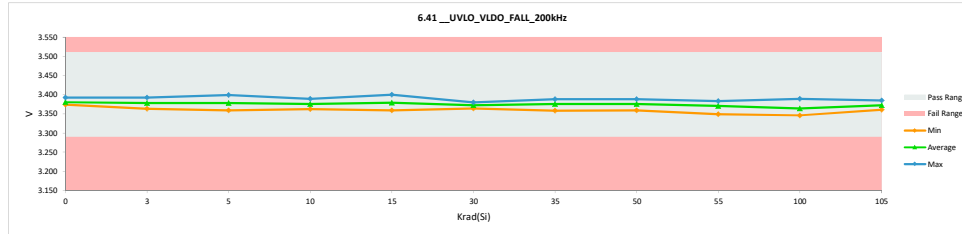


LDR TID Report
TPS7H5001-SP

6.41 UVLO_VLDO_FALL_200kHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.51	3.51		
Min Limit	3.29	3.29		
Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	3.374	3.374	0.000
0	118	3.376	3.375	-0.001
0	122	3.393	3.392	-0.001
3	61	3.374	3.379	0.005
3	62	3.378	3.377	-0.001
3	63	3.365	3.363	-0.002
3	64	3.381	3.380	-0.001
3	65	3.391	3.392	0.001
3	66	3.380	3.380	0.000
5	67	3.384	3.399	0.015
5	68	3.360	3.359	-0.001
5	69	3.389	3.388	-0.001
5	70	3.375	3.374	-0.001
5	71	3.378	3.377	-0.001
5	72	3.370	3.371	0.001
10	73	3.374	3.372	-0.002
10	74	3.378	3.374	-0.004
10	75	3.365	3.362	-0.003
10	76	3.381	3.378	-0.003
10	77	3.391	3.389	-0.002
10	78	3.380	3.379	-0.001
15	79	3.384	3.400	0.016
15	80	3.360	3.359	-0.001
15	81	3.389	3.389	0.000
15	82	3.375	3.374	-0.001
15	83	3.378	3.378	0.000
15	84	3.370	3.375	0.005
30	86	3.374	3.368	-0.006
30	89	3.378	3.373	-0.005
30	91	3.365	3.364	-0.001
30	92	3.381	3.371	-0.010
30	94	3.391	3.380	-0.011
30	95	3.380	3.377	-0.003
35	100	3.384	3.384	0.000
35	101	3.360	3.358	-0.002
35	102	3.389	3.388	-0.001
35	103	3.375	3.376	0.001
35	104	3.378	3.376	-0.002
35	105	3.370	3.373	0.003
50	106	3.390	3.378	-0.012
50	107	3.390	3.379	-0.011
50	108	3.371	3.359	-0.012
50	109	3.390	3.379	-0.011
50	110	3.397	3.388	-0.009
50	111	3.381	3.370	-0.011
55	113	3.377	3.376	-0.001
55	114	3.354	3.349	-0.005
55	115	3.379	3.380	0.001
55	116	3.373	3.366	-0.007
55	126	3.391	3.383	-0.008
55	128	3.372	3.369	-0.003
100	129	3.384	3.375	-0.009
100	131	3.383	3.360	-0.023
100	132	3.385	3.367	-0.018
100	133	3.380	3.374	-0.006
100	134	3.381	3.364	-0.017
100	135	3.375	3.360	-0.015
100	138	3.378	3.363	-0.015
100	139	3.391	3.373	-0.018
100	140	3.369	3.366	-0.003
100	141	3.378	3.358	-0.020
100	142	3.366	3.346	-0.020
100	143	3.375	3.360	-0.015
100	144	3.374	3.368	-0.006
100	220	3.379	3.369	-0.010
100	221	3.387	3.373	-0.014
100	224	3.366	3.346	-0.020
100	225	3.364	3.351	-0.013
100	226	3.392	3.389	-0.003
100	228	3.384	3.367	-0.017
100	232	3.378	3.360	-0.018
100	233	3.374	3.362	-0.012
100	236	3.385	3.369	-0.016
100	238	3.369	3.357	-0.012
100	243	3.368	3.357	-0.011
105	244	3.360	3.361	0.001
105	246	3.380	3.377	-0.003
105	248	3.376	3.370	-0.006
105	374	3.384	3.385	0.001
105	376	3.376	3.369	-0.007
	Max	3.397	3.400	0.016
	Average	3.378	3.372	-0.006
	Min	3.354	3.346	-0.008
	Std Dev	0.009	0.011	0.008

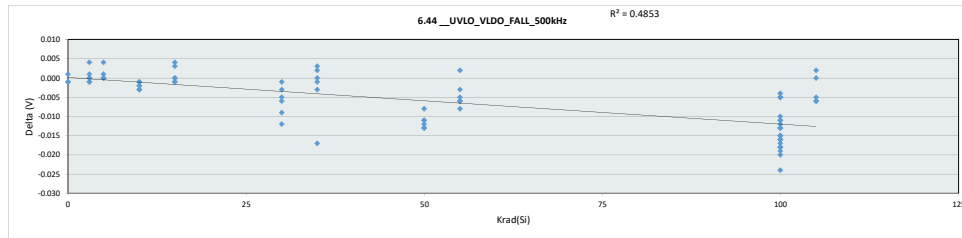


6.41 UVLO_VLDO_FALL_200k											
Test Site											
Tester											
Test Number											
Max Limit	3.51	V									
Min Limit	3.29	V									
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.374	3.363	3.359	3.362	3.359	3.364	3.358	3.359	3.349	3.346	3.361
Average	3.380	3.379	3.378	3.376	3.379	3.372	3.376	3.376	3.371	3.364	3.372
Max	3.392	3.392	3.399	3.389	3.400	3.380	3.388	3.388	3.383	3.389	3.385
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510

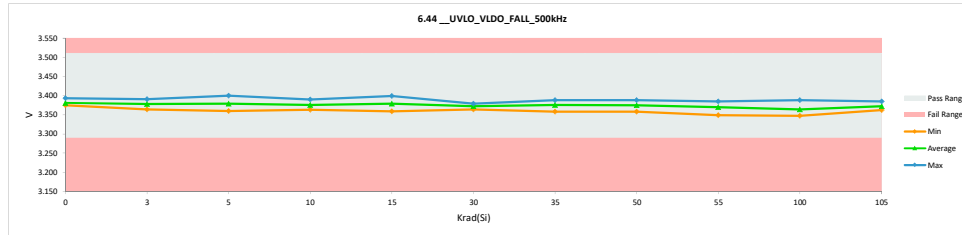


LDR TID Report
TPS7H5001-SP

6.44_UVLO_VLDO_FALL_500kHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.51	3.51		
Min Limit	3.29	3.29		
Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	3.374	3.375	0.001
0	118	3.376	3.375	-0.001
0	122	3.394	3.393	-0.001
3	61	3.374	3.378	0.004
3	62	3.377	3.378	0.001
3	63	3.365	3.364	-0.001
3	64	3.381	3.380	-0.001
3	65	3.391	3.391	0.000
3	66	3.380	3.380	0.000
5	67	3.400	3.400	0.000
5	68	3.359	3.360	0.001
5	69	3.385	3.389	0.004
5	70	3.375	3.375	0.000
5	71	3.378	3.378	0.000
5	72	3.371	3.371	0.000
10	73	3.374	3.373	-0.001
10	74	3.377	3.374	-0.003
10	75	3.365	3.363	-0.002
10	76	3.381	3.378	-0.003
10	77	3.391	3.390	-0.001
10	78	3.380	3.378	-0.002
15	79	3.400	3.399	-0.001
15	80	3.359	3.359	0.000
15	81	3.385	3.388	0.003
15	82	3.375	3.375	0.000
15	83	3.378	3.377	-0.001
15	84	3.371	3.375	0.004
30	86	3.374	3.368	-0.006
30	89	3.377	3.372	-0.005
30	91	3.365	3.364	-0.001
30	92	3.381	3.372	-0.009
30	94	3.391	3.379	-0.012
30	95	3.380	3.377	-0.003
35	100	3.400	3.383	-0.017
35	101	3.359	3.358	-0.001
35	102	3.385	3.388	0.003
35	103	3.375	3.375	0.000
35	104	3.378	3.375	-0.003
35	105	3.371	3.373	0.002
50	106	3.391	3.378	-0.013
50	107	3.390	3.379	-0.011
50	108	3.371	3.358	-0.013
50	109	3.390	3.379	-0.011
50	110	3.396	3.388	-0.008
50	111	3.381	3.369	-0.012
55	113	3.376	3.371	-0.005
55	114	3.355	3.349	-0.006
55	115	3.379	3.381	0.002
55	116	3.374	3.366	-0.008
55	126	3.391	3.385	-0.006
55	128	3.372	3.369	-0.003
100	129	3.384	3.373	-0.011
100	131	3.383	3.359	-0.024
100	132	3.384	3.367	-0.017
100	133	3.379	3.374	-0.005
100	134	3.381	3.365	-0.016
100	135	3.375	3.360	-0.015
100	138	3.378	3.363	-0.015
100	139	3.390	3.372	-0.018
100	140	3.369	3.365	-0.004
100	141	3.377	3.357	-0.020
100	142	3.365	3.347	-0.018
100	143	3.374	3.361	-0.013
100	144	3.374	3.369	-0.005
100	220	3.380	3.369	-0.011
100	221	3.389	3.373	-0.016
100	224	3.367	3.348	-0.019
100	225	3.364	3.351	-0.013
100	226	3.393	3.388	-0.005
100	228	3.383	3.367	-0.016
100	232	3.378	3.360	-0.018
100	233	3.374	3.361	-0.013
100	236	3.384	3.369	-0.015
100	238	3.370	3.358	-0.012
100	243	3.367	3.357	-0.010
105	244	3.360	3.362	0.002
105	246	3.381	3.376	-0.005
105	248	3.376	3.370	-0.006
105	374	3.385	3.385	0.000
105	376	3.376	3.370	-0.006
Max	3.400	3.400	0.004	
Average	3.378	3.372	-0.006	
Min	3.355	3.347	-0.024	
Std Dev	0.010	0.011	0.007	

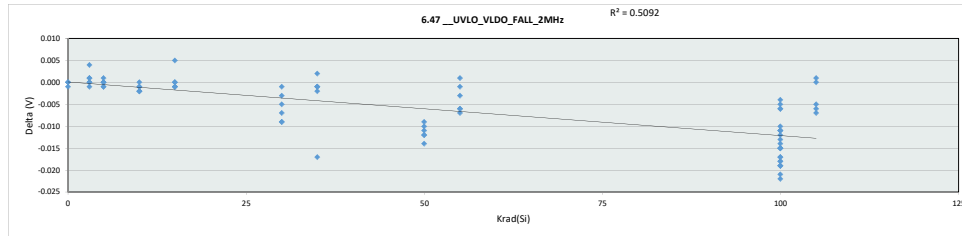


6.44_UVLO_VLDO_FALL_500k											
Test Site											
Tester											
Test Number											
Max Limit	3.51	V									
Min Limit	3.29	V									
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.375	3.364	3.360	3.363	3.359	3.364	3.358	3.358	3.349	3.347	3.362
Average	3.381	3.379	3.379	3.376	3.379	3.372	3.375	3.375	3.370	3.364	3.373
Max	3.393	3.391	3.400	3.390	3.399	3.379	3.388	3.385	3.385	3.388	3.385
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510

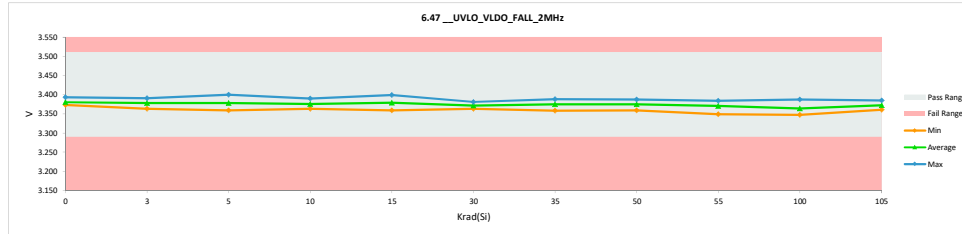


LDR TID Report
TPS7H5001-SP

6.47_UVLO_VLDO_FALL_2MHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.51	3.51		
Min Limit	3.29	3.29		
Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	3.374	3.373	-0.001
0	118	3.375	3.375	0.000
0	122	3.393	3.393	0.000
3	61	3.374	3.378	0.004
3	62	3.376	3.377	0.001
3	63	3.364	3.363	-0.001
3	64	3.380	3.380	0.000
3	65	3.390	3.391	0.001
3	66	3.380	3.380	0.000
5	67	3.400	3.400	0.000
5	68	3.359	3.359	0.000
5	69	3.389	3.388	-0.001
5	70	3.376	3.375	-0.001
5	71	3.377	3.378	0.001
5	72	3.370	3.370	0.000
10	73	3.374	3.372	-0.002
10	74	3.376	3.375	-0.001
10	75	3.364	3.363	-0.001
10	76	3.380	3.378	-0.002
10	77	3.390	3.390	0.000
10	78	3.380	3.378	-0.002
15	79	3.400	3.399	-0.001
15	80	3.359	3.359	0.000
15	81	3.389	3.388	-0.001
15	82	3.376	3.375	-0.001
15	83	3.377	3.377	0.000
15	84	3.370	3.375	0.005
30	86	3.374	3.367	-0.007
30	89	3.376	3.371	-0.005
30	91	3.364	3.363	-0.001
30	92	3.380	3.371	-0.009
30	94	3.390	3.381	-0.009
30	95	3.380	3.377	-0.003
35	100	3.400	3.383	-0.017
35	101	3.359	3.358	-0.001
35	102	3.389	3.388	-0.001
35	103	3.376	3.374	-0.002
35	104	3.377	3.376	-0.001
35	105	3.370	3.372	0.002
50	106	3.391	3.377	-0.014
50	107	3.390	3.380	-0.010
50	108	3.371	3.359	-0.012
50	109	3.389	3.378	-0.011
50	110	3.396	3.387	-0.009
50	111	3.381	3.369	-0.012
55	113	3.377	3.376	-0.001
55	114	3.355	3.349	-0.006
55	115	3.379	3.380	0.001
55	116	3.373	3.367	-0.006
55	126	3.391	3.384	-0.007
55	128	3.371	3.368	-0.003
100	129	3.383	3.373	-0.010
100	131	3.382	3.360	-0.022
100	132	3.386	3.367	-0.019
100	133	3.380	3.374	-0.006
100	134	3.381	3.364	-0.017
100	135	3.375	3.360	-0.015
100	138	3.378	3.363	-0.015
100	139	3.390	3.371	-0.019
100	140	3.370	3.366	-0.004
100	141	3.378	3.357	-0.021
100	142	3.366	3.347	-0.019
100	143	3.374	3.360	-0.014
100	144	3.374	3.369	-0.005
100	220	3.379	3.368	-0.011
100	221	3.388	3.373	-0.015
100	224	3.365	3.347	-0.018
100	225	3.364	3.351	-0.013
100	226	3.393	3.387	-0.006
100	228	3.383	3.366	-0.017
100	232	3.378	3.360	-0.018
100	233	3.374	3.363	-0.011
100	236	3.385	3.370	-0.015
100	238	3.370	3.358	-0.012
100	243	3.368	3.357	-0.011
105	244	3.360	3.361	0.001
105	246	3.381	3.376	-0.005
105	248	3.376	3.370	-0.006
105	374	3.385	3.385	0.000
105	376	3.376	3.369	-0.007
	Max	3.400	3.400	0.005
	Average	3.378	3.372	-0.006
	Min	3.355	3.347	-0.008
	Std Dev	0.010	0.011	0.007

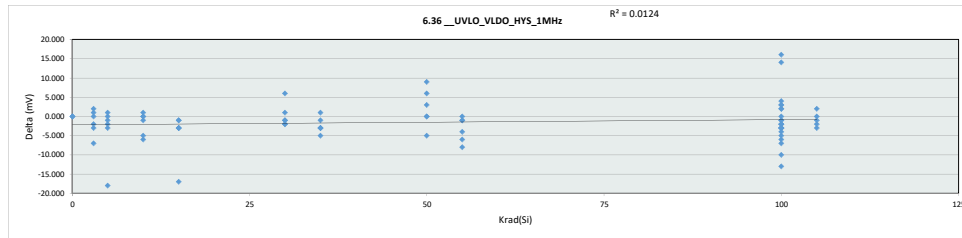


6.47_UVLO_VLDO_FALL_2MHz											
Test Site											
Tester											
Test Number											
Max Limit	3.51	V									
Min Limit	3.29	V									
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.373	3.363	3.359	3.363	3.359	3.363	3.358	3.359	3.349	3.347	3.361
Average	3.380	3.378	3.378	3.376	3.379	3.372	3.375	3.375	3.371	3.364	3.372
Max	3.393	3.391	3.400	3.390	3.399	3.381	3.388	3.387	3.384	3.387	3.385
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510

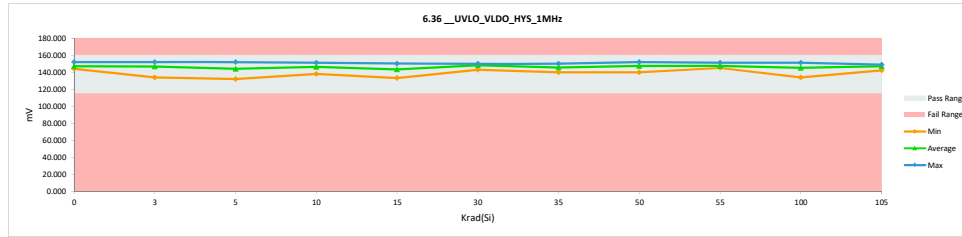


LDR TID Report
TPS7H5001-SP

6.36_UVLO_VLDO_HYS_1MHz				
Test Site				
Tester				
Test Number				
Unit	mV		mV	
Max Limit	160		160	
Min Limit	115		115	
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	145.000	145.000	0.000
0	118	152.000	152.000	0.000
0	122	144.000	144.000	0.000
3	61	152.000	145.000	-7.000
3	62	151.000	149.000	-2.000
3	63	151.000	152.000	1.000
3	64	148.000	150.000	2.000
3	65	137.000	134.000	-3.000
3	66	149.000	149.000	0.000
5	67	150.000	132.000	-18.000
5	68	147.000	145.000	-2.000
5	69	143.000	140.000	-3.000
5	70	149.000	149.000	0.000
5	71	145.000	146.000	1.000
5	72	153.000	152.000	-1.000
10	73	152.000	147.000	-5.000
10	74	151.000	151.000	0.000
10	75	151.000	145.000	-6.000
10	76	148.000	147.000	-1.000
10	77	137.000	138.000	1.000
10	78	149.000	149.000	0.000
15	79	150.000	133.000	-17.000
15	80	147.000	146.000	-1.000
15	81	143.000	140.000	-3.000
15	82	149.000	148.000	-1.000
15	83	145.000	142.000	-3.000
15	84	153.000	150.000	-3.000
30	86	152.000	150.000	-2.000
30	89	151.000	149.000	-2.000
30	91	151.000	150.000	-1.000
30	92	148.000	149.000	1.000
30	94	137.000	143.000	6.000
30	95	149.000	148.000	-1.000
35	100	150.000	149.000	-1.000
35	101	147.000	142.000	-5.000
35	102	143.000	140.000	-3.000
35	103	149.000	150.000	1.000
35	104	145.000	142.000	-3.000
35	105	153.000	150.000	-3.000
50	106	137.000	146.000	9.000
50	107	145.000	140.000	-5.000
50	108	149.000	149.000	0.000
50	109	149.000	152.000	3.000
50	110	149.000	149.000	0.000
50	111	143.000	149.000	6.000
55	113	153.000	145.000	-8.000
55	114	150.000	149.000	-1.000
55	115	152.000	146.000	-6.000
55	116	153.000	149.000	-4.000
55	126	145.000	145.000	0.000
55	128	152.000	151.000	-1.000
100	129	152.000	151.000	-1.000
100	131	136.000	150.000	14.000
100	132	147.000	145.000	-2.000
100	133	140.000	135.000	-5.000
100	134	154.000	151.000	-3.000
100	135	145.000	142.000	-3.000
100	138	149.000	145.000	-4.000
100	139	137.000	140.000	3.000
100	140	150.000	147.000	-3.000
100	141	151.000	149.000	-2.000
100	142	135.000	151.000	16.000
100	143	152.000	151.000	-1.000
100	144	153.000	147.000	-6.000
100	220	150.000	137.000	-13.000
100	221	152.000	150.000	-2.000
100	224	147.000	149.000	2.000
100	225	148.000	145.000	-3.000
100	226	149.000	142.000	-7.000
100	228	141.000	143.000	2.000
100	232	147.000	144.000	-3.000
100	233	150.000	140.000	-10.000
100	236	131.000	134.000	3.000
100	238	146.000	146.000	0.000
100	243	146.000	150.000	4.000
105	244	149.000	149.000	0.000
105	246	150.000	149.000	-1.000
105	248	152.000	149.000	-3.000
105	374	145.000	147.000	2.000
105	376	144.000	142.000	-2.000
	Max	154.000	152.000	16.000
	Average	147.388	145.900	-1.488
	Min	131.000	132.000	-18.000
	Std Dev	4.982	4.864	4.963

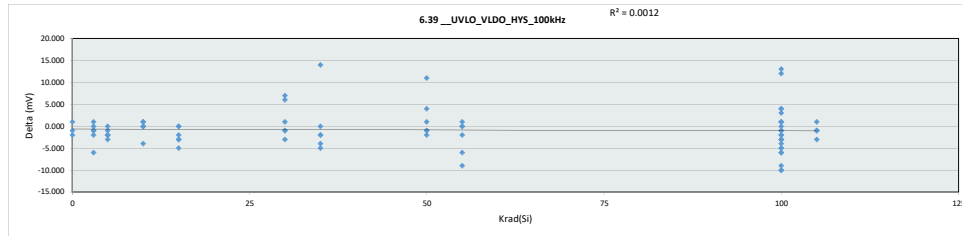


6.36_UVLO_VLDO_HYS_1MHz											
Test Site											
Tester											
Test Number											
Max Limit	160		mV								
Min Limit	115		mV								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min	144.000	134.000	132.000	138.000	133.000	143.000	140.000	140.000	145.000	134.000	142.000
Average	147.000	146.500	144.000	146.167	143.167	148.167	145.500	147.500	147.500	145.167	147.200
Max	152.000	152.000	152.000	151.000	150.000	150.000	150.000	152.000	151.000	151.000	149.000
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000

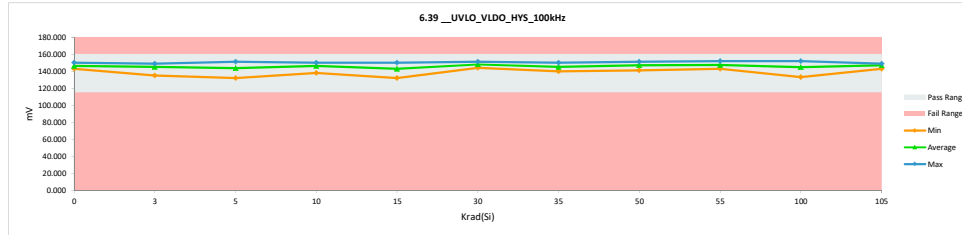


LDR TID Report
TPS7H5001-SP

6.39_UVLO_VLDO_HYS_100kHz				
Test Site				
Tester				
Test Number				
Unit	mV		mV	
Max Limit	160		160	
Min Limit	115		115	
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	145.000	146.000	1.000
0	118	151.000	150.000	-1.000
0	122	145.000	143.000	-2.000
3	61	151.000	145.000	-6.000
3	62	150.000	149.000	-1.000
3	63	145.000	144.000	-1.000
3	64	147.000	148.000	1.000
3	65	137.000	135.000	-2.000
3	66	149.000	149.000	0.000
5	67	134.000	132.000	-2.000
5	68	147.000	144.000	-3.000
5	69	142.000	140.000	-2.000
5	70	150.000	150.000	0.000
5	71	146.000	144.000	-2.000
5	72	152.000	151.000	-1.000
10	73	151.000	147.000	-4.000
10	74	150.000	150.000	0.000
10	75	145.000	146.000	1.000
10	76	147.000	147.000	0.000
10	77	137.000	138.000	1.000
10	78	149.000	149.000	0.000
15	79	134.000	132.000	-2.000
15	80	147.000	147.000	0.000
15	81	142.000	139.000	-3.000
15	82	150.000	150.000	0.000
15	83	146.000	141.000	-5.000
15	84	152.000	149.000	-3.000
30	86	151.000	150.000	-1.000
30	89	150.000	149.000	-1.000
30	91	145.000	151.000	6.000
30	92	147.000	148.000	1.000
30	94	137.000	144.000	7.000
30	95	149.000	146.000	-3.000
35	100	134.000	148.000	14.000
35	101	147.000	142.000	-5.000
35	102	142.000	140.000	-2.000
35	103	150.000	150.000	0.000
35	104	146.000	142.000	-4.000
35	105	152.000	150.000	-2.000
50	106	136.000	147.000	11.000
50	107	143.000	141.000	-2.000
50	108	149.000	148.000	-1.000
50	109	150.000	151.000	1.000
50	110	150.000	149.000	-1.000
50	111	143.000	147.000	4.000
55	113	152.000	143.000	-9.000
55	114	149.000	149.000	0.000
55	115	152.000	146.000	-6.000
55	116	152.000	150.000	-2.000
55	125	143.000	144.000	1.000
55	128	152.000	152.000	0.000
100	129	152.000	152.000	0.000
100	131	137.000	149.000	12.000
100	132	146.000	144.000	-2.000
100	133	140.000	135.000	-5.000
100	134	152.000	151.000	-1.000
100	135	145.000	142.000	-3.000
100	138	150.000	144.000	-6.000
100	139	136.000	140.000	4.000
100	140	151.000	145.000	-6.000
100	141	150.000	148.000	-2.000
100	142	136.000	149.000	13.000
100	143	153.000	150.000	-3.000
100	144	153.000	148.000	-5.000
100	220	149.000	139.000	-10.000
100	221	151.000	150.000	-1.000
100	224	148.000	149.000	1.000
100	225	149.000	145.000	-4.000
100	226	149.000	140.000	-9.000
100	228	140.000	143.000	3.000
100	232	147.000	144.000	-3.000
100	233	150.000	140.000	-10.000
100	236	132.000	133.000	1.000
100	238	146.000	147.000	1.000
100	243	146.000	150.000	4.000
105	244	149.000	148.000	-1.000
105	246	150.000	149.000	-1.000
105	248	151.000	148.000	-3.000
105	374	146.000	147.000	1.000
105	376	144.000	143.000	-1.000
	Max	153.000	152.000	14.000
	Average	146.375	145.550	-0.825
	Min	132.000	132.000	-10.000
	Std Dev	5.371	4.752	4.380

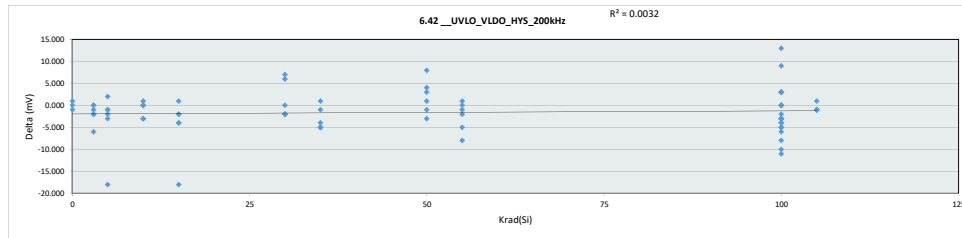


6.39_UVLO_VLDO_HYS_100kHz											
Test Site											
Tester											
Test Number											
Max Limit	160		mV								
Min Limit	115		mV								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min	143.000	135.000	132.000	138.000	132.000	144.000	140.000	141.000	143.000	133.000	143.000
Average	146.333	145.000	143.500	146.167	143.000	148.000	145.333	147.167	147.333	144.875	147.000
Max	150.000	149.000	151.000	150.000	150.000	151.000	150.000	151.000	152.000	152.000	149.000
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000

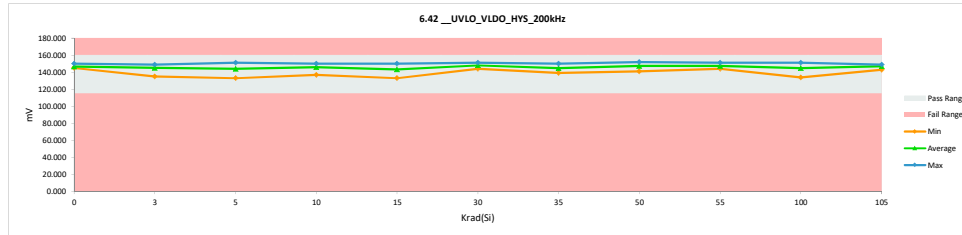


LDR TID Report
TPS7H5001-SP

6.42_UVLO_VLDO_HYS_200kHz				
Test Site				
Tester				
Test Number				
Unit	mV		mV	
Max Limit	160		160	
Min Limit	115		115	
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	145.000	145.000	0.000
0	118	151.000	150.000	-1.000
0	122	144.000	145.000	1.000
3	61	151.000	145.000	-6.000
3	62	150.000	149.000	-1.000
3	63	145.000	145.000	0.000
3	64	149.000	147.000	-2.000
3	65	137.000	135.000	-2.000
3	66	149.000	149.000	0.000
5	67	151.000	133.000	-18.000
5	68	147.000	145.000	-2.000
5	69	144.000	141.000	-3.000
5	70	149.000	151.000	2.000
5	71	146.000	145.000	-1.000
5	72	151.000	150.000	-1.000
10	73	151.000	148.000	-3.000
10	74	150.000	150.000	0.000
10	75	145.000	146.000	1.000
10	76	149.000	146.000	-3.000
10	77	137.000	137.000	0.000
10	78	149.000	149.000	0.000
15	79	151.000	133.000	-18.000
15	80	147.000	145.000	-2.000
15	81	144.000	140.000	-4.000
15	82	149.000	150.000	1.000
15	83	146.000	142.000	-4.000
15	84	151.000	149.000	-2.000
30	86	151.000	149.000	-2.000
30	89	150.000	148.000	-2.000
30	91	145.000	151.000	6.000
30	92	149.000	149.000	0.000
30	94	137.000	144.000	7.000
30	95	149.000	147.000	-2.000
35	100	151.000	147.000	-4.000
35	101	147.000	142.000	-5.000
35	102	144.000	139.000	-5.000
35	103	149.000	150.000	1.000
35	104	146.000	141.000	-5.000
35	105	151.000	150.000	-1.000
50	106	138.000	146.000	8.000
50	107	141.000	141.000	-3.000
50	108	149.000	148.000	-1.000
50	109	149.000	152.000	3.000
50	110	149.000	150.000	1.000
50	111	143.000	147.000	4.000
55	113	152.000	144.000	-8.000
55	114	150.000	149.000	-1.000
55	115	152.000	147.000	-5.000
55	116	151.000	149.000	-2.000
55	126	144.000	145.000	1.000
55	128	151.000	151.000	0.000
100	129	153.000	150.000	-3.000
100	131	135.000	148.000	13.000
100	132	146.000	144.000	-2.000
100	133	140.000	135.000	-5.000
100	134	153.000	150.000	-3.000
100	135	145.000	142.000	-3.000
100	138	150.000	145.000	-5.000
100	139	136.000	139.000	3.000
100	140	152.000	146.000	-6.000
100	141	151.000	148.000	-3.000
100	142	140.000	149.000	9.000
100	143	151.000	151.000	0.000
100	144	152.000	148.000	-4.000
100	220	149.000	138.000	-11.000
100	221	155.000	150.000	-5.000
100	224	148.000	151.000	3.000
100	225	149.000	145.000	-4.000
100	226	149.000	139.000	-10.000
100	228	140.000	143.000	3.000
100	232	147.000	144.000	-3.000
100	233	149.000	141.000	-8.000
100	236	131.000	134.000	3.000
100	238	147.000	147.000	0.000
100	243	151.000	151.000	0.000
105	244	148.000	149.000	1.000
105	246	150.000	149.000	-1.000
105	248	150.000	149.000	-1.000
105	374	147.000	146.000	-1.000
105	376	144.000	143.000	-1.000
Max		155.000	152.000	13.000
Average		147.213	145.625	-1.588
Min		131.000	133.000	-18.000
Std Dev		4.711	4.662	4.673

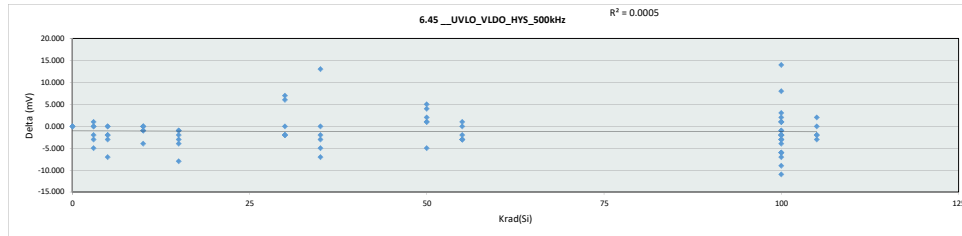


6.42_UVLO_VLDO_HYS_200kHz											
Test Site											
Tester											
Test Number											
Max Limit	160		mV								
Min Limit	115		mV								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min	145.000	135.000	133.000	137.000	133.000	133.000	144.000	139.000	141.000	144.000	143.000
Average	146.667	145.000	144.167	146.000	143.167	148.000	144.833	147.333	147.500	144.917	147.200
Max	150.000	149.000	151.000	150.000	150.000	151.000	150.000	152.000	151.000	151.000	149.000
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000

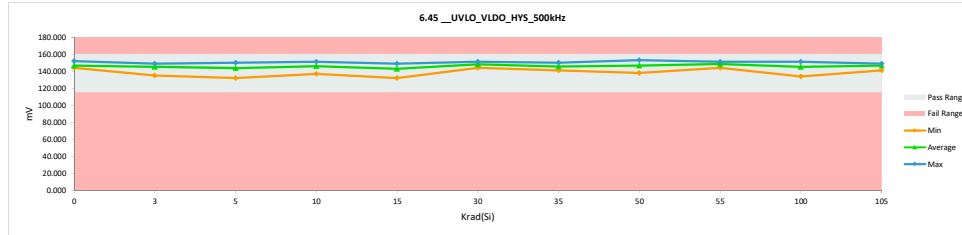


LDR TID Report
TPS7H5001-SP

6.45_UVLO_VLDO_HYS_500kHz				
Test Site				
Tester				
Test Number				
Unit	mV		mV	
Max Limit	160		160	
Min Limit	115		115	
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	144.000	144.000	0.000
0	118	152.000	152.000	0.000
0	122	144.000	144.000	0.000
3	61	151.000	146.000	-5.000
3	62	151.000	148.000	-3.000
3	63	145.000	145.000	0.000
3	64	148.000	149.000	1.000
3	65	137.000	135.000	-2.000
3	66	149.000	149.000	0.000
5	67	135.000	132.000	-3.000
5	68	147.000	145.000	-2.000
5	69	148.000	141.000	-7.000
5	70	150.000	150.000	0.000
5	71	145.000	145.000	0.000
5	72	152.000	150.000	-2.000
10	73	151.000	147.000	-4.000
10	74	151.000	151.000	0.000
10	75	145.000	144.000	-1.000
10	76	148.000	147.000	-1.000
10	77	137.000	137.000	0.000
10	78	149.000	149.000	0.000
15	79	135.000	132.000	-3.000
15	80	147.000	146.000	-1.000
15	81	148.000	140.000	-8.000
15	82	150.000	149.000	-1.000
15	83	145.000	143.000	-2.000
15	84	152.000	148.000	-4.000
30	86	151.000	149.000	-2.000
30	89	151.000	149.000	-2.000
30	91	145.000	151.000	6.000
30	92	148.000	148.000	0.000
30	94	137.000	144.000	7.000
30	95	149.000	147.000	-2.000
35	100	135.000	148.000	13.000
35	101	147.000	142.000	-5.000
35	102	148.000	141.000	-7.000
35	103	150.000	150.000	0.000
35	104	145.000	142.000	-3.000
35	105	152.000	150.000	-2.000
50	106	137.000	138.000	1.000
50	107	145.000	140.000	-5.000
50	108	148.000	150.000	2.000
50	109	149.000	153.000	4.000
50	110	149.000	150.000	1.000
50	111	144.000	149.000	5.000
55	113	152.000	149.000	-3.000
55	114	150.000	150.000	0.000
55	115	150.000	147.000	-3.000
55	116	153.000	150.000	-3.000
55	125	143.000	144.000	1.000
55	128	153.000	151.000	-2.000
100	129	152.000	151.000	-1.000
100	131	135.000	149.000	14.000
100	132	147.000	145.000	-2.000
100	133	141.000	135.000	-6.000
100	134	153.000	150.000	-3.000
100	135	145.000	142.000	-3.000
100	138	150.000	144.000	-6.000
100	139	138.000	139.000	1.000
100	140	152.000	148.000	-4.000
100	141	151.000	149.000	-2.000
100	142	141.000	149.000	8.000
100	143	152.000	150.000	-2.000
100	144	153.000	147.000	-6.000
100	220	149.000	138.000	-11.000
100	221	152.000	150.000	-2.000
100	224	147.000	150.000	3.000
100	225	148.000	145.000	-3.000
100	226	149.000	140.000	-9.000
100	228	142.000	143.000	1.000
100	232	146.000	144.000	-2.000
100	233	150.000	143.000	-7.000
100	236	132.000	134.000	2.000
100	238	147.000	148.000	1.000
100	243	151.000	150.000	-1.000
105	244	149.000	147.000	-2.000
105	246	151.000	149.000	-2.000
105	248	149.000	149.000	0.000
105	374	145.000	147.000	2.000
105	376	144.000	141.000	-3.000
	Max	153.000	153.000	14.000
	Average	146.850	145.700	-1.150
	Min	132.000	132.000	-11.000
	Std Dev	5.139	4.853	4.041

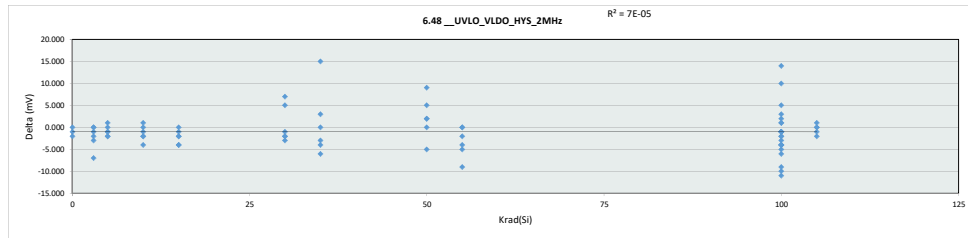


6.45_UVLO_VLDO_HYS_500kHz											
Test Site											
Tester											
Test Number											
Max Limit	160		mV								
Min Limit	115		mV								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min	144.000	135.000	132.000	137.000	132.000	132.000	144.000	138.000	144.000	134.000	141.000
Average	146.667	145.333	143.833	145.833	143.000	148.000	145.500	146.667	148.500	145.125	146.600
Max	152.000	149.000	150.000	151.000	149.000	151.000	150.000	153.000	151.000	151.000	149.000
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000

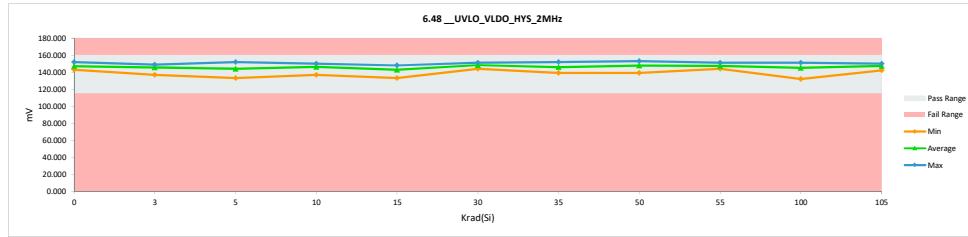


LDR TID Report
TPS7H5001-SP

6.48_UVLO_VLDO_HYS_2MHz				
Test Site				
Tester				
Test Number				
Unit	mV		mV	
Max Limit	160		160	
Min Limit	115		115	
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	146.000	146.000	0.000
0	118	153.000	152.000	-1.000
0	122	145.000	143.000	-2.000
3	61	152.000	145.000	-7.000
3	62	152.000	149.000	-3.000
3	63	146.000	145.000	-1.000
3	64	150.000	148.000	-2.000
3	65	137.000	137.000	0.000
3	66	149.000	149.000	0.000
5	67	134.000	133.000	-1.000
5	68	148.000	146.000	-2.000
5	69	142.000	141.000	-1.000
5	70	148.000	149.000	1.000
5	71	146.000	144.000	-2.000
5	72	152.000	152.000	0.000
10	73	152.000	148.000	-4.000
10	74	152.000	150.000	-2.000
10	75	146.000	145.000	-1.000
10	76	150.000	148.000	-2.000
10	77	137.000	137.000	0.000
10	78	149.000	150.000	1.000
15	79	134.000	133.000	-1.000
15	80	148.000	146.000	-2.000
15	81	142.000	140.000	-2.000
15	82	148.000	148.000	0.000
15	83	146.000	142.000	-4.000
15	84	152.000	148.000	-4.000
30	86	152.000	149.000	-3.000
30	89	152.000	150.000	-2.000
30	91	146.000	151.000	5.000
30	92	150.000	148.000	-2.000
30	94	137.000	144.000	7.000
30	95	149.000	148.000	-1.000
35	100	134.000	149.000	15.000
35	101	148.000	142.000	-6.000
35	102	142.000	139.000	-3.000
35	103	148.000	151.000	3.000
35	104	146.000	142.000	-4.000
35	105	152.000	152.000	0.000
50	106	137.000	146.000	9.000
50	107	144.000	139.000	-5.000
50	108	148.000	148.000	0.000
50	109	151.000	153.000	2.000
50	110	149.000	151.000	2.000
50	111	145.000	150.000	5.000
55	113	153.000	144.000	-9.000
55	114	149.000	149.000	0.000
55	115	152.000	147.000	-5.000
55	116	153.000	149.000	-4.000
55	126	144.000	144.000	0.000
55	128	153.000	151.000	-2.000
100	129	152.000	151.000	-1.000
100	131	135.000	149.000	14.000
100	132	145.000	144.000	-1.000
100	133	140.000	136.000	-4.000
100	134	152.000	151.000	-1.000
100	135	145.000	142.000	-3.000
100	138	148.000	144.000	-4.000
100	139	136.000	141.000	5.000
100	140	152.000	146.000	-6.000
100	141	150.000	149.000	-1.000
100	142	141.000	151.000	10.000
100	143	152.000	150.000	-2.000
100	144	152.000	147.000	-5.000
100	220	150.000	139.000	-11.000
100	221	153.000	149.000	-4.000
100	224	149.000	151.000	2.000
100	225	149.000	145.000	-4.000
100	226	149.000	140.000	-9.000
100	228	140.000	143.000	3.000
100	232	147.000	145.000	-2.000
100	233	150.000	140.000	-10.000
100	236	131.000	132.000	1.000
100	238	146.000	147.000	1.000
100	243	152.000	151.000	-1.000
105	244	149.000	150.000	1.000
105	246	150.000	150.000	0.000
105	248	151.000	149.000	-2.000
105	374	146.000	146.000	0.000
105	376	143.000	142.000	-1.000
Max		153.000	153.000	15.000
Average		146.813	145.875	-0.938
Min		131.000	132.000	-11.000
Std Dev		5.526	4.845	4.385

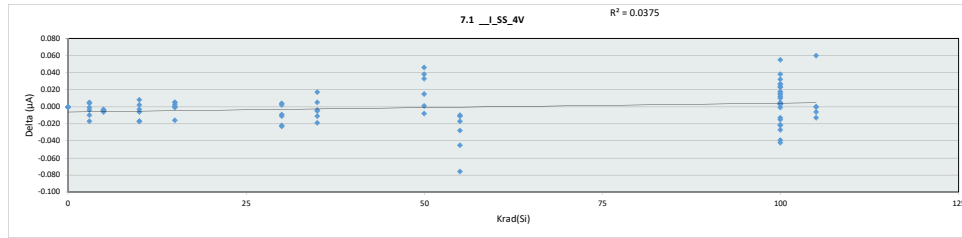


6.48_UVLO_VLDO_HYS_2MHz											
Test Site											
Tester											
Test Number											
Max Limit	160		mV								
Min Limit	115		mV								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min	143.000	137.000	133.000	137.000	133.000	133.000	144.000	139.000	139.000	144.000	142.000
Average	147.000	145.500	144.167	146.333	142.833	148.333	145.833	147.833	147.333	145.125	147.400
Max	152.000	149.000	152.000	150.000	148.000	151.000	152.000	153.000	151.000	151.000	150.000
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000

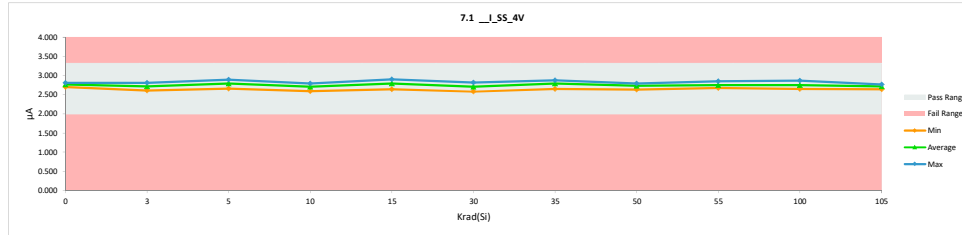


LDR TID Report
TPS7H5001-SP

7.1 _I_SS_4V				
Test Site				
Tester				
Test Number				
Unit	µA	µA		
Max Limit	3.32	3.32		
Min Limit	1.98	1.98		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	2.702	2.702	0.000
0	118	2.786	2.785	-0.001
0	122	2.808	2.808	0.000
3	61	2.747	2.730	-0.017
3	62	2.811	2.810	-0.001
3	63	2.697	2.702	0.005
3	64	2.784	2.780	-0.004
3	65	2.661	2.651	-0.010
3	66	2.601	2.605	0.004
5	67	2.692	2.687	-0.005
5	68	2.837	2.833	-0.004
5	69	2.660	2.657	-0.003
5	70	2.897	2.893	-0.004
5	71	2.834	2.829	-0.005
5	72	2.832	2.826	-0.006
10	73	2.747	2.730	-0.017
10	74	2.811	2.794	-0.017
10	75	2.697	2.699	0.002
10	76	2.784	2.792	0.008
10	77	2.661	2.658	-0.003
10	78	2.601	2.595	-0.006
15	79	2.692	2.697	0.005
15	80	2.837	2.836	-0.001
15	81	2.660	2.644	-0.016
15	82	2.897	2.902	0.005
15	83	2.834	2.834	0.000
15	84	2.832	2.834	0.002
30	86	2.747	2.751	0.004
30	89	2.811	2.813	0.002
30	91	2.697	2.688	-0.009
30	92	2.784	2.761	-0.023
30	94	2.661	2.650	-0.011
30	95	2.601	2.579	-0.022
35	100	2.692	2.687	-0.005
35	101	2.837	2.854	0.017
35	102	2.660	2.649	-0.011
35	103	2.897	2.878	-0.019
35	104	2.834	2.839	0.005
35	105	2.832	2.828	-0.004
50	106	2.705	2.720	0.015
50	107	2.755	2.793	0.038
50	108	2.671	2.717	0.046
50	109	2.721	2.754	0.033
50	110	2.779	2.780	0.001
50	111	2.643	2.635	-0.008
55	113	2.790	2.745	-0.045
55	114	2.758	2.741	-0.017
55	115	2.694	2.683	-0.011
55	116	2.851	2.828	-0.028
55	126	2.864	2.854	-0.010
55	128	2.749	2.673	-0.076
100	129	2.794	2.821	0.027
100	131	2.745	2.703	-0.042
100	132	2.761	2.748	-0.013
100	133	2.843	2.857	0.014
100	134	2.741	2.753	0.012
100	135	2.699	2.660	-0.039
100	138	2.739	2.762	0.023
100	139	2.652	2.670	0.018
100	140	2.832	2.835	0.003
100	141	2.698	2.677	-0.021
100	142	2.821	2.837	0.016
100	143	2.760	2.765	0.005
100	144	2.712	2.716	0.004
100	220	2.772	2.757	-0.015
100	221	2.691	2.690	-0.001
100	224	2.734	2.760	0.026
100	225	2.636	2.646	0.010
100	226	2.829	2.867	0.038
100	228	2.721	2.776	0.055
100	232	2.751	2.783	0.032
100	233	2.780	2.753	-0.027
100	236	2.661	2.664	0.003
100	238	2.702	2.725	0.023
100	243	2.713	2.691	-0.022
105	244	2.767	2.761	-0.006
105	246	2.680	2.740	0.060
105	248	2.645	2.645	0.000
105	374	2.767	2.767	0.000
105	376	2.701	2.688	-0.013
	Max	2.897	2.902	0.060
	Average	2.747	2.747	-0.001
	Min	2.601	2.579	-0.076
	Std Dev	0.074	0.076	0.021

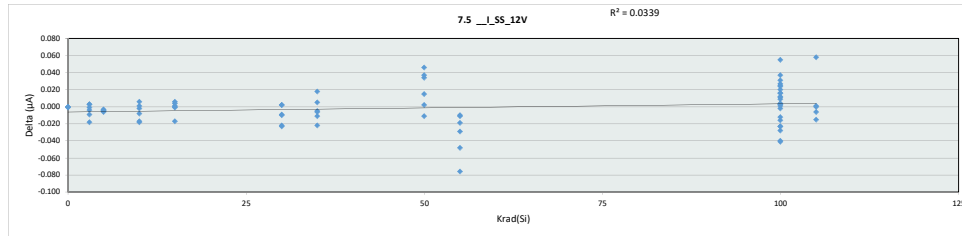


7.1 _I_SS_4V											
Test Site											
Tester											
Test Number											
Max Limit	3.32	µA									
Min Limit	1.98	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min	2.702	2.605	2.657	2.595	2.644	2.579	2.649	2.635	2.673	2.646	2.645
Average	2.765	2.713	2.788	2.711	2.791	2.707	2.789	2.733	2.753	2.747	2.720
Max	2.808	2.810	2.893	2.794	2.902	2.813	2.878	2.793	2.854	2.867	2.767
UL	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320

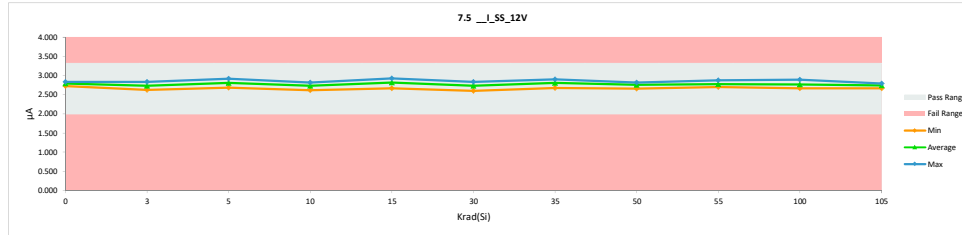


LDR TID Report
TPS7H5001-SP

7.5 _I_SS_12V				
Test Site				
Tester				
Test Number				
Unit	µA	µA		
Max Limit	3.32	3.32		
Min Limit	1.98	1.98		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	2.725	2.725	0.000
0	118	2.811	2.810	-0.001
0	122	2.831	2.831	0.000
3	61	2.772	2.754	-0.018
3	62	2.834	2.833	-0.001
3	63	2.720	2.723	0.003
3	64	2.808	2.804	-0.004
3	65	2.683	2.674	-0.009
3	66	2.624	2.627	0.003
5	67	2.716	2.711	-0.005
5	68	2.859	2.855	-0.004
5	69	2.683	2.679	-0.004
5	70	2.921	2.918	-0.003
5	71	2.858	2.853	-0.005
5	72	2.856	2.850	-0.006
10	73	2.772	2.754	-0.018
10	74	2.834	2.817	-0.017
10	75	2.720	2.721	0.001
10	76	2.808	2.814	0.006
10	77	2.683	2.681	-0.002
10	78	2.624	2.616	-0.008
15	79	2.716	2.722	0.006
15	80	2.859	2.858	-0.001
15	81	2.683	2.666	-0.017
15	82	2.921	2.925	0.004
15	83	2.858	2.859	0.001
15	84	2.856	2.856	0.000
30	86	2.772	2.774	0.002
30	89	2.834	2.836	0.002
30	91	2.720	2.711	-0.009
30	92	2.808	2.785	-0.023
30	94	2.683	2.673	-0.010
30	95	2.624	2.602	-0.022
35	100	2.716	2.710	-0.006
35	101	2.859	2.877	0.018
35	102	2.683	2.672	-0.011
35	103	2.921	2.899	-0.022
35	104	2.858	2.863	0.005
35	105	2.856	2.851	-0.005
50	106	2.729	2.744	0.015
50	107	2.780	2.817	0.037
50	108	2.695	2.741	0.046
50	109	2.743	2.777	0.034
50	110	2.802	2.804	0.002
50	111	2.665	2.654	-0.011
55	113	2.815	2.767	-0.048
55	114	2.780	2.761	-0.019
55	115	2.716	2.705	-0.011
55	116	2.874	2.845	-0.029
55	126	2.888	2.878	-0.010
55	128	2.772	2.696	-0.076
100	129	2.818	2.845	0.027
100	131	2.769	2.728	-0.041
100	132	2.782	2.770	-0.012
100	133	2.868	2.880	0.012
100	134	2.764	2.775	0.011
100	135	2.722	2.682	-0.040
100	138	2.763	2.783	0.020
100	139	2.674	2.690	0.016
100	140	2.856	2.857	0.001
100	141	2.721	2.698	-0.023
100	142	2.845	2.861	0.016
100	143	2.781	2.785	0.004
100	144	2.734	2.737	0.003
100	220	2.795	2.779	-0.016
100	221	2.712	2.710	-0.002
100	224	2.757	2.782	0.025
100	225	2.658	2.667	0.009
100	226	2.853	2.890	0.037
100	228	2.743	2.798	0.055
100	232	2.775	2.806	0.031
100	233	2.803	2.775	-0.028
100	236	2.683	2.686	0.003
100	238	2.723	2.747	0.024
100	243	2.735	2.712	-0.023
105	244	2.791	2.785	-0.006
105	246	2.702	2.760	0.058
105	248	2.666	2.666	0.000
105	374	2.790	2.791	0.001
105	376	2.726	2.711	-0.015
	Max	2.921	2.925	0.058
	Average	2.770	2.769	-0.001
	Min	2.624	2.602	-0.076
	Std Dev	0.074	0.076	0.021

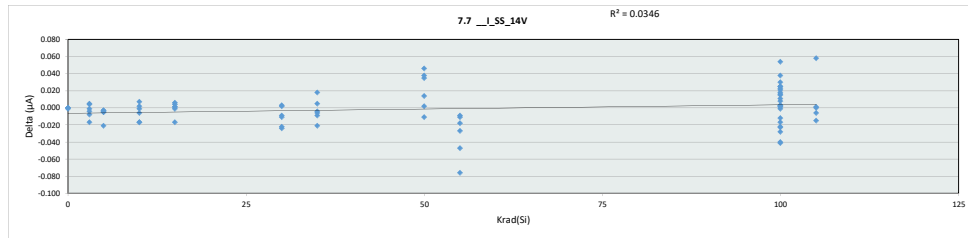


7.5 _I_SS_12V											
Test Site											
Tester											
Test Number											
Max Limit	3.32	µA									
Min Limit	1.98	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min	2.725	2.627	2.679	2.616	2.666	2.602	2.672	2.654	2.696	2.667	2.666
Average	2.789	2.736	2.811	2.734	2.814	2.730	2.812	2.756	2.775	2.768	2.743
Max	2.831	2.833	2.918	2.817	2.925	2.836	2.899	2.817	2.878	2.890	2.791
UL	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320

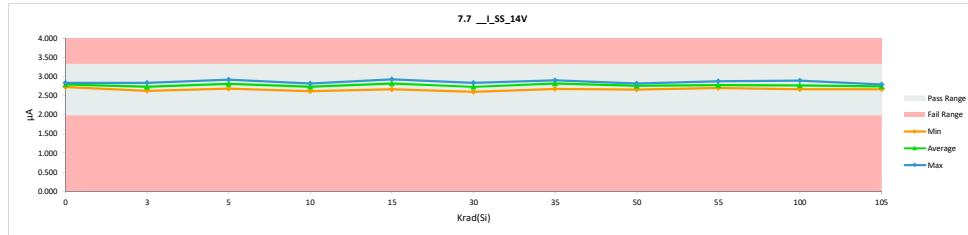


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7.7 _L_SS_14V				
Test Site				
Tester				
Test Number				
Unit	µA	µA		
Max Limit	3.32	3.32		
Min Limit	1.98	1.98		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	2.725	2.725	0.000
0	118	2.810	2.810	0.000
0	122	2.831	2.830	-0.001
3	61	2.771	2.754	-0.017
3	62	2.834	2.833	-0.001
3	63	2.719	2.724	0.005
3	64	2.808	2.804	-0.004
3	65	2.683	2.675	-0.008
3	66	2.623	2.627	0.004
5	67	2.716	2.711	-0.005
5	68	2.860	2.855	-0.005
5	69	2.682	2.679	-0.003
5	70	2.921	2.918	-0.003
5	71	2.858	2.853	-0.005
5	72	2.856	2.835	-0.021
10	73	2.771	2.754	-0.017
10	74	2.834	2.817	-0.017
10	75	2.719	2.721	0.002
10	76	2.808	2.815	0.007
10	77	2.683	2.682	-0.001
10	78	2.623	2.617	-0.006
15	79	2.716	2.722	0.006
15	80	2.860	2.859	-0.001
15	81	2.682	2.665	-0.017
15	82	2.921	2.925	0.004
15	83	2.858	2.859	0.001
15	84	2.856	2.857	0.001
30	86	2.771	2.774	0.003
30	89	2.834	2.836	0.002
30	91	2.719	2.710	-0.009
30	92	2.808	2.784	-0.024
30	94	2.683	2.672	-0.011
30	95	2.623	2.601	-0.022
35	100	2.716	2.710	-0.006
35	101	2.860	2.878	0.018
35	102	2.682	2.673	-0.009
35	103	2.921	2.900	-0.021
35	104	2.858	2.863	0.005
35	105	2.856	2.852	-0.004
50	106	2.729	2.743	0.014
50	107	2.779	2.817	0.038
50	108	2.695	2.741	0.046
50	109	2.743	2.778	0.035
50	110	2.803	2.805	0.002
50	111	2.665	2.654	-0.011
55	113	2.815	2.768	-0.047
55	114	2.781	2.763	-0.018
55	115	2.716	2.705	-0.011
55	116	2.874	2.847	-0.027
55	126	2.888	2.879	-0.009
55	128	2.772	2.696	-0.076
100	129	2.819	2.844	0.025
100	131	2.769	2.728	-0.041
100	132	2.782	2.770	-0.012
100	133	2.867	2.882	0.015
100	134	2.765	2.776	0.011
100	135	2.722	2.682	-0.040
100	138	2.762	2.783	0.021
100	139	2.674	2.690	0.016
100	140	2.856	2.857	0.001
100	141	2.721	2.699	-0.022
100	142	2.844	2.862	0.018
100	143	2.781	2.784	0.003
100	144	2.735	2.737	0.002
100	220	2.795	2.778	-0.017
100	221	2.712	2.711	-0.001
100	224	2.757	2.782	0.025
100	225	2.658	2.666	0.008
100	226	2.853	2.891	0.038
100	228	2.744	2.798	0.054
100	232	2.776	2.806	0.030
100	233	2.804	2.776	-0.028
100	236	2.682	2.685	0.003
100	238	2.723	2.746	0.023
100	243	2.735	2.712	-0.023
105	244	2.791	2.785	-0.006
105	246	2.702	2.760	0.058
105	248	2.666	2.667	0.001
105	374	2.790	2.790	0.000
105	376	2.726	2.711	-0.015
Max		2.921	2.925	0.058
Average		2.770	2.769	-0.001
Min		2.623	2.601	-0.076
Std Dev		0.074	0.076	0.021

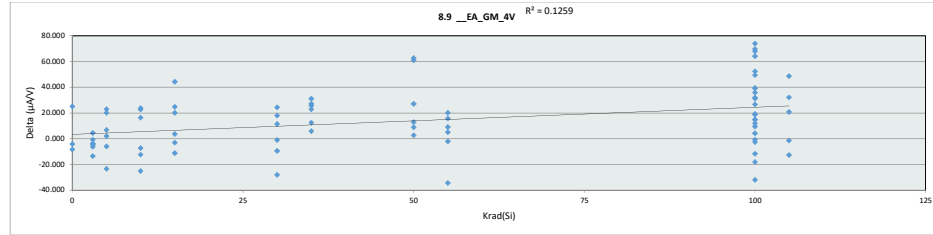


7.7 _L_SS_14V											
Test Site											
Tester											
Test Number											
Max Limit	3.32	µA									
Min Limit	1.98	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min	2.725	2.627	2.679	2.617	2.665	2.601	2.673	2.654	2.696	2.666	2.667
Average	2.788	2.736	2.809	2.734	2.815	2.730	2.813	2.756	2.776	2.769	2.743
Max	2.830	2.833	2.918	2.817	2.925	2.836	2.836	2.900	2.817	2.879	2.790
UL	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320

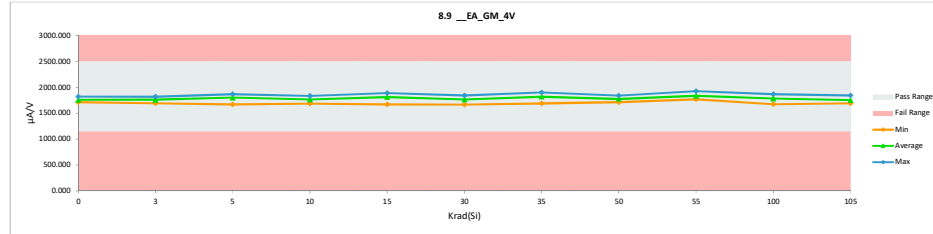


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		8.9_EA_GM_4V		
Test Site	Tester			
Test Number	Unit	µA/V	µA/V	
Max Limit	Min Limit	2500	1150	
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1720.652	1712.302	-8.350
0	118	1747.872	1743.857	-4.015
0	122	1795.843	1821.097	25.254
3	61	1831.434	1818.008	-13.426
3	62	1812.910	1817.506	4.596
3	63	1728.389	1722.321	-6.068
3	64	1810.738	1806.294	-4.444
3	65	1695.071	1691.414	-3.657
3	66	1706.354	1705.678	-0.676
5	67	1781.172	1801.319	20.147
5	68	1787.111	1794.019	6.908
5	69	1666.754	1668.835	2.081
5	70	1872.927	1866.998	-5.929
5	71	1864.032	1840.727	-23.305
5	72	1820.267	1843.205	22.938
10	73	1831.434	1819.142	-12.292
10	74	1812.910	1787.958	-24.952
10	75	1728.389	1752.217	23.828
10	76	1810.738	1833.555	22.817
10	77	1695.071	1687.772	-7.299
10	78	1706.354	1722.772	16.418
15	79	1781.172	1801.329	20.157
15	80	1787.111	1775.978	-11.133
15	81	1666.754	1670.422	3.668
15	82	1872.927	1869.953	-2.974
15	83	1864.032	1888.805	24.773
15	84	1820.267	1864.570	44.303
30	86	1831.434	1842.891	11.457
30	89	1812.910	1811.922	-0.988
30	91	1728.389	1746.399	18.010
30	92	1810.738	1801.329	-9.409
30	94	1695.071	1667.077	-27.994
30	95	1706.354	1730.743	24.389
35	100	1781.172	1806.858	25.686
35	101	1787.111	1799.667	12.556
35	102	1666.754	1689.719	22.965
35	103	1872.927	1800.111	27.184
35	104	1864.032	1869.942	5.910
35	105	1820.267	1851.340	31.073
50	106	1707.344	1734.500	27.156
50	107	1768.294	1781.172	12.878
50	108	1751.906	1812.910	61.004
50	109	1778.020	1840.737	62.717
50	110	1759.900	1768.872	8.972
50	111	1710.809	1713.508	2.699
55	113	1770.860	1768.872	-1.988
55	114	1848.240	1864.021	15.781
55	115	1823.630	1832.714	9.084
55	116	1790.381	1810.553	20.172
55	126	1919.788	1924.942	5.154
55	128	1852.301	1818.008	-34.293
100	129	1774.203	1823.630	49.427
100	131	1748.392	1779.566	31.174
100	132	1746.390	1728.398	-17.992
100	133	1814.273	1833.085	18.812
100	134	1784.934	1852.833	67.899
100	135	1842.456	1810.553	-31.903
100	138	1763.572	1802.829	39.257
100	139	1722.321	1758.305	35.984
100	140	1768.872	1778.378	9.506
100	141	1795.286	1792.811	-2.475
100	142	1823.630	1822.927	-0.703
100	143	1792.562	1866.562	74.000
100	144	1770.197	1784.953	14.756
100	220	1825.525	1844.745	19.220
100	221	1702.415	1729.209	26.794
100	224	1704.595	1719.532	14.937
100	225	1774.193	1762.609	-11.584
100	226	1730.600	1782.989	52.389
100	228	1765.344	1834.999	69.655
100	232	1699.466	1763.591	64.125
100	233	1763.572	1802.974	39.402
100	236	1661.481	1673.388	11.907
100	238	1774.794	1806.848	32.054
100	243	1702.406	1706.684	4.278
105	244	1731.422	1763.572	32.150
105	246	1706.354	1754.976	48.622
105	248	1695.645	1716.462	20.817
105	374	1844.988	1843.599	-1.389
105	376	1705.218	1692.625	-12.593
Max		1919.788	1924.942	74.000
Average		1772.655	1786.881	14.226
Min		1661.481	1667.077	-34.293
Std Dev		59.035	60.061	24.002

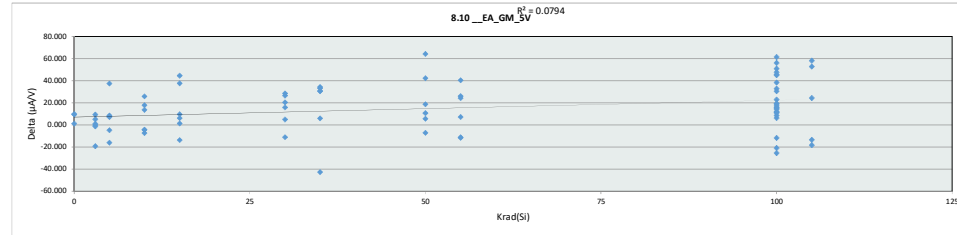


		8.9_EA_GM_4V									
Test Site	Tester										
Test Number	Unit	2500	µA/V								
Max Limit	Min Limit	1150	µA/V								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min	1712.302	1691.414	1668.835	1687.772	1670.422	1667.077	1689.719	1713.508	1768.872	1673.388	1692.625
Average	1759.085	1760.204	1802.517	1767.236	1811.843	1766.727	1819.606	1775.283	1836.518	1785.933	1754.247
Max	1821.097	1818.008	1866.998	1833.555	1888.805	1842.891	1900.111	1840.737	1924.942	1866.562	1843.599
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000

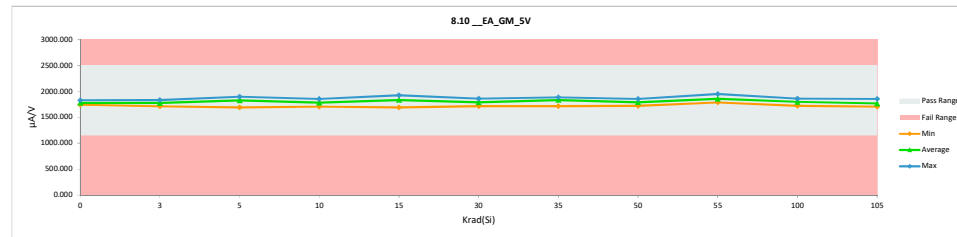


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		8.10_EA_GM_SV		
Test Site	Tester			
Test Number				
Unit	µA/V	µA/V		
Max Limit	2500	2500		
Min Limit	1150	1150		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1733.380	1742.698	9.318
0	118	1755.058	1765.019	9.961
0	122	1828.041	1829.297	1.256
3	61	1834.999	1815.765	-19.234
3	62	1820.514	1819.270	-1.244
3	63	1747.882	1753.078	5.196
3	64	1825.903	1835.311	9.408
3	65	1712.310	1712.258	-0.052
3	66	1726.744	1727.713	0.969
5	67	1795.065	1790.372	-4.693
5	68	1799.947	1807.773	7.826
5	69	1682.956	1691.490	8.534
5	70	1916.181	1900.111	-16.070
5	71	1881.898	1889.039	7.141
5	72	1845.425	1882.912	37.487
10	73	1834.999	1852.833	17.834
10	74	1820.514	1813.224	-7.290
10	75	1747.882	1761.600	13.718
10	76	1825.903	1851.729	25.826
10	77	1712.310	1708.039	-4.271
10	78	1726.744	1722.321	-4.423
15	79	1795.065	1801.329	6.264
15	80	1799.947	1801.329	1.382
15	81	1682.956	1692.625	9.669
15	82	1916.181	1902.556	-13.625
15	83	1881.898	1926.522	44.624
15	84	1845.425	1883.102	37.677
30	86	1834.999	1861.508	26.509
30	89	1820.514	1849.075	28.561
30	91	1747.882	1768.284	20.402
30	92	1825.903	1814.803	-11.100
30	94	1712.310	1717.297	4.987
30	95	1726.744	1742.707	15.963
35	100	1795.065	1825.913	30.848
35	101	1799.947	1834.287	34.340
35	102	1682.956	1716.304	33.348
35	103	1916.181	1873.513	-42.668
35	104	1881.898	1887.923	6.025
35	105	1845.425	1875.901	30.476
50	106	1729.984	1748.747	18.763
50	107	1790.630	1801.329	10.699
50	108	1767.632	1831.834	64.202
50	109	1813.537	1855.876	42.339
50	110	1786.749	1792.351	5.602
50	111	1728.897	1721.755	-7.142
55	113	1761.813	1787.939	26.126
55	114	1863.700	1887.934	24.234
55	115	1829.297	1869.755	40.458
55	116	1805.186	1812.411	7.225
55	126	1961.582	1950.442	-11.140
55	128	1852.311	1840.737	-11.574
100	129	1806.848	1852.311	45.463
100	131	1762.701	1777.935	15.234
100	132	1781.087	1755.692	-25.395
100	133	1834.999	1846.501	11.512
100	134	1801.329	1852.291	50.962
100	135	1828.061	1807.228	-20.833
100	138	1788.902	1777.106	-11.796
100	139	1737.563	1782.781	45.218
100	140	1806.848	1822.610	15.762
100	141	1807.968	1818.935	10.967
100	142	1804.691	1823.819	19.128
100	143	1834.999	1852.311	17.312
100	144	1791.041	1799.677	8.636
100	220	1846.867	1861.085	14.218
100	221	1704.196	1760.269	56.073
100	224	1726.104	1732.450	6.346
100	225	1779.557	1812.411	32.854
100	226	1760.787	1799.185	38.398
100	228	1773.612	1834.999	61.387
100	232	1733.022	1755.986	22.964
100	233	1808.708	1828.051	19.343
100	236	1674.557	1722.321	47.764
100	238	1777.775	1808.240	30.465
100	243	1716.190	1728.300	12.110
105	244	1722.321	1780.454	58.133
105	246	1717.305	1770.206	52.901
105	248	1709.998	1734.357	24.359
105	374	1869.671	1856.195	-13.476
105	376	1724.339	1708.094	-16.245
Min		1661.582	1691.490	64.202
Average		1790.116	1805.097	14.980
Max		1674.557	1691.490	-42.668
Std Dev		61.263	59.207	22.015

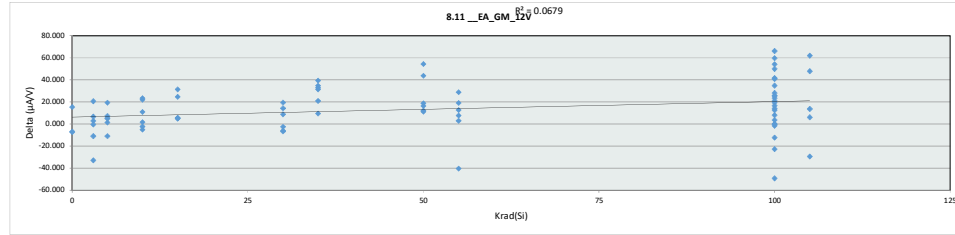


		8.10_EA_GM_SV									
Test Site	Tester										
Test Number											
Unit	µA/V	µA/V									
Max Limit	2500	2500									
Min Limit	1150	1150									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min	1742.698	1712.258	1691.490	1708.039	1692.625	1717.297	1716.304	1721.755	1787.939	1722.321	1706.094
Average	1779.005	1777.233	1826.950	1784.958	1834.577	1792.279	1835.640	1791.982	1858.203	1800.521	1769.461
Max	1829.297	1835.311	1900.111	1852.833	1926.522	1861.508	1887.923	1855.876	1950.442	1861.085	1856.195
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000

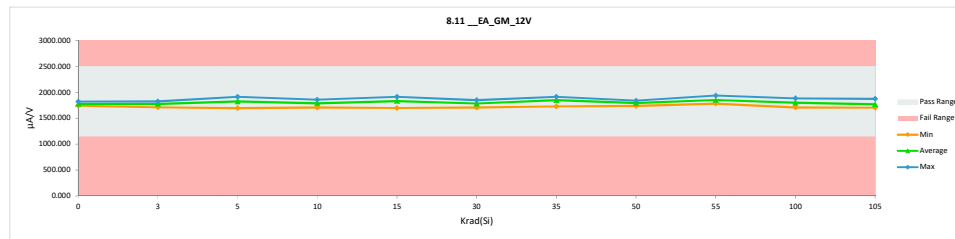


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8.11 EA_GM_12V				
Test Site				
Tester				
Test Number				
Unit	µA/V	µA/V		
Max Limit	2500	2500		
Min Limit	1150	1150		
Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1749.951	1743.232	-6.719
0	118	1748.155	1763.572	15.417
0	122	1828.320	1821.008	-7.312
3	61	1856.482	1823.620	-32.862
3	62	1816.601	1823.244	6.643
3	63	1737.563	1758.305	20.742
3	64	1823.125	1812.196	-10.929
3	65	1707.344	1710.242	2.898
3	66	1728.389	1728.007	-0.382
5	67	1801.329	1790.372	-10.957
5	68	1807.101	1808.513	1.412
5	69	1692.625	1697.506	4.881
5	70	1906.881	1914.213	7.332
5	71	1862.552	1881.898	19.346
5	72	1842.931	1848.434	5.503
10	73	1856.482	1858.158	1.676
10	74	1816.601	1827.593	10.992
10	75	1737.563	1759.632	22.069
10	76	1823.125	1846.501	23.376
10	77	1707.344	1705.106	-2.238
10	78	1728.389	1723.330	-5.059
15	79	1801.329	1806.848	5.519
15	80	1807.101	1812.411	5.310
15	81	1692.625	1697.497	4.872
15	82	1906.881	1912.446	5.565
15	83	1862.552	1894.008	31.456
15	84	1842.931	1867.632	24.701
30	86	1856.482	1850.370	-6.112
30	89	1816.601	1830.924	14.323
30	91	1737.563	1746.462	8.899
30	92	1823.125	1816.483	-6.642
30	94	1707.344	1704.872	-2.472
30	95	1728.389	1747.872	19.483
35	100	1801.329	1822.214	20.885
35	101	1807.101	1846.501	39.400
35	102	1692.625	1727.366	34.741
35	103	1906.881	1916.411	9.530
35	104	1862.552	1893.997	31.445
35	105	1842.931	1875.901	32.970
50	106	1740.411	1752.601	12.190
50	107	1790.391	1806.858	16.467
50	108	1767.753	1822.214	54.461
50	109	1795.834	1839.587	43.753
50	110	1789.598	1800.836	11.238
50	111	1717.297	1736.089	18.792
55	113	1774.193	1781.863	7.670
55	114	1872.927	1875.912	2.985
55	115	1837.864	1866.707	28.843
55	116	1803.158	1822.234	19.076
55	126	1924.942	1937.614	12.672
55	128	1864.021	1823.630	-40.391
100	129	1806.848	1834.989	28.141
100	131	1764.750	1784.174	19.424
100	132	1794.739	1745.572	-49.167
100	133	1829.297	1850.370	21.073
100	134	1823.006	1846.501	23.495
100	135	1832.144	1809.450	-22.694
100	138	1801.329	1801.329	0.000
100	139	1729.637	1795.834	66.197
100	140	1801.319	1818.008	16.689
100	141	1808.084	1816.129	8.045
100	142	1813.998	1828.041	14.043
100	143	1825.515	1885.239	59.724
100	144	1806.848	1806.848	-0.010
100	220	1858.158	1861.798	3.640
100	221	1705.236	1740.131	34.895
100	224	1722.321	1720.811	-1.510
100	225	1767.548	1780.161	12.613
100	226	1755.471	1797.209	41.738
100	228	1780.756	1834.989	54.233
100	232	1727.873	1768.872	40.999
100	233	1800.642	1826.459	25.817
100	236	1681.876	1707.344	25.468
100	238	1784.943	1834.989	50.046
100	243	1729.556	1717.297	-12.259
105	244	1737.051	1784.953	47.902
105	246	1706.884	1768.872	61.988
105	248	1708.700	1714.804	6.104
105	374	1860.910	1874.560	13.650
105	376	1732.450	1703.029	-29.421
Min		1824.942	1937.614	66.197
Average		1790.644	1804.248	13.604
Max		1681.876	1697.497	-49.167
Std Dev		58.886	59.591	21.992

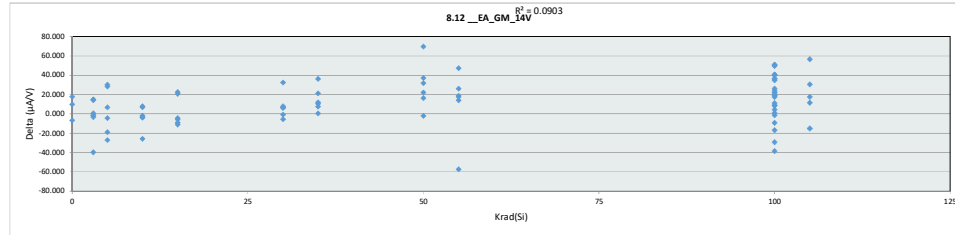


8.11 EA_GM_12V																						
Test Site																						
Tester																						
Test Number																						
Max Limit	2500	µA/V																				
Min Limit	1150	µA/V																				
Krad(S)	0	3	5	10	15	30	35	50	55	100	105											
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000		
Min	1743.232	1710.242	1697.506	1705.106	1697.497	1704.872	1727.366	1736.089	1781.863	1707.344	1703.029	1775.937	1775.936	1823.489	1786.720	1831.807	1782.831	1847.065	1793.031	1851.327	1800.523	1769.244
Average	1775.937	1775.936	1823.489	1786.720	1831.807	1782.831	1847.065	1793.031	1851.327	1800.523	1874.560	1821.008	1823.620	1914.213	1858.158	1912.446	1850.370	1916.411	1839.587	1937.614	1885.239	1874.560
Max	1821.008	1823.620	1914.213	1858.158	1912.446	1850.370	1916.411	1839.587	1937.614	1885.239	1874.560	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000

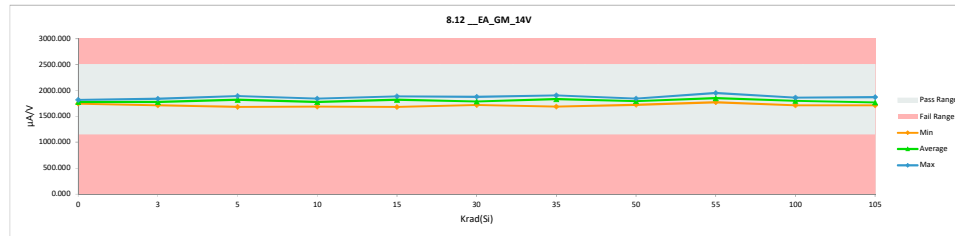


LDR TID Report
TPS7H5001-SP

		8.12_EA_GM_14V		
Test Site	Tester			
Test Number				
Unit		µA/V	µA/V	
Max Limit		2500	2500	
Min Limit		1150	1150	
Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1727.366	1745.045	17.679
0	118	1758.959	1768.872	9.913
0	122	1824.711	1818.008	-6.703
3	61	1846.501	1806.848	-39.653
3	62	1829.297	1827.872	-1.425
3	63	1742.698	1757.549	14.851
3	64	1824.463	1838.821	14.358
3	65	1712.302	1712.870	0.568
3	66	1720.493	1717.297	-3.196
5	67	1812.411	1793.472	-18.939
5	68	1805.613	1812.411	6.798
5	69	1687.280	1682.956	-4.324
5	70	1897.049	1869.953	-27.096
5	71	1864.032	1892.342	28.310
5	72	1837.693	1867.964	30.271
10	73	1846.501	1842.648	-3.853
10	74	1829.297	1827.414	-1.883
10	75	1742.698	1750.590	7.892
10	76	1824.463	1831.424	6.961
10	77	1712.302	1686.576	-25.726
10	78	1720.493	1717.297	-3.196
15	79	1812.411	1801.319	-11.092
15	80	1805.613	1801.329	-4.284
15	81	1687.280	1681.522	-5.758
15	82	1897.049	1887.923	-9.126
15	83	1864.032	1886.723	22.691
15	84	1837.693	1858.426	20.733
30	86	1846.501	1878.895	32.394
30	89	1829.297	1828.579	-0.718
30	91	1742.698	1749.030	6.332
30	92	1824.463	1818.935	-5.528
30	94	1712.302	1718.669	6.367
30	95	1720.493	1728.389	7.896
35	100	1812.411	1822.966	10.555
35	101	1805.613	1826.857	21.244
35	102	1687.280	1687.781	0.501
35	103	1897.049	1904.586	7.537
35	104	1864.032	1875.912	11.880
35	105	1837.693	1873.921	36.228
50	106	1742.698	1740.989	-16.709
50	107	1784.953	1822.026	37.073
50	108	1759.642	1829.297	69.655
50	109	1812.411	1844.218	31.807
50	110	1786.578	1808.708	22.130
50	111	1725.350	1723.321	-2.029
55	113	1758.305	1772.422	14.117
55	114	1868.464	1886.055	17.591
55	115	1816.139	1863.390	47.251
55	116	1810.563	1829.297	18.734
55	126	1924.257	1950.442	26.185
55	128	1864.032	1806.858	-57.174
100	129	1822.917	1834.047	11.130
100	131	1759.051	1778.020	18.969
100	132	1783.956	1745.527	-38.429
100	133	1836.717	1860.672	23.955
100	134	1827.175	1847.060	19.885
100	135	1823.620	1806.760	-16.860
100	138	1801.329	1802.325	0.996
100	139	1747.872	1788.559	40.687
100	140	1817.998	1816.611	-1.387
100	141	1783.871	1818.806	34.935
100	142	1801.319	1852.301	50.982
100	143	1818.008	1844.187	26.179
100	144	1819.615	1790.381	-29.234
100	220	1852.311	1861.085	8.774
100	221	1707.692	1747.872	40.180
100	224	1727.375	1731.824	4.449
100	225	1761.461	1778.887	17.426
100	226	1751.760	1801.329	49.569
100	228	1790.381	1840.727	50.346
100	232	1763.572	1785.551	21.979
100	233	1806.858	1815.441	8.583
100	236	1675.769	1712.310	36.541
100	238	1777.775	1817.998	40.223
100	243	1732.065	1722.825	-9.240
105	244	1739.284	1795.834	56.550
105	246	1717.297	1747.872	30.575
105	248	1706.441	1723.994	17.553
105	374	1859.002	1870.547	11.545
105	376	1727.375	1712.302	-15.073
Max		1924.257	1950.442	69.655
Average		1790.322	1801.608	11.286
Min		1675.769	1681.522	-57.174
Std Dev		57.982	59.970	22.818

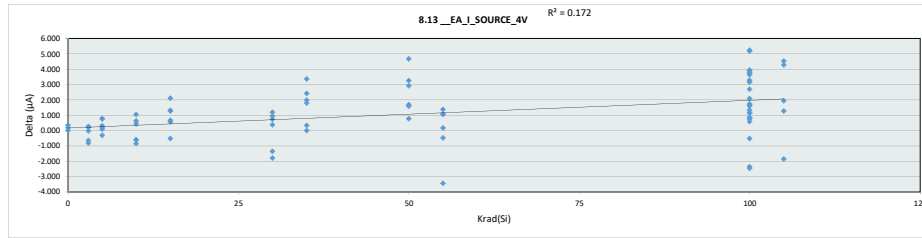


		8.12_EA_GM_14V									
Test Site	Tester										
Test Number											
Max Limit		2500	µA/V								
Min Limit		1150	µA/V								
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min	1745.045	1712.870	1682.956	1686.576	1681.522	1718.669	1687.781	1723.321	1772.422	1712.310	1712.302
Average	1777.308	1776.876	1819.850	1775.992	1819.540	1787.083	1832.004	1794.760	1851.411	1800.046	1870.110
Max	1818.008	1838.821	1892.342	1842.648	1887.923	1878.895	1904.586	1844.218	1950.442	1861.085	1870.547
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000

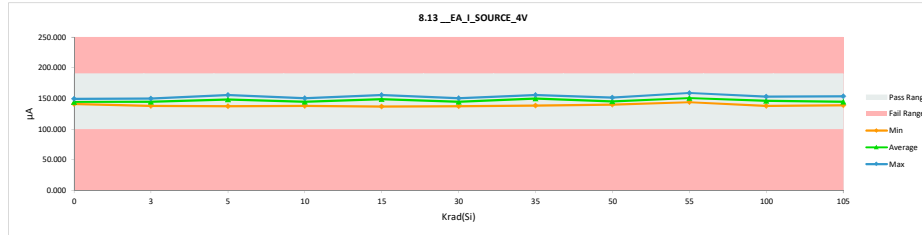


LDR TID Report
TPS7H5001-SP

8.13_EA_I_SOURCE_4V				
Test Site				
Tester				
Test Number				
Unit	µA	µA		
Max Limit	190	190		
Min Limit	100	100		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	140.651	140.845	0.194
0	118	143.043	143.056	0.013
0	122	148.956	149.325	0.369
3	61	149.422	148.622	-0.800
3	62	149.498	149.759	0.261
3	63	141.504	141.480	-0.024
3	64	149.209	149.443	0.234
3	65	138.718	138.083	-0.635
3	66	139.106	139.356	0.250
5	67	146.661	146.850	0.189
5	68	146.312	146.630	0.318
5	69	136.469	137.241	0.772
5	70	155.753	155.849	0.096
5	71	154.389	154.090	-0.299
5	72	149.521	150.307	0.786
10	73	149.422	148.824	-0.598
10	74	149.498	148.643	-0.855
10	75	141.504	142.134	0.630
10	76	149.209	150.253	1.044
10	77	138.718	138.119	-0.599
10	78	139.106	139.541	0.435
15	79	146.661	147.944	1.283
15	80	146.312	146.979	0.667
15	81	136.469	137.036	0.567
15	82	155.753	155.232	-0.521
15	83	154.389	155.704	1.315
15	84	149.521	151.637	2.116
30	86	149.422	150.369	0.947
30	89	149.498	150.229	0.731
30	91	141.504	141.890	0.386
30	92	149.209	147.419	-1.790
30	94	138.718	137.367	-1.351
30	95	139.106	140.292	1.186
35	100	146.661	149.075	2.414
35	101	146.312	149.672	3.360
35	102	136.469	138.460	1.991
35	103	155.753	155.768	0.015
35	104	154.389	154.730	0.341
35	105	149.521	151.318	1.797
50	106	140.265	141.965	1.700
50	107	144.182	147.111	2.929
50	108	143.729	146.991	3.262
50	109	146.800	151.469	4.669
50	110	143.722	145.314	1.592
50	111	139.166	139.936	0.770
55	113	143.822	143.988	0.166
55	114	153.088	154.152	1.064
55	115	150.260	151.642	1.382
55	116	146.426	147.488	1.062
55	126	159.480	159.006	-0.474
55	128	151.725	148.288	-3.437
100	129	145.716	148.860	3.144
100	131	142.808	143.585	0.777
100	132	143.352	140.904	-2.448
100	133	148.291	150.031	1.740
100	134	148.271	150.364	2.093
100	135	149.864	147.515	-2.349
100	138	144.962	146.296	1.334
100	139	141.841	145.607	3.766
100	140	146.809	147.587	0.778
100	141	145.189	146.352	1.163
100	142	147.741	148.609	0.868
100	143	147.641	152.881	5.240
100	144	146.013	147.617	1.604
100	220	150.885	152.024	1.139
100	221	138.278	142.215	3.937
100	224	139.091	139.675	0.584
100	225	143.554	145.220	1.666
100	226	142.215	146.118	3.903
100	228	144.558	149.731	5.173
100	232	140.989	144.614	3.625
100	233	146.943	150.226	3.283
100	236	135.132	137.831	2.699
100	238	144.767	148.572	3.805
100	243	139.397	138.869	-0.508
105	244	141.628	145.908	4.280
105	246	139.171	143.699	4.528
105	248	138.750	140.694	1.944
105	374	152.350	153.619	1.269
105	376	140.930	139.080	-1.850
	Max	159.480	159.006	5.240
	Average	145.527	146.641	1.114
	Min	135.132	137.036	-3.437
	Std Dev	5.370	5.293	1.761

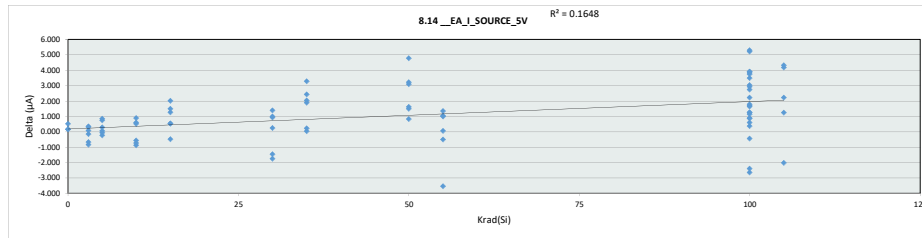


8.13_EA_I_SOURCE_4V											
Test Site											
Tester											
Test Number											
Max Limit	190	µA									
Min Limit	100	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	140.845	138.083	137.241	138.119	137.036	137.367	138.460	139.936	143.988	137.831	139.080
Average	144.409	144.457	148.495	144.586	149.089	144.594	149.837	145.464	150.761	146.305	144.600
Max	149.325	149.759	155.849	150.253	155.704	150.369	155.768	151.469	159.006	152.881	153.619
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

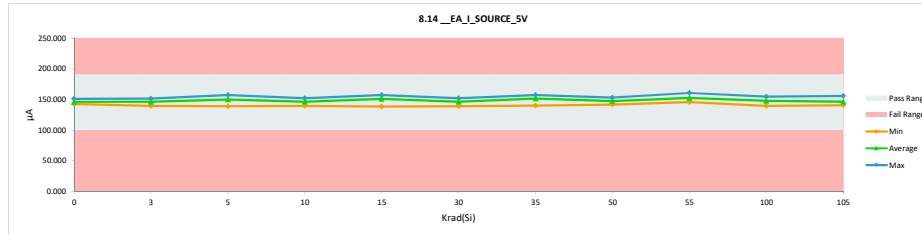


LDR TID Report
TPS7H5001-SP

8.14_EA_I_SOURCE_SV				
Test Site				
Tester				
Test Number				
Unit	µA	µA		
Max Limit	190	190		
Min Limit	100	100		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	142.229	142.397	0.168
0	118	144.538	144.707	0.169
0	122	150.453	150.961	0.508
3	61	151.142	150.315	-0.827
3	62	151.076	151.420	0.344
3	63	143.219	143.068	-0.151
3	64	150.915	151.005	0.090
3	65	140.364	139.698	-0.666
3	66	140.653	140.971	0.318
5	67	148.308	148.240	-0.068
5	68	148.000	148.294	0.294
5	69	138.014	138.755	0.741
5	70	157.440	157.497	0.057
5	71	156.018	155.779	-0.239
5	72	151.268	152.119	0.851
10	73	151.142	150.572	-0.570
10	74	151.076	150.197	-0.879
10	75	143.219	143.815	0.596
10	76	150.915	151.796	0.881
10	77	140.364	139.632	-0.732
10	78	140.653	141.161	0.508
15	79	148.308	149.568	1.260
15	80	148.000	148.542	0.542
15	81	138.014	138.554	0.540
15	82	157.440	156.968	-0.472
15	83	156.018	157.506	1.488
15	84	151.268	153.289	2.021
30	86	151.142	152.131	0.989
30	89	151.076	152.012	0.936
30	91	143.219	143.465	0.246
30	92	150.915	149.172	-1.743
30	94	140.364	138.910	-1.454
30	95	140.653	142.042	1.389
35	100	148.308	150.736	2.428
35	101	148.000	151.284	3.284
35	102	138.014	140.048	2.034
35	103	157.440	157.470	0.030
35	104	156.018	156.251	0.233
35	105	151.268	153.157	1.889
50	106	141.837	143.460	1.623
50	107	145.852	148.955	3.103
50	108	145.361	148.584	3.223
50	109	148.365	153.140	4.775
50	110	145.448	146.940	1.492
50	111	140.712	141.543	0.831
55	113	145.549	145.600	0.051
55	114	154.708	155.747	1.039
55	115	152.045	153.389	1.344
55	116	147.833	148.834	1.001
55	126	161.156	160.659	-0.497
55	128	153.469	149.932	-3.537
100	129	147.386	150.323	2.937
100	131	144.541	145.135	0.594
100	132	145.153	142.517	-2.636
100	133	150.196	151.908	1.712
100	134	149.930	152.154	2.224
100	135	151.559	149.157	-2.402
100	138	146.704	147.956	1.252
100	139	143.249	147.158	3.909
100	140	148.448	149.313	0.865
100	141	146.932	148.073	1.141
100	142	149.375	150.268	0.893
100	143	149.355	154.566	5.211
100	144	147.727	149.375	1.648
100	220	152.619	153.882	1.263
100	221	139.870	143.701	3.831
100	224	140.788	141.168	0.380
100	225	145.108	146.901	1.793
100	226	143.843	147.746	3.903
100	228	146.180	151.488	5.308
100	232	142.707	146.207	3.500
100	233	148.801	151.828	3.027
100	236	136.601	139.338	2.737
100	238	146.480	150.216	3.736
100	243	140.947	140.511	-0.436
105	244	143.138	147.322	4.184
105	246	140.839	145.158	4.319
105	248	140.188	142.411	2.223
105	374	154.197	155.451	1.254
105	376	142.514	140.489	-2.025
Max		161.156	160.659	5.308
Average		147.177	148.275	1.098
Min		136.601	138.554	-3.537
Std Dev		5.410	5.344	1.775

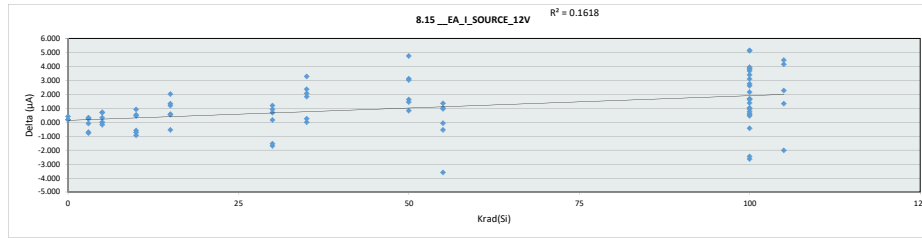


8.14_EA_I_SOURCE_SV											
Test Site											
Tester											
Test Number											
Max Limit	190	µA									
Min Limit	100	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	142.397	139.698	138.755	139.632	138.554	138.910	140.048	141.543	145.600	139.338	140.489
Average	146.022	146.080	150.114	146.196	150.738	146.289	151.491	147.104	152.360	147.954	146.166
Max	150.961	151.420	157.497	151.796	157.506	152.131	157.470	153.140	160.659	154.566	155.451
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

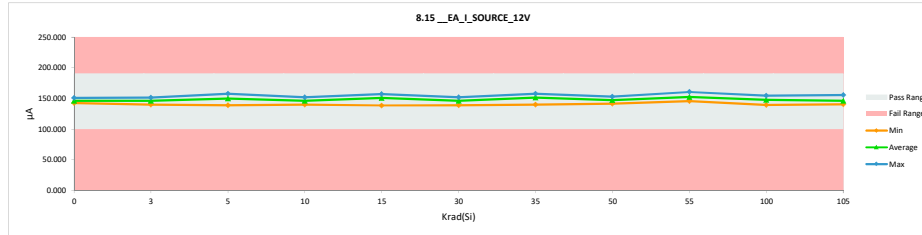


LDR TID Report
TPS7H5001-SP

8.15_EA_I_SOURCE_12V				
Test Site				
Tester				
Test Number				
Unit	µA	µA		
Max Limit	190	190		
Min Limit	100	100		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	142.269	142.456	0.187
0	118	144.699	144.876	0.177
0	122	150.635	151.057	0.422
3	61	151.213	150.441	-0.772
3	62	151.231	151.520	0.289
3	63	143.285	143.184	-0.101
3	64	150.936	151.164	0.228
3	65	140.503	139.794	-0.709
3	66	140.814	141.128	0.314
5	67	148.382	148.354	-0.028
5	68	148.033	148.349	0.316
5	69	138.129	138.820	0.691
5	70	157.610	157.620	0.010
5	71	156.078	155.899	-0.179
5	72	151.345	152.049	0.704
10	73	151.213	150.623	-0.590
10	74	151.231	150.293	-0.938
10	75	143.285	143.804	0.519
10	76	150.936	151.853	0.917
10	77	140.503	139.769	-0.734
10	78	140.814	141.277	0.463
15	79	148.382	149.566	1.184
15	80	148.033	148.610	0.577
15	81	138.129	138.688	0.559
15	82	157.610	157.051	-0.559
15	83	156.078	157.420	1.342
15	84	151.345	153.373	2.028
30	86	151.213	152.138	0.925
30	89	151.231	151.924	0.693
30	91	143.285	143.446	0.161
30	92	150.936	149.232	-1.704
30	94	140.503	138.971	-1.532
30	95	140.814	142.014	1.200
35	100	148.382	150.756	2.374
35	101	148.033	151.315	3.282
35	102	138.129	140.183	2.054
35	103	157.610	157.608	-0.002
35	104	156.078	156.331	0.253
35	105	151.345	153.184	1.839
50	106	141.868	143.491	1.623
50	107	146.073	149.200	3.127
50	108	145.504	148.542	3.038
50	109	148.391	153.147	4.756
50	110	145.612	147.058	1.446
50	111	140.913	141.733	0.820
55	113	145.737	145.678	-0.059
55	114	154.751	155.760	1.009
55	115	151.965	153.330	1.365
55	116	147.970	148.944	0.974
55	126	161.258	160.711	-0.547
55	128	153.559	149.951	-3.608
100	129	147.567	150.309	2.742
100	131	144.639	145.175	0.536
100	132	145.160	142.530	-2.630
100	133	150.303	151.988	1.685
100	134	150.026	152.187	2.161
100	135	151.672	149.218	-2.454
100	138	146.691	148.066	1.375
100	139	143.342	147.133	3.791
100	140	148.742	149.372	0.630
100	141	147.227	148.266	1.039
100	142	149.455	150.217	0.762
100	143	149.482	154.630	5.148
100	144	147.838	149.471	1.633
100	220	152.696	153.645	0.949
100	221	139.929	143.794	3.865
100	224	140.853	141.312	0.459
100	225	145.269	146.940	1.671
100	226	143.990	147.940	3.950
100	228	146.301	151.448	5.147
100	232	142.825	146.224	3.399
100	233	148.804	151.912	3.108
100	236	136.872	139.477	2.605
100	238	146.531	150.196	3.665
100	243	141.116	140.687	-0.429
105	244	143.336	147.495	4.159
105	246	140.912	145.367	4.455
105	248	140.304	142.573	2.269
105	374	154.309	155.640	1.331
105	376	142.728	140.720	-2.008
	Max	161.258	160.711	5.148
	Average	147.285	148.345	1.060
	Min	136.872	138.688	-3.608
	Std Dev	5.395	5.319	1.768

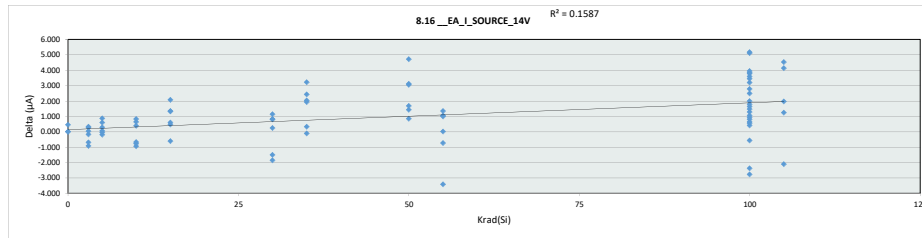


8.15_EA_I_SOURCE_12V											
Test Site											
Tester											
Test Number											
Max Limit	190	µA									
Min Limit	100	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	142.456	139.794	138.820	139.769	138.688	138.971	140.183	141.733	145.678	139.477	140.720
Average	146.130	146.205	150.182	146.270	150.785	146.288	151.563	147.195	152.396	148.006	146.359
Max	151.057	151.520	157.620	151.853	157.420	152.138	157.608	153.147	160.711	154.630	155.640
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

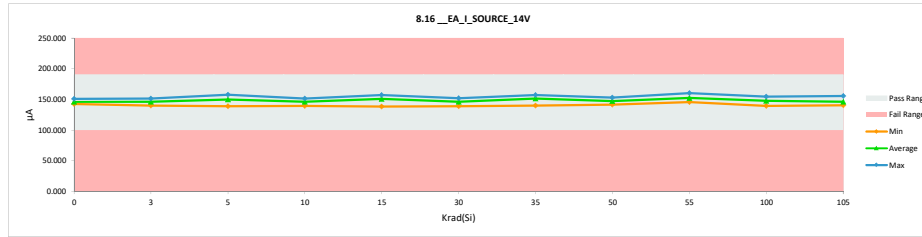


LDR TID Report
TPS7H5001-SP

8.16_EA_I_SOURCE_14V				
Test Site				
Tester				
Test Number				
Unit	µA	µA		
Max Limit	190	190		
Min Limit	100	100		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	142.401	142.420	0.019
0	118	144.644	144.641	-0.003
0	122	150.561	151.022	0.461
3	61	151.180	150.269	-0.911
3	62	151.147	151.483	0.336
3	63	143.236	143.067	-0.169
3	64	150.870	150.900	0.030
3	65	140.498	139.803	-0.695
3	66	140.793	141.064	0.271
5	67	148.223	148.280	0.057
5	68	147.968	148.264	0.296
5	69	138.127	138.736	0.609
5	70	157.546	157.510	-0.036
5	71	156.024	155.834	-0.190
5	72	151.279	152.140	0.861
10	73	151.180	150.518	-0.662
10	74	151.147	150.211	-0.936
10	75	143.236	143.876	0.640
10	76	150.870	151.704	0.834
10	77	140.498	139.725	-0.773
10	78	140.793	141.188	0.395
15	79	148.223	149.565	1.342
15	80	147.968	148.559	0.591
15	81	138.127	138.600	0.473
15	82	157.546	156.942	-0.604
15	83	156.024	157.355	1.331
15	84	151.279	153.350	2.071
30	86	151.180	152.002	0.822
30	89	151.147	151.964	0.817
30	91	143.236	143.479	0.243
30	92	150.870	149.014	-1.856
30	94	140.498	138.990	-1.508
30	95	140.793	141.941	1.148
35	100	148.223	150.661	2.438
35	101	147.968	151.190	3.222
35	102	138.127	140.163	2.036
35	103	157.546	157.438	-0.108
35	104	156.024	156.351	0.327
35	105	151.279	153.214	1.935
50	106	141.867	143.547	1.680
50	107	145.981	149.042	3.061
50	108	145.466	148.573	3.107
50	109	148.441	153.164	4.723
50	110	145.598	147.020	1.422
50	111	140.791	141.642	0.851
55	113	145.596	145.623	0.027
55	114	154.741	155.735	0.994
55	115	152.003	153.360	1.357
55	116	147.907	148.961	1.054
55	126	161.248	160.518	-0.730
55	128	153.467	150.063	-3.404
100	129	147.495	150.287	2.792
100	131	144.585	145.222	0.637
100	132	145.232	142.454	-2.778
100	133	150.148	151.943	1.795
100	134	150.024	152.016	1.992
100	135	151.574	149.207	-2.367
100	138	146.730	148.024	1.294
100	139	143.358	147.133	3.775
100	140	148.710	149.130	0.420
100	141	147.167	148.092	0.925
100	142	149.442	150.259	0.817
100	143	149.477	154.656	5.179
100	144	147.881	149.357	1.476
100	220	152.673	153.733	1.060
100	221	139.926	143.770	3.844
100	224	140.836	141.402	0.566
100	225	145.247	146.879	1.632
100	226	143.917	147.866	3.949
100	228	146.237	151.340	5.103
100	232	142.786	146.230	3.444
100	233	148.780	151.972	3.192
100	236	136.879	139.375	2.496
100	238	146.420	150.008	3.588
100	243	141.064	140.493	-0.571
105	244	143.296	147.425	4.129
105	246	140.793	145.319	4.526
105	248	140.428	142.393	1.965
105	374	154.289	155.542	1.253
105	376	142.636	140.533	-2.103
	Max	161.248	160.518	5.179
	Average	147.243	148.284	1.042
	Min	136.879	138.600	-3.404
	Std Dev	5.382	5.316	1.774

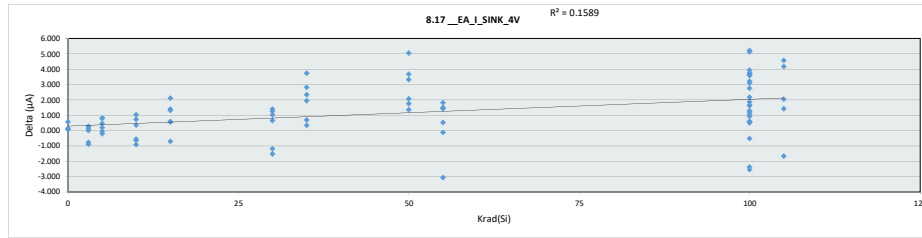


8.16_EA_I_SOURCE_14V											
Test Site											
Tester											
Test Number											
Max Limit	190	µA									
Min Limit	100	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	142.420	139.803	138.736	139.725	138.600	138.990	140.163	141.642	145.623	139.375	140.533
Average	146.028	146.098	150.127	146.204	150.729	146.232	151.503	147.165	152.377	147.952	146.242
Max	151.022	151.483	157.510	151.704	157.355	152.002	157.438	153.164	160.518	154.656	155.542
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

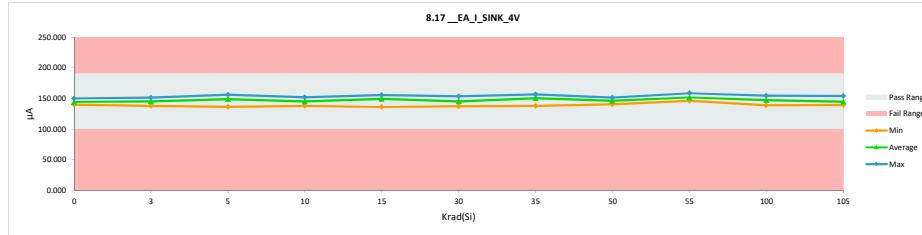


LDR TID Report
TPS7H5001-SP

8.17_EA_I_SINK_4V				
Test Site				
Tester				
Test Number				
Unit	µA	µA		
Max Limit	190	190		
Min Limit	100	100		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	140.018	140.172	0.154
0	118	142.967	143.040	0.073
0	122	149.556	150.124	0.568
3	61	152.333	151.445	-0.888
3	62	148.168	148.430	0.262
3	63	142.376	142.391	0.015
3	64	149.422	149.525	0.103
3	65	138.736	137.972	-0.764
3	66	140.251	140.533	0.282
5	67	147.276	147.466	0.190
5	68	147.179	147.642	0.463
5	69	135.543	136.340	0.797
5	70	156.191	156.124	-0.067
5	71	154.011	153.816	-0.195
5	72	151.596	152.411	0.815
10	73	152.333	151.770	-0.563
10	74	148.168	147.255	-0.913
10	75	142.376	143.104	0.728
10	76	149.422	150.455	1.033
10	77	138.736	138.097	-0.639
10	78	140.251	140.614	0.363
15	79	147.276	148.672	1.396
15	80	147.179	147.766	0.587
15	81	135.543	136.106	0.563
15	82	156.191	155.491	-0.700
15	83	154.011	155.323	1.312
15	84	151.596	153.716	2.120
30	86	152.333	153.590	1.257
30	89	148.168	149.185	1.017
30	91	142.376	143.020	0.644
30	92	149.422	147.902	-1.520
30	94	138.736	137.558	-1.178
30	95	140.251	141.652	1.401
35	100	147.276	150.098	2.822
35	101	147.179	150.911	3.732
35	102	135.543	137.879	2.336
35	103	156.191	156.521	0.330
35	104	154.011	154.712	0.701
35	105	151.596	153.547	1.951
50	106	141.086	143.151	2.065
50	107	144.483	147.801	3.318
50	108	144.210	147.874	3.664
50	109	146.625	151.680	5.055
50	110	144.611	146.375	1.764
50	111	139.249	140.600	1.351
55	113	145.667	146.197	0.530
55	114	153.230	154.745	1.515
55	115	150.295	152.112	1.817
55	116	147.675	149.100	1.425
55	126	156.700	158.577	-0.123
55	128	152.617	149.549	-3.068
100	129	148.214	151.298	3.084
100	131	144.315	144.898	0.583
100	132	145.720	143.173	-2.547
100	133	149.941	151.594	1.653
100	134	149.543	151.714	2.171
100	135	149.652	147.289	-2.363
100	138	146.184	147.483	1.299
100	139	141.481	145.116	3.635
100	140	147.730	148.311	0.581
100	141	146.837	147.913	1.076
100	142	147.849	148.765	0.916
100	143	149.496	154.637	5.141
100	144	146.787	148.425	1.638
100	220	151.690	152.908	1.218
100	221	139.826	143.497	3.671
100	224	140.621	141.117	0.496
100	225	145.560	147.423	1.863
100	226	143.464	147.416	3.952
100	228	144.977	150.188	5.211
100	232	140.777	144.371	3.594
100	233	147.128	150.348	3.220
100	236	136.064	138.815	2.751
100	238	144.559	148.334	3.775
100	243	140.298	139.781	-0.517
105	244	140.434	144.603	4.169
105	246	139.344	143.905	4.561
105	248	139.197	141.253	2.056
105	374	152.775	154.191	1.416
105	376	141.010	139.347	-1.663
	Max	158.700	158.577	5.211
	Average	146.146	147.353	1.207
	Min	135.543	136.106	-3.068
	Std Dev	5.471	5.422	1.766

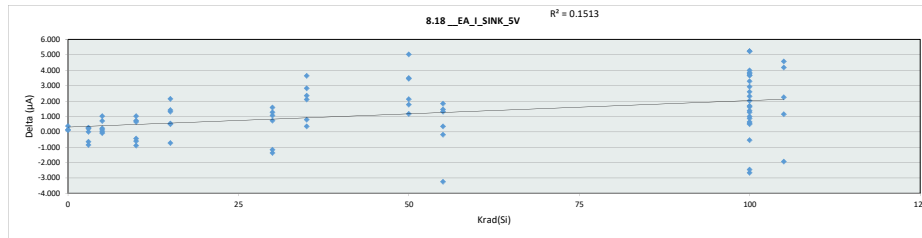


8.17_EA_I_SINK_4V											
Test Site											
Tester											
Test Number											
Max Limit	190	µA									
Min Limit	100	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	140.172	137.972	136.340	138.097	136.106	137.558	137.879	140.600	146.197	138.815	139.347
Average	144.445	145.049	148.967	145.216	149.512	145.485	150.611	146.247	151.713	147.284	144.660
Max	150.124	151.445	156.124	151.770	155.491	153.590	156.521	151.680	158.577	154.637	154.191
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

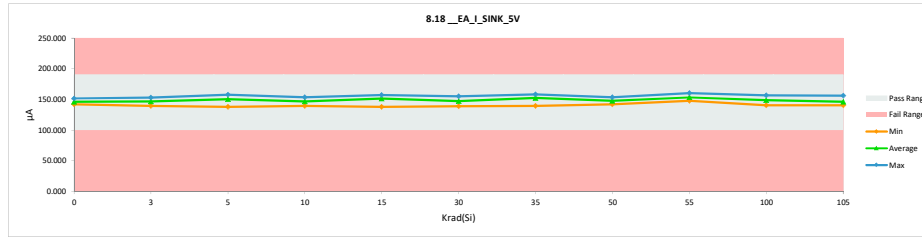


LDR TID Report
TPS7H5001-SP

8.18_EA_I_SINK_5V				
Test Site				
Tester				
Test Number				
Unit	µA	µA		
Max Limit	190	190		
Min Limit	100	100		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	141.845	141.951	0.106
0	118	144.567	144.704	0.137
0	122	151.324	151.699	0.375
3	61	154.118	153.261	-0.857
3	62	149.835	150.105	0.270
3	63	144.177	144.181	0.004
3	64	151.003	151.207	0.204
3	65	140.295	139.647	-0.648
3	66	141.977	142.221	0.244
5	67	148.969	149.083	0.114
5	68	149.037	149.258	0.221
5	69	137.352	138.046	0.694
5	70	157.986	157.977	-0.009
5	71	155.732	155.637	-0.095
5	72	153.292	154.311	1.019
10	73	154.118	153.670	-0.448
10	74	149.835	148.934	-0.901
10	75	144.177	144.896	0.719
10	76	151.003	152.024	1.021
10	77	140.295	139.681	-0.614
10	78	141.977	142.614	0.637
15	79	148.969	150.277	1.308
15	80	149.037	149.575	0.538
15	81	137.352	137.852	0.500
15	82	157.986	157.256	-0.730
15	83	155.732	157.150	1.418
15	84	153.292	155.439	2.147
30	86	154.118	155.381	1.263
30	89	149.835	150.896	1.061
30	91	144.177	144.895	0.718
30	92	151.003	149.637	-1.366
30	94	140.295	139.123	-1.172
30	95	141.977	143.555	1.578
35	100	148.969	151.796	2.827
35	101	149.037	152.671	3.634
35	102	137.352	139.445	2.093
35	103	157.986	158.329	0.343
35	104	155.732	156.520	0.788
35	105	153.292	155.647	2.355
50	106	142.742	144.857	2.115
50	107	146.312	149.759	3.447
50	108	146.019	149.512	3.493
50	109	148.334	153.360	5.026
50	110	146.378	148.144	1.766
50	111	141.073	142.242	1.169
55	113	147.581	147.931	0.350
55	114	155.024	156.476	1.452
55	115	152.045	153.878	1.833
55	116	149.290	150.594	1.304
55	126	160.462	160.276	-0.186
55	128	154.497	151.253	-3.244
100	129	150.153	153.079	2.926
100	131	146.071	146.635	0.564
100	132	147.512	144.852	-2.660
100	133	151.693	153.369	1.676
100	134	151.210	153.521	2.311
100	135	151.431	148.969	-2.462
100	138	147.885	149.252	1.367
100	139	143.029	146.805	3.776
100	140	149.629	150.135	0.506
100	141	148.739	149.736	0.997
100	142	149.627	150.501	0.874
100	143	151.304	156.550	5.246
100	144	148.652	150.271	1.619
100	220	153.552	154.808	1.256
100	221	141.409	145.256	3.847
100	224	142.244	142.893	0.649
100	225	147.208	149.217	2.009
100	226	145.156	149.150	3.994
100	228	146.783	152.024	5.241
100	232	142.562	146.223	3.661
100	233	149.032	152.310	3.278
100	236	137.731	140.329	2.598
100	238	146.393	150.064	3.671
100	243	141.911	141.369	-0.542
105	244	142.110	146.278	4.168
105	246	141.136	145.698	4.562
105	248	140.868	143.115	2.247
105	374	154.847	155.984	1.137
105	376	142.677	140.746	-1.931
Max		160.462	160.276	5.246
Average		147.892	149.100	1.208
Min		137.352	137.852	-3.244
Std Dev		5.496	5.464	1.785

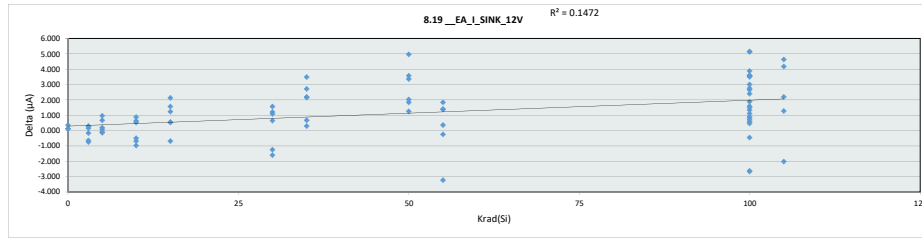


8.18_EA_I_SINK_5V											
Test Site											
Tester											
Test Number											
Max Limit	190	µA									
Min Limit	100	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	141.951	139.647	138.046	139.681	137.852	139.123	139.445	142.242	147.931	140.329	140.746
Average	146.118	146.770	150.719	146.970	151.258	147.248	152.401	147.979	153.401	149.055	146.364
Max	151.699	153.261	157.977	153.670	157.256	155.381	158.329	153.360	160.276	156.550	155.984
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

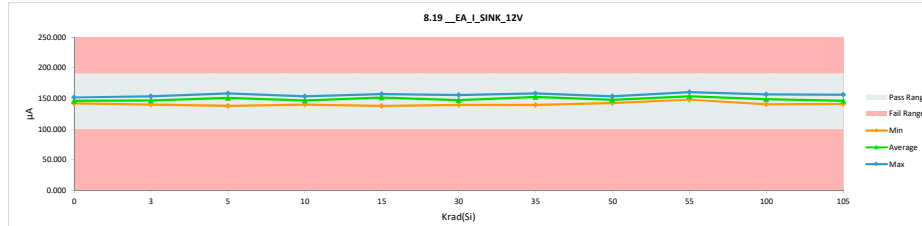


LDR TID Report
TPS7H5001-SP

8.19_EA_I_SINK_12V				
Test Site				
Tester				
Test Number				
Unit	µA	µA		
Max Limit	190	190		
Min Limit	100	100		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	141.912	142.038	0.126
0	118	144.694	144.812	0.118
0	122	151.458	151.822	0.364
3	61	154.236	153.489	-0.747
3	62	150.007	150.312	0.305
3	63	144.245	144.089	-0.156
3	64	151.257	151.398	0.141
3	65	140.511	139.871	-0.640
3	66	142.085	142.369	0.284
5	67	149.118	149.198	0.080
5	68	149.139	149.335	0.196
5	69	137.458	138.128	0.670
5	70	158.143	158.067	-0.076
5	71	155.829	155.691	-0.138
5	72	153.489	154.447	0.958
10	73	154.236	153.750	-0.486
10	74	150.007	149.024	-0.983
10	75	144.245	144.871	0.626
10	76	151.257	152.136	0.879
10	77	140.511	139.838	-0.673
10	78	142.085	142.620	0.535
15	79	149.118	150.345	1.227
15	80	149.139	149.679	0.540
15	81	137.458	138.003	0.545
15	82	158.143	157.452	-0.691
15	83	155.829	157.394	1.565
15	84	153.489	155.623	2.134
30	86	154.236	155.449	1.213
30	89	150.007	151.104	1.097
30	91	144.245	144.899	0.654
30	92	151.257	149.663	-1.594
30	94	140.511	139.272	-1.239
30	95	142.085	143.662	1.577
35	100	149.118	151.832	2.714
35	101	149.139	152.631	3.492
35	102	137.458	139.617	2.159
35	103	158.143	158.437	0.294
35	104	155.829	156.511	0.682
35	105	153.489	155.673	2.184
50	106	142.884	144.917	2.033
50	107	146.447	149.813	3.366
50	108	146.137	149.709	3.572
50	109	148.412	153.384	4.972
50	110	146.542	148.376	1.834
50	111	141.178	142.428	1.250
55	113	147.727	148.082	0.355
55	114	155.100	156.510	1.410
55	115	152.234	154.065	1.831
55	116	149.392	150.780	1.388
55	126	160.623	160.371	-0.252
55	128	154.528	151.305	-3.223
100	129	150.271	153.028	2.757
100	131	146.180	146.741	0.561
100	132	147.602	144.943	-2.659
100	133	151.961	153.551	1.590
100	134	151.302	153.695	2.393
100	135	151.687	149.069	-2.618
100	138	147.996	149.341	1.345
100	139	143.120	146.660	3.540
100	140	149.779	150.248	0.469
100	141	148.889	149.799	0.910
100	142	149.734	150.548	0.814
100	143	151.401	156.543	5.142
100	144	148.704	150.200	1.496
100	220	153.704	154.809	1.105
100	221	141.642	145.237	3.595
100	224	142.311	142.996	0.685
100	225	147.413	149.291	1.878
100	226	145.314	149.204	3.890
100	228	146.865	152.021	5.156
100	232	142.626	146.207	3.581
100	233	149.205	152.204	2.999
100	236	137.838	140.494	2.656
100	238	146.468	149.976	3.508
100	243	142.022	141.559	-0.463
105	244	142.265	146.439	4.174
105	246	141.130	145.762	4.632
105	248	141.093	143.279	2.186
105	374	154.981	156.265	1.284
105	376	142.966	140.952	-2.014
	Max	160.623	160.371	5.156
	Average	148.029	149.192	1.163
	Min	137.458	138.003	-3.223
	Std Dev	5.503	5.456	1.770

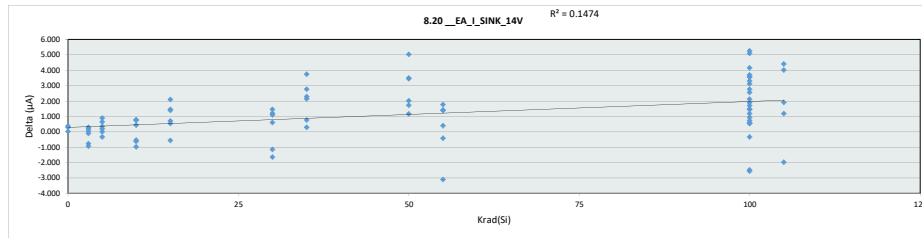


8.19_EA_I_SINK_12V											
Test Site											
Tester											
Test Number											
Max Limit	190	µA									
Min Limit	100	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	142.038	139.871	138.128	139.838	138.003	139.272	139.617	142.428	148.082	140.494	140.952
Average	146.224	146.921	150.811	147.040	151.416	147.342	152.450	148.105	153.519	149.099	146.539
Max	151.822	153.489	158.067	153.750	157.452	155.449	158.437	153.384	160.371	156.543	156.265
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

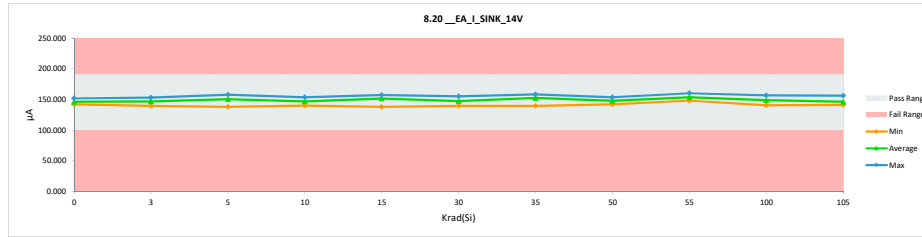


LDR TID Report
TPS7H5001-SP

8.20_EA_I_SINK_14V				
Test Site				
Tester				
Test Number				
Unit	µA	µA		
Max Limit	190	190		
Min Limit	100	100		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	141.925	142.215	0.290
0	118	144.791	144.812	0.021
0	122	151.436	151.799	0.363
3	61	154.209	153.280	-0.929
3	62	149.929	150.217	0.288
3	63	144.207	144.095	-0.109
3	64	151.270	151.309	0.039
3	65	140.457	139.680	-0.777
3	66	142.084	142.269	0.185
5	67	148.947	149.121	0.174
5	68	148.949	149.289	0.340
5	69	137.447	138.097	0.650
5	70	157.972	157.973	0.001
5	71	155.760	155.426	-0.334
5	72	153.439	154.328	0.889
10	73	154.209	153.664	-0.545
10	74	149.929	148.939	-0.990
10	75	144.207	144.960	0.753
10	76	151.270	152.048	0.778
10	77	140.457	139.837	-0.620
10	78	142.084	142.524	0.440
15	79	148.947	150.329	1.382
15	80	148.949	149.654	0.705
15	81	137.447	137.985	0.538
15	82	157.972	157.401	-0.571
15	83	155.760	157.220	1.460
15	84	153.439	155.545	2.106
30	86	154.209	155.418	1.209
30	89	149.929	151.022	1.093
30	91	144.207	144.800	0.593
30	92	151.270	149.616	-1.654
30	94	140.457	139.304	-1.153
30	95	142.084	143.547	1.463
35	100	148.947	151.719	2.772
35	101	148.949	152.687	3.738
35	102	137.447	139.593	2.146
35	103	157.972	158.251	0.279
35	104	155.760	156.526	0.766
35	105	153.439	155.730	2.291
50	106	142.888	144.895	2.007
50	107	146.460	149.945	3.485
50	108	146.010	149.455	3.445
50	109	148.325	153.358	5.033
50	110	146.582	148.308	1.726
50	111	141.168	142.336	1.168
55	113	147.660	148.044	0.384
55	114	155.106	156.503	1.397
55	115	152.197	153.954	1.757
55	116	149.295	150.711	1.416
55	126	160.594	160.176	-0.418
55	128	154.532	151.430	-3.102
100	129	150.204	152.971	2.767
100	131	146.173	146.731	0.558
100	132	147.569	144.998	-2.571
100	133	151.851	153.544	1.693
100	134	151.382	153.507	2.125
100	135	151.492	149.020	-2.472
100	138	147.915	149.373	1.458
100	139	143.079	146.626	3.547
100	140	149.696	150.230	0.534
100	141	148.890	149.613	0.723
100	142	149.630	150.566	0.936
100	143	151.425	156.681	5.256
100	144	148.709	150.191	1.482
100	220	153.626	154.818	1.192
100	221	141.598	145.290	3.692
100	224	142.451	143.024	0.573
100	225	147.407	149.290	1.883
100	226	145.230	149.390	4.160
100	228	146.896	151.992	5.096
100	232	142.615	146.185	3.570
100	233	149.156	152.268	3.112
100	236	137.872	140.433	2.561
100	238	146.488	149.793	3.305
100	243	141.998	141.666	-0.332
105	244	142.330	146.332	4.002
105	246	141.183	145.586	4.403
105	248	141.122	143.036	1.914
105	374	154.961	156.143	1.182
105	376	142.790	140.806	-1.984
Max		160.594	160.176	5.256
Average		147.984	149.143	1.159
Min		137.447	137.985	-3.102
Std Dev		5.479	5.442	1.762

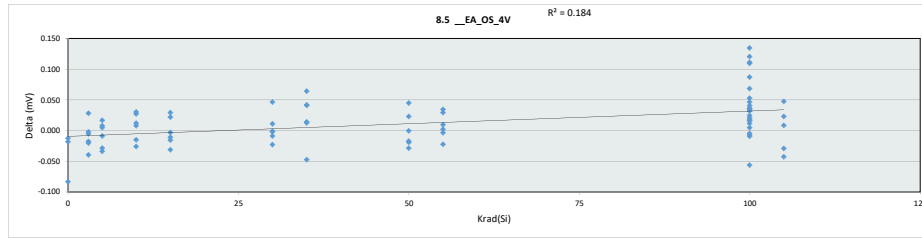


8.20_EA_I_SINK_14V											
Test Site											
Tester											
Test Number											
Max Limit	190	µA									
Min Limit	100	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	142.215	139.680	138.097	139.837	137.985	139.304	139.593	142.336	148.044	140.433	140.806
Average	146.275	146.809	150.706	146.995	151.356	147.285	152.418	148.050	153.470	149.092	146.381
Max	151.799	153.280	157.973	153.664	157.401	155.418	158.251	153.358	160.176	156.681	156.143
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

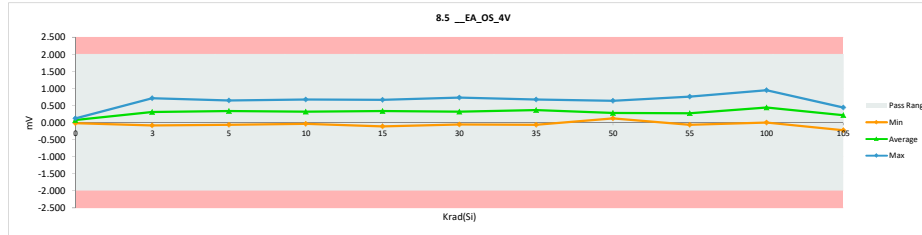


LDR TID Report
TPS7H5001-SP

8.5_EA_OS_4V				
Test Site				
Tester				
Test Number				
Unit	mV		mV	
Max Limit	2		2	
Min Limit	-2		-2	
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	-0.003	-0.021	-0.018
0	118	0.137	0.125	-0.013
0	122	0.208	0.125	-0.083
3	61	0.688	0.716	0.028
3	62	0.010	-0.029	-0.040
3	63	0.373	0.353	-0.020
3	64	0.318	0.313	-0.005
3	65	-0.064	-0.081	-0.018
3	66	0.576	0.573	-0.002
5	67	0.393	0.398	0.005
5	68	0.455	0.426	-0.029
5	69	-0.080	-0.064	0.017
5	70	0.407	0.398	-0.009
5	71	0.256	0.222	-0.034
5	72	0.639	0.647	0.008
10	73	0.688	0.673	-0.015
10	74	0.010	0.019	0.008
10	75	0.373	0.385	0.012
10	76	0.318	0.292	-0.026
10	77	-0.064	-0.036	0.027
10	78	0.576	0.606	0.030
15	79	0.393	0.378	-0.015
15	80	0.455	0.477	0.022
15	81	-0.080	-0.112	-0.031
15	82	0.407	0.404	-0.003
15	83	0.256	0.245	-0.011
15	84	0.639	0.668	0.029
30	86	0.688	0.734	0.046
30	89	0.010	0.009	-0.001
30	91	0.373	0.372	-0.001
30	92	0.318	0.309	-0.009
30	94	-0.064	-0.052	0.011
30	95	0.576	0.553	-0.023
35	100	0.393	0.457	0.064
35	101	0.455	0.496	0.041
35	102	-0.080	-0.066	0.014
35	103	0.407	0.360	-0.047
35	104	0.256	0.269	0.012
35	105	0.639	0.681	0.042
50	106	0.594	0.638	0.045
50	107	0.125	0.124	-0.000
50	108	0.236	0.260	0.023
50	109	0.140	0.123	-0.017
50	110	0.364	0.345	-0.019
50	111	0.226	0.197	-0.028
55	113	0.753	0.762	0.010
55	114	0.089	0.067	-0.022
55	115	0.094	0.096	0.002
55	116	0.328	0.362	0.035
55	126	-0.059	-0.062	-0.004
55	128	0.370	0.399	0.029
100	129	0.692	0.704	0.012
100	131	0.612	0.658	0.047
100	132	0.729	0.840	0.111
100	133	0.483	0.518	0.035
100	134	0.542	0.651	0.109
100	135	0.063	0.067	0.005
100	138	0.415	0.484	0.069
100	139	0.037	0.028	-0.009
100	140	0.299	0.324	0.025
100	141	0.493	0.514	0.021
100	142	0.361	0.305	-0.056
100	143	0.488	0.525	0.037
100	144	0.471	0.524	0.053
100	220	0.390	0.407	0.017
100	221	0.380	0.412	0.032
100	224	0.494	0.581	0.087
100	225	0.812	0.947	0.135
100	226	0.436	0.454	0.018
100	228	0.273	0.289	0.016
100	232	-0.118	0.003	0.120
100	233	0.187	0.228	0.041
100	236	0.461	0.456	-0.005
100	238	0.334	0.327	-0.007
100	243	0.285	0.319	0.034
105	244	-0.195	-0.224	-0.029
105	246	0.219	0.266	0.047
105	248	0.432	0.440	0.008
105	374	0.239	0.262	0.023
105	376	0.386	0.344	-0.042
	Max	0.812	0.947	0.135
	Average	0.328	0.340	0.012
	Min	-0.195	-0.224	-0.029
	Std Dev	0.239	0.254	0.039

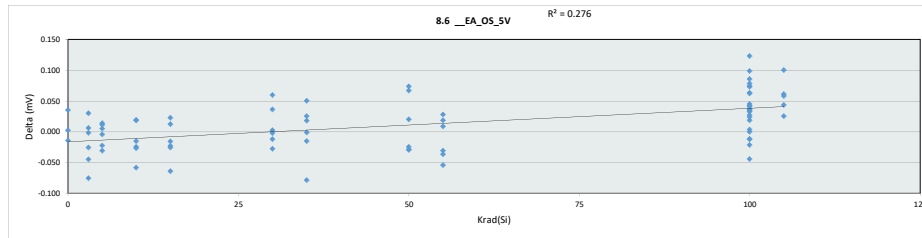


8.5_EA_OS_4V											
Test Site											
Tester											
Test Number											
Max Limit	2		mV								
Min Limit	-2		mV								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	-0.021	-0.081	-0.064	-0.036	-0.112	-0.052	-0.066	0.123	-0.062	0.003	-0.224
Average	0.076	0.308	0.338	0.323	0.343	0.321	0.366	0.281	0.271	0.440	0.218
Max	0.125	0.717	0.647	0.673	0.668	0.735	0.681	0.639	0.762	0.947	0.440
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000

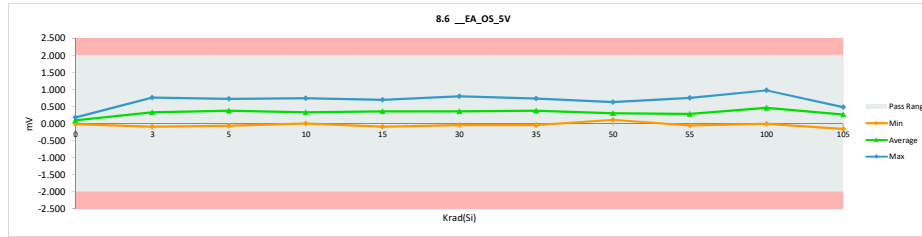


LDR TID Report
TPS7H5001-SP

8.6_EA_OS_5V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	2	2		
Min Limit	-2	-2		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	-0.023	-0.021	0.002
0	118	0.144	0.129	-0.014
0	122	0.146	0.181	0.036
3	61	0.760	0.759	-0.002
3	62	-0.021	0.009	0.030
3	63	0.389	0.363	-0.025
3	64	0.361	0.368	0.006
3	65	-0.016	-0.091	-0.075
3	66	0.591	0.547	-0.045
5	67	0.455	0.433	-0.023
5	68	0.519	0.488	-0.031
5	69	-0.069	-0.064	0.005
5	70	0.386	0.400	0.014
5	71	0.264	0.260	-0.004
5	72	0.713	0.724	0.011
10	73	0.760	0.745	-0.015
10	74	-0.021	-0.003	0.018
10	75	0.389	0.362	-0.027
10	76	0.361	0.337	-0.024
10	77	-0.016	0.004	0.019
10	78	0.591	0.533	-0.058
15	79	0.455	0.430	-0.025
15	80	0.519	0.455	-0.064
15	81	-0.069	-0.092	-0.023
15	82	0.386	0.409	0.023
15	83	0.264	0.277	0.012
15	84	0.713	0.697	-0.016
30	86	0.760	0.797	0.036
30	89	-0.021	0.038	0.060
30	91	0.389	0.377	-0.012
30	92	0.361	0.359	-0.002
30	94	-0.016	-0.044	-0.028
30	95	0.591	0.594	0.003
35	100	0.455	0.440	-0.015
35	101	0.519	0.518	-0.001
35	102	-0.069	-0.044	0.025
35	103	0.386	0.436	0.050
35	104	0.264	0.186	-0.078
35	105	0.713	0.731	0.018
50	106	0.657	0.628	-0.029
50	107	0.132	0.108	-0.024
50	108	0.270	0.290	0.020
50	109	0.100	0.174	0.074
50	110	0.404	0.375	-0.029
50	111	0.171	0.238	0.067
55	113	0.802	0.748	-0.054
55	114	0.091	0.110	0.019
55	115	0.146	0.109	-0.037
55	116	0.353	0.381	0.028
55	126	-0.026	-0.057	-0.031
55	128	0.390	0.399	0.009
100	129	0.648	0.721	0.073
100	131	0.645	0.677	0.033
100	132	0.817	0.858	0.041
100	133	0.558	0.620	0.062
100	134	0.592	0.691	0.099
100	135	0.057	0.135	0.078
100	138	0.475	0.499	0.024
100	139	0.083	0.061	-0.022
100	140	0.277	0.320	0.042
100	141	0.529	0.532	0.003
100	142	0.348	0.348	0.000
100	143	0.511	0.529	0.019
100	144	0.471	0.516	0.045
100	220	0.399	0.433	0.034
100	221	0.407	0.434	0.027
100	224	0.617	0.573	-0.044
100	225	0.850	0.974	0.123
100	226	0.484	0.472	-0.012
100	228	0.226	0.300	0.074
100	232	-0.067	-0.004	0.063
100	233	0.209	0.197	-0.012
100	236	0.424	0.510	0.086
100	238	0.323	0.360	0.037
100	243	0.331	0.367	0.036
105	244	-0.186	-0.160	0.026
105	246	0.203	0.247	0.044
105	248	0.378	0.479	0.100
105	374	0.249	0.310	0.061
105	376	0.365	0.423	0.058
	Max	0.850	0.974	0.123
	Average	0.350	0.362	0.012
	Min	-0.186	-0.160	0.078
	Std Dev	0.255	0.257	0.042

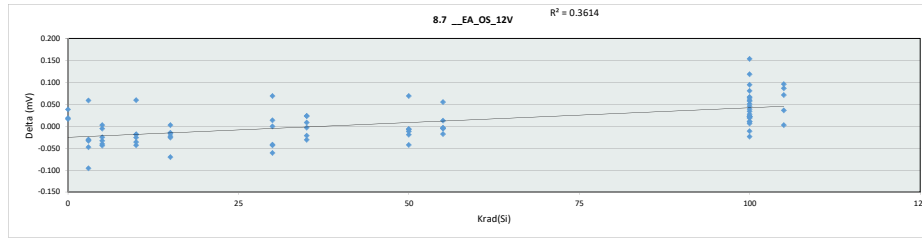


8.6_EA_OS_5V											
Test Site											
Tester											
Test Number											
Max Limit	2	mV									
Min Limit	-2	mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	-0.021	-0.091	-0.064	-0.003	-0.092	-0.044	-0.044	0.108	-0.057	-0.004	-0.160
Average	0.097	0.326	0.373	0.330	0.363	0.354	0.378	0.302	0.281	0.464	0.260
Max	0.181	0.759	0.724	0.745	0.745	0.697	0.797	0.731	0.628	0.748	0.479
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000

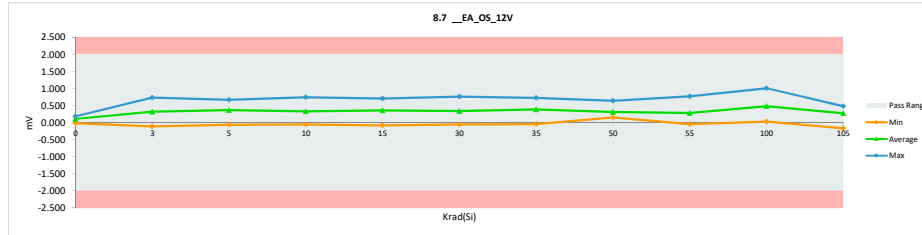


LDR TID Report
TPS7H5001-SP

8.7_EA_OS_12V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	2	2		
Min Limit	-2	-2		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	-0.038	-0.019	0.019
0	118	0.125	0.163	0.038
0	122	0.167	0.183	0.016
3	61	0.761	0.729	-0.032
3	62	0.043	0.013	-0.031
3	63	0.425	0.377	-0.048
3	64	0.288	0.346	0.058
3	65	-0.014	-0.110	-0.096
3	66	0.584	0.551	-0.033
5	67	0.450	0.417	-0.033
5	68	0.526	0.482	-0.044
5	69	-0.065	-0.063	0.003
5	70	0.424	0.419	-0.005
5	71	0.286	0.261	-0.025
5	72	0.705	0.664	-0.040
10	73	0.761	0.742	-0.018
10	74	0.043	0.024	-0.019
10	75	0.425	0.399	-0.026
10	76	0.288	0.347	0.060
10	77	-0.014	-0.057	-0.043
10	78	0.584	0.548	-0.036
15	79	0.450	0.436	-0.015
15	80	0.526	0.455	-0.071
15	81	-0.065	-0.083	-0.017
15	82	0.424	0.398	-0.026
15	83	0.286	0.263	-0.023
15	84	0.705	0.707	0.003
30	86	0.761	0.760	0.000
30	89	0.043	-0.018	-0.061
30	91	0.425	0.383	-0.042
30	92	0.288	0.356	0.069
30	94	-0.014	-0.058	-0.044
30	95	0.584	0.598	0.014
35	100	0.450	0.459	0.008
35	101	0.526	0.522	-0.004
35	102	-0.065	-0.042	0.023
35	103	0.424	0.394	-0.031
35	104	0.286	0.264	-0.022
35	105	0.705	0.728	0.023
50	106	0.644	0.637	-0.007
50	107	0.166	0.152	-0.014
50	108	0.289	0.247	-0.043
50	109	0.104	0.173	0.069
50	110	0.407	0.400	-0.008
50	111	0.265	0.246	-0.019
55	113	0.780	0.775	-0.005
55	114	0.107	0.120	0.013
55	115	0.070	0.065	-0.005
55	116	0.334	0.389	0.055
55	126	-0.028	-0.046	-0.018
55	128	0.404	0.401	-0.003
100	129	0.679	0.685	0.006
100	131	0.662	0.682	0.021
100	132	0.796	0.829	0.034
100	133	0.555	0.577	0.022
100	134	0.622	0.702	0.080
100	135	0.073	0.131	0.058
100	138	0.515	0.503	-0.011
100	139	0.077	0.087	0.010
100	140	0.308	0.359	0.051
100	141	0.554	0.599	0.045
100	142	0.342	0.352	0.011
100	143	0.511	0.532	0.021
100	144	0.456	0.575	0.119
100	220	0.399	0.422	0.023
100	221	0.388	0.454	0.067
100	224	0.581	0.620	0.039
100	225	0.854	1.008	0.154
100	226	0.478	0.496	0.018
100	228	0.310	0.286	-0.023
100	232	-0.066	0.029	0.095
100	233	0.198	0.263	0.065
100	236	0.490	0.512	0.022
100	238	0.347	0.374	0.027
100	243	0.315	0.375	0.060
105	244	-0.172	-0.169	0.002
105	246	0.260	0.296	0.036
105	248	0.380	0.476	0.096
105	374	0.242	0.313	0.071
105	376	0.351	0.438	0.087
	Max	0.854	1.008	0.154
	Average	0.357	0.366	0.010
	Min	-0.172	-0.169	-0.006
	Std Dev	0.250	0.257	0.045

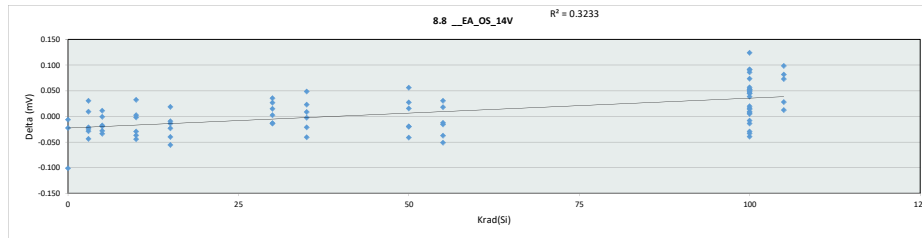


8.7_EA_OS_12V											
Test Site											
Tester											
Test Number											
Max Limit	2	mV									
Min Limit	-2	mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	-0.019	-0.110	-0.063	-0.057	-0.083	-0.058	-0.042	0.154	-0.046	0.029	-0.169
Average	0.109	0.318	0.363	0.334	0.363	0.337	0.387	0.309	0.284	0.477	0.271
Max	0.183	0.729	0.665	0.742	0.708	0.760	0.728	0.637	0.775	1.008	0.476
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000

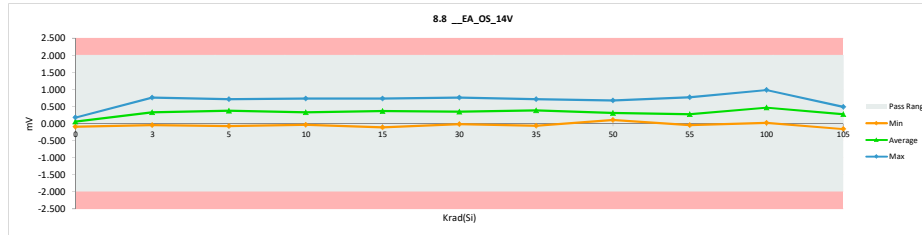


LDR TID Report
TPS7H5001-SP

8.8_EA_OS_14V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	2	2		
Min Limit	-2	-2		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	0.012	-0.090	-0.101
0	118	0.107	0.085	-0.022
0	122	0.182	0.176	-0.006
3	61	0.732	0.762	0.030
3	62	0.037	0.016	-0.021
3	63	0.402	0.373	-0.029
3	64	0.323	0.298	-0.024
3	65	-0.005	-0.049	-0.044
3	66	0.558	0.568	0.010
5	67	0.437	0.417	-0.020
5	68	0.526	0.492	-0.034
5	69	-0.086	-0.075	0.011
5	70	0.433	0.405	-0.028
5	71	0.301	0.283	-0.017
5	72	0.713	0.712	-0.001
10	73	0.732	0.730	-0.002
10	74	0.037	0.000	-0.037
10	75	0.402	0.358	-0.044
10	76	0.323	0.325	0.002
10	77	-0.005	-0.034	-0.029
10	78	0.558	0.590	0.032
15	79	0.437	0.428	-0.010
15	80	0.526	0.470	-0.056
15	81	-0.086	-0.109	-0.023
15	82	0.433	0.419	-0.014
15	83	0.301	0.260	-0.040
15	84	0.713	0.732	0.019
30	86	0.732	0.758	0.026
30	89	0.037	0.039	0.002
30	91	0.402	0.388	-0.014
30	92	0.323	0.338	0.015
30	94	-0.005	-0.018	-0.013
30	95	0.558	0.594	0.036
35	100	0.437	0.486	0.049
35	101	0.526	0.534	0.008
35	102	-0.086	-0.063	0.023
35	103	0.433	0.412	-0.021
35	104	0.301	0.260	-0.041
35	105	0.713	0.711	-0.003
50	106	0.646	0.674	0.027
50	107	0.145	-0.041	-0.041
50	108	0.297	0.277	-0.020
50	109	0.120	0.176	0.056
50	110	0.410	0.390	-0.020
50	111	0.218	0.234	0.016
55	113	0.782	0.766	-0.016
55	114	0.127	0.090	-0.037
55	115	0.123	0.072	-0.051
55	116	0.337	0.367	0.030
55	126	-0.033	-0.046	-0.013
55	128	0.403	0.421	0.018
100	129	0.694	0.680	-0.014
100	131	0.659	0.669	0.009
100	132	0.777	0.782	0.005
100	133	0.541	0.594	0.053
100	134	0.597	0.688	0.091
100	135	0.087	0.133	0.045
100	138	0.488	0.533	0.045
100	139	0.086	0.106	0.020
100	140	0.323	0.284	-0.039
100	141	0.532	0.581	0.049
100	142	0.363	0.334	-0.030
100	143	0.513	0.527	0.014
100	144	0.472	0.546	0.074
100	220	0.398	0.415	0.016
100	221	0.379	0.429	0.051
100	224	0.563	0.615	0.053
100	225	0.859	0.983	0.124
100	226	0.468	0.435	-0.033
100	228	0.307	0.299	-0.008
100	232	-0.069	0.022	0.091
100	233	0.190	0.276	0.086
100	236	0.471	0.528	0.057
100	238	0.336	0.374	0.039
100	243	0.352	0.359	0.007
105	244	-0.235	-0.162	0.073
105	246	0.210	0.308	0.098
105	248	0.409	0.490	0.082
105	374	0.301	0.313	0.013
105	376	0.381	0.409	0.028
	Max	0.859	0.983	0.124
	Average	0.356	0.363	0.008
	Min	-0.235	-0.162	-0.101
	Std Dev	0.248	0.258	0.042

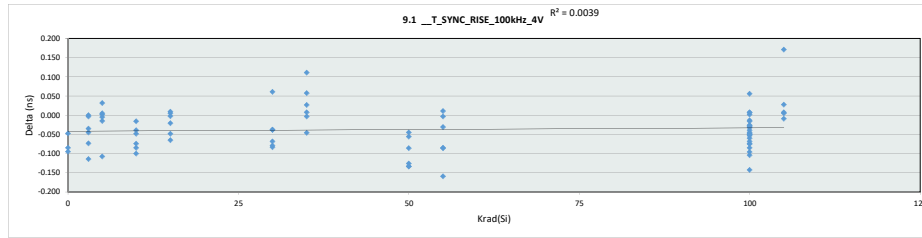


8.8_EA_OS_14V											
Test Site											
Tester											
Test Number											
Max Limit	2	mV									
Min Limit	-2	mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	-0.090	-0.049	-0.075	-0.034	-0.109	-0.018	-0.063	0.103	-0.046	0.022	-0.162
Average	0.057	0.328	0.373	0.328	0.367	0.350	0.390	0.309	0.278	0.466	0.272
Max	0.176	0.762	0.713	0.730	0.732	0.758	0.711	0.674	0.766	0.983	0.490
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000

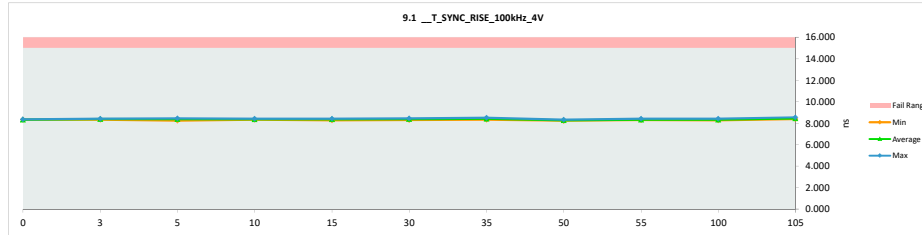


LDR TID Report
TPS7H5001-SP

9.1 _T_SYNC_RISE_100kHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.423	8.328	-0.095
0	118	8.422	8.337	-0.085
0	122	8.400	8.353	-0.047
3	61	8.510	8.396	-0.114
3	62	8.475	8.430	-0.045
3	63	8.378	8.375	-0.003
3	64	8.389	8.316	-0.073
3	65	8.426	8.426	0.000
3	66	8.431	8.396	-0.035
5	67	8.377	8.379	0.002
5	68	8.393	8.388	-0.005
5	69	8.404	8.436	0.032
5	70	8.350	8.243	-0.107
5	71	8.432	8.437	0.005
5	72	8.390	8.375	-0.015
10	73	8.510	8.425	-0.085
10	74	8.475	8.401	-0.074
10	75	8.378	8.330	-0.048
10	76	8.389	8.350	-0.039
10	77	8.426	8.410	-0.016
10	78	8.431	8.331	-0.100
15	79	8.377	8.386	0.009
15	80	8.393	8.398	0.005
15	81	8.404	8.402	-0.002
15	82	8.350	8.285	-0.065
15	83	8.432	8.411	-0.021
15	84	8.390	8.342	-0.048
30	86	8.510	8.442	-0.068
30	89	8.475	8.392	-0.083
30	91	8.378	8.439	0.061
30	92	8.389	8.310	-0.079
30	94	8.426	8.388	-0.038
30	95	8.431	8.394	-0.037
35	100	8.377	8.331	-0.046
35	101	8.393	8.504	0.111
35	102	8.404	8.462	0.058
35	103	8.350	8.347	-0.003
35	104	8.432	8.459	0.027
35	105	8.390	8.398	0.008
50	106	8.391	8.258	-0.133
50	107	8.316	8.230	-0.086
50	108	8.424	8.291	-0.133
50	109	8.351	8.225	-0.126
50	110	8.386	8.330	-0.056
50	111	8.372	8.327	-0.045
55	113	8.454	8.295	-0.159
55	114	8.397	8.311	-0.086
55	115	8.398	8.313	-0.085
55	116	8.376	8.345	-0.031
55	126	8.336	8.333	-0.003
55	128	8.400	8.411	0.011
100	129	8.309	8.311	0.002
100	131	8.373	8.269	-0.104
100	132	8.365	8.317	-0.048
100	133	8.415	8.383	-0.032
100	134	8.375	8.300	-0.075
100	135	8.383	8.298	-0.085
100	138	8.451	8.309	-0.142
100	139	8.284	8.340	0.056
100	140	8.337	8.298	-0.039
100	141	8.426	8.352	-0.074
100	142	8.364	8.371	0.007
100	143	8.419	8.366	-0.053
100	144	8.321	8.329	0.008
100	220	8.372	8.304	-0.068
100	221	8.329	8.302	-0.027
100	224	8.409	8.393	-0.016
100	225	8.373	8.360	-0.013
100	226	8.468	8.423	-0.045
100	228	8.390	8.364	-0.026
100	232	8.361	8.312	-0.049
100	233	8.383	8.287	-0.096
100	236	8.431	8.371	-0.060
100	238	8.401	8.351	-0.050
100	243	8.443	8.413	-0.030
105	244	8.451	8.442	-0.009
105	246	8.393	8.421	0.028
105	248	8.383	8.554	0.171
105	374	8.462	8.470	0.008
105	376	8.375	8.380	0.005
Max	8.510	8.554	0.171	
Average	8.399	8.363	-0.037	
Min	8.284	8.225	-0.159	
Std Dev	0.044	0.062	0.056	

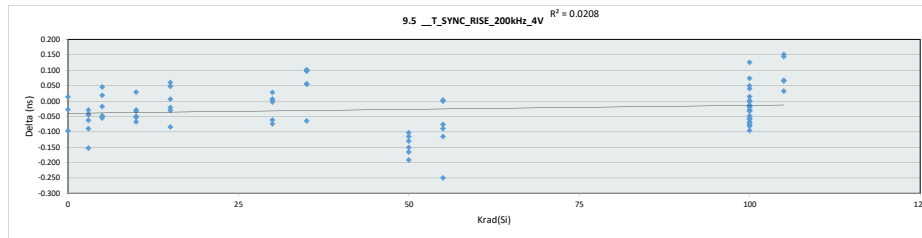


9.1 _T_SYNC_RISE_100kHz_4											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.328	8.316	8.243	8.330	8.285	8.310	8.331	8.225	8.295	8.269	8.380
Average	8.339	8.390	8.376	8.375	8.371	8.394	8.417	8.277	8.335	8.338	8.453
Max	8.353	8.430	8.437	8.425	8.411	8.442	8.504	8.330	8.411	8.423	8.554
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

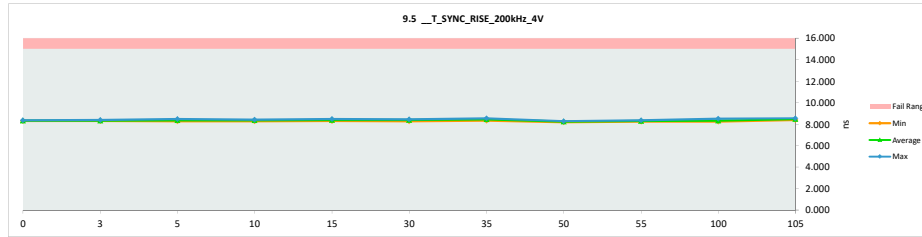


LDR TID Report
TPS7H5001-SP

9.5_T_SYNC_RISE_200kHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.455	8.358	-0.097
0	118	8.294	8.307	0.013
0	122	8.384	8.357	-0.027
3	61	8.443	8.380	-0.063
3	62	8.408	8.363	-0.045
3	63	8.397	8.307	-0.090
3	64	8.341	8.298	-0.043
3	65	8.366	8.337	-0.029
3	66	8.451	8.298	-0.153
5	67	8.300	8.282	-0.018
5	68	8.433	8.478	0.045
5	69	8.382	8.400	0.018
5	70	8.329	8.274	-0.055
5	71	8.437	8.389	-0.048
5	72	8.401	8.351	-0.050
10	73	8.443	8.410	-0.033
10	74	8.408	8.379	-0.029
10	75	8.397	8.347	-0.050
10	76	8.341	8.273	-0.068
10	77	8.366	8.395	0.029
10	78	8.451	8.398	-0.053
15	79	8.300	8.306	0.006
15	80	8.433	8.493	0.060
15	81	8.382	8.351	-0.031
15	82	8.329	8.308	-0.021
15	83	8.437	8.484	0.047
15	84	8.401	8.316	-0.085
30	86	8.443	8.449	0.006
30	89	8.408	8.412	0.004
30	91	8.397	8.394	-0.003
30	92	8.341	8.279	-0.062
30	94	8.366	8.394	0.028
30	95	8.451	8.377	-0.074
35	100	8.300	8.402	0.102
35	101	8.433	8.530	0.097
35	102	8.382	8.437	0.055
35	103	8.329	8.385	0.056
35	104	8.437	8.534	0.097
35	105	8.401	8.336	-0.065
50	106	8.418	8.226	-0.192
50	107	8.314	8.184	-0.130
50	108	8.396	8.280	-0.116
50	109	8.306	8.203	-0.103
50	110	8.426	8.260	-0.166
50	111	8.423	8.272	-0.151
55	113	8.394	8.278	-0.116
55	114	8.373	8.283	-0.090
55	115	8.534	8.284	-0.250
55	116	8.375	8.375	0.000
55	126	8.320	8.244	-0.076
55	128	8.348	8.351	0.003
100	129	8.379	8.307	-0.072
100	131	8.367	8.309	-0.058
100	132	8.332	8.372	0.040
100	133	8.340	8.320	-0.020
100	134	8.328	8.309	-0.019
100	135	8.343	8.311	-0.032
100	138	8.358	8.298	-0.060
100	139	8.351	8.255	-0.096
100	140	8.326	8.324	-0.002
100	141	8.345	8.347	0.002
100	142	8.386	8.307	-0.079
100	143	8.393	8.360	-0.033
100	144	8.326	8.295	-0.031
100	220	8.312	8.362	0.050
100	221	8.337	8.256	-0.081
100	224	8.312	8.385	0.073
100	225	8.388	8.359	-0.029
100	226	8.407	8.339	-0.068
100	228	8.416	8.360	-0.056
100	232	8.295	8.247	-0.048
100	233	8.332	8.318	-0.014
100	236	8.400	8.322	-0.078
100	238	8.386	8.511	0.125
100	243	8.334	8.348	0.014
105	244	8.418	8.483	0.065
105	246	8.370	8.437	0.067
105	248	8.362	8.394	0.032
105	374	8.400	8.552	0.152
105	376	8.331	8.475	0.144
	Max	8.534	8.552	0.152
	Average	8.377	8.351	-0.027
	Min	8.294	8.184	-0.250
	Std Dev	0.048	0.077	0.074

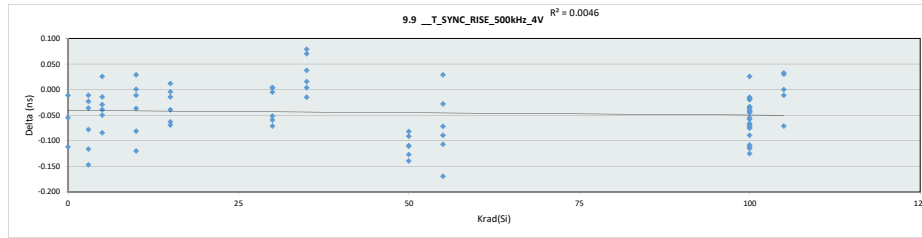


9.5_T_SYNC_RISE_200kHz_4											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.307	8.298	8.274	8.273	8.306	8.279	8.336	8.184	8.244	8.247	8.394
Average	8.341	8.331	8.362	8.367	8.376	8.384	8.437	8.238	8.303	8.330	8.468
Max	8.358	8.380	8.478	8.410	8.493	8.449	8.534	8.280	8.375	8.511	8.552
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

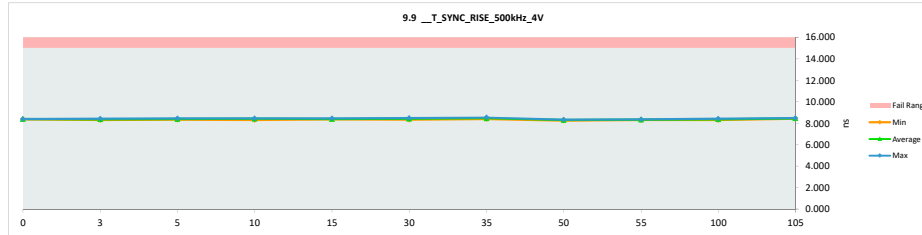


LDR TID Report
TPS7H5001-SP

9.9_T_SYNC_RISE_500kHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.513	8.401	-0.112
0	118	8.379	8.368	-0.011
0	122	8.418	8.363	-0.055
3	61	8.543	8.427	-0.116
3	62	8.449	8.426	-0.023
3	63	8.419	8.383	-0.036
3	64	8.392	8.314	-0.078
3	65	8.462	8.315	-0.147
3	66	8.398	8.387	-0.011
5	67	8.378	8.404	0.026
5	68	8.488	8.438	-0.050
5	69	8.488	8.449	-0.039
5	70	8.336	8.322	-0.014
5	71	8.439	8.410	-0.029
5	72	8.443	8.359	-0.084
10	73	8.543	8.423	-0.120
10	74	8.449	8.438	-0.011
10	75	8.419	8.382	-0.037
10	76	8.392	8.311	-0.081
10	77	8.462	8.463	0.001
10	78	8.398	8.427	0.029
15	79	8.378	8.374	-0.004
15	80	8.488	8.449	-0.039
15	81	8.488	8.419	-0.069
15	82	8.336	8.348	0.012
15	83	8.439	8.376	-0.063
15	84	8.443	8.429	-0.014
30	86	8.543	8.472	-0.071
30	89	8.449	8.390	-0.059
30	91	8.419	8.414	-0.005
30	92	8.392	8.340	-0.052
30	94	8.462	8.466	0.004
30	95	8.398	8.402	0.004
35	100	8.378	8.394	0.016
35	101	8.488	8.526	0.038
35	102	8.488	8.473	-0.015
35	103	8.336	8.415	0.079
35	104	8.439	8.509	0.070
35	105	8.443	8.447	0.004
50	106	8.401	8.319	-0.082
50	107	8.367	8.257	-0.110
50	108	8.415	8.324	-0.091
50	109	8.389	8.250	-0.139
50	110	8.425	8.315	-0.110
50	111	8.419	8.292	-0.127
55	113	8.462	8.355	-0.107
55	114	8.396	8.324	-0.072
55	115	8.460	8.371	-0.089
55	116	8.483	8.314	-0.169
55	126	8.300	8.329	0.029
55	128	8.375	8.347	-0.028
100	129	8.393	8.322	-0.071
100	131	8.413	8.347	-0.066
100	132	8.431	8.316	-0.115
100	133	8.421	8.380	-0.041
100	134	8.371	8.316	-0.055
100	135	8.433	8.365	-0.068
100	138	8.370	8.351	-0.019
100	139	8.342	8.307	-0.035
100	140	8.397	8.423	0.026
100	141	8.465	8.376	-0.089
100	142	8.434	8.376	-0.058
100	143	8.452	8.415	-0.037
100	144	8.386	8.311	-0.075
100	220	8.397	8.364	-0.033
100	221	8.420	8.312	-0.108
100	224	8.422	8.380	-0.042
100	225	8.440	8.395	-0.045
100	226	8.500	8.375	-0.125
100	228	8.429	8.413	-0.016
100	232	8.367	8.292	-0.075
100	233	8.397	8.378	-0.019
100	236	8.451	8.339	-0.112
100	238	8.437	8.422	-0.015
100	243	8.408	8.366	-0.042
105	244	8.504	8.433	-0.071
105	246	8.420	8.409	-0.011
105	248	8.443	8.476	0.033
105	374	8.480	8.480	0.000
105	376	8.406	8.436	0.030
Max		8.543	8.526	0.017
Average		8.427	8.382	-0.045
Min		8.300	8.250	-0.150
Std Dev		0.049	0.057	0.051

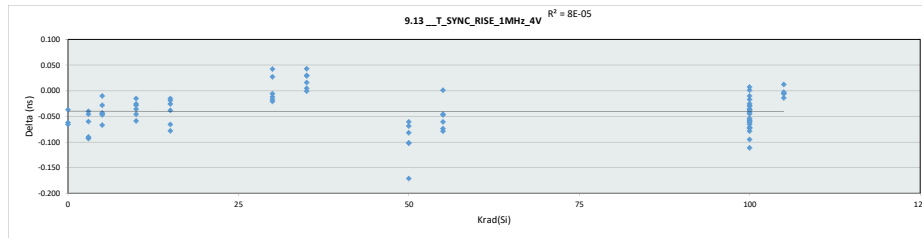


9.9_T_SYNC_RISE_500kHz_4											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.363	8.314	8.322	8.311	8.348	8.340	8.394	8.250	8.314	8.292	8.409
Average	8.377	8.375	8.397	8.407	8.399	8.414	8.461	8.293	8.340	8.360	8.447
Max	8.401	8.427	8.449	8.463	8.449	8.472	8.526	8.324	8.371	8.423	8.480
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

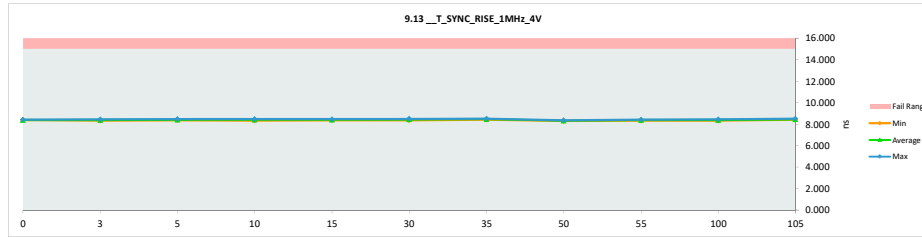


LDR TID Report
TPS7H5001-SP

9.13_T_SYNC_RISE_1MHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.469	8.407	-0.062
0	118	8.433	8.396	-0.037
0	122	8.461	8.395	-0.066
3	61	8.493	8.401	-0.092
3	62	8.476	8.436	-0.040
3	63	8.457	8.364	-0.093
3	64	8.380	8.320	-0.060
3	65	8.470	8.424	-0.046
3	66	8.460	8.370	-0.090
5	67	8.430	8.387	-0.043
5	68	8.511	8.466	-0.045
5	69	8.460	8.450	-0.010
5	70	8.377	8.349	-0.028
5	71	8.469	8.422	-0.047
5	72	8.447	8.380	-0.067
10	73	8.493	8.467	-0.026
10	74	8.476	8.430	-0.046
10	75	8.457	8.429	-0.028
10	76	8.380	8.344	-0.036
10	77	8.470	8.411	-0.059
10	78	8.460	8.445	-0.015
15	79	8.430	8.352	-0.078
15	80	8.511	8.445	-0.066
15	81	8.460	8.445	-0.015
15	82	8.377	8.351	-0.026
15	83	8.469	8.431	-0.038
15	84	8.447	8.428	-0.019
30	86	8.493	8.487	-0.006
30	89	8.476	8.464	-0.012
30	91	8.457	8.484	0.027
30	92	8.380	8.359	-0.021
30	94	8.470	8.512	0.042
30	95	8.460	8.443	-0.017
35	100	8.430	8.446	0.016
35	101	8.511	8.510	-0.001
35	102	8.460	8.465	0.005
35	103	8.377	8.406	0.029
35	104	8.469	8.512	0.043
35	105	8.447	8.477	0.030
50	106	8.437	8.335	-0.102
50	107	8.375	8.314	-0.061
50	108	8.419	8.350	-0.069
50	109	8.393	8.291	-0.102
50	110	8.466	8.295	-0.171
50	111	8.415	8.333	-0.082
55	113	8.440	8.366	-0.074
55	114	8.437	8.391	-0.046
55	115	8.490	8.411	-0.079
55	116	8.469	8.422	-0.047
55	126	8.324	8.325	0.001
55	128	8.447	8.386	-0.061
100	129	8.408	8.365	-0.043
100	131	8.459	8.402	-0.057
100	132	8.363	8.364	0.001
100	133	8.436	8.391	-0.045
100	134	8.372	8.342	-0.030
100	135	8.434	8.361	-0.073
100	138	8.374	8.381	0.007
100	139	8.353	8.336	-0.017
100	140	8.401	8.365	-0.036
100	141	8.463	8.424	-0.039
100	142	8.444	8.408	-0.036
100	143	8.447	8.381	-0.066
100	144	8.405	8.347	-0.058
100	220	8.465	8.354	-0.111
100	221	8.412	8.333	-0.079
100	224	8.462	8.400	-0.062
100	225	8.455	8.383	-0.072
100	226	8.452	8.442	-0.010
100	228	8.464	8.392	-0.072
100	232	8.387	8.333	-0.054
100	233	8.382	8.354	-0.028
100	236	8.455	8.360	-0.095
100	238	8.448	8.423	-0.025
100	243	8.463	8.402	-0.061
105	244	8.513	8.507	-0.006
105	246	8.428	8.440	0.012
105	248	8.471	8.466	-0.005
105	374	8.475	8.461	-0.014
105	376	8.427	8.424	-0.003
Max	8.513	8.512	0.043	
Average	8.441	8.401	-0.040	
Min	8.324	8.291	-0.171	
Std Dev	0.041	0.053	0.038	

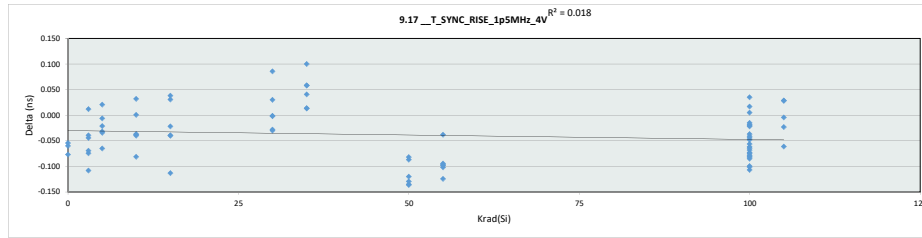


9.13_T_SYNC_RISE_1MHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.395	8.320	8.349	8.344	8.351	8.359	8.406	8.291	8.325	8.333	8.424
Average	8.399	8.386	8.409	8.421	8.409	8.458	8.469	8.320	8.384	8.377	8.460
Max	8.407	8.436	8.466	8.467	8.445	8.512	8.512	8.350	8.422	8.442	8.507
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

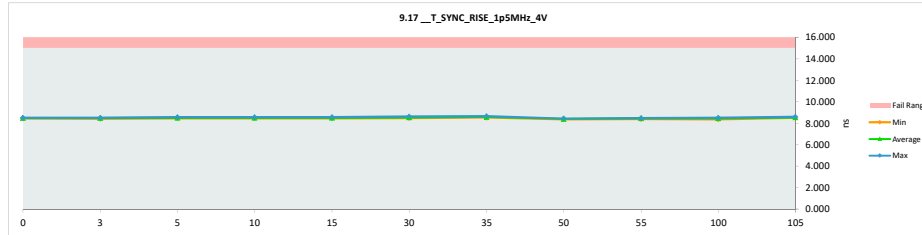


LDR TID Report
TPS7H5001-SP

9.17_T_SYNC_RISE_1p5MHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.537	8.460	-0.077
0	118	8.504	8.444	-0.060
0	122	8.566	8.512	-0.054
3	61	8.617	8.509	-0.108
3	62	8.594	8.520	-0.074
3	63	8.518	8.474	-0.044
3	64	8.501	8.432	-0.069
3	65	8.528	8.489	-0.039
3	66	8.494	8.506	0.012
5	67	8.449	8.470	0.021
5	68	8.639	8.574	-0.065
5	69	8.567	8.533	-0.034
5	70	8.479	8.447	-0.032
5	71	8.541	8.520	-0.021
5	72	8.520	8.514	-0.006
10	73	8.617	8.580	-0.037
10	74	8.594	8.513	-0.081
10	75	8.518	8.519	0.001
10	76	8.501	8.463	-0.038
10	77	8.528	8.488	-0.040
10	78	8.494	8.526	0.032
15	79	8.449	8.480	0.031
15	80	8.639	8.526	-0.113
15	81	8.567	8.528	-0.039
15	82	8.479	8.439	-0.040
15	83	8.541	8.579	0.038
15	84	8.520	8.498	-0.022
30	86	8.617	8.615	-0.002
30	89	8.594	8.564	-0.030
30	91	8.518	8.548	0.030
30	92	8.501	8.473	-0.028
30	94	8.528	8.527	-0.001
30	95	8.494	8.580	0.086
35	100	8.449	8.549	0.100
35	101	8.639	8.652	0.013
35	102	8.567	8.581	0.014
35	103	8.479	8.520	0.041
35	104	8.541	8.599	0.058
35	105	8.520	8.578	0.058
50	106	8.553	8.417	-0.136
50	107	8.435	8.353	-0.082
50	108	8.513	8.426	-0.087
50	109	8.529	8.400	-0.129
50	110	8.571	8.436	-0.135
50	111	8.513	8.393	-0.120
55	113	8.519	8.425	-0.094
55	114	8.557	8.455	-0.102
55	115	8.521	8.423	-0.098
55	116	8.595	8.471	-0.124
55	126	8.489	8.392	-0.097
55	128	8.532	8.494	-0.038
100	129	8.513	8.412	-0.101
100	131	8.552	8.490	-0.062
100	132	8.488	8.466	-0.022
100	133	8.543	8.461	-0.082
100	134	8.487	8.469	-0.018
100	135	8.515	8.435	-0.080
100	138	8.453	8.412	-0.041
100	139	8.446	8.378	-0.068
100	140	8.492	8.455	-0.037
100	141	8.543	8.436	-0.107
100	142	8.534	8.470	-0.064
100	143	8.529	8.509	-0.020
100	144	8.478	8.463	-0.015
100	220	8.518	8.433	-0.085
100	221	8.480	8.381	-0.099
100	224	8.474	8.509	0.035
100	225	8.565	8.491	-0.074
100	226	8.557	8.513	-0.044
100	228	8.504	8.521	0.017
100	232	8.420	8.372	-0.048
100	233	8.484	8.489	0.005
100	236	8.544	8.471	-0.073
100	238	8.528	8.472	-0.056
100	243	8.546	8.468	-0.078
105	244	8.628	8.567	-0.061
105	246	8.516	8.544	0.028
105	248	8.566	8.543	-0.023
105	374	8.574	8.603	0.029
105	376	8.515	8.511	-0.004
Max		8.639	8.652	0.100
Average		8.528	8.489	-0.039
Min		8.420	8.353	-0.136
Std Dev		0.049	0.062	0.053



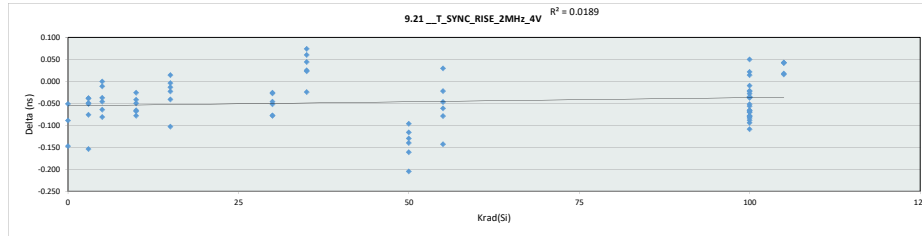
9.17_T_SYNC_RISE_1p5MHz											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.444	8.432	8.447	8.463	8.439	8.473	8.520	8.353	8.392	8.372	8.511
Average	8.472	8.488	8.510	8.515	8.508	8.551	8.580	8.404	8.443	8.457	8.554
Max	8.512	8.520	8.574	8.580	8.579	8.615	8.652	8.436	8.494	8.521	8.603
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



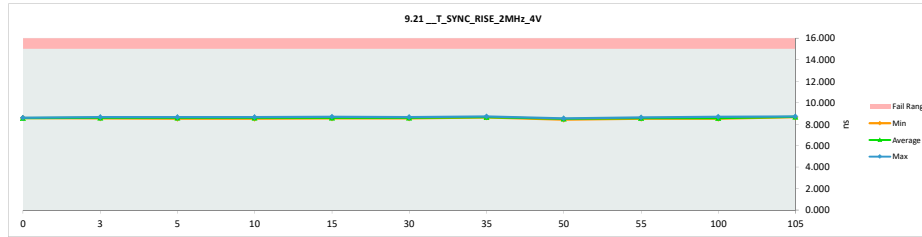
LDR TID Report
TPS7H5001-SP

9.21_T_SYNC_RISE_2MHz_4V		
Test Site		
Tester		
Test Number		
Unit	ns	ns
Max Limit	15	15
Min Limit		

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.712	8.565	-0.147
0	118	8.650	8.561	-0.089
0	122	8.651	8.600	-0.051
3	61	8.722	8.646	-0.076
3	62	8.663	8.614	-0.049
3	63	8.699	8.545	-0.154
3	64	8.578	8.539	-0.039
3	65	8.643	8.592	-0.051
3	66	8.664	8.626	-0.038
5	67	8.577	8.566	-0.011
5	68	8.694	8.613	-0.081
5	69	8.673	8.673	0.000
5	70	8.558	8.521	-0.037
5	71	8.689	8.625	-0.064
5	72	8.637	8.591	-0.046
10	73	8.722	8.644	-0.078
10	74	8.663	8.613	-0.050
10	75	8.699	8.633	-0.066
10	76	8.578	8.510	-0.068
10	77	8.643	8.601	-0.042
10	78	8.664	8.638	-0.026
15	79	8.577	8.564	-0.013
15	80	8.694	8.653	-0.041
15	81	8.673	8.687	0.014
15	82	8.558	8.535	-0.023
15	83	8.689	8.586	-0.103
15	84	8.637	8.633	-0.004
30	86	8.722	8.644	-0.078
30	89	8.663	8.586	-0.077
30	91	8.699	8.653	-0.046
30	92	8.578	8.526	-0.052
30	94	8.643	8.616	-0.027
30	95	8.664	8.638	-0.026
35	100	8.577	8.651	0.074
35	101	8.694	8.717	0.023
35	102	8.673	8.717	0.044
35	103	8.558	8.618	0.060
35	104	8.689	8.665	-0.024
35	105	8.637	8.662	0.025
50	106	8.627	8.531	-0.096
50	107	8.553	8.413	-0.140
50	108	8.721	8.516	-0.205
50	109	8.591	8.430	-0.161
50	110	8.676	8.546	-0.130
50	111	8.657	8.541	-0.116
55	113	8.663	8.520	-0.143
55	114	8.645	8.584	-0.061
55	115	8.646	8.567	-0.079
55	116	8.689	8.642	-0.047
55	126	8.489	8.519	0.030
55	128	8.597	8.575	-0.022
100	129	8.580	8.545	-0.035
100	131	8.644	8.556	-0.088
100	132	8.588	8.505	-0.083
100	133	8.654	8.584	-0.070
100	134	8.615	8.537	-0.078
100	135	8.615	8.535	-0.080
100	138	8.575	8.565	-0.010
100	139	8.579	8.551	-0.028
100	140	8.625	8.588	-0.037
100	141	8.642	8.576	-0.066
100	142	8.655	8.586	-0.069
100	143	8.627	8.677	0.050
100	144	8.582	8.561	-0.021
100	220	8.638	8.559	-0.079
100	221	8.588	8.536	-0.052
100	224	8.581	8.546	-0.035
100	225	8.629	8.643	0.014
100	226	8.679	8.623	-0.056
100	228	8.698	8.604	-0.094
100	232	8.576	8.540	-0.036
100	233	8.627	8.561	-0.066
100	236	8.709	8.600	-0.109
100	238	8.673	8.695	0.022
100	243	8.631	8.608	-0.023
105	244	8.675	8.691	0.016
105	246	8.631	8.673	0.042
105	248	8.673	8.716	0.043
105	374	8.695	8.713	0.018
105	376	8.595	8.638	0.043
	Max	8.722	8.717	0.074
	Average	8.640	8.595	-0.046
	Min	8.489	8.413	-0.205
	Std Dev	0.050	0.062	0.055

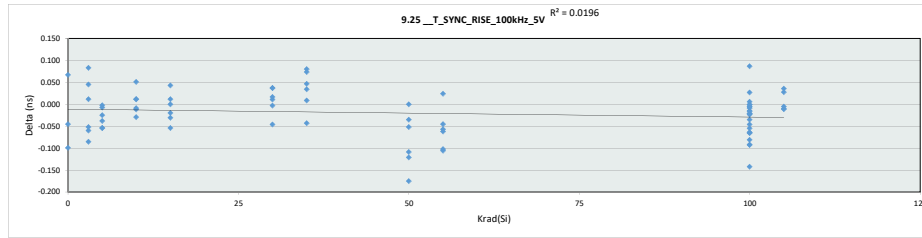


9.21_T_SYNC_RISE_2MHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.561	8.539	8.521	8.510	8.535	8.526	8.618	8.413	8.519	8.505	8.638
Average	8.575	8.594	8.598	8.607	8.610	8.611	8.672	8.496	8.568	8.578	8.686
Max	8.600	8.646	8.673	8.644	8.687	8.653	8.717	8.546	8.642	8.695	8.716
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

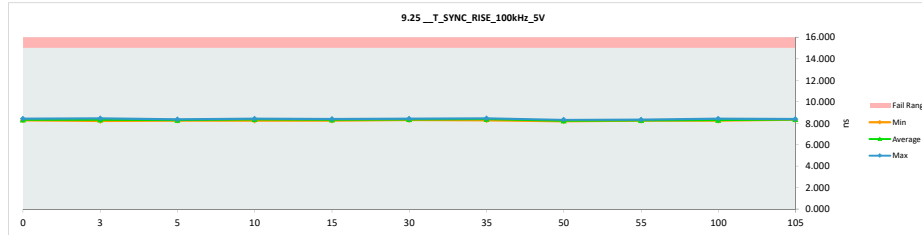


LDR TID Report
TPS7H5001-SP

9.25 _T_SYNC_RISE_100kHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.360	8.427	0.067
0	118	8.314	8.269	-0.045
0	122	8.357	8.258	-0.099
3	61	8.374	8.386	0.012
3	62	8.322	8.367	0.045
3	63	8.355	8.438	0.083
3	64	8.283	8.223	-0.060
3	65	8.394	8.309	-0.085
3	66	8.360	8.308	-0.052
5	67	8.293	8.268	-0.025
5	68	8.376	8.369	-0.007
5	69	8.371	8.369	-0.002
5	70	8.307	8.253	-0.054
5	71	8.388	8.334	-0.054
5	72	8.337	8.299	-0.038
10	73	8.374	8.425	0.051
10	74	8.322	8.334	0.012
10	75	8.355	8.343	-0.012
10	76	8.283	8.254	-0.029
10	77	8.394	8.385	-0.009
10	78	8.360	8.372	0.012
15	79	8.293	8.336	0.043
15	80	8.376	8.376	0.000
15	81	8.371	8.383	0.012
15	82	8.307	8.253	-0.054
15	83	8.388	8.357	-0.031
15	84	8.337	8.317	-0.020
30	86	8.374	8.411	0.037
30	89	8.322	8.319	-0.003
30	91	8.355	8.309	-0.046
30	92	8.283	8.300	0.017
30	94	8.394	8.405	0.011
30	95	8.360	8.397	0.037
35	100	8.293	8.373	0.080
35	101	8.376	8.410	0.034
35	102	8.371	8.445	0.074
35	103	8.307	8.264	-0.043
35	104	8.388	8.397	0.009
35	105	8.337	8.384	0.047
50	106	8.380	8.205	-0.175
50	107	8.248	8.213	-0.035
50	108	8.368	8.316	-0.052
50	109	8.308	8.187	-0.121
50	110	8.392	8.283	-0.109
50	111	8.279	8.279	0.000
55	113	8.347	8.241	-0.106
55	114	8.339	8.237	-0.102
55	115	8.348	8.303	-0.045
55	116	8.381	8.324	-0.057
55	126	8.276	8.250	0.024
55	128	8.359	8.297	-0.062
100	129	8.309	8.245	-0.064
100	131	8.341	8.319	-0.022
100	132	8.268	8.247	-0.021
100	133	8.369	8.303	-0.066
100	134	8.294	8.321	0.027
100	135	8.320	8.326	0.006
100	138	8.337	8.322	-0.015
100	139	8.257	8.251	-0.006
100	140	8.341	8.260	-0.081
100	141	8.403	8.311	-0.092
100	142	8.319	8.304	-0.015
100	143	8.389	8.296	-0.093
100	144	8.293	8.270	-0.023
100	220	8.323	8.268	-0.055
100	221	8.273	8.238	-0.035
100	224	8.448	8.306	-0.142
100	225	8.371	8.350	-0.021
100	226	8.339	8.426	0.087
100	228	8.326	8.317	-0.009
100	232	8.298	8.234	-0.064
100	233	8.292	8.291	-0.001
100	236	8.362	8.316	-0.046
100	238	8.325	8.320	-0.005
100	243	8.334	8.333	-0.001
105	244	8.383	8.378	-0.005
105	246	8.325	8.315	-0.010
105	248	8.376	8.365	-0.011
105	374	8.346	8.382	0.036
105	376	8.315	8.343	0.028
	Max	8.448	8.445	0.087
	Average	8.340	8.319	-0.021
	Min	8.226	8.187	-0.175
	Std Dev	0.041	0.060	0.052

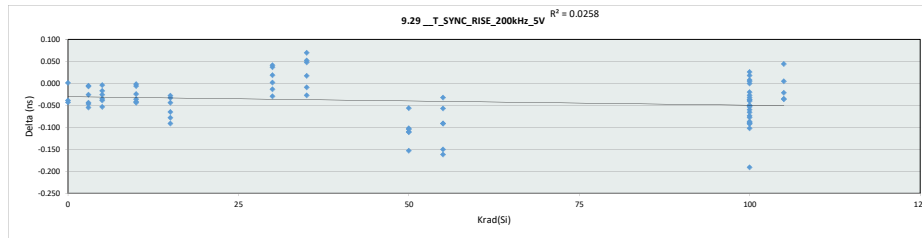


9.25 _T_SYNC_RISE_100kHz_5V											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.258	8.223	8.253	8.254	8.253	8.300	8.264	8.187	8.237	8.234	8.315
Average	8.318	8.339	8.315	8.352	8.337	8.357	8.379	8.247	8.275	8.299	8.357
Max	8.427	8.438	8.369	8.425	8.383	8.411	8.445	8.316	8.324	8.426	8.382
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

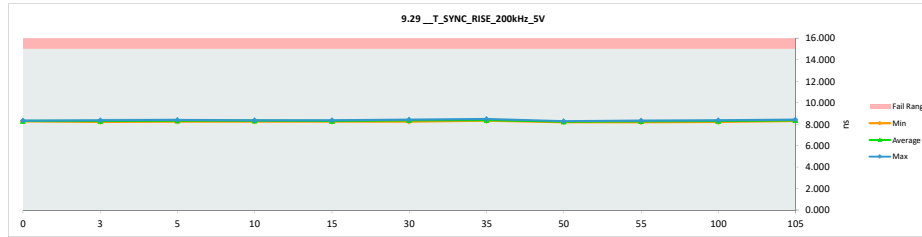


LDR TID Report
TPS7H5001-SP

9.29_T_SYNC_RISE_200kHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.331	8.332	0.001
0	118	8.352	8.309	-0.043
0	122	8.323	8.284	-0.039
3	61	8.372	8.365	-0.007
3	62	8.370	8.326	-0.044
3	63	8.370	8.315	-0.055
3	64	8.272	8.225	-0.047
3	65	8.333	8.307	-0.026
3	66	8.348	8.343	-0.005
5	67	8.334	8.317	-0.017
5	68	8.408	8.404	-0.004
5	69	8.415	8.376	-0.039
5	70	8.262	8.236	-0.026
5	71	8.375	8.322	-0.053
5	72	8.407	8.372	-0.035
10	73	8.372	8.336	-0.036
10	74	8.370	8.328	-0.042
10	75	8.370	8.346	-0.024
10	76	8.272	8.228	-0.044
10	77	8.333	8.331	-0.002
10	78	8.348	8.342	-0.006
15	79	8.334	8.290	-0.044
15	80	8.408	8.375	-0.033
15	81	8.415	8.337	-0.078
15	82	8.262	8.234	-0.028
15	83	8.375	8.310	-0.065
15	84	8.407	8.316	-0.091
30	86	8.372	8.413	0.041
30	89	8.370	8.357	-0.013
30	91	8.370	8.372	0.002
30	92	8.272	8.243	-0.029
30	94	8.333	8.370	0.037
30	95	8.348	8.367	0.019
35	100	8.334	8.351	0.017
35	101	8.408	8.478	0.070
35	102	8.415	8.406	-0.009
35	103	8.262	8.310	0.048
35	104	8.375	8.427	0.052
35	105	8.407	8.380	-0.027
50	106	8.373	8.220	-0.153
50	107	8.280	8.170	-0.110
50	108	8.360	8.249	-0.111
50	109	8.265	8.209	-0.056
50	110	8.385	8.281	-0.104
50	111	8.351	8.249	-0.102
55	113	8.392	8.242	-0.150
55	114	8.357	8.300	-0.057
55	115	8.391	8.229	-0.162
55	116	8.395	8.304	-0.091
55	126	8.269	8.178	-0.091
55	128	8.358	8.326	-0.032
100	129	8.409	8.218	-0.191
100	131	8.366	8.313	-0.053
100	132	8.303	8.266	-0.037
100	133	8.362	8.302	-0.060
100	134	8.309	8.282	-0.027
100	135	8.345	8.256	-0.089
100	138	8.316	8.284	-0.032
100	139	8.253	8.271	0.018
100	140	8.317	8.251	-0.066
100	141	8.395	8.318	-0.077
100	142	8.360	8.258	-0.102
100	143	8.344	8.349	0.005
100	144	8.310	8.290	-0.020
100	220	8.328	8.354	0.026
100	221	8.262	8.262	0.000
100	224	8.363	8.289	-0.074
100	225	8.392	8.355	-0.037
100	226	8.404	8.353	-0.051
100	228	8.379	8.292	-0.087
100	232	8.308	8.216	-0.092
100	233	8.305	8.313	0.008
100	236	8.359	8.309	-0.050
100	238	8.410	8.319	-0.091
100	243	8.343	8.303	-0.040
105	244	8.380	8.424	0.044
105	246	8.319	8.298	-0.021
105	248	8.370	8.375	0.005
105	374	8.372	8.336	-0.036
105	376	8.360	8.325	-0.035
Max	8.415	8.478	0.070	
Average	8.350	8.310	-0.040	
Min	8.253	8.170	-0.191	
Std Dev	0.044	0.059	0.049	

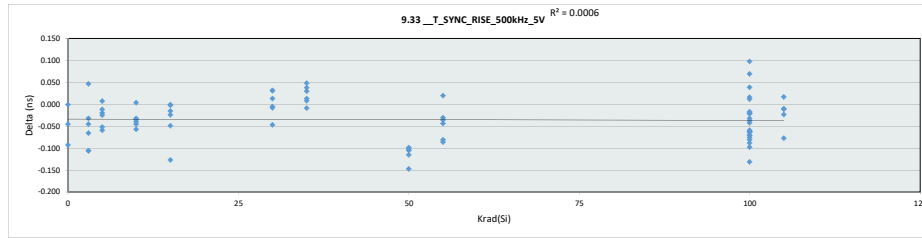


9.29_T_SYNC_RISE_200kHz											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.284	8.225	8.236	8.228	8.234	8.243	8.310	8.170	8.178	8.216	8.298
Average	8.308	8.314	8.338	8.319	8.310	8.354	8.392	8.230	8.263	8.293	8.352
Max	8.332	8.365	8.404	8.346	8.375	8.413	8.478	8.281	8.326	8.355	8.424
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

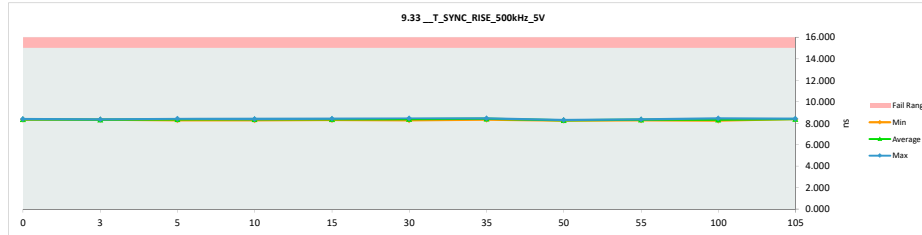


LDR TID Report
TPS7H5001-SP

9.33 _T_SYNC_RISE_500kHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.419	8.374	-0.045
0	118	8.395	8.394	-0.001
0	122	8.425	8.332	-0.093
3	61	8.432	8.366	-0.066
3	62	8.395	8.350	-0.045
3	63	8.368	8.336	-0.032
3	64	8.273	8.320	0.047
3	65	8.423	8.317	-0.106
3	66	8.429	8.323	-0.106
5	67	8.357	8.332	-0.025
5	68	8.439	8.427	-0.012
5	69	8.449	8.390	-0.059
5	70	8.295	8.275	-0.020
5	71	8.434	8.382	-0.052
5	72	8.377	8.384	0.007
10	73	8.432	8.400	-0.032
10	74	8.395	8.360	-0.035
10	75	8.368	8.323	-0.045
10	76	8.273	8.277	0.004
10	77	8.423	8.383	-0.040
10	78	8.429	8.372	-0.057
15	79	8.357	8.355	-0.002
15	80	8.439	8.390	-0.049
15	81	8.449	8.322	-0.127
15	82	8.295	8.294	-0.001
15	83	8.434	8.419	-0.015
15	84	8.377	8.353	-0.024
30	86	8.432	8.445	0.013
30	89	8.395	8.387	-0.008
30	91	8.368	8.399	0.031
30	92	8.273	8.268	-0.005
30	94	8.423	8.376	-0.047
30	95	8.429	8.460	0.031
35	100	8.357	8.395	0.038
35	101	8.439	8.452	0.013
35	102	8.449	8.440	-0.009
35	103	8.295	8.325	0.030
35	104	8.434	8.442	0.008
35	105	8.377	8.425	0.048
50	106	8.357	8.253	-0.104
50	107	8.334	8.235	-0.099
50	108	8.376	8.277	-0.099
50	109	8.337	8.232	-0.105
50	110	8.442	8.295	-0.147
50	111	8.406	8.291	-0.115
55	113	8.428	8.347	-0.081
55	114	8.369	8.333	-0.036
55	115	8.348	8.368	0.020
55	116	8.407	8.321	-0.086
55	125	8.327	8.283	-0.044
55	128	8.353	8.322	-0.031
100	129	8.314	8.326	0.012
100	131	8.372	8.330	-0.042
100	132	8.334	8.313	-0.021
100	133	8.347	8.445	0.098
100	134	8.332	8.261	-0.071
100	135	8.426	8.328	-0.098
100	138	8.331	8.311	-0.020
100	139	8.331	8.268	-0.063
100	140	8.358	8.326	-0.032
100	141	8.374	8.303	-0.071
100	142	8.399	8.318	-0.081
100	143	8.426	8.367	-0.059
100	144	8.321	8.304	-0.017
100	220	8.347	8.386	0.039
100	221	8.344	8.256	-0.088
100	224	8.303	8.372	0.069
100	225	8.398	8.377	-0.021
100	226	8.400	8.416	0.016
100	228	8.416	8.354	-0.062
100	232	8.298	8.234	-0.064
100	233	8.366	8.290	-0.076
100	236	8.398	8.361	-0.037
100	238	8.411	8.280	-0.131
100	243	8.421	8.359	-0.062
105	244	8.431	8.408	-0.023
105	246	8.447	8.370	-0.077
105	248	8.371	8.388	0.017
105	374	8.407	8.397	-0.010
105	376	8.413	8.402	-0.011
	Max	8.449	8.460	0.098
	Average	8.382	8.347	-0.035
	Min	8.273	8.232	-0.147
	Std Dev	0.048	0.056	0.049

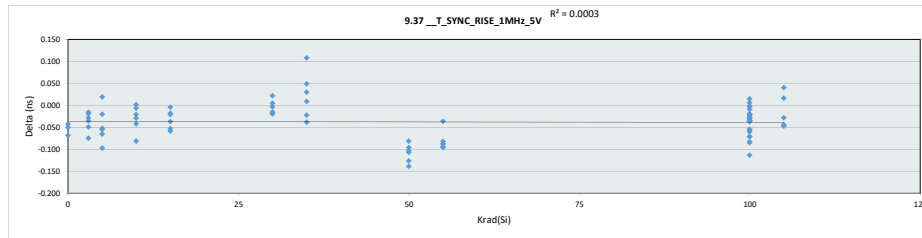


9.33 _T_SYNC_RISE_500kHz											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.332	8.317	8.275	8.277	8.294	8.268	8.325	8.232	8.283	8.234	8.370
Average	8.367	8.335	8.365	8.353	8.356	8.389	8.413	8.264	8.329	8.329	8.393
Max	8.394	8.366	8.427	8.400	8.419	8.460	8.452	8.295	8.368	8.445	8.408
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

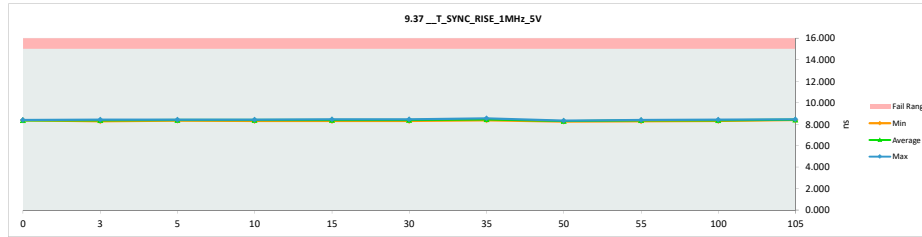


LDR TID Report
TPS7H5001-SP

9.37_T_SYNC_RISE_1MHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.443	8.393	-0.050
0	118	8.416	8.348	-0.068
0	122	8.411	8.368	-0.043
3	61	8.435	8.419	-0.016
3	62	8.425	8.376	-0.049
3	63	8.444	8.369	-0.075
3	64	8.312	8.283	-0.029
3	65	8.413	8.378	-0.035
3	66	8.427	8.408	-0.019
5	67	8.367	8.386	0.019
5	68	8.484	8.419	-0.065
5	69	8.474	8.419	-0.055
5	70	8.338	8.318	-0.020
5	71	8.484	8.387	-0.097
5	72	8.424	8.372	-0.052
10	73	8.435	8.406	-0.029
10	74	8.425	8.404	-0.021
10	75	8.444	8.363	-0.081
10	76	8.312	8.314	0.002
10	77	8.413	8.371	-0.042
10	78	8.427	8.421	-0.006
15	79	8.367	8.363	-0.004
15	80	8.484	8.431	-0.053
15	81	8.474	8.456	-0.018
15	82	8.338	8.301	-0.037
15	83	8.484	8.425	-0.059
15	84	8.424	8.403	-0.021
30	86	8.435	8.440	0.005
30	89	8.425	8.422	-0.003
30	91	8.444	8.429	-0.015
30	92	8.312	8.293	-0.019
30	94	8.413	8.435	0.022
30	95	8.427	8.410	-0.017
35	100	8.367	8.475	0.108
35	101	8.484	8.533	0.049
35	102	8.474	8.436	-0.038
35	103	8.338	8.368	0.030
35	104	8.484	8.462	-0.022
35	105	8.424	8.433	0.009
50	106	8.425	8.286	-0.139
50	107	8.358	8.251	-0.107
50	108	8.420	8.294	-0.126
50	109	8.313	8.232	-0.081
50	110	8.433	8.337	-0.096
50	111	8.431	8.328	-0.103
55	113	8.436	8.348	-0.088
55	114	8.444	8.357	-0.087
55	115	8.466	8.370	-0.096
55	116	8.428	8.392	-0.036
55	126	8.372	8.278	-0.094
55	128	8.424	8.342	-0.082
100	129	8.343	8.306	-0.037
100	131	8.425	8.371	-0.054
100	132	8.316	8.296	-0.020
100	133	8.383	8.382	-0.001
100	134	8.384	8.313	-0.071
100	135	8.375	8.390	0.015
100	138	8.343	8.324	-0.019
100	139	8.377	8.321	-0.056
100	140	8.399	8.286	-0.113
100	141	8.397	8.388	-0.009
100	142	8.401	8.372	-0.029
100	143	8.406	8.379	-0.027
100	144	8.365	8.331	-0.034
100	220	8.384	8.324	-0.060
100	221	8.361	8.326	-0.035
100	224	8.363	8.369	0.006
100	225	8.419	8.395	-0.024
100	226	8.447	8.417	-0.030
100	228	8.439	8.418	-0.021
100	232	8.341	8.338	-0.003
100	233	8.387	8.353	-0.034
100	236	8.429	8.346	-0.083
100	238	8.439	8.354	-0.085
100	243	8.427	8.356	-0.071
105	244	8.438	8.410	-0.028
105	246	8.470	8.426	-0.044
105	248	8.440	8.456	0.016
105	374	8.418	8.458	0.040
105	376	8.420	8.373	-0.047
Max		8.484	8.533	0.108
Average		8.410	8.373	-0.038
Min		8.312	8.232	-0.139
Std Dev		0.046	0.055	0.042

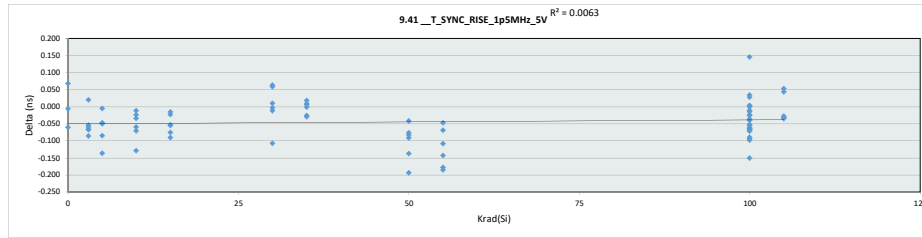


9.37_T_SYNC_RISE_1MHz_5V											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.348	8.283	8.318	8.314	8.301	8.293	8.368	8.232	8.278	8.286	8.373
Average	8.370	8.372	8.384	8.380	8.397	8.405	8.451	8.288	8.348	8.352	8.425
Max	8.393	8.419	8.419	8.421	8.456	8.440	8.533	8.337	8.392	8.418	8.458
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

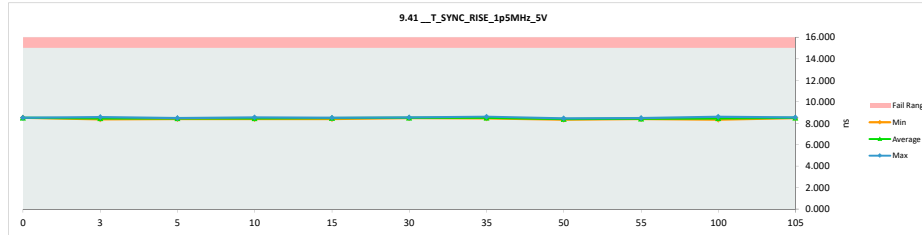


LDR TID Report
TPS7H5001-SP

9.41_T_SYNC_RISE_1p5MHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.563	8.503	-0.060
0	118	8.501	8.495	-0.006
0	122	8.456	8.524	0.068
3	61	8.538	8.558	0.020
3	62	8.598	8.539	-0.059
3	63	8.493	8.407	-0.086
3	64	8.422	8.356	-0.066
3	65	8.492	8.425	-0.067
3	66	8.544	8.490	-0.054
5	67	8.462	8.457	-0.005
5	68	8.606	8.470	-0.136
5	69	8.531	8.482	-0.049
5	70	8.447	8.398	-0.049
5	71	8.560	8.475	-0.085
5	72	8.495	8.448	-0.047
10	73	8.538	8.526	-0.012
10	74	8.598	8.469	-0.129
10	75	8.493	8.434	-0.059
10	76	8.422	8.388	-0.034
10	77	8.492	8.468	-0.024
10	78	8.544	8.473	-0.071
15	79	8.462	8.439	-0.023
15	80	8.606	8.516	-0.090
15	81	8.531	8.476	-0.055
15	82	8.447	8.394	-0.053
15	83	8.560	8.485	-0.075
15	84	8.495	8.480	-0.015
30	86	8.538	8.548	0.010
30	89	8.598	8.491	-0.107
30	91	8.493	8.490	-0.003
30	92	8.422	8.485	0.063
30	94	8.492	8.551	0.059
30	95	8.544	8.532	-0.012
35	100	8.462	8.436	-0.026
35	101	8.606	8.612	0.006
35	102	8.531	8.540	0.009
35	103	8.447	8.465	0.018
35	104	8.560	8.531	-0.029
35	105	8.495	8.494	-0.001
50	106	8.553	8.359	-0.194
50	107	8.412	8.329	-0.083
50	108	8.534	8.443	-0.091
50	109	8.491	8.354	-0.137
50	110	8.451	8.409	-0.042
50	111	8.475	8.399	-0.076
55	113	8.562	8.377	-0.185
55	114	8.489	8.381	-0.108
55	115	8.576	8.433	-0.143
55	116	8.509	8.440	-0.069
55	126	8.568	8.390	-0.178
55	128	8.519	8.472	-0.047
100	129	8.456	8.432	-0.024
100	131	8.544	8.446	-0.098
100	132	8.468	8.318	-0.150
100	133	8.472	8.475	0.003
100	134	8.395	8.429	0.034
100	135	8.485	8.423	-0.062
100	138	8.456	8.430	-0.026
100	139	8.460	8.408	-0.052
100	140	8.476	8.421	-0.055
100	141	8.554	8.491	-0.063
100	142	8.464	8.610	0.146
100	143	8.489	8.453	-0.036
100	144	8.485	8.396	-0.089
100	220	8.466	8.452	-0.014
100	221	8.501	8.408	-0.093
100	224	8.483	8.486	0.003
100	225	8.543	8.476	-0.067
100	226	8.530	8.490	-0.040
100	228	8.523	8.454	-0.069
100	232	8.411	8.439	0.028
100	233	8.434	8.423	-0.011
100	236	8.530	8.468	-0.062
100	238	8.579	8.507	-0.072
100	243	8.465	8.465	0.000
105	244	8.525	8.494	-0.031
105	246	8.509	8.481	-0.028
105	248	8.564	8.529	-0.035
105	374	8.473	8.526	0.053
105	376	8.496	8.540	0.044
Max		8.606	8.612	0.146
Average		8.506	8.461	-0.044
Min		8.395	8.318	-0.194
Std Dev		0.051	0.058	0.059

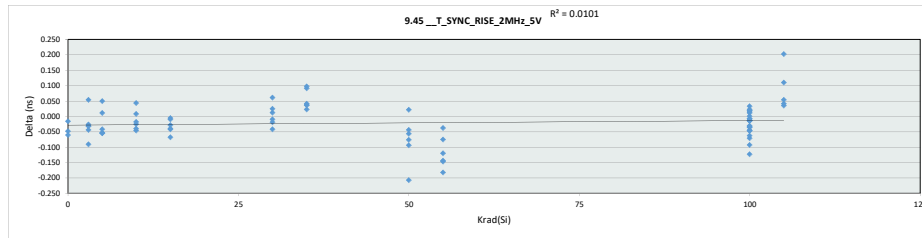


9.41_T_SYNC_RISE_1p5MHz											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.495	8.356	8.398	8.388	8.394	8.485	8.436	8.329	8.377	8.318	8.481
Average	8.507	8.463	8.455	8.460	8.465	8.516	8.513	8.382	8.416	8.450	8.514
Max	8.524	8.558	8.482	8.526	8.516	8.551	8.612	8.443	8.472	8.610	8.540
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

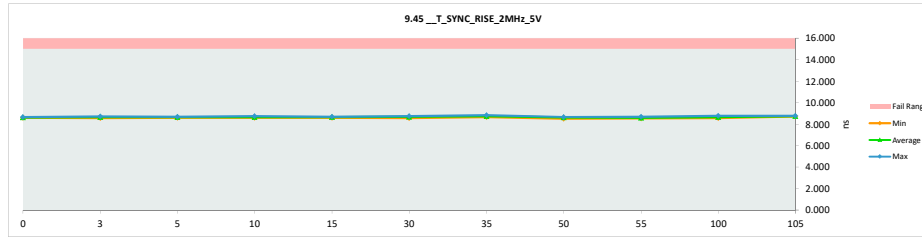


LDR TID Report
TPS7H5001-SP

9.45 __T_SYNC_RISE_2MHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.672	8.611	-0.061
0	118	8.720	8.672	-0.048
0	122	8.659	8.643	-0.016
3	61	8.707	8.681	-0.026
3	62	8.676	8.730	0.054
3	63	8.665	8.574	-0.091
3	64	8.600	8.556	-0.044
3	65	8.701	8.670	-0.031
3	66	8.734	8.703	-0.031
5	67	8.655	8.613	-0.042
5	68	8.748	8.695	-0.053
5	69	8.716	8.662	-0.054
5	70	8.626	8.637	0.011
5	71	8.711	8.657	-0.054
5	72	8.646	8.696	0.050
10	73	8.707	8.750	0.043
10	74	8.676	8.636	-0.040
10	75	8.665	8.619	-0.046
10	76	8.600	8.608	0.008
10	77	8.701	8.683	-0.018
10	78	8.734	8.709	-0.025
15	79	8.655	8.613	-0.042
15	80	8.748	8.680	-0.068
15	81	8.716	8.698	-0.018
15	82	8.626	8.586	-0.040
15	83	8.711	8.701	-0.010
15	84	8.646	8.641	-0.005
30	86	8.707	8.697	-0.010
30	89	8.676	8.737	0.061
30	91	8.665	8.677	0.012
30	92	8.600	8.581	-0.019
30	94	8.701	8.726	0.025
30	95	8.734	8.692	-0.042
35	100	8.655	8.752	0.097
35	101	8.748	8.839	0.091
35	102	8.716	8.757	0.041
35	103	8.626	8.649	0.023
35	104	8.711	8.748	0.037
35	105	8.646	8.682	0.036
50	106	8.686	8.610	-0.076
50	107	8.718	8.511	-0.207
50	108	8.630	8.652	0.022
50	109	8.699	8.643	-0.056
50	110	8.713	8.669	-0.044
50	111	8.703	8.609	-0.094
55	113	8.725	8.687	-0.038
55	114	8.776	8.629	-0.147
55	115	8.752	8.608	-0.144
55	116	8.765	8.584	-0.182
55	126	8.659	8.539	-0.120
55	128	8.671	8.596	-0.075
100	129	8.651	8.669	0.018
100	131	8.701	8.630	-0.071
100	132	8.671	8.608	-0.063
100	133	8.712	8.677	-0.035
100	134	8.642	8.665	0.023
100	135	8.661	8.568	-0.093
100	138	8.665	8.635	-0.030
100	139	8.644	8.630	-0.014
100	140	8.659	8.644	-0.015
100	141	8.693	8.694	0.001
100	142	8.733	8.610	-0.123
100	143	8.680	8.713	0.033
100	144	8.655	8.675	0.020
100	220	8.688	8.675	-0.013
100	221	8.648	8.614	-0.034
100	224	8.653	8.645	-0.008
100	225	8.729	8.716	-0.013
100	226	8.772	8.783	0.011
100	228	8.747	8.739	-0.008
100	232	8.650	8.664	0.014
100	233	8.663	8.617	-0.046
100	236	8.720	8.675	-0.045
100	238	8.715	8.679	-0.036
100	243	8.745	8.698	-0.047
105	244	8.667	8.777	0.110
105	246	8.701	8.755	0.054
105	248	8.702	8.743	0.041
105	374	8.576	8.779	0.203
105	376	8.674	8.709	0.035
Max		8.776	8.839	0.203
Average		8.687	8.666	-0.021
Min		8.576	8.511	-0.207
Std Dev		0.043	0.061	0.063

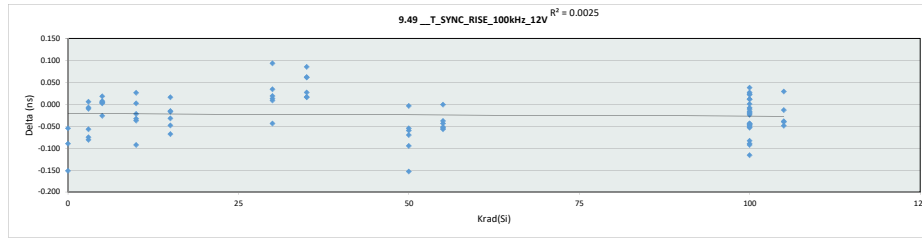


9.45 __T_SYNC_RISE_2MHz_5V											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.611	8.556	8.613	8.608	8.586	8.581	8.649	8.511	8.539	8.568	8.709
Average	8.642	8.652	8.660	8.668	8.652	8.685	8.738	8.616	8.607	8.663	8.753
Max	8.672	8.730	8.696	8.650	8.701	8.737	8.839	8.669	8.687	8.783	8.779
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

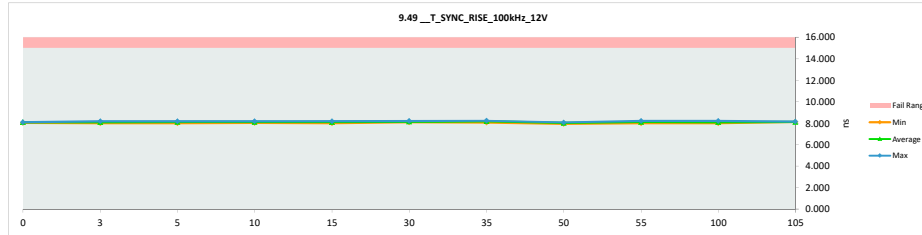


LDR TID Report
TPS7H5001-SP

9.49 _T_SYNC_RISE_100kHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.254	8.102	-0.152
0	118	8.087	8.032	-0.055
0	122	8.197	8.107	-0.090
3	61	8.168	8.087	-0.081
3	62	8.172	8.162	-0.010
3	63	8.195	8.188	-0.007
3	64	8.077	8.002	-0.075
3	65	8.004	8.010	0.006
3	66	8.193	8.136	-0.057
5	67	8.130	8.134	0.004
5	68	8.188	8.195	0.007
5	69	8.200	8.174	-0.026
5	70	7.998	8.003	0.005
5	71	8.143	8.145	0.002
5	72	8.143	8.161	0.018
10	73	8.168	8.131	-0.037
10	74	8.172	8.174	0.002
10	75	8.195	8.102	-0.093
10	76	8.077	8.055	-0.022
10	77	8.004	8.030	0.026
10	78	8.193	8.160	-0.033
15	79	8.130	8.062	-0.068
15	80	8.188	8.173	-0.015
15	81	8.200	8.168	-0.032
15	82	7.998	8.014	0.016
15	83	8.143	8.126	-0.017
15	84	8.143	8.095	-0.048
30	86	8.168	8.124	-0.044
30	89	8.172	8.206	0.034
30	91	8.195	8.204	0.009
30	92	8.077	8.096	0.019
30	94	8.004	8.097	0.093
30	95	8.193	8.206	0.013
35	100	8.130	8.191	0.061
35	101	8.188	8.215	0.027
35	102	8.200	8.216	0.016
35	103	7.998	8.060	0.062
35	104	8.143	8.228	0.085
35	105	8.143	8.160	0.017
50	106	8.143	8.073	-0.070
50	107	8.083	7.930	-0.153
50	108	8.090	8.030	-0.060
50	109	8.101	8.046	-0.055
50	110	8.032	8.028	-0.004
50	111	8.160	8.065	-0.095
55	113	8.043	8.005	-0.038
55	114	8.191	8.147	-0.044
55	115	8.183	8.129	-0.054
55	116	8.180	8.123	-0.057
55	126	8.076	8.024	-0.052
55	128	8.223	8.222	-0.001
100	129	8.056	8.012	-0.044
100	131	8.219	8.136	-0.083
100	132	8.118	8.110	-0.008
100	133	8.038	8.050	0.012
100	134	8.075	8.097	0.022
100	135	8.191	8.175	-0.016
100	138	8.133	8.086	-0.047
100	139	8.125	8.035	-0.090
100	140	8.120	8.071	-0.049
100	141	8.237	8.226	-0.011
100	142	8.145	8.146	0.001
100	143	8.161	8.143	-0.018
100	144	8.122	8.134	0.012
100	220	8.165	8.141	-0.024
100	221	8.153	8.132	-0.021
100	224	8.210	8.094	-0.116
100	225	8.062	8.016	-0.046
100	226	8.117	8.144	0.027
100	228	8.135	8.083	-0.052
100	232	8.023	8.046	0.023
100	233	8.099	8.055	-0.044
100	236	8.153	8.100	-0.053
100	238	8.266	8.173	-0.093
100	243	8.115	8.153	0.038
105	244	8.163	8.123	-0.040
105	246	8.153	8.140	-0.013
105	248	8.133	8.162	0.029
105	374	8.152	8.113	-0.039
105	376	8.157	8.108	-0.049
Max		8.266	8.228	0.038
Average		8.136	8.112	-0.024
Min		7.998	7.930	-0.153
Std Dev		0.064	0.065	0.047

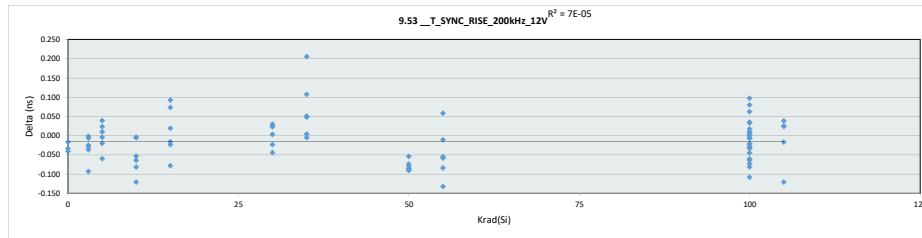


9.49 _T_SYNC_RISE_100kHz											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.032	8.002	8.003	8.030	8.014	8.096	8.060	7.930	8.005	8.012	8.108
Average	8.080	8.098	8.135	8.109	8.106	8.156	8.178	8.029	8.108	8.107	8.129
Max	8.107	8.188	8.195	8.174	8.173	8.206	8.228	8.073	8.222	8.226	8.162
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

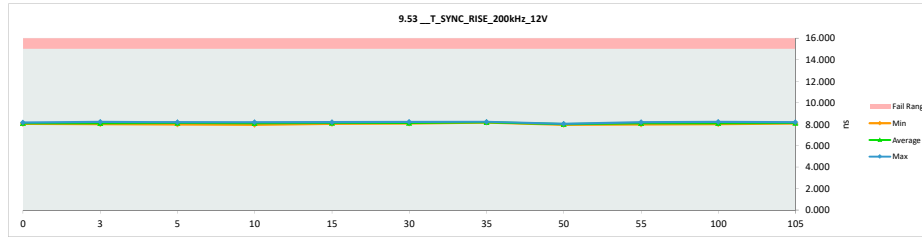


LDR TID Report
TPS7H5001-SP

9.53_T_SYNC_RISE_200kHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.189	8.156	-0.033
0	118	8.088	8.047	-0.041
0	122	8.112	8.095	-0.017
3	61	8.149	8.112	-0.037
3	62	8.175	8.150	-0.025
3	63	8.208	8.207	-0.007
3	64	8.093	8.000	-0.093
3	65	8.038	8.008	-0.030
3	66	8.139	8.137	-0.002
5	67	8.103	8.083	-0.020
5	68	8.186	8.196	0.010
5	69	8.175	8.171	-0.004
5	70	7.945	7.968	0.023
5	71	8.197	8.137	-0.060
5	72	8.052	8.091	0.039
10	73	8.149	8.096	-0.053
10	74	8.175	8.171	-0.004
10	75	8.208	8.144	-0.064
10	76	8.093	7.972	-0.121
10	77	8.038	7.956	-0.082
10	78	8.139	8.134	-0.005
15	79	8.103	8.087	-0.016
15	80	8.186	8.163	-0.023
15	81	8.175	8.194	0.019
15	82	7.945	8.018	0.073
15	83	8.197	8.119	-0.078
15	84	8.052	8.144	0.092
30	86	8.149	8.152	0.003
30	89	8.175	8.198	0.023
30	91	8.208	8.185	-0.023
30	92	8.093	8.049	-0.044
30	94	8.038	8.063	0.025
30	95	8.139	8.169	0.030
35	100	8.103	8.154	0.051
35	101	8.186	8.190	0.004
35	102	8.175	8.223	0.048
35	103	7.945	8.150	0.205
35	104	8.197	8.192	-0.005
35	105	8.052	8.159	0.107
50	106	8.105	8.016	-0.089
50	107	8.012	7.938	-0.074
50	108	8.093	8.007	-0.086
50	109	8.131	8.041	-0.090
50	110	8.064	8.010	-0.054
50	111	8.099	8.019	-0.080
55	113	8.093	7.961	-0.132
55	114	8.206	8.151	-0.055
55	115	8.235	8.151	-0.084
55	116	8.153	8.142	-0.011
55	126	8.051	7.993	-0.058
55	128	8.135	8.193	0.058
100	129	8.093	8.011	-0.082
100	131	8.133	8.136	0.003
100	132	8.113	8.085	-0.028
100	133	8.086	8.053	-0.033
100	134	8.115	8.053	-0.062
100	135	8.164	8.164	0.000
100	138	8.106	8.123	0.017
100	139	8.070	8.006	-0.064
100	140	8.097	8.104	0.007
100	141	8.184	8.162	-0.022
100	142	8.159	8.194	0.035
100	143	8.152	8.162	0.010
100	144	8.129	8.121	-0.008
100	220	8.106	8.186	0.080
100	221	8.155	8.123	-0.032
100	224	8.122	8.101	-0.021
100	225	8.059	8.014	-0.045
100	226	8.123	8.050	-0.073
100	228	8.048	8.145	0.097
100	232	8.030	8.063	0.033
100	233	8.119	8.058	-0.061
100	236	8.122	8.014	-0.108
100	238	8.155	8.217	0.062
100	243	8.095	8.090	-0.005
105	244	8.215	8.094	-0.121
105	246	8.152	8.190	0.038
105	248	8.085	8.109	0.024
105	374	8.078	8.061	-0.017
105	376	8.137	8.162	0.025
Max		8.235	8.223	0.205
Average		8.119	8.104	-0.016
Min		7.945	7.938	-0.132
Std Dev		0.062	0.073	0.058

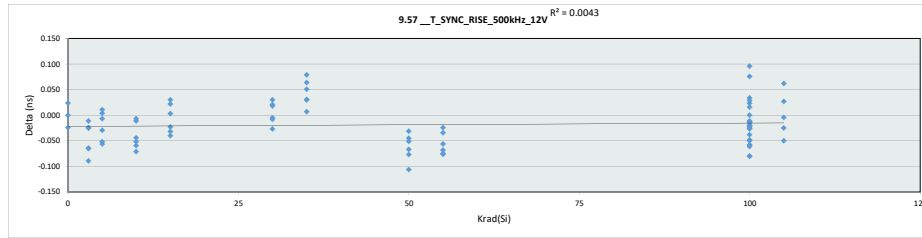


9.53_T_SYNC_RISE_200kHz											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.047	8.000	7.968	7.956	8.018	8.049	8.150	7.938	7.961	8.006	8.061
Average	8.099	8.101	8.108	8.079	8.121	8.136	8.178	8.005	8.099	8.101	8.123
Max	8.156	8.201	8.196	8.171	8.194	8.198	8.223	8.041	8.193	8.217	8.190
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

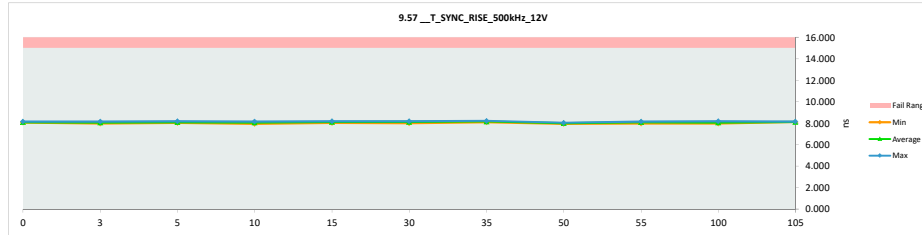


LDR TID Report
TPS7H5001-SP

9.57_T_SYNC_RISE_500kHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.162	8.138	-0.024
0	118	8.063	8.063	0.000
0	122	8.046	8.070	0.024
3	61	8.133	8.044	-0.089
3	62	8.154	8.130	-0.024
3	63	8.209	8.144	-0.065
3	64	8.034	7.970	-0.064
3	65	8.013	7.988	-0.025
3	66	8.133	8.122	-0.011
5	67	8.070	8.063	-0.007
5	68	8.172	8.176	0.004
5	69	8.192	8.140	-0.052
5	70	8.019	8.030	0.011
5	71	8.139	8.110	-0.029
5	72	8.137	8.081	-0.056
10	73	8.133	8.062	-0.071
10	74	8.154	8.147	-0.007
10	75	8.209	8.165	-0.044
10	76	8.034	8.023	-0.011
10	77	8.013	7.954	-0.059
10	78	8.133	8.081	-0.052
15	79	8.070	8.100	0.030
15	80	8.172	8.149	-0.023
15	81	8.192	8.195	0.003
15	82	8.019	8.041	0.022
15	83	8.139	8.099	-0.040
15	84	8.137	8.105	-0.032
30	86	8.133	8.125	-0.008
30	89	8.154	8.172	0.018
30	91	8.209	8.182	-0.027
30	92	8.034	8.055	0.021
30	94	8.013	8.008	-0.005
30	95	8.133	8.163	0.030
35	100	8.070	8.134	0.064
35	101	8.172	8.223	0.051
35	102	8.192	8.199	0.007
35	103	8.019	8.098	0.079
35	104	8.139	8.169	0.030
35	105	8.137	8.168	0.031
50	106	8.089	7.983	-0.106
50	107	8.002	7.951	-0.051
50	108	8.083	8.038	-0.045
50	109	8.113	8.046	-0.067
50	110	8.024	7.993	-0.031
50	111	8.092	8.015	-0.077
55	113	8.101	8.026	-0.075
55	114	8.201	8.125	-0.076
55	115	8.180	8.146	-0.034
55	116	8.175	8.119	-0.056
55	126	8.054	7.986	-0.068
55	128	8.184	8.160	-0.024
100	129	8.000	7.980	-0.020
100	131	8.172	8.092	-0.080
100	132	8.063	8.049	-0.014
100	133	7.999	7.977	-0.022
100	134	8.022	8.098	0.076
100	135	8.122	8.097	-0.025
100	138	8.114	8.066	-0.048
100	139	8.067	8.009	-0.058
100	140	8.008	8.104	0.096
100	141	8.167	8.190	0.023
100	142	8.097	8.097	0.000
100	143	8.140	8.127	-0.013
100	144	8.115	8.035	-0.080
100	220	8.103	8.090	-0.013
100	221	8.144	8.083	-0.061
100	224	8.114	8.094	-0.020
100	225	8.027	7.989	-0.038
100	226	8.069	8.098	0.029
100	228	8.120	8.070	-0.050
100	232	8.024	8.058	0.034
100	233	8.026	8.042	0.016
100	236	8.117	8.059	-0.058
100	238	8.187	8.176	-0.011
100	243	8.116	8.089	-0.027
105	244	8.176	8.126	-0.050
105	246	8.088	8.150	0.062
105	248	8.091	8.118	0.027
105	374	8.141	8.116	-0.025
105	376	8.106	8.102	-0.004
Max		8.209	8.223	0.096
Average		8.106	8.088	-0.018
Min		7.999	7.951	-0.106
Std Dev		0.061	0.064	0.042

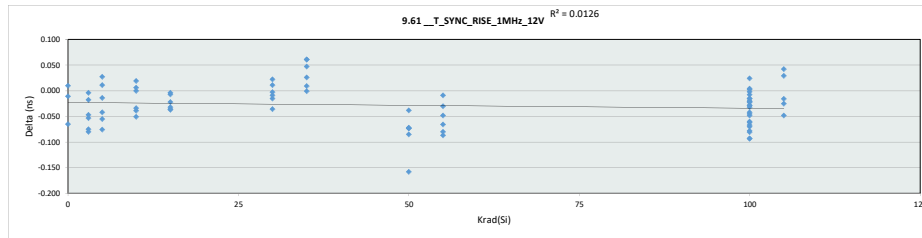


9.57_T_SYNC_RISE_500kHz											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.063	7.970	8.030	7.954	8.041	8.008	8.098	7.951	7.986	7.977	8.102
Average	8.090	8.066	8.100	8.072	8.115	8.118	8.165	8.004	8.094	8.074	8.122
Max	8.138	8.144	8.176	8.165	8.195	8.182	8.223	8.046	8.160	8.190	8.150
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

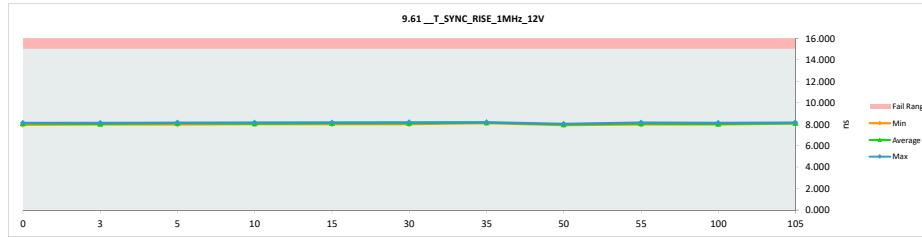


LDR TID Report
TPS7H5001-SP

9.61_T_SYNC_RISE_1MHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.130	8.119	-0.011
0	118	8.012	7.947	-0.065
0	122	8.088	8.098	0.010
3	61	8.092	8.039	-0.053
3	62	8.137	8.119	-0.018
3	63	8.183	8.103	-0.080
3	64	8.046	7.971	-0.075
3	65	7.996	7.992	-0.004
3	66	8.103	8.056	-0.047
5	67	8.080	8.107	0.027
5	68	8.180	8.125	-0.055
5	69	8.132	8.143	0.011
5	70	8.017	7.975	-0.042
5	71	8.132	8.056	-0.076
5	72	8.094	8.080	-0.014
10	73	8.092	8.098	0.006
10	74	8.137	8.103	-0.034
10	75	8.183	8.144	-0.039
10	76	8.046	7.995	-0.051
10	77	7.996	7.996	0.000
10	78	8.103	8.122	0.019
15	79	8.080	8.044	-0.036
15	80	8.180	8.143	-0.037
15	81	8.132	8.100	-0.032
15	82	8.017	8.010	-0.007
15	83	8.132	8.110	-0.022
15	84	8.094	8.090	-0.004
30	86	8.092	8.083	-0.009
30	89	8.137	8.134	-0.003
30	91	8.183	8.168	-0.015
30	92	8.046	8.068	0.022
30	94	7.996	8.007	0.011
30	95	8.103	8.067	-0.036
35	100	8.080	8.141	0.061
35	101	8.180	8.179	-0.001
35	102	8.132	8.179	0.047
35	103	8.017	8.078	0.061
35	104	8.132	8.141	0.009
35	105	8.094	8.120	0.026
50	106	8.113	7.955	-0.158
50	107	7.999	7.914	-0.085
50	108	8.062	7.989	-0.073
50	109	8.110	8.037	-0.073
50	110	7.972	7.934	-0.038
50	111	8.106	8.034	-0.072
55	113	8.048	7.961	-0.087
55	114	8.140	8.092	-0.048
55	115	8.167	8.101	-0.066
55	116	8.170	8.090	-0.080
55	126	8.025	7.995	-0.030
55	128	8.157	8.148	-0.009
100	129	8.007	7.992	-0.015
100	131	8.104	8.096	-0.008
100	132	8.089	8.029	-0.060
100	133	7.995	8.019	0.024
100	134	8.083	8.016	-0.067
100	135	8.135	8.112	-0.023
100	138	8.061	8.032	-0.029
100	139	8.036	8.034	-0.002
100	140	8.068	7.998	-0.070
100	141	8.148	8.128	-0.020
100	142	8.122	8.044	-0.078
100	143	8.083	8.038	-0.045
100	144	8.106	8.013	-0.093
100	220	8.098	8.066	-0.032
100	221	8.088	8.090	0.002
100	224	8.070	8.049	-0.021
100	225	8.069	7.988	-0.081
100	226	8.070	8.028	-0.042
100	228	8.116	8.023	-0.093
100	232	8.023	7.975	-0.048
100	233	8.055	7.993	-0.062
100	236	8.083	8.055	-0.028
100	238	8.118	8.122	0.004
100	243	8.087	8.072	-0.015
105	244	8.076	8.118	0.042
105	246	8.175	8.159	-0.016
105	248	8.099	8.051	-0.048
105	374	8.087	8.062	-0.025
105	376	8.073	8.102	0.029
Max		8.183	8.179	0.061
Average		8.091	8.063	-0.029
Min		7.972	7.914	-0.158
Std Dev		0.052	0.062	0.040

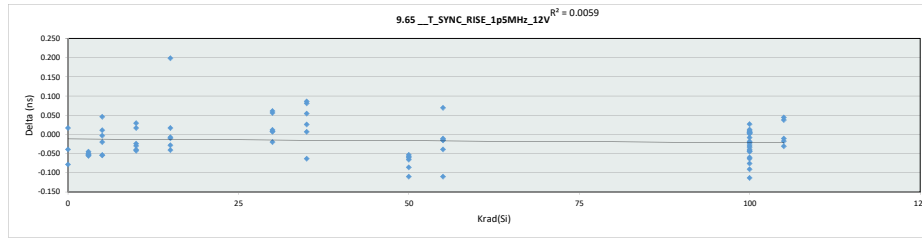


9.61_T_SYNC_RISE_1MHz_12V											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.947	7.971	7.975	7.995	8.010	8.007	8.078	7.914	7.961	7.975	8.051
Average	8.055	8.047	8.081	8.076	8.083	8.088	8.140	7.977	8.065	8.042	8.098
Max	8.119	8.119	8.143	8.144	8.143	8.168	8.179	8.037	8.148	8.128	8.159
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

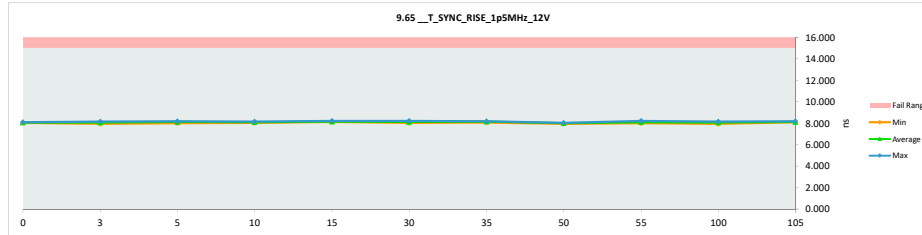


LDR TID Report
TPS7H5001-SP

9.65 __T_SYNC_RISE_1p5MHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.131	8.092	-0.039
0	118	8.019	8.036	0.017
0	122	8.159	8.081	-0.078
3	61	8.123	8.069	-0.054
3	62	8.198	8.142	-0.056
3	63	8.165	8.112	-0.053
3	64	7.991	7.942	-0.049
3	65	8.008	7.963	-0.045
3	66	8.148	8.096	-0.052
5	67	8.096	8.093	-0.003
5	68	8.236	8.182	-0.054
5	69	8.159	8.105	-0.054
5	70	7.969	8.015	0.046
5	71	8.163	8.143	-0.020
5	72	8.115	8.126	0.011
10	73	8.123	8.099	-0.024
10	74	8.198	8.158	-0.040
10	75	8.165	8.135	-0.030
10	76	7.991	8.020	0.029
10	77	8.008	8.025	0.017
10	78	8.148	8.106	-0.042
15	79	8.096	8.113	0.017
15	80	8.236	8.208	-0.028
15	81	8.159	8.118	-0.041
15	82	7.969	8.188	0.199
15	83	8.163	8.156	-0.007
15	84	8.115	8.105	-0.010
30	86	8.123	8.131	0.008
30	89	8.198	8.178	-0.020
30	91	8.165	8.173	0.008
30	92	7.991	8.052	0.061
30	94	8.008	8.020	0.012
30	95	8.148	8.204	0.056
35	100	8.096	8.150	0.054
35	101	8.236	8.173	-0.063
35	102	8.159	8.166	0.007
35	103	7.969	8.055	0.086
35	104	8.163	8.189	0.026
35	105	8.115	8.196	0.081
50	106	8.118	8.008	-0.110
50	107	8.010	7.953	-0.057
50	108	8.094	8.041	-0.053
50	109	8.113	8.027	-0.086
50	110	8.046	7.985	-0.061
50	111	8.083	8.017	-0.066
55	113	8.032	8.017	-0.015
55	114	8.124	8.085	-0.039
55	115	8.139	8.208	0.069
55	116	8.177	8.067	-0.110
55	125	8.032	8.017	-0.015
55	128	8.193	8.182	-0.011
100	129	8.000	7.957	-0.043
100	131	8.183	8.153	-0.030
100	132	8.113	8.119	0.006
100	133	8.010	7.965	-0.045
100	134	8.105	8.080	-0.025
100	135	8.108	8.100	-0.008
100	138	8.068	8.095	0.027
100	139	8.107	8.031	-0.076
100	140	8.100	8.037	-0.063
100	141	8.125	8.107	-0.018
100	142	8.137	8.104	-0.033
100	143	8.124	8.131	0.007
100	144	8.154	8.041	-0.113
100	220	8.092	8.101	0.009
100	221	8.127	8.143	-0.021
100	224	8.082	8.084	0.002
100	225	8.026	7.987	-0.039
100	226	8.075	8.032	-0.043
100	228	8.117	8.057	-0.060
100	232	8.002	8.006	0.004
100	233	8.004	8.007	0.003
100	236	8.096	8.101	0.005
100	238	8.164	8.143	-0.021
100	243	8.064	8.077	0.013
105	244	8.136	8.119	-0.017
105	246	8.197	8.186	-0.011
105	248	8.092	8.130	0.038
105	374	8.036	8.080	0.044
105	376	8.109	8.078	-0.031
	Max	8.236	8.208	0.199
	Average	8.104	8.088	-0.016
	Min	7.969	7.942	-0.113
	Std Dev	0.068	0.067	0.049

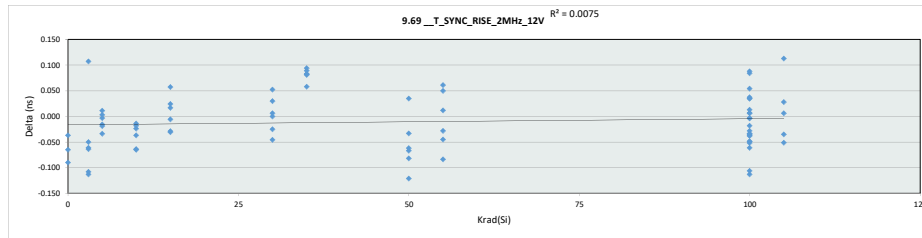


9.65 __T_SYNC_RISE_1p5MHz											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.036	7.942	8.015	8.020	8.105	8.020	8.055	7.953	8.017	7.957	8.078
Average	8.070	8.054	8.111	8.091	8.145	8.126	8.155	8.005	8.096	8.065	8.119
Max	8.092	8.142	8.182	8.158	8.208	8.204	8.196	8.041	8.208	8.153	8.186
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

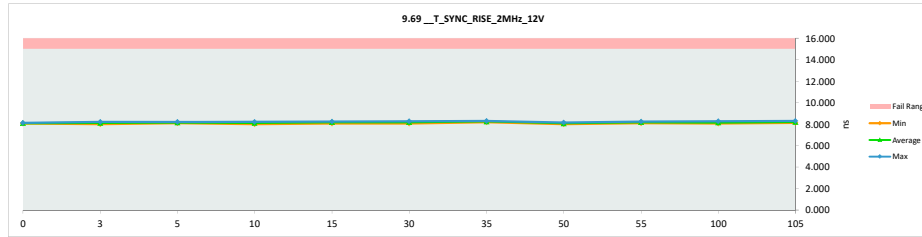


LDR TID Report
TPS7H5001-SP

9.69_T_SYNC_RISE_2MHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.158	8.068	-0.090
0	118	8.098	8.061	-0.037
0	122	8.178	8.113	-0.065
3	61	8.189	8.139	-0.050
3	62	8.260	8.199	-0.061
3	63	8.239	8.175	-0.064
3	64	8.105	7.997	-0.108
3	65	8.024	8.131	0.107
3	66	8.218	8.105	-0.113
5	67	8.145	8.129	-0.016
5	68	8.224	8.190	-0.034
5	69	8.186	8.189	0.003
5	70	8.093	8.104	0.011
5	71	8.226	8.207	-0.019
5	72	8.172	8.169	-0.003
10	73	8.189	8.175	-0.014
10	74	8.260	8.195	-0.065
10	75	8.239	8.215	-0.024
10	76	8.105	8.068	-0.037
10	77	8.024	8.006	-0.018
10	78	8.218	8.154	-0.064
15	79	8.145	8.202	0.057
15	80	8.224	8.248	0.024
15	81	8.186	8.203	0.017
15	82	8.093	8.062	-0.031
15	83	8.226	8.220	-0.006
15	84	8.172	8.143	-0.029
30	86	8.189	8.164	-0.025
30	89	8.260	8.260	0.000
30	91	8.239	8.193	-0.046
30	92	8.105	8.111	0.006
30	94	8.024	8.076	0.052
30	95	8.218	8.248	0.030
35	100	8.145	8.234	0.089
35	101	8.224	8.307	0.083
35	102	8.186	8.267	0.081
35	103	8.093	8.187	0.094
35	104	8.226	8.284	0.058
35	105	8.172	8.254	0.082
50	106	8.175	8.108	-0.067
50	107	8.092	8.010	-0.082
50	108	8.049	8.084	0.035
50	109	8.180	8.147	-0.033
50	110	8.113	8.051	-0.062
50	111	8.166	8.045	-0.121
55	113	8.024	8.085	0.061
55	114	8.173	8.185	0.012
55	115	8.305	8.221	-0.084
55	116	8.248	8.203	-0.045
55	126	8.071	8.121	0.050
55	128	8.271	8.243	-0.028
100	129	8.052	8.089	0.037
100	131	8.225	8.176	-0.049
100	132	8.131	8.185	0.054
100	133	8.054	8.089	0.035
100	134	8.152	8.114	-0.038
100	135	8.225	8.260	0.035
100	138	8.163	8.057	-0.106
100	139	8.162	8.113	-0.049
100	140	8.131	8.095	-0.036
100	141	8.241	8.128	-0.113
100	142	8.236	8.175	-0.061
100	143	8.190	8.196	0.006
100	144	8.178	8.141	-0.037
100	220	8.211	8.193	-0.018
100	221	8.193	8.159	-0.034
100	224	8.177	8.174	-0.003
100	225	8.045	8.129	0.084
100	226	8.169	8.141	-0.028
100	228	8.211	8.159	-0.052
100	232	8.060	8.056	-0.004
100	233	8.122	8.088	-0.034
100	236	8.168	8.256	0.088
100	238	8.245	8.252	0.007
100	243	8.186	8.199	0.013
105	244	8.209	8.215	0.006
105	246	8.180	8.293	0.113
105	248	8.156	8.184	0.028
105	374	8.226	8.175	-0.051
105	376	8.160	8.125	-0.035
	Max	8.305	8.307	0.113
	Average	8.168	8.157	-0.010
	Min	8.024	7.997	-0.121
	Std Dev	0.066	0.071	0.056

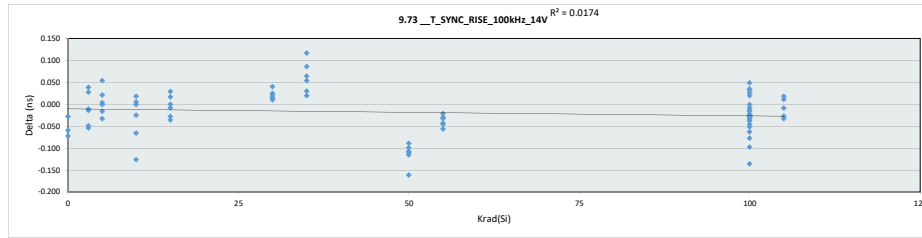


9.69_T_SYNC_RISE_2MHz_12V											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.061	7.997	8.104	8.006	8.062	8.076	8.187	8.010	8.085	8.056	8.125
Average	8.081	8.124	8.165	8.136	8.180	8.175	8.256	8.074	8.176	8.151	8.198
Max	8.113	8.199	8.207	8.215	8.248	8.260	8.307	8.147	8.243	8.260	8.293
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

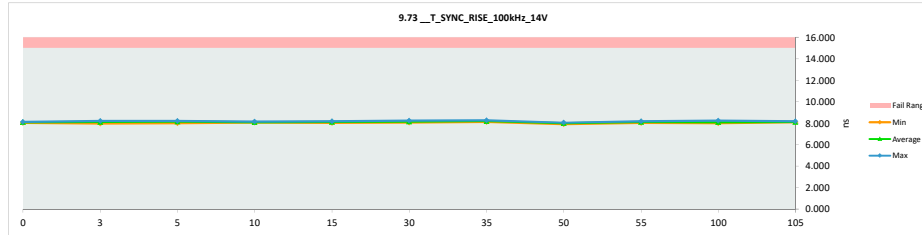


LDR TID Report
TPS7H5001-SP

9.73_T_SYNC_RISE_100kHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.197	8.125	-0.072
0	118	8.105	8.046	-0.059
0	122	8.120	8.092	-0.028
3	61	8.106	8.145	0.039
3	62	8.229	8.218	-0.011
3	63	8.219	8.165	-0.054
3	64	8.036	7.987	-0.049
3	65	8.055	8.083	0.028
3	66	8.181	8.167	-0.014
5	67	8.113	8.117	0.004
5	68	8.183	8.167	-0.016
5	69	8.160	8.214	0.054
5	70	8.043	8.010	-0.033
5	71	8.143	8.164	0.021
5	72	8.158	8.157	-0.001
10	73	8.106	8.111	0.005
10	74	8.229	8.163	-0.066
10	75	8.219	8.093	-0.126
10	76	8.036	8.054	0.018
10	77	8.055	8.054	-0.001
10	78	8.181	8.156	-0.025
15	79	8.113	8.142	0.029
15	80	8.183	8.147	-0.036
15	81	8.160	8.177	0.017
15	82	8.043	8.043	0.000
15	83	8.143	8.115	-0.028
15	84	8.158	8.150	-0.008
30	86	8.106	8.131	0.025
30	89	8.229	8.245	0.016
30	91	8.219	8.229	0.010
30	92	8.036	8.057	0.021
30	94	8.055	8.095	0.040
30	95	8.181	8.194	0.013
35	100	8.113	8.133	0.020
35	101	8.183	8.213	0.030
35	102	8.160	8.277	0.117
35	103	8.043	8.129	0.086
35	104	8.143	8.207	0.064
35	105	8.158	8.212	0.054
50	106	8.116	8.027	-0.089
50	107	7.903	8.064	-0.161
50	108	8.149	8.034	-0.115
50	109	8.147	8.040	-0.107
50	110	8.060	7.950	-0.110
50	111	8.150	8.051	-0.099
55	113	8.078	8.033	-0.045
55	114	8.216	8.183	-0.033
55	115	8.178	8.149	-0.029
55	116	8.173	8.130	-0.043
55	126	8.096	8.040	-0.056
55	128	8.205	8.184	-0.021
100	129	8.019	8.004	-0.015
100	131	8.156	8.182	0.026
100	132	8.163	8.127	-0.036
100	133	8.020	7.989	-0.031
100	134	8.060	8.109	0.049
100	135	8.172	8.204	0.032
100	138	8.113	8.061	-0.052
100	139	8.075	8.067	-0.008
100	140	8.082	8.081	-0.001
100	141	8.219	8.189	-0.030
100	142	8.136	8.073	-0.063
100	143	8.121	8.092	-0.029
100	144	8.128	8.114	-0.014
100	220	8.173	8.147	-0.026
100	221	8.151	8.105	-0.046
100	224	8.133	8.095	-0.038
100	225	8.103	8.026	-0.077
100	226	8.142	8.006	-0.136
100	228	8.144	8.118	-0.026
100	232	8.113	8.015	-0.098
100	233	8.091	8.069	-0.022
100	236	8.122	8.094	-0.028
100	238	8.205	8.240	0.035
100	243	8.145	8.165	0.020
105	244	8.156	8.147	-0.009
105	246	8.159	8.170	0.011
105	248	8.162	8.135	-0.027
105	374	8.128	8.095	-0.033
105	376	8.069	8.087	0.018
	Max	8.229	8.277	0.117
	Average	8.132	8.114	-0.018
	Min	8.019	7.903	-0.161
	Std Dev	0.056	0.073	0.050

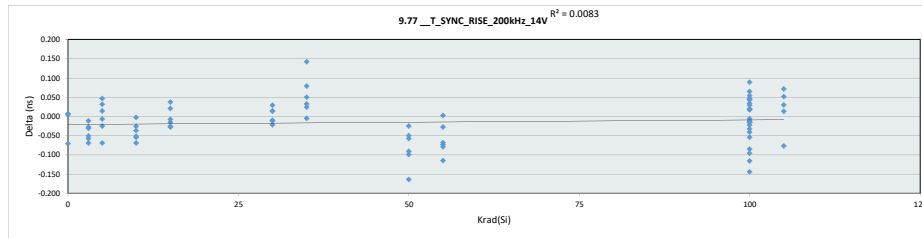


9.73_T_SYNC_RISE_100kHz											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.046	7.987	8.010	8.054	8.043	8.057	8.129	7.903	8.033	7.989	8.087
Average	8.088	8.128	8.138	8.105	8.129	8.159	8.195	8.001	8.120	8.099	8.127
Max	8.125	8.218	8.214	8.163	8.177	8.245	8.277	8.051	8.184	8.240	8.170
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

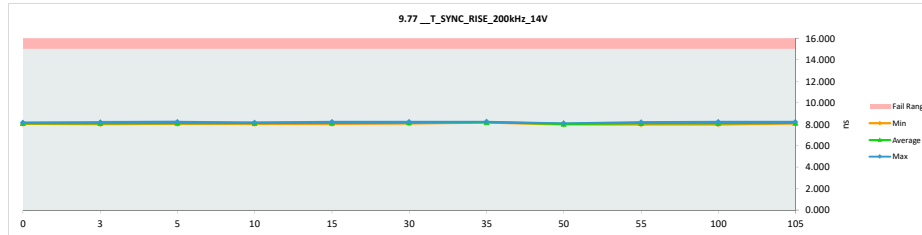


LDR TID Report
TPS7H5001-SP

9.77_T_SYNC_RISE_200kHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.148	8.152	0.004
0	118	8.039	8.046	0.007
0	122	8.132	8.061	-0.071
3	61	8.146	8.134	-0.012
3	62	8.215	8.146	-0.069
3	63	8.225	8.167	-0.058
3	64	8.027	7.996	-0.031
3	65	8.067	8.016	-0.051
3	66	8.160	8.132	-0.028
5	67	8.134	8.127	-0.007
5	68	8.176	8.207	0.031
5	69	8.206	8.137	-0.069
5	70	7.998	8.044	0.046
5	71	8.177	8.152	-0.025
5	72	8.089	8.103	0.014
10	73	8.146	8.092	-0.054
10	74	8.215	8.163	-0.052
10	75	8.225	8.156	-0.069
10	76	8.027	8.024	-0.003
10	77	8.067	8.030	-0.037
10	78	8.160	8.134	-0.026
15	79	8.134	8.108	-0.026
15	80	8.176	8.197	0.021
15	81	8.206	8.191	-0.015
15	82	7.998	8.035	0.037
15	83	8.177	8.169	-0.008
15	84	8.089	8.061	-0.028
30	86	8.146	8.124	-0.022
30	89	8.215	8.203	-0.012
30	91	8.225	8.215	-0.010
30	92	8.027	8.056	0.029
30	94	8.067	8.081	0.014
30	95	8.160	8.174	0.014
35	100	8.134	8.158	0.024
35	101	8.176	8.208	0.032
35	102	8.206	8.201	-0.005
35	103	7.998	8.140	0.142
35	104	8.177	8.227	0.050
35	105	8.089	8.168	0.079
50	106	8.126	8.035	-0.091
50	107	8.025	7.950	-0.075
50	108	8.130	8.031	-0.099
50	109	8.164	8.000	-0.164
50	110	8.105	8.047	-0.058
50	111	8.103	8.078	-0.025
55	113	8.047	7.979	-0.068
55	114	8.148	8.150	0.002
55	115	8.250	8.135	-0.115
55	116	8.182	8.154	-0.028
55	126	8.075	8.002	-0.073
55	128	8.249	8.170	-0.079
100	129	8.101	7.985	-0.116
100	131	8.154	8.148	-0.006
100	132	8.114	8.099	-0.015
100	133	8.052	8.070	0.018
100	134	8.121	8.025	-0.096
100	135	8.131	8.177	0.046
100	138	8.113	8.156	0.043
100	139	8.074	8.033	-0.041
100	140	8.086	8.071	-0.015
100	141	8.178	8.195	0.017
100	142	8.128	8.217	0.089
100	143	8.153	8.198	0.045
100	144	8.090	8.110	0.020
100	220	8.121	8.067	-0.054
100	221	8.138	8.123	-0.015
100	224	8.086	8.140	0.054
100	225	8.032	7.999	-0.033
100	226	8.072	8.101	0.029
100	228	8.149	8.005	-0.144
100	232	8.004	8.038	0.034
100	233	8.085	8.073	-0.012
100	236	8.087	8.152	0.065
100	238	8.197	8.112	-0.085
100	243	8.107	8.084	-0.023
105	244	8.151	8.074	-0.077
105	246	8.146	8.217	0.071
105	248	8.116	8.146	0.030
105	374	8.060	8.073	0.013
105	376	8.079	8.130	0.051
Max		8.250	8.227	0.142
Average		8.124	8.109	-0.015
Min		7.998	7.975	-0.164
Std Dev		0.063	0.068	0.055

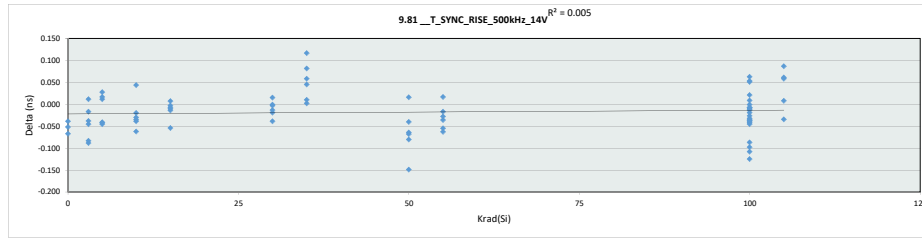


9.77_T_SYNC_RISE_200kHz											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.046	7.996	8.044	8.024	8.035	8.056	8.140	7.975	7.979	7.985	8.073
Average	8.086	8.099	8.128	8.100	8.127	8.142	8.184	8.028	8.098	8.099	8.128
Max	8.152	8.167	8.207	8.163	8.197	8.215	8.227	8.078	8.170	8.217	8.217
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

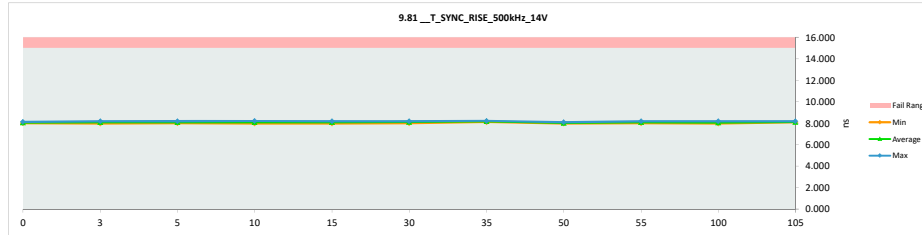


LDR TID Report
TPS7H5001-SP

9.81_T_SYNC_RISE_500kHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.163	8.111	-0.052
0	118	8.066	7.999	-0.067
0	122	8.122	8.083	-0.039
3	61	8.126	8.088	-0.038
3	62	8.165	8.177	0.012
3	63	8.174	8.157	-0.017
3	64	8.083	8.000	-0.083
3	65	8.023	7.978	-0.045
3	66	8.172	8.084	-0.088
5	67	8.067	8.095	0.028
5	68	8.224	8.183	-0.041
5	69	8.178	8.136	-0.042
5	70	7.989	8.006	0.017
5	71	8.118	8.130	0.012
5	72	8.127	8.082	-0.045
10	73	8.126	8.096	-0.030
10	74	8.165	8.209	0.044
10	75	8.174	8.154	-0.020
10	76	8.083	8.021	-0.062
10	77	8.023	7.984	-0.039
10	78	8.172	8.137	-0.035
15	79	8.067	8.074	0.007
15	80	8.224	8.170	-0.054
15	81	8.178	8.164	-0.014
15	82	7.989	7.982	-0.007
15	83	8.118	8.115	-0.003
15	84	8.127	8.117	-0.010
30	86	8.126	8.123	-0.003
30	89	8.165	8.146	-0.019
30	91	8.174	8.189	0.015
30	92	8.083	8.082	-0.001
30	94	8.023	8.010	-0.013
30	95	8.172	8.133	-0.039
35	100	8.067	8.125	0.058
35	101	8.224	8.226	0.002
35	102	8.178	8.188	0.010
35	103	7.989	8.106	0.117
35	104	8.118	8.200	0.082
35	105	8.127	8.172	0.045
50	106	8.094	8.030	-0.064
50	107	7.973	7.973	0.000
50	108	8.051	7.971	-0.080
50	109	8.126	8.058	-0.068
50	110	8.155	8.006	-0.149
50	111	8.138	8.098	-0.040
55	113	8.072	8.009	-0.063
55	114	8.163	8.135	-0.028
55	115	8.206	8.151	-0.055
55	116	8.171	8.135	-0.036
55	126	8.027	8.010	-0.017
55	128	8.150	8.167	0.017
100	129	8.000	7.981	-0.019
100	131	8.123	8.115	-0.008
100	132	8.075	8.062	-0.013
100	133	8.045	8.013	-0.032
100	134	8.110	8.068	-0.042
100	135	8.113	8.164	0.051
100	138	8.099	8.062	-0.037
100	139	8.039	8.031	-0.008
100	140	8.137	8.039	-0.098
100	141	8.140	8.133	-0.007
100	142	8.168	8.060	-0.108
100	143	8.086	8.149	0.063
100	144	8.102	8.068	-0.034
100	220	8.095	8.104	0.009
100	221	8.149	8.024	-0.125
100	224	8.120	8.094	-0.026
100	225	8.060	7.973	-0.087
100	226	8.088	8.087	-0.001
100	228	8.119	8.074	-0.045
100	232	8.012	7.972	-0.040
100	233	8.035	8.024	-0.011
100	236	8.096	8.117	0.021
100	238	8.136	8.189	0.053
100	243	8.090	8.053	-0.037
105	244	8.135	8.101	-0.034
105	246	8.169	8.177	0.008
105	248	8.050	8.137	0.087
105	374	8.060	8.118	0.058
105	376	8.067	8.128	0.061
Max		8.224	8.226	0.117
Average		8.109	8.091	-0.018
Min		7.957	7.971	-0.149
Std Dev		0.060	0.067	0.048

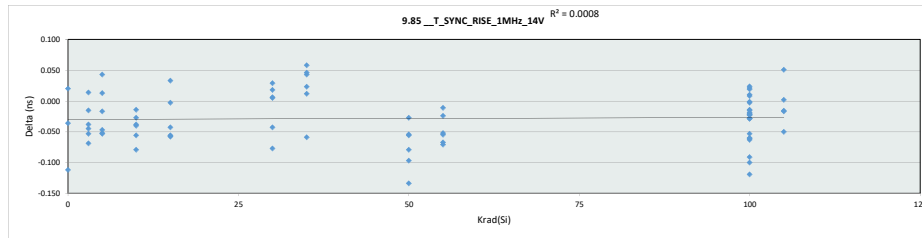


9.81_T_SYNC_RISE_500kHz											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.999	7.978	8.006	7.984	7.982	8.010	8.106	7.971	8.009	7.972	8.101
Average	8.064	8.081	8.105	8.100	8.104	8.114	8.170	8.023	8.101	8.069	8.132
Max	8.111	8.177	8.183	8.209	8.170	8.189	8.226	8.098	8.167	8.189	8.177
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

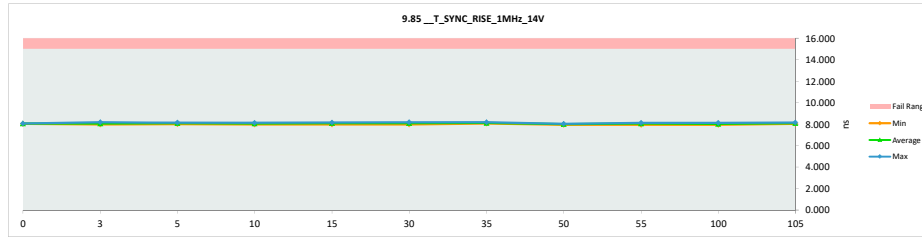


LDR TID Report
TPS7H5001-SP

9.85_T_SYNC_RISE_1MHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.184	8.072	-0.112
0	118	8.006	8.026	0.020
0	122	8.089	8.053	-0.036
3	61	8.091	8.053	-0.038
3	62	8.157	8.171	0.014
3	63	8.170	8.117	-0.053
3	64	8.018	8.003	-0.015
3	65	8.029	7.984	-0.045
3	66	8.173	8.104	-0.069
5	67	8.070	8.083	0.013
5	68	8.193	8.140	-0.053
5	69	8.109	8.152	0.043
5	70	8.009	7.992	-0.017
5	71	8.144	8.092	-0.052
5	72	8.135	8.088	-0.047
10	73	8.091	8.051	-0.040
10	74	8.157	8.130	-0.027
10	75	8.170	8.132	-0.038
10	76	8.018	8.004	-0.014
10	77	8.029	7.973	-0.056
10	78	8.173	8.094	-0.079
15	79	8.070	8.067	-0.003
15	80	8.193	8.135	-0.058
15	81	8.109	8.142	0.033
15	82	8.009	7.966	-0.043
15	83	8.144	8.087	-0.057
15	84	8.135	8.079	-0.056
30	86	8.091	8.109	0.018
30	89	8.157	8.162	0.005
30	91	8.170	8.176	0.006
30	92	8.018	8.047	0.029
30	94	8.029	7.986	-0.043
30	95	8.173	8.096	-0.077
35	100	8.070	8.128	0.058
35	101	8.193	8.134	-0.059
35	102	8.109	8.152	0.043
35	103	8.009	8.055	0.046
35	104	8.144	8.167	0.023
35	105	8.135	8.147	0.012
50	106	8.104	8.007	-0.097
50	107	7.990	7.934	-0.056
50	108	8.051	8.024	-0.027
50	109	8.088	8.034	-0.054
50	110	8.077	7.943	-0.134
50	111	8.092	8.013	-0.079
55	113	8.003	7.992	-0.011
55	114	8.145	8.074	-0.071
55	115	8.154	8.130	-0.024
55	116	8.155	8.088	-0.067
55	126	8.007	7.955	-0.052
55	128	8.169	8.114	-0.055
100	129	7.973	7.983	0.010
100	131	8.169	8.069	-0.100
100	132	8.046	8.065	0.019
100	133	7.987	7.995	0.008
100	134	8.030	8.010	-0.020
100	135	8.118	8.115	-0.003
100	138	8.048	8.047	-0.001
100	139	8.064	8.041	-0.023
100	140	8.084	8.056	-0.028
100	141	8.136	8.073	-0.063
100	142	8.079	8.057	-0.022
100	143	8.104	8.089	-0.015
100	144	8.126	8.035	-0.091
100	220	8.102	8.088	-0.014
100	221	8.122	8.069	-0.053
100	224	8.072	8.044	-0.028
100	225	8.105	7.986	-0.119
100	226	8.019	8.043	0.024
100	228	8.086	8.026	-0.060
100	232	8.006	7.945	-0.061
100	233	7.985	8.006	0.021
100	236	8.075	8.056	-0.019
100	238	8.157	8.135	-0.022
100	243	8.069	8.040	-0.029
105	244	8.126	8.076	-0.050
105	246	8.113	8.164	0.051
105	248	8.055	8.038	-0.017
105	374	8.063	8.065	0.002
105	376	8.090	8.074	-0.016
Max		8.193	8.176	0.058
Average		8.093	8.064	-0.028
Min		7.973	7.934	-0.134
Std Dev		0.060	0.060	0.041

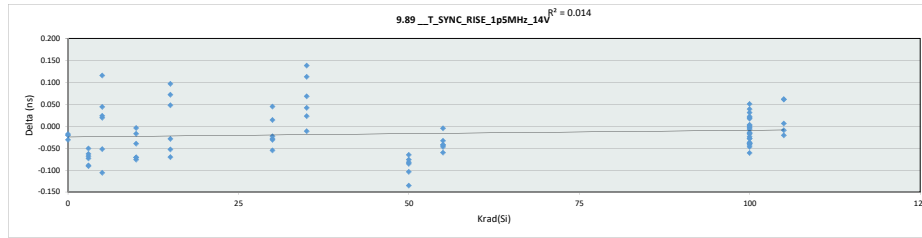


9.85_T_SYNC_RISE_1MHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.026	7.984	7.992	7.973	7.966	7.986	8.055	7.934	7.955	7.945	8.038
Average	8.050	8.072	8.091	8.064	8.079	8.096	8.131	7.993	8.059	8.045	8.083
Max	8.072	8.171	8.152	8.132	8.142	8.176	8.167	8.034	8.130	8.135	8.164
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

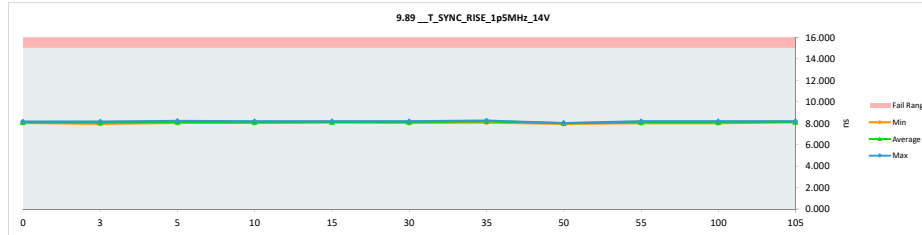


LDR TID Report
TPS7H5001-SP

9.89 __T_SYNC_RISE_1p5MHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.156	8.138	-0.018
0	118	8.039	8.018	-0.021
0	122	8.130	8.099	-0.031
3	61	8.139	8.071	-0.068
3	62	8.219	8.156	-0.063
3	63	8.223	8.150	-0.073
3	64	8.092	8.003	-0.089
3	65	8.048	7.957	-0.091
3	66	8.137	8.086	-0.051
5	67	8.022	8.041	0.019
5	68	8.187	8.211	0.024
5	69	8.131	8.175	0.044
5	70	7.994	8.110	0.116
5	71	8.183	8.077	-0.106
5	72	8.159	8.107	-0.052
10	73	8.139	8.068	-0.071
10	74	8.219	8.179	-0.040
10	75	8.223	8.147	-0.076
10	76	8.092	8.021	-0.071
10	77	8.048	8.031	-0.017
10	78	8.137	8.133	-0.004
15	79	8.022	8.119	0.097
15	80	8.187	8.158	-0.029
15	81	8.131	8.179	0.048
15	82	7.994	8.066	0.072
15	83	8.183	8.113	-0.070
15	84	8.159	8.106	-0.053
30	86	8.139	8.153	0.014
30	89	8.219	8.188	-0.031
30	91	8.223	8.168	-0.055
30	92	8.092	8.063	-0.029
30	94	8.048	8.025	-0.023
30	95	8.137	8.182	0.045
35	100	8.022	8.160	0.138
35	101	8.187	8.176	-0.011
35	102	8.131	8.244	0.113
35	103	7.994	8.062	0.068
35	104	8.183	8.206	0.023
35	105	8.159	8.201	0.042
50	106	8.109	8.005	-0.104
50	107	8.003	7.938	-0.065
50	108	8.103	8.021	-0.082
50	109	8.152	8.017	-0.135
50	110	8.100	8.014	-0.086
50	111	8.080	8.004	-0.076
55	113	8.041	8.008	-0.033
55	114	8.146	8.099	-0.047
55	115	8.222	8.162	-0.060
55	116	8.145	8.102	-0.043
55	126	8.077	8.034	-0.043
55	128	8.183	8.178	-0.005
100	129	8.048	8.008	-0.040
100	131	8.148	8.134	-0.014
100	132	8.100	8.096	-0.004
100	133	8.009	8.010	0.001
100	134	8.112	8.095	-0.017
100	135	8.105	8.108	0.003
100	138	8.084	8.103	0.019
100	139	8.063	8.080	0.017
100	140	8.054	8.015	-0.039
100	141	8.140	8.179	0.039
100	142	8.185	8.142	-0.043
100	143	8.141	8.104	-0.037
100	144	8.104	8.102	-0.002
100	220	8.073	8.104	0.031
100	221	8.103	8.064	-0.039
100	224	8.141	8.113	-0.028
100	225	7.996	8.018	0.022
100	226	8.091	8.084	-0.007
100	228	8.073	8.049	-0.024
100	232	8.050	7.989	-0.061
100	233	7.980	8.031	0.051
100	236	8.079	8.062	-0.017
100	238	8.206	8.159	-0.047
100	243	8.067	8.039	-0.028
105	244	8.150	8.129	-0.021
105	246	8.125	8.187	0.062
105	248	8.109	8.100	-0.009
105	374	8.052	8.113	0.061
105	376	8.073	8.079	0.006
Max		8.223	8.244	0.138
Average		8.112	8.095	-0.017
Min		7.980	7.938	-0.135
Std Dev		0.065	0.066	0.054

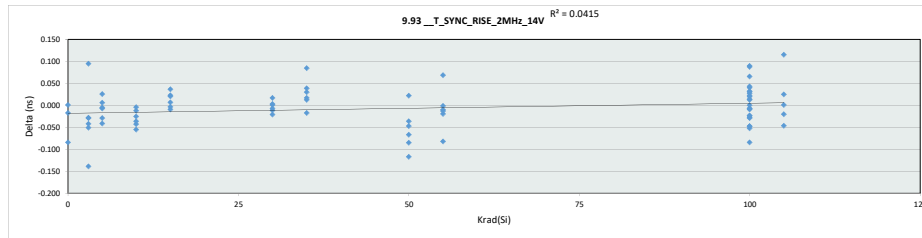


9.89 __T_SYNC_RISE_1p5MHz											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.018	7.957	8.041	8.021	8.066	8.025	8.062	7.938	8.008	7.989	8.079
Average	8.085	8.071	8.120	8.097	8.124	8.130	8.175	8.000	8.097	8.079	8.122
Max	8.138	8.156	8.211	8.179	8.179	8.188	8.244	8.021	8.178	8.179	8.187
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

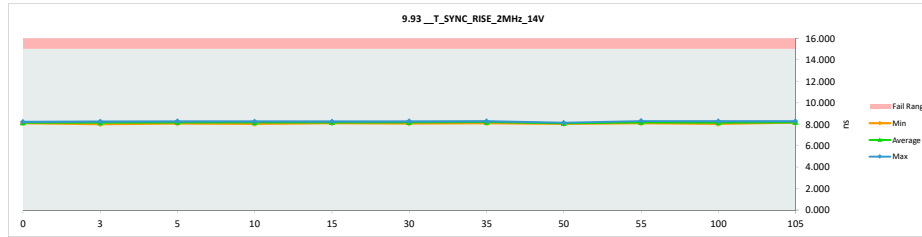


LDR TID Report
TPS7H5001-SP

9.93_T_SYNC_RISE_2MHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.209	8.210	0.001
0	118	8.111	8.094	-0.017
0	122	8.206	8.122	-0.084
3	61	8.199	8.157	-0.042
3	62	8.260	8.232	-0.028
3	63	8.246	8.195	-0.051
3	64	8.129	7.990	-0.139
3	65	8.045	8.140	0.095
3	66	8.254	8.225	-0.029
5	67	8.151	8.157	0.006
5	68	8.232	8.258	0.026
5	69	8.209	8.202	-0.007
5	70	8.087	8.058	-0.029
5	71	8.220	8.179	-0.041
5	72	8.179	8.174	-0.005
10	73	8.199	8.163	-0.036
10	74	8.260	8.248	-0.012
10	75	8.246	8.242	-0.004
10	76	8.129	8.074	-0.055
10	77	8.045	8.020	-0.025
10	78	8.254	8.211	-0.043
15	79	8.151	8.174	0.023
15	80	8.232	8.229	-0.003
15	81	8.209	8.246	0.037
15	82	8.087	8.094	0.007
15	83	8.220	8.211	-0.009
15	84	8.179	8.200	0.021
30	86	8.199	8.216	0.017
30	89	8.260	8.261	0.001
30	91	8.246	8.225	-0.021
30	92	8.129	8.118	-0.011
30	94	8.045	8.048	0.003
30	95	8.254	8.247	-0.007
35	100	8.151	8.236	0.085
35	101	8.232	8.262	0.030
35	102	8.209	8.192	-0.017
35	103	8.087	8.100	0.013
35	104	8.220	8.237	0.017
35	105	8.179	8.218	0.039
50	106	8.194	8.109	-0.085
50	107	8.073	8.037	-0.036
50	108	8.047	8.069	0.022
50	109	8.181	8.134	-0.047
50	110	8.130	8.063	-0.067
50	111	8.185	8.068	-0.117
55	113	8.015	8.084	0.069
55	114	8.247	8.165	-0.082
55	115	8.269	8.259	-0.010
55	116	8.207	8.188	-0.019
55	126	8.098	8.085	-0.013
55	128	8.288	8.287	-0.001
100	129	8.050	8.041	-0.009
100	131	8.221	8.198	-0.023
100	132	8.157	8.171	0.014
100	133	8.078	8.100	0.022
100	134	8.141	8.112	-0.029
100	135	8.194	8.284	0.090
100	138	8.158	8.110	-0.048
100	139	8.119	8.067	-0.052
100	140	8.129	8.102	-0.027
100	141	8.212	8.212	0.000
100	142	8.215	8.242	0.027
100	143	8.165	8.205	0.040
100	144	8.204	8.120	-0.084
100	220	8.162	8.175	0.013
100	221	8.178	8.154	-0.024
100	224	8.146	8.177	0.031
100	225	8.049	8.069	0.020
100	226	8.143	8.136	-0.007
100	228	8.196	8.189	-0.007
100	232	8.104	8.057	-0.047
100	233	8.050	8.093	0.043
100	236	8.154	8.242	0.088
100	238	8.219	8.285	0.066
100	243	8.159	8.191	0.032
105	244	8.199	8.179	-0.020
105	246	8.257	8.258	0.001
105	248	8.181	8.206	0.025
105	374	8.062	8.177	0.115
105	376	8.188	8.142	-0.046
Max		8.288	8.287	0.115
Average		8.170	8.164	-0.006
Min		8.015	7.990	-0.139
Std Dev		0.067	0.072	0.046



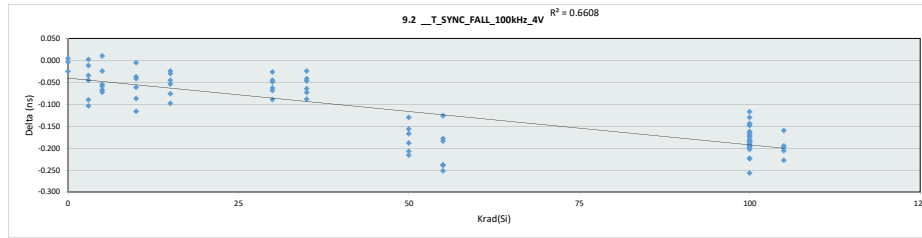
9.93_T_SYNC_RISE_2MHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.094	7.990	8.058	8.020	8.094	8.048	8.100	8.037	8.084	8.041	8.142
Average	8.142	8.157	8.171	8.160	8.192	8.186	8.208	8.080	8.178	8.156	8.192
Max	8.210	8.232	8.258	8.248	8.246	8.261	8.262	8.134	8.287	8.285	8.258
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



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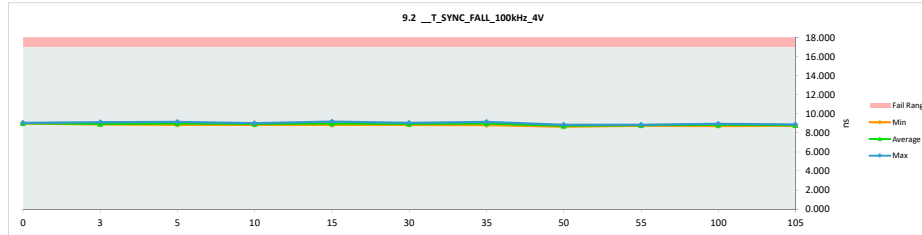
9.2_T_SYNC_FALL_100kHz_4V		
Test Site		
Tester		
Test Number		
Unit	ns	ns
Max Limit	17	17
Min Limit		

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	9.007	8.982	-0.025
0	118	9.034	9.030	-0.004
0	122	8.952	8.956	0.004
3	61	9.014	8.910	-0.104
3	62	9.097	9.099	0.002
3	63	8.939	8.894	-0.045
3	64	8.890	8.878	-0.012
3	65	8.935	8.901	-0.034
3	66	9.044	8.954	-0.090
5	67	8.882	8.827	-0.055
5	68	9.196	9.124	-0.072
5	69	9.038	8.979	-0.059
5	70	8.885	8.861	-0.024
5	71	8.940	8.950	0.010
5	72	9.140	9.073	-0.067
10	73	9.014	8.898	-0.116
10	74	9.097	9.010	-0.087
10	75	8.939	8.934	-0.005
10	76	8.890	8.829	-0.061
10	77	8.935	8.893	-0.042
10	78	9.044	9.007	-0.037
15	79	8.882	8.806	-0.076
15	80	9.196	9.172	-0.024
15	81	9.038	9.008	-0.030
15	82	8.885	8.831	-0.054
15	83	8.940	8.895	-0.045
15	84	9.140	9.042	-0.098
30	86	9.014	8.925	-0.089
30	89	9.097	9.028	-0.069
30	91	8.939	8.876	-0.063
30	92	8.890	8.845	-0.045
30	94	8.935	8.909	-0.026
30	95	9.044	8.995	-0.049
35	100	8.882	8.818	-0.064
35	101	9.196	9.149	-0.047
35	102	9.038	8.950	-0.088
35	103	8.885	8.861	-0.024
35	104	8.940	8.898	-0.042
35	105	9.140	9.067	-0.073
50	106	8.913	8.746	-0.167
50	107	8.809	8.821	-0.088
50	108	8.992	8.836	-0.156
50	109	8.766	8.636	-0.130
50	110	9.002	8.795	-0.207
50	111	8.930	8.714	-0.216
55	113	9.067	8.815	-0.252
55	114	8.998	8.759	-0.239
55	115	9.025	8.787	-0.238
55	116	8.982	8.798	-0.184
55	126	8.911	8.733	-0.178
55	128	8.967	8.841	-0.126
100	129	9.000	8.804	-0.196
100	131	9.086	8.893	-0.193
100	132	9.117	8.934	-0.183
100	133	9.121	8.937	-0.184
100	134	9.054	8.860	-0.194
100	135	9.027	8.852	-0.175
100	138	8.969	8.807	-0.162
100	139	8.949	8.765	-0.184
100	140	9.034	8.904	-0.130
100	141	9.004	8.823	-0.181
100	142	9.004	8.819	-0.185
100	143	8.942	8.798	-0.144
100	144	8.889	8.686	-0.203
100	220	9.036	8.889	-0.147
100	221	8.916	8.693	-0.223
100	224	8.989	8.872	-0.117
100	225	9.085	8.886	-0.199
100	226	8.992	8.768	-0.224
100	228	9.065	8.917	-0.148
100	232	8.962	8.793	-0.169
100	233	8.966	8.778	-0.188
100	236	8.945	8.688	-0.257
100	238	9.043	8.871	-0.172
100	243	9.001	8.836	-0.165
105	244	9.017	8.811	-0.206
105	246	9.100	8.872	-0.228
105	248	8.987	8.827	-0.160
105	374	8.918	8.723	-0.195
105	376	9.059	8.860	-0.199
	Max	9.196	9.172	0.010
	Average	8.995	8.876	-0.119
	Min	8.766	8.621	-0.257
	Std Dev	0.087	0.112	0.076



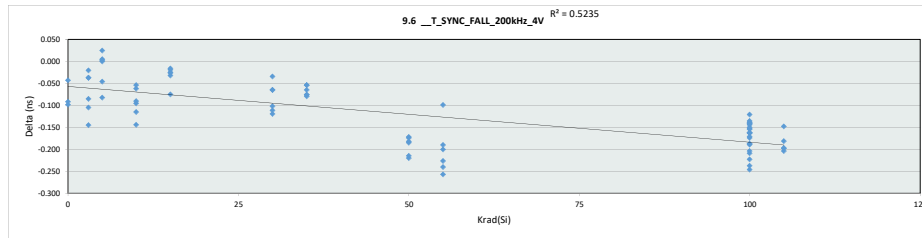
9.2_T_SYNC_FALL_100kHz_4		
Test Site		
Tester		
Test Number		
Max Limit	17	ns
Min Limit		ns

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.956	8.878	8.827	8.829	8.806	8.845	8.818	8.621	8.733	8.686	8.723
Average	8.989	8.939	8.969	8.929	8.959	8.930	8.957	8.725	8.789	8.828	8.819
Max	9.030	9.099	9.124	9.010	9.172	9.028	9.149	8.836	8.841	8.937	8.872
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

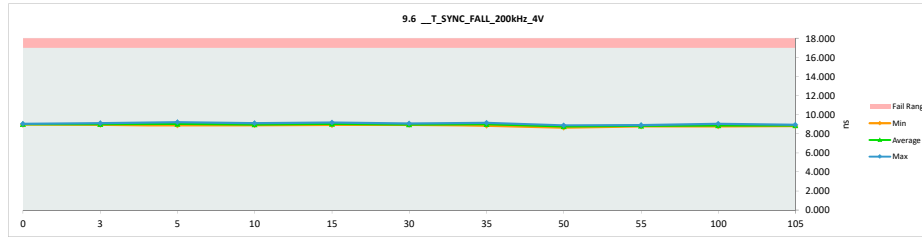


LDR TID Report
TPS7H5001-SP

9.6_T_SYNC_FALL_200kHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	9.072	9.029	-0.043
0	118	9.137	9.045	-0.092
0	122	9.047	8.949	-0.098
3	61	9.081	8.976	-0.105
3	62	9.190	9.105	-0.085
3	63	9.071	8.926	-0.145
3	64	8.968	8.931	-0.037
3	65	8.995	8.975	-0.020
3	66	9.093	9.056	-0.037
5	67	8.917	8.917	0.000
5	68	9.194	9.199	0.005
5	69	9.023	9.026	0.003
5	70	8.966	8.884	-0.082
5	71	9.042	8.996	-0.046
5	72	9.181	9.206	0.025
10	73	9.081	8.937	-0.144
10	74	9.190	9.095	-0.095
10	75	9.071	8.956	-0.115
10	76	8.968	8.906	-0.062
10	77	8.995	8.941	-0.054
10	78	9.093	9.003	-0.090
15	79	8.917	8.898	-0.019
15	80	9.194	9.178	-0.016
15	81	9.023	8.998	-0.025
15	82	8.966	8.891	-0.075
15	83	9.042	9.010	-0.032
15	84	9.181	9.155	-0.026
30	86	9.081	8.962	-0.119
30	89	9.190	9.079	-0.111
30	91	9.071	8.969	-0.102
30	92	8.968	8.934	-0.034
30	94	8.995	8.930	-0.065
30	95	9.093	9.028	-0.065
35	100	8.917	8.841	-0.076
35	101	9.194	9.140	-0.054
35	102	9.023	8.969	-0.054
35	103	8.966	8.901	-0.065
35	104	9.042	8.963	-0.079
35	105	9.181	9.105	-0.076
50	106	8.982	8.797	-0.185
50	107	8.871	8.699	-0.172
50	108	9.100	8.880	-0.220
50	109	8.814	8.632	-0.182
50	110	9.028	8.813	-0.215
50	111	8.953	8.779	-0.174
55	113	9.149	8.892	-0.257
55	114	9.024	8.784	-0.240
55	115	9.062	8.836	-0.226
55	116	9.068	8.868	-0.200
55	126	8.974	8.784	-0.190
55	128	8.967	8.868	-0.099
100	129	9.058	8.905	-0.153
100	131	9.123	8.935	-0.188
100	132	9.171	9.036	-0.135
100	133	9.113	8.939	-0.174
100	134	9.076	8.913	-0.163
100	135	9.054	8.865	-0.189
100	138	9.046	8.902	-0.144
100	139	8.946	8.784	-0.162
100	140	9.066	8.945	-0.121
100	141	9.032	8.823	-0.209
100	142	9.053	8.807	-0.246
100	143	8.963	8.825	-0.138
100	144	8.949	8.762	-0.187
100	220	9.115	8.961	-0.154
100	221	8.961	8.757	-0.204
100	224	9.100	8.950	-0.150
100	225	9.066	8.904	-0.162
100	226	9.013	8.872	-0.141
100	228	9.093	8.953	-0.140
100	232	9.009	8.847	-0.162
100	233	9.025	8.788	-0.237
100	236	8.984	8.761	-0.223
100	238	9.056	8.914	-0.142
100	243	9.028	8.857	-0.171
105	244	9.047	8.850	-0.197
105	246	9.109	8.928	-0.181
105	248	9.060	8.856	-0.204
105	374	8.959	8.811	-0.148
105	376	9.133	8.936	-0.197
Max		9.194	9.206	0.025
Average		9.048	8.925	-0.122
Min		8.814	8.632	-0.257
Std Dev		0.081	0.113	0.071

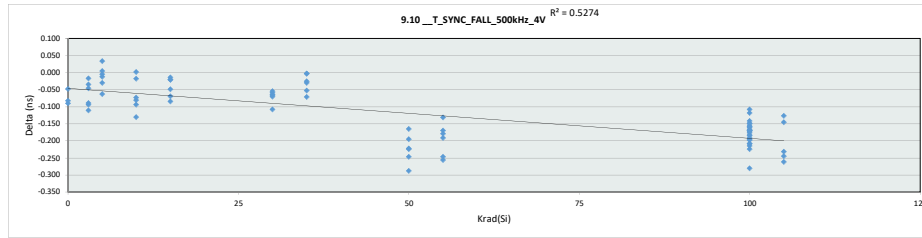


9.6_T_SYNC_FALL_200kHz_4											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.949	8.926	8.884	8.906	8.891	8.930	8.841	8.632	8.784	8.757	8.811
Average	9.008	8.995	9.038	8.973	9.022	8.984	8.987	8.767	8.839	8.875	8.876
Max	9.045	9.105	9.206	9.095	9.095	9.178	9.079	9.140	8.880	8.892	9.036
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

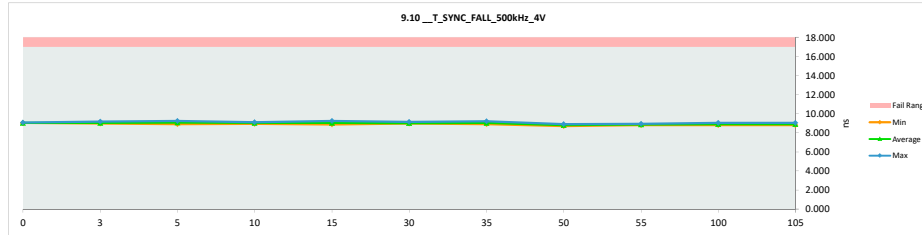


LDR TID Report
TPS7H5001-SP

9.10_T_SYNC_FALL_500kHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	9.155	9.072	-0.083
0	118	9.129	9.039	-0.090
0	122	9.067	9.019	-0.048
3	61	9.008	8.973	-0.035
3	62	9.191	9.174	-0.017
3	63	9.065	8.971	-0.094
3	64	9.065	8.954	-0.111
3	65	9.094	9.005	-0.089
3	66	9.109	9.063	-0.046
5	67	8.930	8.918	-0.012
5	68	9.273	9.243	-0.030
5	69	9.068	9.073	0.005
5	70	8.939	8.934	-0.005
5	71	9.087	9.024	-0.063
5	72	9.206	9.240	0.034
10	73	9.008	8.990	-0.018
10	74	9.191	9.097	-0.094
10	75	9.065	8.992	-0.073
10	76	9.065	8.935	-0.130
10	77	9.094	9.013	-0.081
10	78	9.109	9.111	0.002
15	79	8.930	8.861	-0.069
15	80	9.273	9.224	-0.049
15	81	9.068	9.047	-0.021
15	82	8.939	8.925	-0.014
15	83	9.087	9.003	-0.084
15	84	9.206	9.185	-0.021
30	86	9.008	8.954	-0.054
30	89	9.191	9.126	-0.065
30	91	9.065	9.006	-0.059
30	92	9.065	8.957	-0.108
30	94	9.094	9.024	-0.070
30	95	9.109	9.043	-0.066
35	100	8.930	8.905	-0.025
35	101	9.273	9.220	-0.053
35	102	9.068	9.065	-0.003
35	103	8.939	8.936	-0.003
35	104	9.087	9.016	-0.071
35	105	9.206	9.176	-0.030
50	106	8.981	8.759	-0.222
50	107	8.897	8.732	-0.165
50	108	9.190	8.902	-0.288
50	109	8.940	8.693	-0.247
50	110	9.100	8.876	-0.224
50	111	9.009	8.814	-0.195
55	113	9.160	8.913	-0.247
55	114	9.037	8.846	-0.191
55	115	9.065	8.886	-0.179
55	116	9.083	8.952	-0.131
55	126	9.046	8.790	-0.256
55	128	9.052	8.882	-0.170
100	129	9.035	8.885	-0.150
100	131	9.243	9.035	-0.208
100	132	9.198	9.022	-0.176
100	133	9.148	8.978	-0.170
100	134	9.117	8.934	-0.183
100	135	9.125	8.910	-0.215
100	138	9.058	8.897	-0.161
100	139	8.994	8.824	-0.170
100	140	9.159	8.988	-0.171
100	141	9.031	8.874	-0.157
100	142	9.088	8.903	-0.185
100	143	8.956	8.848	-0.108
100	144	8.982	8.834	-0.148
100	220	9.154	8.930	-0.224
100	221	8.986	8.791	-0.195
100	224	9.088	8.946	-0.142
100	225	9.185	8.905	-0.280
100	226	9.035	8.917	-0.118
100	228	9.204	9.013	-0.191
100	232	9.049	8.882	-0.167
100	233	9.065	8.894	-0.171
100	236	8.998	8.790	-0.208
100	238	9.098	8.900	-0.198
100	243	9.061	8.903	-0.158
105	244	9.141	8.909	-0.232
105	246	9.150	9.023	-0.127
105	248	9.138	8.876	-0.262
105	374	8.962	8.817	-0.145
105	376	9.165	8.920	-0.245
Max		9.273	9.243	0.034
Average		9.083	8.961	-0.122
Min		8.897	8.693	-0.288
Std Dev		0.089	0.116	0.081

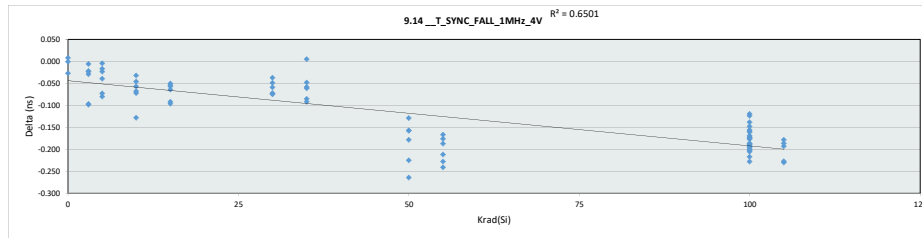


9.10_T_SYNC_FALL_500kHz											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.019	8.954	8.918	8.935	8.861	8.954	8.905	8.693	8.790	8.790	8.817
Average	9.043	9.023	9.072	9.023	9.041	9.018	9.053	8.796	8.878	8.908	8.909
Max	9.072	9.174	9.243	9.111	9.224	9.126	9.220	8.902	8.952	9.035	9.023
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

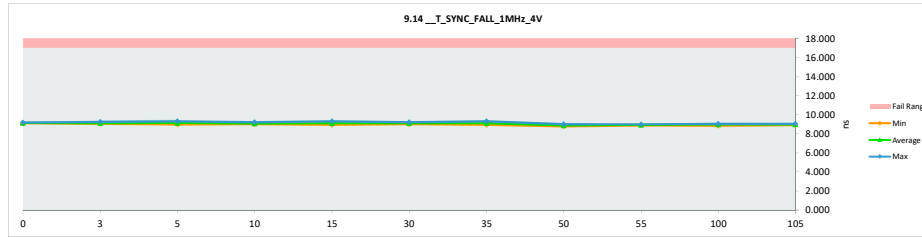


LDR TID Report
TPS7H5001-SP

9.14_T_SYNC_FALL_1MHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	9.159	9.158	-0.001
0	118	9.173	9.146	-0.027
0	122	9.076	9.084	0.008
3	61	9.133	9.104	-0.029
3	62	9.257	9.235	-0.022
3	63	9.136	9.039	-0.097
3	64	9.050	9.027	-0.023
3	65	9.151	9.053	-0.098
3	66	9.189	9.183	-0.006
5	67	8.999	8.982	-0.017
5	68	9.383	9.303	-0.080
5	69	9.165	9.161	-0.004
5	70	9.004	8.981	-0.023
5	71	9.158	9.085	-0.073
5	72	9.299	9.260	-0.039
10	73	9.133	9.076	-0.057
10	74	9.257	9.189	-0.068
10	75	9.136	9.064	-0.072
10	76	9.050	9.018	-0.032
10	77	9.151	9.023	-0.128
10	78	9.189	9.143	-0.046
15	79	8.999	8.945	-0.054
15	80	9.383	9.291	-0.092
15	81	9.165	9.115	-0.050
15	82	9.004	8.940	-0.064
15	83	9.158	9.062	-0.096
15	84	9.299	9.242	-0.057
30	86	9.133	9.096	-0.037
30	89	9.257	9.198	-0.059
30	91	9.136	9.064	-0.072
30	92	9.050	9.001	-0.049
30	94	9.151	9.077	-0.074
30	95	9.189	9.115	-0.074
35	100	8.999	8.951	-0.048
35	101	9.383	9.291	-0.092
35	102	9.165	9.107	-0.058
35	103	9.004	9.009	0.005
35	104	9.158	9.097	-0.061
35	105	9.299	9.214	-0.085
50	106	9.136	8.872	-0.264
50	107	8.990	8.765	-0.225
50	108	9.121	8.992	-0.129
50	109	8.949	8.771	-0.178
50	110	9.122	8.965	-0.157
50	111	9.043	8.886	-0.157
55	113	9.211	8.983	-0.228
55	114	9.136	8.895	-0.241
55	115	9.143	8.931	-0.212
55	116	9.102	8.926	-0.176
55	126	9.052	8.885	-0.167
55	128	9.124	8.937	-0.187
100	129	9.164	8.947	-0.217
100	131	9.227	9.027	-0.200
100	132	9.256	9.051	-0.205
100	133	9.223	9.048	-0.175
100	134	9.191	9.004	-0.187
100	135	9.126	9.007	-0.119
100	138	9.153	8.976	-0.177
100	139	9.043	8.882	-0.161
100	140	9.248	9.045	-0.203
100	141	9.143	8.984	-0.159
100	142	9.139	8.983	-0.156
100	143	9.105	8.981	-0.124
100	144	9.030	8.841	-0.189
100	220	9.206	9.034	-0.172
100	221	9.039	8.866	-0.173
100	224	9.181	9.043	-0.138
100	225	9.211	9.017	-0.194
100	226	9.125	8.977	-0.148
100	228	9.225	9.030	-0.195
100	232	9.095	8.909	-0.186
100	233	9.115	8.915	-0.200
100	236	9.071	8.843	-0.228
100	238	9.176	9.007	-0.169
100	243	9.158	8.968	-0.190
105	244	9.155	8.925	-0.230
105	246	9.201	8.974	-0.227
105	248	9.133	8.947	-0.186
105	374	9.082	8.904	-0.178
105	376	9.226	9.033	-0.193
Max		9.383	9.303	0.008
Average		9.147	9.026	-0.121
Min		8.949	8.765	-0.264
Std Dev		0.091	0.116	0.074

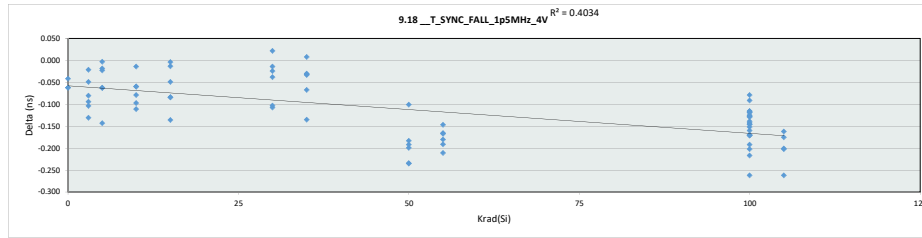


9.14_T_SYNC_FALL_1MHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.084	9.027	8.981	9.018	8.940	9.001	8.951	8.765	8.885	8.841	8.904
Average	9.129	9.107	9.129	9.086	9.099	9.092	9.112	8.875	8.926	8.974	8.957
Max	9.158	9.235	9.303	9.189	9.189	9.291	9.291	8.992	8.983	9.051	9.033
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

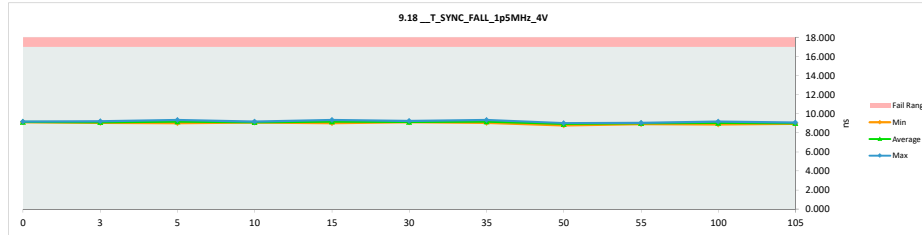


LDR TID Report
TPS7H5001-SP

9.18_T_SYNC_FALL_1p5MHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	9.244	9.182	-0.062
0	118	9.230	9.168	-0.062
0	122	9.145	9.103	-0.042
3	61	9.208	9.077	-0.131
3	62	9.289	9.185	-0.104
3	63	9.149	9.100	-0.049
3	64	9.120	9.026	-0.094
3	65	9.148	9.068	-0.080
3	66	9.209	9.188	-0.021
5	67	9.061	8.998	-0.063
5	68	9.480	9.337	-0.143
5	69	9.209	9.191	-0.018
5	70	9.055	9.052	-0.003
5	71	9.158	9.135	-0.023
5	72	9.347	9.285	-0.062
10	73	9.208	9.111	-0.097
10	74	9.289	9.178	-0.111
10	75	9.149	9.135	-0.014
10	76	9.120	9.060	-0.060
10	77	9.148	9.069	-0.079
10	78	9.209	9.150	-0.059
15	79	9.061	9.057	-0.004
15	80	9.480	9.344	-0.136
15	81	9.209	9.196	-0.013
15	82	9.055	9.006	-0.049
15	83	9.158	9.074	-0.084
15	84	9.347	9.263	-0.084
30	86	9.208	9.101	-0.107
30	89	9.289	9.186	-0.103
30	91	9.149	9.111	-0.038
30	92	9.120	9.106	-0.014
30	94	9.148	9.124	-0.024
30	95	9.209	9.231	0.022
35	100	9.061	9.031	-0.030
35	101	9.480	9.345	-0.135
35	102	9.209	9.177	-0.032
35	103	9.055	9.063	0.008
35	104	9.158	9.125	-0.033
35	105	9.347	9.280	-0.067
50	106	9.138	8.904	-0.234
50	107	8.982	8.881	-0.101
50	108	9.216	9.017	-0.199
50	109	8.968	8.776	-0.192
50	110	9.183	9.000	-0.183
50	111	9.152	8.917	-0.235
55	113	9.173	9.026	-0.147
55	114	9.202	9.011	-0.191
55	115	9.172	8.992	-0.180
55	116	9.214	9.003	-0.211
55	126	9.077	8.911	-0.166
55	128	9.160	8.993	-0.167
100	129	9.184	9.024	-0.160
100	131	9.284	9.115	-0.169
100	132	9.282	9.155	-0.127
100	133	9.291	9.146	-0.145
100	134	9.254	9.062	-0.192
100	135	9.202	9.075	-0.127
100	138	9.164	9.039	-0.125
100	139	9.077	8.935	-0.142
100	140	9.217	9.088	-0.129
100	141	9.249	8.987	-0.262
100	142	9.214	9.068	-0.146
100	143	9.102	9.011	-0.091
100	144	9.062	8.946	-0.116
100	220	9.242	9.071	-0.171
100	221	9.035	8.864	-0.171
100	224	9.223	9.104	-0.119
100	225	9.234	9.032	-0.202
100	226	9.139	9.024	-0.115
100	228	9.229	9.150	-0.079
100	232	9.127	8.988	-0.139
100	233	9.120	9.002	-0.118
100	236	9.122	8.951	-0.171
100	238	9.211	9.059	-0.152
100	243	9.251	9.034	-0.217
105	244	9.255	8.993	-0.262
105	246	9.230	9.068	-0.162
105	248	9.208	9.006	-0.202
105	374	9.112	8.937	-0.175
105	376	9.253	9.052	-0.201
	Max	9.480	9.345	0.022
	Average	9.189	9.075	-0.114
	Min	8.968	8.776	-0.262
	Std Dev	0.098	0.109	0.069

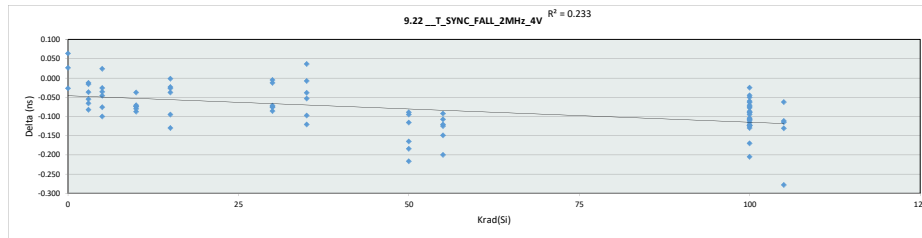


9.18_T_SYNC_FALL_1p5MHz											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.103	9.026	8.998	9.060	9.006	9.101	9.031	8.776	8.911	8.864	8.937
Average	9.151	9.107	9.166	9.117	9.157	9.143	9.170	8.916	8.989	9.039	9.011
Max	9.182	9.188	9.337	9.178	9.344	9.231	9.345	9.017	9.026	9.155	9.068
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

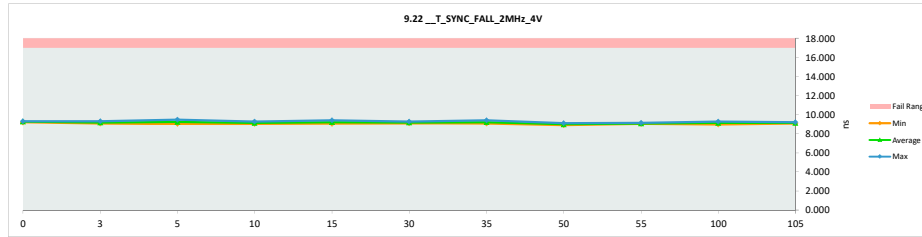


LDR TID Report
TPS7H5001-SP

9.22_T_SYNC_FALL_2MHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	9.207	9.271	0.064
0	118	9.335	9.308	-0.027
0	122	9.161	9.187	0.026
3	61	9.206	9.140	-0.066
3	62	9.351	9.296	-0.055
3	63	9.244	9.161	-0.083
3	64	9.116	9.079	-0.037
3	65	9.142	9.129	-0.013
3	66	9.264	9.248	-0.016
5	67	9.072	9.096	0.024
5	68	9.542	9.466	-0.076
5	69	9.246	9.200	-0.046
5	70	9.072	9.046	-0.026
5	71	9.236	9.136	-0.100
5	72	9.426	9.390	-0.036
10	73	9.206	9.135	-0.071
10	74	9.351	9.263	-0.088
10	75	9.244	9.164	-0.080
10	76	9.116	9.042	-0.074
10	77	9.142	9.104	-0.038
10	78	9.264	9.190	-0.074
15	79	9.072	9.070	-0.002
15	80	9.542	9.412	-0.130
15	81	9.246	9.208	-0.038
15	82	9.072	9.048	-0.024
15	83	9.236	9.141	-0.095
15	84	9.426	9.399	-0.027
30	86	9.206	9.130	-0.076
30	89	9.351	9.276	-0.075
30	91	9.244	9.158	-0.086
30	92	9.116	9.103	-0.013
30	94	9.142	9.137	-0.005
30	95	9.264	9.192	-0.072
35	100	9.072	9.064	-0.008
35	101	9.542	9.421	-0.121
35	102	9.246	9.207	-0.039
35	103	9.072	9.108	0.036
35	104	9.236	9.138	-0.098
35	105	9.426	9.372	-0.054
50	106	9.163	8.998	-0.165
50	107	9.018	8.902	-0.116
50	108	9.200	9.111	-0.089
50	109	9.013	8.918	-0.095
50	110	9.271	9.054	-0.217
50	111	9.118	8.934	-0.184
55	113	9.208	9.087	-0.121
55	114	9.240	9.091	-0.149
55	115	9.257	9.057	-0.200
55	116	9.247	9.139	-0.108
55	126	9.153	9.028	-0.125
55	128	9.175	9.082	-0.093
100	129	9.276	9.152	-0.124
100	131	9.383	9.213	-0.170
100	132	9.316	9.238	-0.078
100	133	9.412	9.207	-0.205
100	134	9.245	9.181	-0.064
100	135	9.141	9.091	-0.050
100	138	9.211	9.124	-0.087
100	139	9.087	9.062	-0.025
100	140	9.353	9.263	-0.090
100	141	9.158	9.113	-0.045
100	142	9.314	9.198	-0.116
100	143	9.191	9.102	-0.089
100	144	9.138	9.030	-0.108
100	220	9.340	9.229	-0.111
100	221	9.100	8.977	-0.123
100	224	9.297	9.226	-0.071
100	225	9.184	9.054	-0.130
100	226	9.205	9.081	-0.124
100	228	9.327	9.232	-0.095
100	232	9.186	9.112	-0.074
100	233	9.150	9.043	-0.107
100	236	9.070	9.010	-0.060
100	238	9.259	9.155	-0.104
100	243	9.270	9.147	-0.123
105	244	9.352	9.074	-0.278
105	246	9.340	9.209	-0.131
105	248	9.249	9.137	-0.112
105	374	9.151	9.088	-0.063
105	376	9.278	9.163	-0.115
Max		9.542	9.466	0.064
Average		9.231	9.149	-0.082
Min		9.013	8.902	-0.278
Std Dev		0.116	0.112	0.058

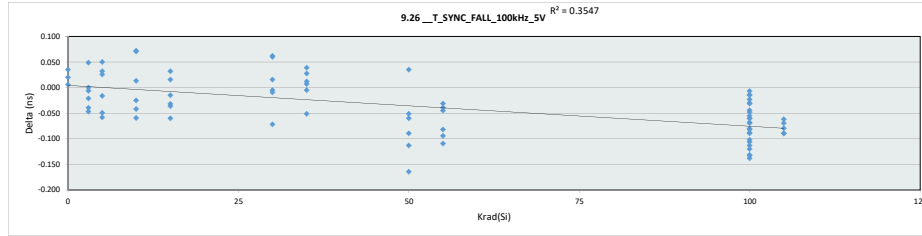


9.22_T_SYNC_FALL_2MHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.187	9.079	9.046	9.042	9.048	9.103	9.064	8.902	9.028	8.977	9.074
Average	9.255	9.176	9.222	9.150	9.213	9.166	9.218	8.986	9.081	9.135	9.134
Max	9.308	9.296	9.466	9.263	9.412	9.276	9.421	9.111	9.139	9.263	9.209
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

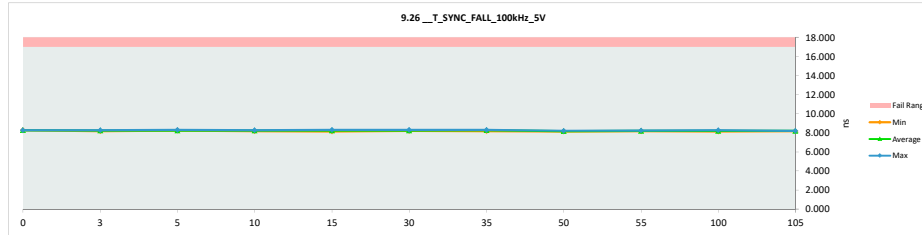


LDR TID Report
TPS7H5001-SP

9.26 __T_SYNC_FALL_100kHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.261	8.281	0.020
0	118	8.236	8.271	0.035
0	122	8.261	8.267	0.006
3	61	8.186	8.180	-0.006
3	62	8.304	8.257	-0.047
3	63	8.230	8.209	-0.021
3	64	8.213	8.213	0.000
3	65	8.192	8.241	0.049
3	66	8.282	8.243	-0.039
5	67	8.174	8.224	0.050
5	68	8.266	8.298	0.032
5	69	8.277	8.219	-0.058
5	70	8.193	8.219	0.026
5	71	8.275	8.226	-0.049
5	72	8.284	8.268	-0.016
10	73	8.186	8.257	0.071
10	74	8.304	8.262	-0.042
10	75	8.230	8.243	0.013
10	76	8.213	8.154	-0.059
10	77	8.192	8.264	0.072
10	78	8.282	8.257	-0.025
15	79	8.174	8.138	-0.036
15	80	8.266	8.298	0.032
15	81	8.277	8.245	-0.032
15	82	8.193	8.209	0.016
15	83	8.275	8.260	-0.015
15	84	8.284	8.224	-0.060
30	86	8.186	8.246	0.060
30	89	8.304	8.299	-0.005
30	91	8.230	8.221	-0.009
30	92	8.213	8.229	0.016
30	94	8.192	8.254	0.062
30	95	8.282	8.210	-0.072
35	100	8.174	8.181	0.007
35	101	8.266	8.294	0.028
35	102	8.277	8.272	-0.005
35	103	8.193	8.232	0.039
35	104	8.275	8.224	-0.051
35	105	8.284	8.296	0.012
50	106	8.260	8.209	-0.051
50	107	8.216	8.156	-0.060
50	108	8.297	8.133	-0.164
50	109	8.262	8.149	-0.113
50	110	8.257	8.168	-0.089
50	111	8.161	8.196	0.035
55	113	8.218	8.173	-0.045
55	114	8.258	8.164	-0.094
55	115	8.277	8.238	-0.039
55	116	8.247	8.216	-0.031
55	126	8.276	8.167	-0.109
55	128	8.252	8.170	-0.082
100	129	8.297	8.191	-0.106
100	131	8.284	8.253	-0.031
100	132	8.291	8.231	-0.060
100	133	8.317	8.179	-0.138
100	134	8.329	8.196	-0.133
100	135	8.236	8.176	-0.060
100	138	8.266	8.164	-0.102
100	139	8.235	8.146	-0.089
100	140	8.259	8.179	-0.080
100	141	8.259	8.177	-0.082
100	142	8.277	8.189	-0.088
100	143	8.185	8.171	-0.014
100	144	8.239	8.195	-0.044
100	220	8.205	8.198	-0.007
100	221	8.271	8.151	-0.120
100	224	8.304	8.191	-0.113
100	225	8.286	8.217	-0.069
100	226	8.216	8.202	-0.014
100	228	8.292	8.224	-0.068
100	232	8.219	8.189	-0.030
100	233	8.245	8.197	-0.048
100	236	8.204	8.149	-0.055
100	238	8.315	8.184	-0.131
100	243	8.207	8.184	-0.023
105	244	8.299	8.210	-0.089
105	246	8.295	8.206	-0.089
105	248	8.255	8.193	-0.062
105	374	8.237	8.168	-0.069
105	376	8.277	8.198	-0.079
Max		8.329	8.299	0.072
Average		8.250	8.213	-0.037
Min		8.161	8.133	-0.164
Std Dev		0.041	0.042	0.054

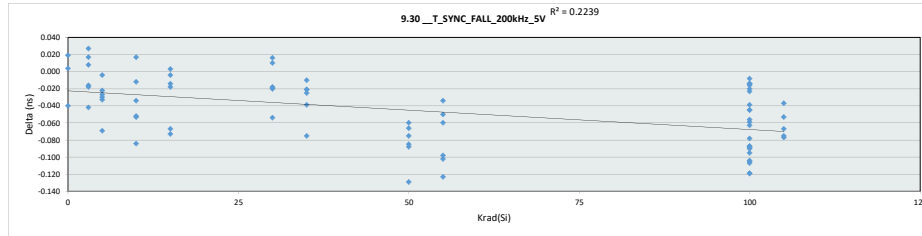


9.26 __T_SYNC_FALL_100kHz_											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.267	8.180	8.219	8.154	8.138	8.210	8.181	8.133	8.164	8.146	8.168
Average	8.273	8.224	8.242	8.240	8.229	8.243	8.250	8.169	8.188	8.189	8.195
Max	8.281	8.257	8.298	8.264	8.298	8.299	8.296	8.209	8.238	8.253	8.210
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

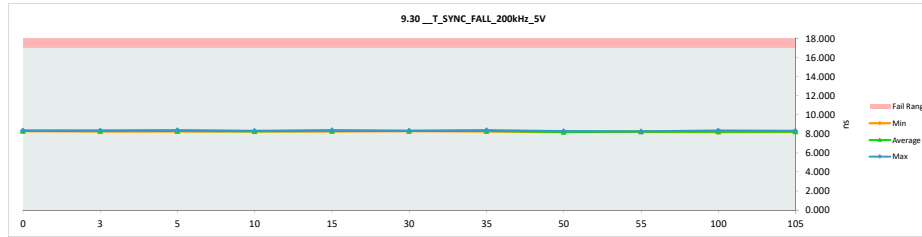


LDR TID Report
TPS7H5001-SP

9.30_T_SYNC_FALL_200kHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.313	8.332	0.019
0	118	8.323	8.283	-0.040
0	122	8.269	8.273	0.004
3	61	8.307	8.324	0.017
3	62	8.332	8.316	-0.016
3	63	8.282	8.290	0.008
3	64	8.284	8.242	-0.042
3	65	8.241	8.268	0.027
3	66	8.336	8.318	-0.018
5	67	8.280	8.258	-0.022
5	68	8.372	8.368	-0.004
5	69	8.312	8.282	-0.030
5	70	8.274	8.241	-0.033
5	71	8.339	8.270	-0.069
5	72	8.326	8.299	-0.027
10	73	8.307	8.223	-0.084
10	74	8.332	8.279	-0.053
10	75	8.282	8.248	-0.034
10	76	8.284	8.272	-0.012
10	77	8.241	8.258	0.017
10	78	8.336	8.284	-0.052
15	79	8.280	8.276	-0.004
15	80	8.372	8.375	0.003
15	81	8.312	8.245	-0.067
15	82	8.274	8.256	-0.018
15	83	8.339	8.266	-0.073
15	84	8.326	8.312	-0.014
30	86	8.307	8.289	-0.018
30	89	8.332	8.312	-0.020
30	91	8.282	8.298	0.016
30	92	8.284	8.265	-0.019
30	94	8.241	8.251	0.010
30	95	8.336	8.282	-0.054
35	100	8.280	8.241	-0.039
35	101	8.372	8.351	-0.021
35	102	8.312	8.287	-0.025
35	103	8.274	8.253	-0.021
35	104	8.339	8.264	-0.075
35	105	8.326	8.316	-0.010
50	106	8.266	8.178	-0.088
50	107	8.288	8.159	-0.129
50	108	8.327	8.252	-0.075
50	109	8.234	8.168	-0.066
50	110	8.308	8.223	-0.085
50	111	8.271	8.211	-0.060
55	113	8.279	8.245	-0.034
55	114	8.321	8.219	-0.102
55	115	8.304	8.206	-0.098
55	116	8.299	8.239	-0.060
55	126	8.307	8.184	-0.123
55	128	8.294	8.244	-0.050
100	129	8.337	8.250	-0.087
100	131	8.354	8.235	-0.119
100	132	8.349	8.260	-0.089
100	133	8.328	8.265	-0.063
100	134	8.325	8.220	-0.105
100	135	8.305	8.282	-0.023
100	138	8.298	8.239	-0.059
100	139	8.320	8.213	-0.107
100	140	8.336	8.291	-0.045
100	141	8.284	8.165	-0.119
100	142	8.329	8.273	-0.056
100	143	8.286	8.191	-0.095
100	144	8.254	8.209	-0.045
100	220	8.333	8.317	-0.016
100	221	8.269	8.249	-0.020
100	224	8.323	8.284	-0.039
100	225	8.316	8.238	-0.078
100	226	8.270	8.256	-0.014
100	228	8.329	8.225	-0.104
100	232	8.307	8.217	-0.090
100	233	8.275	8.267	-0.008
100	236	8.252	8.238	-0.014
100	238	8.295	8.281	-0.014
100	243	8.282	8.195	-0.087
105	244	8.327	8.290	-0.037
105	246	8.331	8.254	-0.077
105	248	8.310	8.257	-0.053
105	374	8.272	8.205	-0.067
105	376	8.307	8.232	-0.075
Max		8.372	8.375	0.027
Average		8.305	8.259	-0.046
Min		8.234	8.159	-0.119
Std Dev		0.031	0.044	0.038

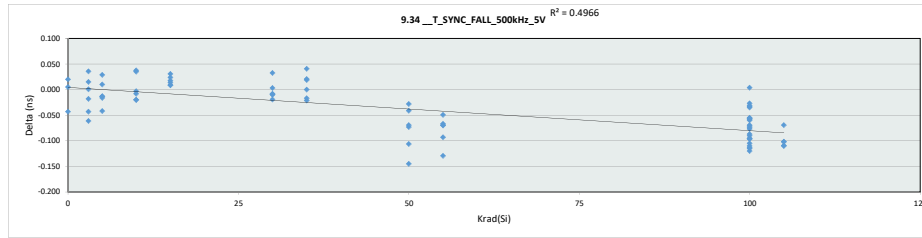


9.30_T_SYNC_FALL_200kHz											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.273	8.242	8.241	8.223	8.245	8.251	8.241	8.159	8.184	8.165	8.205
Average	8.296	8.293	8.286	8.261	8.288	8.283	8.285	8.199	8.223	8.244	8.248
Max	8.332	8.324	8.368	8.284	8.375	8.312	8.351	8.252	8.245	8.317	8.290
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

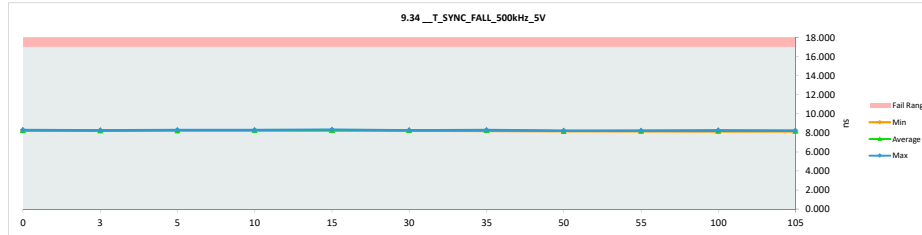


LDR TID Report
TPS7H5001-SP

9.34 __T_SYNC_FALL_500kHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.303	8.308	0.005
0	118	8.240	8.197	-0.043
0	122	8.231	8.251	0.020
3	61	8.264	8.279	0.015
3	62	8.287	8.226	-0.061
3	63	8.236	8.218	-0.018
3	64	8.233	8.234	0.001
3	65	8.251	8.208	-0.043
3	66	8.246	8.282	0.036
5	67	8.199	8.186	-0.013
5	68	8.307	8.294	-0.013
5	69	8.246	8.230	-0.016
5	70	8.216	8.245	0.029
5	71	8.264	8.222	-0.042
5	72	8.270	8.280	0.010
10	73	8.264	8.244	-0.020
10	74	8.287	8.267	-0.020
10	75	8.236	8.271	0.035
10	76	8.233	8.225	-0.008
10	77	8.251	8.248	-0.003
10	78	8.246	8.284	0.038
15	79	8.199	8.230	0.031
15	80	8.307	8.325	0.018
15	81	8.245	8.270	0.024
15	82	8.216	8.225	0.009
15	83	8.264	8.273	0.009
15	84	8.270	8.284	0.014
30	86	8.264	8.245	-0.019
30	89	8.287	8.278	-0.009
30	91	8.236	8.269	0.033
30	92	8.233	8.223	-0.010
30	94	8.251	8.243	-0.008
30	95	8.245	8.249	0.003
35	100	8.199	8.218	0.019
35	101	8.307	8.307	0.000
35	102	8.246	8.267	0.021
35	103	8.216	8.199	-0.017
35	104	8.264	8.243	-0.021
35	105	8.270	8.311	0.041
50	106	8.291	8.185	-0.106
50	107	8.214	8.173	-0.041
50	108	8.293	8.220	-0.073
50	109	8.200	8.172	-0.028
50	110	8.272	8.127	-0.145
50	111	8.245	8.176	-0.069
55	113	8.272	8.223	-0.049
55	114	8.258	8.189	-0.069
55	115	8.296	8.203	-0.093
55	116	8.252	8.182	-0.070
55	125	8.259	8.130	-0.129
55	128	8.293	8.226	-0.067
100	129	8.248	8.143	-0.105
100	131	8.325	8.230	-0.095
100	132	8.314	8.218	-0.096
100	133	8.281	8.166	-0.115
100	134	8.299	8.179	-0.120
100	135	8.292	8.182	-0.110
100	138	8.220	8.162	-0.058
100	139	8.214	8.137	-0.077
100	140	8.277	8.206	-0.071
100	141	8.286	8.190	-0.096
100	142	8.266	8.192	-0.074
100	143	8.224	8.228	0.004
100	144	8.196	8.169	-0.027
100	220	8.294	8.207	-0.087
100	221	8.230	8.197	-0.033
100	224	8.296	8.261	-0.035
100	225	8.285	8.225	-0.060
100	226	8.261	8.205	-0.056
100	228	8.279	8.224	-0.055
100	232	8.233	8.202	-0.031
100	233	8.243	8.187	-0.056
100	236	8.268	8.178	-0.090
100	238	8.293	8.180	-0.113
100	243	8.241	8.172	-0.069
105	244	8.269	8.200	-0.069
105	246	8.327	8.225	-0.102
105	248	8.298	8.188	-0.110
105	374	8.238	8.129	-0.109
105	376	8.330	8.228	-0.102
Max		8.330	8.325	0.041
Average		8.260	8.221	-0.039
Min		8.196	8.127	-0.145
Std Dev		0.033	0.045	0.048

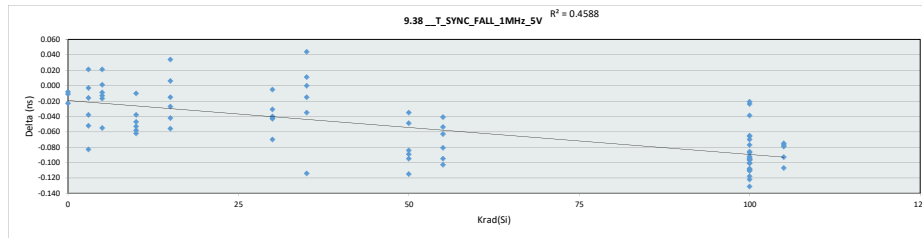


9.34 __T_SYNC_FALL_500kHz_5V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.197	8.208	8.186	8.225	8.225	8.223	8.199	8.127	8.130	8.137	8.129
Average	8.252	8.241	8.243	8.257	8.268	8.251	8.258	8.176	8.192	8.193	8.194
Max	8.308	8.282	8.294	8.284	8.325	8.278	8.311	8.220	8.226	8.261	8.228
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

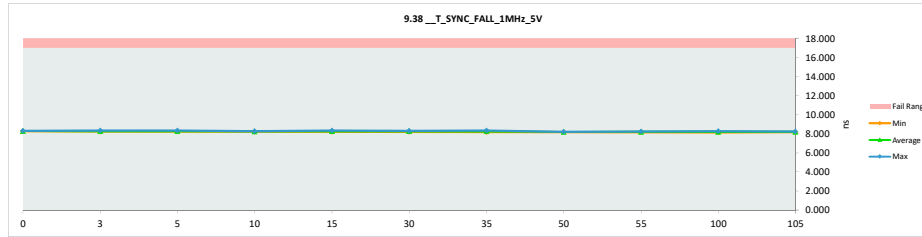


LDR TID Report
TPS7H5001-SP

9.38_T_SYNC_FALL_1MHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit		17		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.313	8.290	-0.023
0	118	8.291	8.283	-0.008
0	122	8.273	8.262	-0.011
3	61	8.262	8.283	0.021
3	62	8.331	8.328	-0.003
3	63	8.296	8.244	-0.052
3	64	8.249	8.233	-0.016
3	65	8.303	8.220	-0.083
3	66	8.298	8.260	-0.038
5	67	8.227	8.218	-0.009
5	68	8.336	8.337	0.001
5	69	8.274	8.295	0.021
5	70	8.242	8.229	-0.013
5	71	8.295	8.240	-0.055
5	72	8.316	8.299	-0.017
10	73	8.262	8.252	-0.010
10	74	8.331	8.269	-0.062
10	75	8.296	8.249	-0.047
10	76	8.249	8.211	-0.038
10	77	8.303	8.250	-0.053
10	78	8.298	8.240	-0.058
15	79	8.227	8.261	0.034
15	80	8.336	8.321	-0.015
15	81	8.274	8.280	0.006
15	82	8.242	8.200	-0.042
15	83	8.295	8.239	-0.056
15	84	8.316	8.289	-0.027
30	86	8.262	8.222	-0.040
30	89	8.331	8.288	-0.043
30	91	8.296	8.265	-0.031
30	92	8.249	8.207	-0.042
30	94	8.303	8.233	-0.070
30	95	8.298	8.293	-0.005
35	100	8.227	8.271	0.044
35	101	8.336	8.336	0.000
35	102	8.274	8.239	-0.035
35	103	8.242	8.227	-0.015
35	104	8.295	8.181	-0.114
35	105	8.316	8.327	0.011
50	106	8.254	8.205	-0.049
50	107	8.252	8.168	-0.084
50	108	8.294	8.199	-0.095
50	109	8.276	8.161	-0.115
50	110	8.277	8.188	-0.089
50	111	8.211	8.176	-0.035
55	113	8.251	8.197	-0.054
55	114	8.262	8.181	-0.081
55	115	8.258	8.217	-0.041
55	116	8.263	8.200	-0.063
55	126	8.285	8.190	-0.095
55	128	8.307	8.204	-0.103
100	129	8.306	8.205	-0.101
100	131	8.343	8.247	-0.096
100	132	8.326	8.256	-0.070
100	133	8.293	8.171	-0.122
100	134	8.326	8.208	-0.118
100	135	8.274	8.209	-0.065
100	138	8.308	8.214	-0.094
100	139	8.268	8.191	-0.077
100	140	8.294	8.183	-0.111
100	141	8.286	8.190	-0.096
100	142	8.274	8.208	-0.066
100	143	8.268	8.160	-0.108
100	144	8.252	8.157	-0.095
100	220	8.318	8.226	-0.092
100	221	8.261	8.164	-0.097
100	224	8.294	8.185	-0.109
100	225	8.316	8.215	-0.101
100	226	8.258	8.219	-0.039
100	228	8.283	8.262	-0.021
100	232	8.271	8.140	-0.131
100	233	8.264	8.240	-0.024
100	236	8.246	8.160	-0.086
100	238	8.317	8.207	-0.110
100	243	8.274	8.177	-0.097
105	244	8.262	8.155	-0.107
105	246	8.317	8.240	-0.077
105	248	8.280	8.187	-0.093
105	374	8.225	8.150	-0.075
105	376	8.318	8.239	-0.079
Max		8.343	8.337	0.044
Average		8.283	8.228	-0.056
Min		8.211	8.140	-0.131
Std Dev		0.031	0.047	0.042

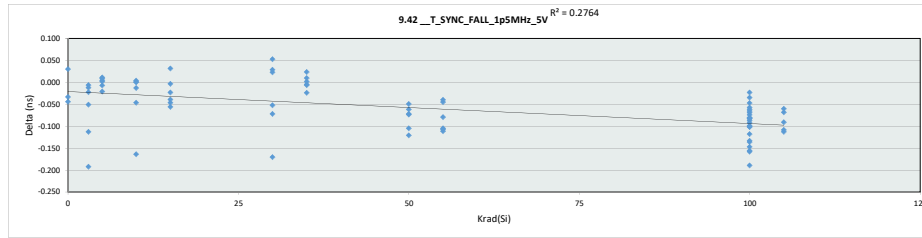


9.38_T_SYNC_FALL_1MHz_5V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.262	8.220	8.218	8.211	8.200	8.207	8.181	8.161	8.181	8.140	8.150
Average	8.278	8.261	8.270	8.245	8.265	8.251	8.264	8.183	8.198	8.200	8.194
Max	8.290	8.328	8.337	8.269	8.321	8.293	8.336	8.205	8.217	8.262	8.240
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

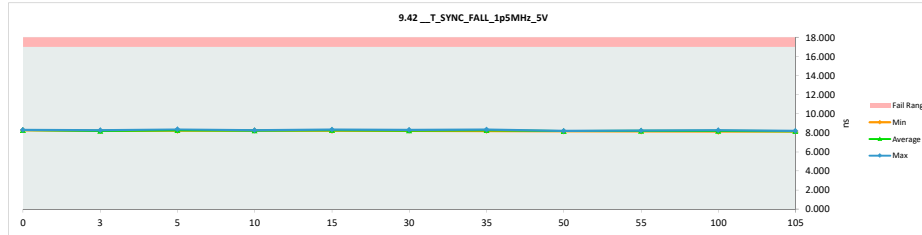


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9.42 _T_SYNC_FALL_1p5MHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.302	8.258	-0.044
0	118	8.282	8.312	0.030
0	122	8.300	8.267	-0.033
3	61	8.300	8.187	-0.113
3	62	8.419	8.227	-0.192
3	63	8.220	8.208	-0.012
3	64	8.216	8.194	-0.022
3	65	8.249	8.198	-0.051
3	66	8.272	8.266	-0.006
5	67	8.215	8.208	-0.007
5	68	8.340	8.351	0.011
5	69	8.244	8.223	-0.021
5	70	8.238	8.240	0.002
5	71	8.277	8.282	0.005
5	72	8.332	8.341	0.009
10	73	8.300	8.254	-0.046
10	74	8.419	8.255	-0.164
10	75	8.220	8.221	0.001
10	76	8.216	8.220	0.004
10	77	8.249	8.249	0.000
10	78	8.272	8.259	-0.013
15	79	8.215	8.192	-0.023
15	80	8.340	8.337	-0.003
15	81	8.244	8.276	0.032
15	82	8.238	8.199	-0.039
15	83	8.277	8.231	-0.046
15	84	8.332	8.276	-0.056
30	86	8.300	8.228	-0.072
30	89	8.419	8.249	-0.170
30	91	8.220	8.249	0.029
30	92	8.216	8.239	0.023
30	94	8.249	8.302	0.053
30	95	8.272	8.220	-0.052
35	100	8.215	8.239	0.024
35	101	8.340	8.334	-0.006
35	102	8.244	8.254	0.010
35	103	8.238	8.214	-0.024
35	104	8.277	8.279	0.002
35	105	8.332	8.327	-0.005
50	106	8.258	8.137	-0.121
50	107	8.211	8.162	-0.049
50	108	8.300	8.195	-0.105
50	109	8.250	8.177	-0.073
50	110	8.272	8.200	-0.072
50	111	8.244	8.182	-0.062
55	113	8.243	8.138	-0.105
55	114	8.274	8.167	-0.107
55	115	8.253	8.213	-0.040
55	116	8.221	8.176	-0.045
55	126	8.321	8.242	-0.079
55	128	8.325	8.214	-0.111
100	129	8.280	8.199	-0.081
100	131	8.265	8.230	-0.035
100	132	8.394	8.205	-0.189
100	133	8.385	8.249	-0.136
100	134	8.342	8.184	-0.158
100	135	8.230	8.147	-0.083
100	138	8.256	8.164	-0.092
100	139	8.252	8.151	-0.101
100	140	8.283	8.218	-0.065
100	141	8.270	8.183	-0.087
100	142	8.310	8.241	-0.069
100	143	8.220	8.158	-0.062
100	144	8.287	8.188	-0.099
100	220	8.284	8.237	-0.047
100	221	8.245	8.112	-0.133
100	224	8.258	8.177	-0.081
100	225	8.302	8.245	-0.057
100	226	8.302	8.184	-0.118
100	228	8.299	8.152	-0.147
100	232	8.254	8.231	-0.023
100	233	8.272	8.190	-0.082
100	236	8.218	8.116	-0.102
100	238	8.248	8.174	-0.074
100	243	8.294	8.138	-0.156
105	244	8.237	8.169	-0.068
105	246	8.306	8.198	-0.108
105	248	8.296	8.205	-0.091
105	374	8.190	8.130	-0.060
105	376	8.302	8.189	-0.113
Max		8.419	8.351	0.053
Average		8.276	8.218	-0.058
Min		8.190	8.112	-0.192
Std Dev		0.050	0.053	0.056



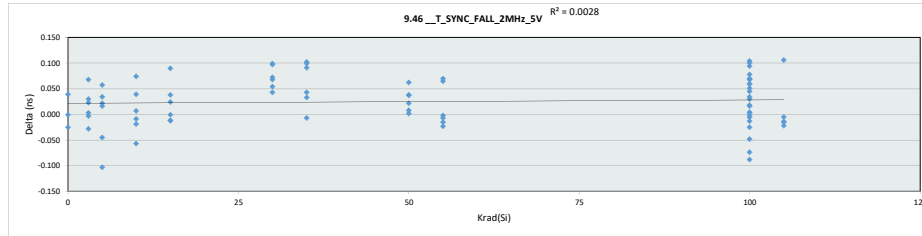
9.42 _T_SYNC_FALL_1p5MHz											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.258	8.187	8.208	8.220	8.192	8.220	8.214	8.137	8.138	8.112	8.130
Average	8.279	8.213	8.274	8.243	8.252	8.248	8.275	8.176	8.192	8.186	8.178
Max	8.312	8.266	8.351	8.259	8.337	8.302	8.334	8.200	8.242	8.249	8.205
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



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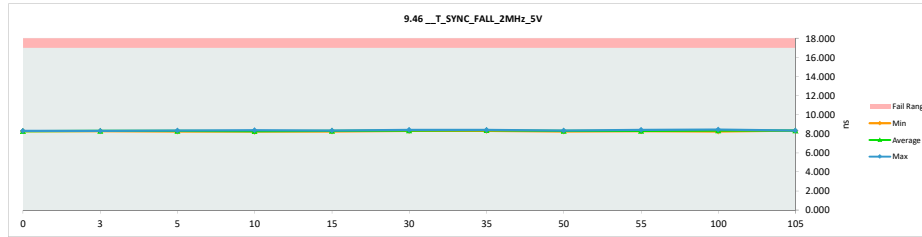
9.46_T_SYNC_FALL_2MHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.228	8.267	0.039
0	118	8.296	8.295	-0.001
0	122	8.289	8.264	-0.025
3	61	8.283	8.280	-0.003
3	62	8.291	8.314	0.023
3	63	8.232	8.300	0.068
3	64	8.283	8.255	-0.028
3	65	8.233	8.263	0.030
3	66	8.296	8.299	0.003
5	67	8.330	8.285	-0.045
5	68	8.313	8.347	0.034
5	69	8.254	8.270	0.016
5	70	8.228	8.285	0.057
5	71	8.272	8.294	0.022
5	72	8.345	8.242	-0.103
10	73	8.283	8.226	-0.057
10	74	8.291	8.365	0.074
10	75	8.232	8.271	0.039
10	76	8.283	8.274	-0.009
10	77	8.233	8.214	-0.019
10	78	8.296	8.303	0.007
15	79	8.330	8.329	-0.001
15	80	8.313	8.337	0.024
15	81	8.254	8.242	-0.012
15	82	8.228	8.318	0.090
15	83	8.272	8.310	0.038
15	84	8.345	8.333	-0.012
30	86	8.283	8.380	0.097
30	89	8.291	8.363	0.072
30	91	8.232	8.286	0.054
30	92	8.283	8.326	0.043
30	94	8.233	8.301	0.068
30	95	8.296	8.395	0.099
35	100	8.330	8.363	0.033
35	101	8.313	8.412	0.099
35	102	8.254	8.297	0.043
35	103	8.228	8.330	0.102
35	104	8.272	8.363	0.091
35	105	8.345	8.338	-0.007
50	106	8.326	8.334	0.008
50	107	8.249	8.251	0.002
50	108	8.263	8.300	0.037
50	109	8.207	8.245	0.038
50	110	8.281	8.343	0.062
50	111	8.260	8.282	0.022
55	113	8.317	8.387	0.070
55	114	8.257	8.322	0.065
55	115	8.299	8.292	-0.007
55	116	8.312	8.297	-0.015
55	126	8.280	8.257	-0.023
55	128	8.305	8.303	-0.002
100	129	8.278	8.347	0.069
100	131	8.377	8.411	0.034
100	132	8.318	8.422	0.104
100	133	8.370	8.388	0.018
100	134	8.330	8.408	0.078
100	135	8.282	8.234	-0.048
100	138	8.261	8.361	0.100
100	139	8.252	8.320	0.068
100	140	8.312	8.406	0.094
100	141	8.312	8.310	-0.002
100	142	8.313	8.225	-0.088
100	143	8.299	8.286	-0.013
100	144	8.328	8.322	-0.006
100	220	8.389	8.315	-0.074
100	221	8.283	8.353	0.070
100	224	8.287	8.346	0.059
100	225	8.311	8.356	0.045
100	226	8.337	8.388	0.051
100	228	8.394	8.369	-0.025
100	232	8.341	8.344	0.003
100	233	8.266	8.326	0.060
100	236	8.275	8.279	0.004
100	238	8.344	8.374	0.030
100	243	8.335	8.351	0.016
105	244	8.351	8.329	-0.022
105	246	8.343	8.338	-0.005
105	248	8.354	8.339	-0.015
105	374	8.208	8.314	0.106
105	376	8.363	8.349	-0.014
Max		8.394	8.422	0.106
Average		8.293	8.318	0.025
Min		8.207	8.214	-0.103
Std Dev		0.043	0.049	0.047



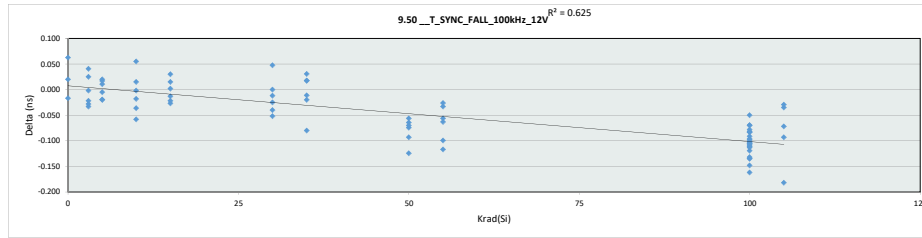
9.46_T_SYNC_FALL_2MHz_5V	
Test Site	
Tester	
Test Number	
Max Limit	17
Min Limit	ns

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.264	8.255	8.242	8.214	8.242	8.286	8.297	8.245	8.257	8.225	8.314
Average	8.275	8.285	8.287	8.276	8.312	8.342	8.351	8.293	8.310	8.343	8.334
Max	8.295	8.314	8.347	8.365	8.337	8.395	8.412	8.343	8.387	8.422	8.349
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

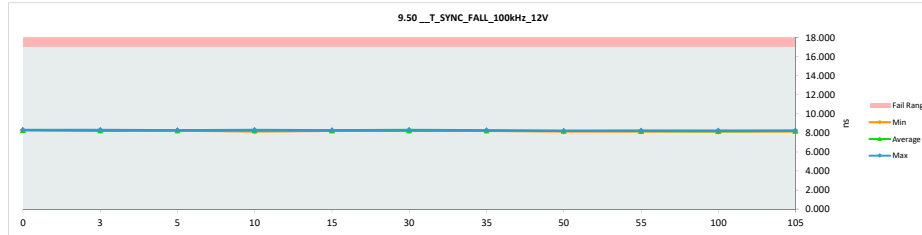


LDR TID Report
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9.50_T_SYNC_FALL_100kHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.242	8.305	0.063
0	118	8.306	8.289	-0.017
0	122	8.213	8.233	0.020
3	61	8.267	8.234	-0.033
3	62	8.236	8.234	-0.002
3	63	8.161	8.202	0.041
3	64	8.189	8.214	0.025
3	65	8.323	8.301	-0.022
3	66	8.263	8.235	-0.028
5	67	8.220	8.231	0.011
5	68	8.256	8.236	-0.020
5	69	8.203	8.220	0.017
5	70	8.268	8.249	-0.019
5	71	8.230	8.225	-0.005
5	72	8.246	8.266	0.020
10	73	8.267	8.282	0.015
10	74	8.236	8.234	-0.002
10	75	8.161	8.125	-0.036
10	76	8.189	8.244	0.055
10	77	8.323	8.305	-0.018
10	78	8.263	8.205	-0.058
15	79	8.220	8.193	-0.027
15	80	8.256	8.234	-0.022
15	81	8.203	8.233	0.030
15	82	8.268	8.255	-0.013
15	83	8.230	8.232	0.002
15	84	8.246	8.261	0.015
30	86	8.267	8.215	-0.052
30	89	8.236	8.196	-0.040
30	91	8.161	8.209	0.048
30	92	8.189	8.189	0.000
30	94	8.323	8.311	-0.012
30	95	8.263	8.238	-0.025
35	100	8.220	8.251	0.031
35	101	8.256	8.274	0.018
35	102	8.203	8.192	-0.011
35	103	8.268	8.188	-0.080
35	104	8.230	8.210	-0.020
35	105	8.246	8.264	0.018
50	106	8.264	8.190	-0.074
50	107	8.265	8.172	-0.093
50	108	8.263	8.207	-0.056
50	109	8.209	8.145	-0.064
50	110	8.324	8.200	-0.124
50	111	8.257	8.187	-0.070
55	113	8.282	8.226	-0.056
55	114	8.229	8.166	-0.063
55	115	8.183	8.150	-0.033
55	116	8.248	8.131	-0.117
55	126	8.292	8.193	-0.099
55	128	8.213	8.187	-0.026
100	129	8.317	8.214	-0.103
100	131	8.306	8.158	-0.148
100	132	8.264	8.195	-0.069
100	133	8.340	8.242	-0.098
100	134	8.280	8.145	-0.135
100	135	8.244	8.161	-0.083
100	138	8.280	8.173	-0.107
100	139	8.286	8.181	-0.105
100	140	8.302	8.224	-0.078
100	141	8.269	8.159	-0.110
100	142	8.233	8.128	-0.105
100	143	8.287	8.153	-0.134
100	144	8.240	8.157	-0.083
100	220	8.277	8.145	-0.132
100	221	8.225	8.114	-0.111
100	224	8.311	8.149	-0.162
100	225	8.338	8.247	-0.091
100	226	8.260	8.190	-0.070
100	228	8.278	8.228	-0.050
100	232	8.274	8.192	-0.082
100	233	8.312	8.193	-0.119
100	236	8.281	8.168	-0.113
100	238	8.299	8.198	-0.101
100	243	8.314	8.218	-0.096
105	244	8.292	8.220	-0.072
105	246	8.216	8.187	-0.029
105	248	8.280	8.187	-0.093
105	374	8.270	8.235	-0.035
105	376	8.316	8.134	-0.182
	Max	8.340	8.311	0.063
	Average	8.257	8.208	-0.049
	Min	8.161	8.114	-0.182
	Std Dev	0.042	0.045	0.055



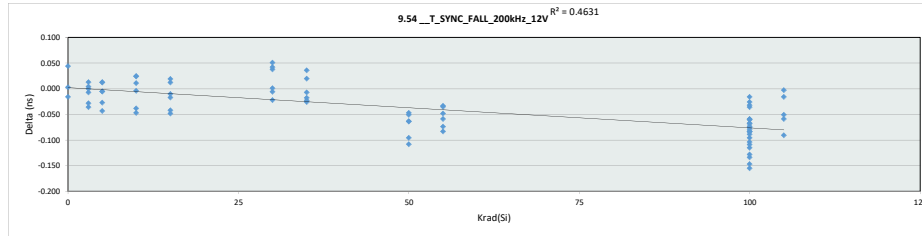
9.50_T_SYNC_FALL_100kHz											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.233	8.202	8.220	8.125	8.193	8.189	8.188	8.145	8.131	8.114	8.134
Average	8.276	8.237	8.238	8.233	8.235	8.226	8.230	8.184	8.176	8.181	8.193
Max	8.305	8.301	8.266	8.305	8.261	8.311	8.274	8.207	8.226	8.247	8.235
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



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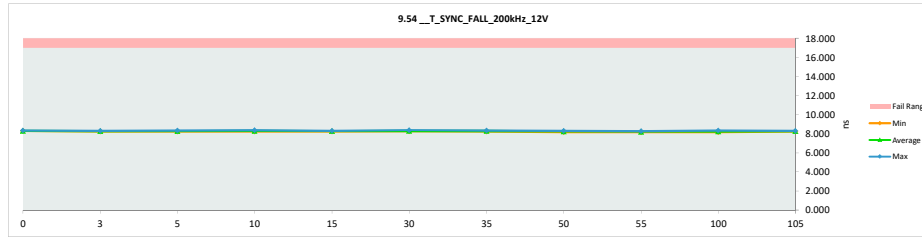
9.54_T_SYNC_FALL_200kHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.302	8.286	-0.016
0	118	8.333	8.336	0.003
0	122	8.246	8.290	0.044
3	61	8.291	8.295	0.004
3	62	8.302	8.274	-0.028
3	63	8.230	8.243	0.013
3	64	8.273	8.273	0.000
3	65	8.339	8.303	-0.036
3	66	8.291	8.284	-0.007
5	67	8.249	8.244	-0.005
5	68	8.314	8.326	0.012
5	69	8.298	8.311	0.013
5	70	8.280	8.253	-0.027
5	71	8.263	8.258	-0.005
5	72	8.309	8.266	-0.043
10	73	8.291	8.287	-0.004
10	74	8.302	8.255	-0.047
10	75	8.230	8.254	0.024
10	76	8.273	8.235	-0.038
10	77	8.339	8.350	0.011
10	78	8.291	8.316	0.025
15	79	8.249	8.268	0.019
15	80	8.314	8.304	-0.010
15	81	8.298	8.310	0.012
15	82	8.280	8.232	-0.048
15	83	8.263	8.246	-0.017
15	84	8.309	8.267	-0.042
30	86	8.291	8.342	0.051
30	89	8.302	8.280	-0.022
30	91	8.230	8.268	0.038
30	92	8.273	8.274	0.001
30	94	8.339	8.381	0.042
30	95	8.291	8.285	-0.006
35	100	8.249	8.231	-0.018
35	101	8.314	8.307	-0.007
35	102	8.298	8.272	-0.026
35	103	8.280	8.316	0.036
35	104	8.263	8.283	0.020
35	105	8.309	8.286	-0.023
50	106	8.300	8.253	-0.047
50	107	8.298	8.202	-0.096
50	108	8.357	8.293	-0.064
50	109	8.265	8.157	-0.108
50	110	8.340	8.277	-0.063
50	111	8.270	8.219	-0.051
55	113	8.340	8.257	-0.083
55	114	8.236	8.203	-0.033
55	115	8.252	8.178	-0.074
55	116	8.263	8.228	-0.035
55	126	8.261	8.213	-0.048
55	128	8.253	8.194	-0.059
100	129	8.347	8.288	-0.059
100	131	8.351	8.236	-0.115
100	132	8.339	8.255	-0.084
100	133	8.357	8.268	-0.089
100	134	8.312	8.229	-0.083
100	135	8.279	8.196	-0.083
100	138	8.324	8.249	-0.075
100	139	8.342	8.195	-0.147
100	140	8.363	8.331	-0.032
100	141	8.267	8.171	-0.096
100	142	8.324	8.220	-0.104
100	143	8.285	8.206	-0.079
100	144	8.273	8.213	-0.060
100	220	8.289	8.180	-0.109
100	221	8.262	8.202	-0.060
100	224	8.308	8.292	-0.016
100	225	8.388	8.320	-0.068
100	226	8.350	8.283	-0.067
100	228	8.384	8.229	-0.155
100	232	8.303	8.169	-0.134
100	233	8.275	8.249	-0.026
100	236	8.326	8.198	-0.128
100	238	8.285	8.249	-0.036
100	243	8.318	8.245	-0.073
105	244	8.292	8.241	-0.051
105	246	8.302	8.243	-0.059
105	248	8.291	8.288	-0.003
105	374	8.304	8.288	-0.016
105	376	8.330	8.239	-0.091
Max		8.388	8.381	0.051
Average		8.298	8.259	-0.038
Min		8.230	8.157	-0.155
Std Dev		0.036	0.046	0.047



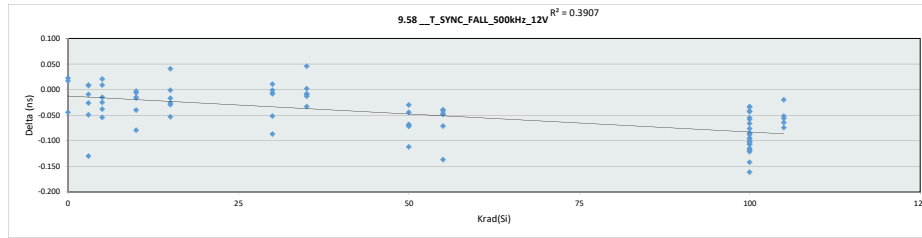
9.54_T_SYNC_FALL_200kHz	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.286	8.243	8.244	8.235	8.232	8.268	8.231	8.157	8.178	8.169	8.239
Average	8.304	8.279	8.276	8.283	8.271	8.305	8.283	8.234	8.212	8.236	8.260
Max	8.336	8.303	8.326	8.350	8.310	8.381	8.316	8.293	8.257	8.331	8.288
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

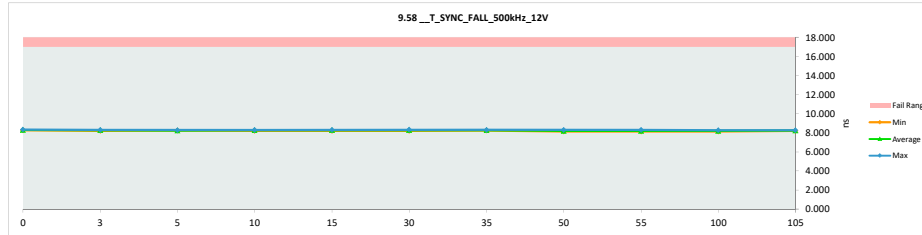


LDR TID Report
TPS7H5001-SP

9.58_T_SYNC_FALL_500kHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.312	8.268	-0.044
0	118	8.302	8.319	0.017
0	122	8.240	8.263	0.023
3	61	8.311	8.285	-0.026
3	62	8.243	8.251	0.008
3	63	8.198	8.207	0.009
3	64	8.321	8.191	-0.130
3	65	8.349	8.300	-0.049
3	66	8.273	8.264	-0.009
5	67	8.235	8.220	-0.015
5	68	8.298	8.273	-0.025
5	69	8.295	8.241	-0.054
5	70	8.231	8.240	0.009
5	71	8.290	8.252	-0.038
5	72	8.238	8.259	0.021
10	73	8.311	8.308	-0.003
10	74	8.243	8.227	-0.016
10	75	8.198	8.192	-0.006
10	76	8.321	8.242	-0.079
10	77	8.349	8.309	-0.040
10	78	8.273	8.258	-0.015
15	79	8.235	8.206	-0.029
15	80	8.298	8.297	-0.001
15	81	8.295	8.278	-0.017
15	82	8.231	8.205	-0.026
15	83	8.290	8.237	-0.053
15	84	8.238	8.279	0.041
30	86	8.311	8.259	-0.052
30	89	8.243	8.236	-0.007
30	91	8.198	8.197	-0.001
30	92	8.321	8.234	-0.087
30	94	8.349	8.341	-0.008
30	95	8.273	8.284	0.011
35	100	8.235	8.227	-0.008
35	101	8.298	8.265	-0.033
35	102	8.295	8.297	0.002
35	103	8.231	8.222	-0.009
35	104	8.290	8.277	-0.013
35	105	8.238	8.284	0.046
50	106	8.289	8.217	-0.072
50	107	8.271	8.241	-0.030
50	108	8.293	8.249	-0.044
50	109	8.226	8.158	-0.068
50	110	8.357	8.288	-0.069
50	111	8.248	8.136	-0.112
55	113	8.322	8.283	-0.039
55	114	8.230	8.182	-0.048
55	115	8.243	8.202	-0.041
55	116	8.281	8.210	-0.071
55	126	8.236	8.189	-0.047
55	128	8.274	8.137	-0.137
100	129	8.323	8.228	-0.095
100	131	8.333	8.214	-0.119
100	132	8.295	8.194	-0.101
100	133	8.381	8.282	-0.099
100	134	8.351	8.275	-0.076
100	135	8.237	8.152	-0.085
100	138	8.296	8.201	-0.095
100	139	8.303	8.142	-0.161
100	140	8.305	8.217	-0.088
100	141	8.294	8.152	-0.142
100	142	8.292	8.206	-0.086
100	143	8.294	8.236	-0.058
100	144	8.231	8.197	-0.034
100	220	8.300	8.185	-0.115
100	221	8.206	8.153	-0.043
100	224	8.286	8.231	-0.055
100	225	8.337	8.271	-0.066
100	226	8.317	8.214	-0.103
100	228	8.329	8.222	-0.107
100	232	8.323	8.207	-0.116
100	233	8.320	8.199	-0.121
100	236	8.289	8.247	-0.042
100	238	8.300	8.194	-0.106
100	243	8.289	8.256	-0.033
105	244	8.290	8.234	-0.056
105	246	8.276	8.212	-0.064
105	248	8.290	8.238	-0.052
105	374	8.290	8.270	-0.020
105	376	8.276	8.202	-0.074
	Max	8.381	8.341	0.046
	Average	8.283	8.234	-0.049
	Min	8.198	8.136	-0.161
	Std Dev	0.041	0.044	0.045

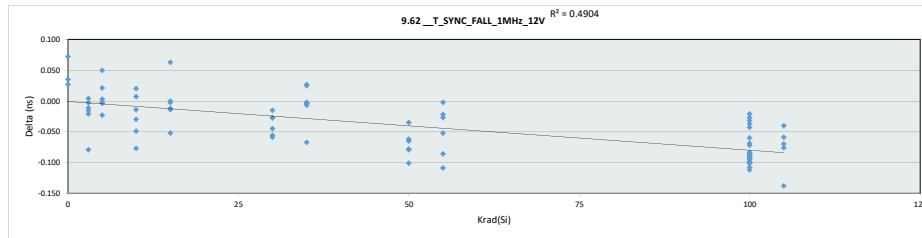


9.58_T_SYNC_FALL_500kHz											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.263	8.191	8.220	8.192	8.205	8.197	8.222	8.136	8.137	8.142	8.202
Average	8.283	8.250	8.248	8.256	8.250	8.259	8.262	8.215	8.201	8.212	8.231
Max	8.319	8.300	8.273	8.309	8.297	8.341	8.297	8.341	8.288	8.283	8.270
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

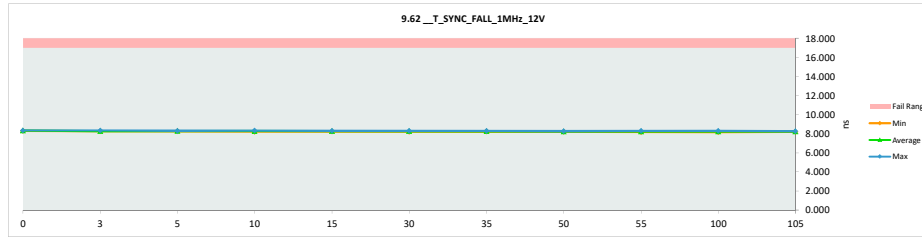


LDR TID Report
TPS7H5001-SP

9.62_T_SYNC_FALL_1MHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.281	8.308	0.027
0	118	8.306	8.341	0.035
0	122	8.276	8.348	0.072
3	61	8.296	8.280	-0.016
3	62	8.280	8.259	-0.021
3	63	8.262	8.251	-0.011
3	64	8.266	8.270	0.004
3	65	8.351	8.348	-0.003
3	66	8.335	8.256	-0.079
5	67	8.255	8.258	0.003
5	68	8.306	8.302	-0.004
5	69	8.291	8.312	0.021
5	70	8.216	8.266	0.050
5	71	8.294	8.292	-0.002
5	72	8.308	8.285	-0.023
10	73	8.296	8.303	0.007
10	74	8.280	8.266	-0.014
10	75	8.262	8.213	-0.049
10	76	8.266	8.286	0.020
10	77	8.351	8.321	-0.030
10	78	8.335	8.258	-0.077
15	79	8.255	8.243	-0.012
15	80	8.306	8.303	-0.003
15	81	8.291	8.277	-0.014
15	82	8.216	8.279	0.063
15	83	8.294	8.242	-0.052
15	84	8.308	8.308	0.000
30	86	8.296	8.281	-0.015
30	89	8.280	8.253	-0.027
30	91	8.262	8.235	-0.027
30	92	8.266	8.207	-0.059
30	94	8.351	8.306	-0.045
30	95	8.335	8.279	-0.056
35	100	8.255	8.280	0.025
35	101	8.306	8.304	-0.002
35	102	8.291	8.284	-0.007
35	103	8.216	8.243	0.027
35	104	8.294	8.290	-0.004
35	105	8.308	8.241	-0.067
50	106	8.295	8.230	-0.065
50	107	8.312	8.233	-0.079
50	108	8.331	8.253	-0.078
50	109	8.222	8.187	-0.035
50	110	8.368	8.267	-0.101
50	111	8.282	8.220	-0.062
55	113	8.298	8.296	-0.002
55	114	8.247	8.195	-0.052
55	115	8.277	8.250	-0.027
55	116	8.259	8.237	-0.022
55	126	8.318	8.232	-0.086
55	128	8.279	8.170	-0.109
100	129	8.371	8.281	-0.090
100	131	8.305	8.221	-0.084
100	132	8.312	8.243	-0.069
100	133	8.389	8.301	-0.088
100	134	8.330	8.237	-0.093
100	135	8.244	8.160	-0.084
100	138	8.292	8.193	-0.099
100	139	8.299	8.239	-0.060
100	140	8.330	8.237	-0.093
100	141	8.298	8.208	-0.090
100	142	8.265	8.244	-0.021
100	143	8.263	8.236	-0.027
100	144	8.296	8.184	-0.112
100	220	8.303	8.202	-0.101
100	221	8.278	8.170	-0.108
100	224	8.329	8.231	-0.098
100	225	8.380	8.286	-0.094
100	226	8.313	8.270	-0.043
100	228	8.294	8.257	-0.037
100	232	8.302	8.201	-0.101
100	233	8.330	8.235	-0.095
100	236	8.288	8.202	-0.086
100	238	8.244	8.212	-0.032
100	243	8.311	8.239	-0.072
105	244	8.317	8.247	-0.070
105	246	8.274	8.198	-0.076
105	248	8.307	8.267	-0.040
105	374	8.300	8.241	-0.059
105	376	8.332	8.194	-0.138
Max		8.389	8.348	0.072
Average		8.295	8.254	-0.042
Min		8.216	8.160	-0.138
Std Dev		0.036	0.042	0.046

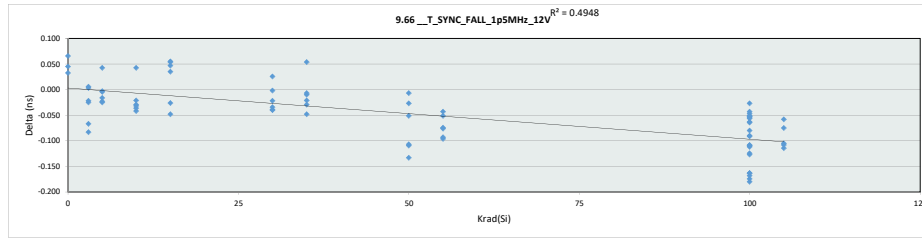


9.62_T_SYNC_FALL_1MHz_12V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.308	8.251	8.258	8.213	8.242	8.207	8.241	8.187	8.170	8.160	8.194
Average	8.332	8.277	8.286	8.275	8.275	8.260	8.274	8.232	8.230	8.229	8.229
Max	8.348	8.348	8.312	8.321	8.308	8.306	8.304	8.267	8.296	8.301	8.267
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

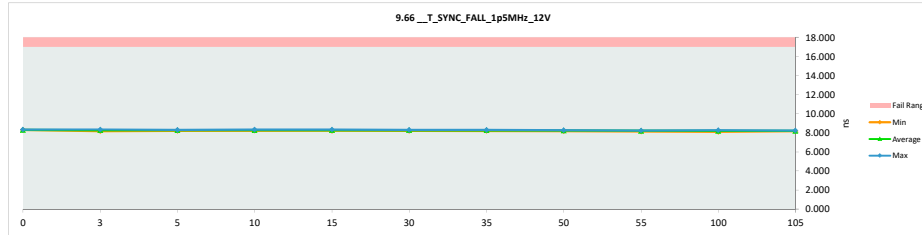


LDR TID Report
TPS7H5001-SP

9.66 _T_SYNC_FALL_1p5MHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.251	8.296	0.045
0	118	8.260	8.293	0.033
0	122	8.252	8.318	0.066
3	61	8.302	8.305	0.003
3	62	8.311	8.228	-0.083
3	63	8.187	8.152	-0.025
3	64	8.277	8.210	-0.067
3	65	8.339	8.345	0.006
3	66	8.285	8.263	-0.022
5	67	8.201	8.185	-0.016
5	68	8.319	8.296	-0.023
5	69	8.219	8.262	0.043
5	70	8.268	8.265	-0.003
5	71	8.252	8.248	-0.004
5	72	8.297	8.272	-0.025
10	73	8.302	8.273	-0.029
10	74	8.311	8.269	-0.042
10	75	8.187	8.230	0.043
10	76	8.277	8.241	-0.036
10	77	8.339	8.318	-0.021
10	78	8.285	8.253	-0.032
15	79	8.201	8.256	0.055
15	80	8.319	8.293	-0.026
15	81	8.219	8.266	0.047
15	82	8.268	8.322	0.054
15	83	8.252	8.204	-0.048
15	84	8.297	8.332	0.035
30	86	8.302	8.268	-0.034
30	89	8.311	8.309	-0.002
30	91	8.187	8.213	0.026
30	92	8.277	8.238	-0.039
30	94	8.339	8.299	-0.040
30	95	8.285	8.263	-0.022
35	100	8.201	8.255	0.054
35	101	8.319	8.271	-0.048
35	102	8.219	8.190	-0.029
35	103	8.268	8.258	-0.010
35	104	8.252	8.231	-0.021
35	105	8.297	8.290	-0.007
50	106	8.299	8.248	-0.051
50	107	8.283	8.256	-0.027
50	108	8.309	8.200	-0.109
50	109	8.231	8.224	-0.007
50	110	8.357	8.250	-0.107
50	111	8.286	8.153	-0.133
55	113	8.281	8.230	-0.051
55	114	8.222	8.126	-0.096
55	115	8.213	8.170	-0.043
55	116	8.245	8.169	-0.076
55	126	8.321	8.228	-0.093
55	128	8.275	8.201	-0.074
100	129	8.336	8.212	-0.124
100	131	8.307	8.195	-0.112
100	132	8.304	8.248	-0.056
100	133	8.319	8.272	-0.047
100	134	8.374	8.211	-0.163
100	135	8.206	8.143	-0.063
100	138	8.265	8.213	-0.052
100	139	8.251	8.187	-0.064
100	140	8.297	8.189	-0.108
100	141	8.278	8.098	-0.180
100	142	8.303	8.176	-0.127
100	143	8.212	8.169	-0.043
100	144	8.219	8.139	-0.080
100	220	8.228	8.173	-0.055
100	221	8.225	8.115	-0.110
100	224	8.338	8.175	-0.163
100	225	8.415	8.241	-0.174
100	226	8.299	8.248	-0.051
100	228	8.363	8.195	-0.168
100	232	8.263	8.154	-0.109
100	233	8.238	8.186	-0.052
100	236	8.245	8.154	-0.091
100	238	8.245	8.218	-0.027
100	243	8.290	8.200	-0.090
105	244	8.284	8.176	-0.108
105	246	8.232	8.174	-0.058
105	248	8.297	8.222	-0.075
105	374	8.280	8.175	-0.105
105	376	8.319	8.205	-0.114
Max		8.415	8.345	0.066
Average		8.276	8.228	-0.049
Min		8.187	8.098	-0.180
Std Dev		0.047	0.054	0.058

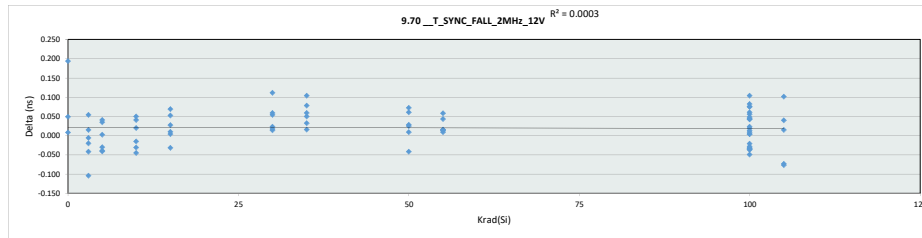


9.66 _T_SYNC_FALL_1p5MHz											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.293	8.162	8.185	8.230	8.204	8.213	8.190	8.153	8.126	8.098	8.174
Average	8.302	8.252	8.255	8.264	8.279	8.265	8.249	8.222	8.187	8.188	8.190
Max	8.318	8.345	8.296	8.318	8.332	8.309	8.290	8.256	8.230	8.272	8.222
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

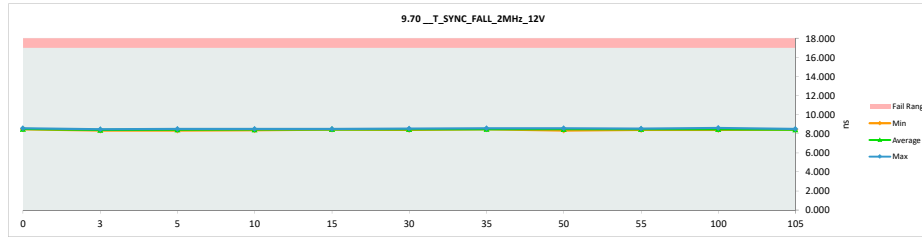


LDR TID Report
TPS7H5001-SP

9.70_T_SYNC_FALL_2MHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.359	8.553	0.194
0	118	8.487	8.495	0.008
0	122	8.419	8.468	0.049
3	61	8.492	8.472	-0.020
3	62	8.424	8.439	0.015
3	63	8.397	8.391	-0.006
3	64	8.370	8.328	-0.042
3	65	8.467	8.363	-0.104
3	66	8.362	8.416	0.054
5	67	8.411	8.446	0.035
5	68	8.501	8.503	0.002
5	69	8.418	8.388	-0.030
5	70	8.388	8.429	0.041
5	71	8.381	8.340	-0.041
5	72	8.521	8.482	-0.039
10	73	8.492	8.512	0.020
10	74	8.424	8.409	-0.015
10	75	8.397	8.352	-0.045
10	76	8.370	8.420	0.050
10	77	8.467	8.436	-0.031
10	78	8.362	8.403	0.041
15	79	8.411	8.438	0.027
15	80	8.501	8.511	0.010
15	81	8.418	8.422	0.004
15	82	8.388	8.440	0.052
15	83	8.381	8.450	0.069
15	84	8.521	8.489	-0.032
30	86	8.492	8.546	0.054
30	89	8.424	8.438	0.014
30	91	8.397	8.414	0.017
30	92	8.370	8.429	0.059
30	94	8.467	8.490	0.023
30	95	8.362	8.473	0.111
35	100	8.411	8.461	0.050
35	101	8.501	8.517	0.016
35	102	8.418	8.477	0.059
35	103	8.388	8.492	0.104
35	104	8.381	8.459	0.078
35	105	8.521	8.553	0.032
50	106	8.467	8.476	0.009
50	107	8.452	8.524	0.072
50	108	8.432	8.457	0.025
50	109	8.414	8.442	0.028
50	110	8.496	8.557	0.061
50	111	8.373	8.331	-0.042
55	113	8.482	8.525	0.043
55	114	8.355	8.413	0.058
55	115	8.419	8.434	0.015
55	116	8.458	8.471	0.013
55	126	8.496	8.505	0.009
55	128	8.425	8.441	0.016
100	129	8.531	8.535	0.004
100	131	8.431	8.473	0.042
100	132	8.413	8.495	0.082
100	133	8.554	8.518	-0.036
100	134	8.480	8.523	0.043
100	135	8.365	8.421	0.056
100	138	8.461	8.509	0.048
100	139	8.454	8.420	-0.034
100	140	8.488	8.532	0.044
100	141	8.385	8.408	0.023
100	142	8.477	8.440	-0.037
100	143	8.474	8.425	-0.049
100	144	8.474	8.453	-0.021
100	220	8.467	8.486	0.019
100	221	8.382	8.391	0.009
100	224	8.463	8.429	-0.034
100	225	8.460	8.521	0.061
100	226	8.455	8.530	0.075
100	228	8.516	8.592	0.076
100	232	8.481	8.494	0.013
100	233	8.436	8.440	0.004
100	236	8.421	8.389	-0.032
100	238	8.386	8.490	0.104
100	243	8.483	8.455	-0.028
105	244	8.500	8.427	-0.073
105	246	8.500	8.423	-0.077
105	248	8.403	8.504	0.101
105	374	8.396	8.436	0.040
105	376	8.424	8.439	0.015
Max		8.554	8.592	0.194
Average		8.439	8.459	0.020
Min		8.355	8.328	-0.104
Std Dev		0.051	0.055	0.049

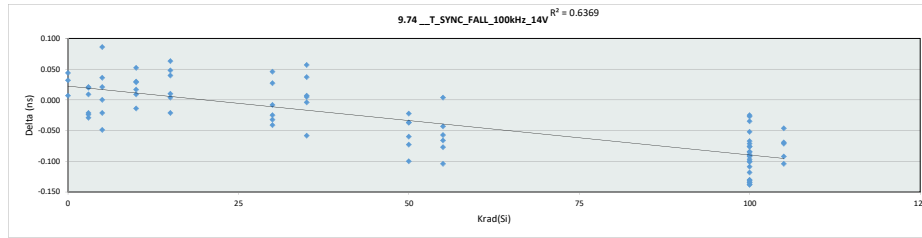


9.70_T_SYNC_FALL_2MHz_12V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.468	8.328	8.340	8.352	8.422	8.414	8.459	8.331	8.413	8.389	8.423
Average	8.505	8.402	8.431	8.422	8.458	8.465	8.493	8.465	8.465	8.474	8.446
Max	8.553	8.472	8.503	8.512	8.511	8.546	8.553	8.557	8.525	8.592	8.504
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

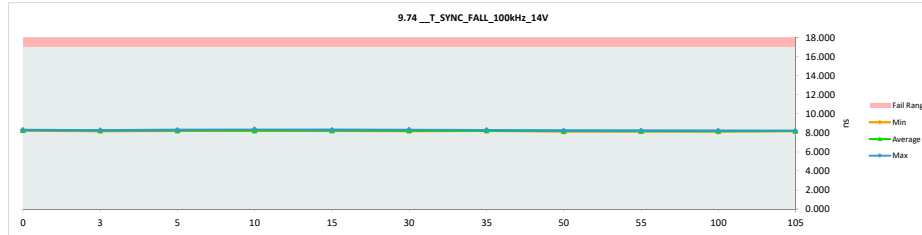


LDR TID Report
TPS7H5001-SP

9.74_T_SYNC_FALL_100kHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.269	8.301	0.032
0	118	8.251	8.295	0.044
0	122	8.215	8.222	0.007
3	61	8.221	8.242	0.021
3	62	8.216	8.187	-0.029
3	63	8.186	8.162	-0.024
3	64	8.203	8.212	0.009
3	65	8.298	8.277	-0.021
3	66	8.218	8.238	0.020
5	67	8.165	8.201	0.036
5	68	8.279	8.300	0.021
5	69	8.201	8.287	0.086
5	70	8.204	8.204	0.000
5	71	8.236	8.187	-0.049
5	72	8.257	8.236	-0.021
10	73	8.221	8.238	0.017
10	74	8.216	8.245	0.029
10	75	8.186	8.216	0.030
10	76	8.203	8.212	0.009
10	77	8.298	8.350	0.052
10	78	8.218	8.204	-0.014
15	79	8.165	8.213	0.048
15	80	8.279	8.283	0.004
15	81	8.201	8.241	0.040
15	82	8.204	8.267	0.063
15	83	8.236	8.215	-0.021
15	84	8.257	8.267	0.010
30	86	8.221	8.267	0.046
30	89	8.216	8.208	-0.008
30	91	8.186	8.161	-0.025
30	92	8.203	8.162	-0.041
30	94	8.298	8.325	0.027
30	95	8.218	8.186	-0.032
35	100	8.165	8.202	0.037
35	101	8.279	8.284	0.005
35	102	8.201	8.258	0.057
35	103	8.204	8.211	0.007
35	104	8.236	8.232	-0.004
35	105	8.257	8.199	-0.058
50	106	8.254	8.154	-0.100
50	107	8.235	8.232	-0.003
50	108	8.266	8.193	-0.073
50	109	8.170	8.110	-0.060
50	110	8.258	8.221	-0.037
50	111	8.205	8.168	-0.037
55	113	8.292	8.235	-0.057
55	114	8.186	8.143	-0.043
55	115	8.198	8.121	-0.077
55	116	8.187	8.191	0.004
55	126	8.264	8.160	-0.104
55	128	8.222	8.156	-0.066
100	129	8.306	8.222	-0.084
100	131	8.268	8.167	-0.101
100	132	8.246	8.170	-0.076
100	133	8.342	8.204	-0.138
100	134	8.325	8.195	-0.130
100	135	8.296	8.161	-0.135
100	138	8.206	8.181	-0.025
100	139	8.267	8.136	-0.131
100	140	8.286	8.215	-0.071
100	141	8.268	8.134	-0.134
100	142	8.259	8.183	-0.076
100	143	8.266	8.214	-0.052
100	144	8.205	8.113	-0.092
100	220	8.270	8.152	-0.118
100	221	8.201	8.110	-0.091
100	224	8.280	8.191	-0.089
100	225	8.345	8.236	-0.109
100	226	8.326	8.229	-0.097
100	228	8.266	8.239	-0.027
100	232	8.244	8.177	-0.067
100	233	8.269	8.131	-0.138
100	236	8.276	8.191	-0.085
100	238	8.219	8.184	-0.035
100	243	8.279	8.179	-0.100
105	244	8.269	8.198	-0.071
105	246	8.267	8.163	-0.104
105	248	8.271	8.202	-0.069
105	374	8.254	8.208	-0.046
105	376	8.307	8.215	-0.092
	Max	8.345	8.350	0.006
	Average	8.243	8.207	-0.036
	Min	8.165	8.110	-0.138
	Std Dev	0.043	0.050	0.057



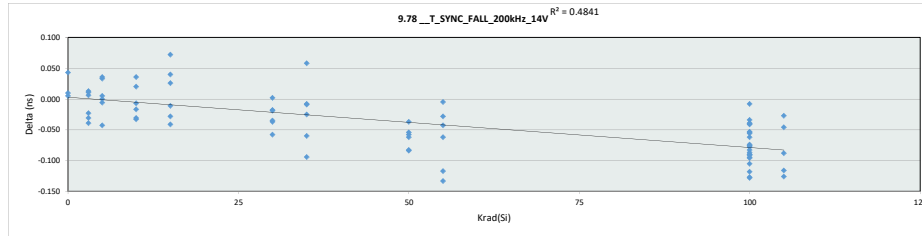
9.74_T_SYNC_FALL_100kHz											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.222	8.162	8.187	8.204	8.213	8.161	8.199	8.110	8.121	8.110	8.163
Average	8.273	8.220	8.236	8.244	8.248	8.218	8.231	8.177	8.168	8.180	8.197
Max	8.301	8.277	8.300	8.350	8.283	8.325	8.284	8.221	8.235	8.239	8.215
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



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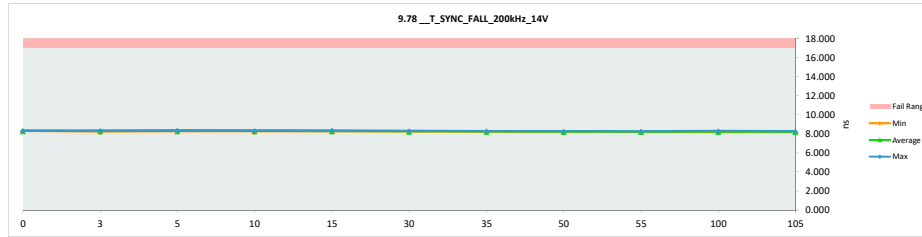
9.78_T_SYNC_FALL_200kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.296	8.307	0.011
0	118	8.284	8.328	0.044
0	122	8.284	8.290	0.006
3	61	8.315	8.322	0.007
3	62	8.286	8.256	-0.030
3	63	8.256	8.234	-0.022
3	64	8.271	8.285	0.014
3	65	8.328	8.340	0.012
3	66	8.314	8.276	-0.038
5	67	8.216	8.253	0.037
5	68	8.294	8.300	0.006
5	69	8.286	8.287	0.001
5	70	8.294	8.252	-0.042
5	71	8.299	8.294	-0.005
5	72	8.320	8.354	0.034
10	73	8.315	8.285	-0.030
10	74	8.286	8.254	-0.032
10	75	8.256	8.240	-0.016
10	76	8.271	8.308	0.037
10	77	8.328	8.322	-0.006
10	78	8.314	8.335	0.021
15	79	8.216	8.257	0.041
15	80	8.294	8.367	0.073
15	81	8.286	8.259	-0.027
15	82	8.294	8.284	-0.010
15	83	8.299	8.326	0.027
15	84	8.320	8.280	-0.040
30	86	8.315	8.279	-0.036
30	89	8.286	8.252	-0.034
30	91	8.256	8.259	0.003
30	92	8.271	8.254	-0.017
30	94	8.328	8.310	-0.018
30	95	8.314	8.257	-0.057
35	100	8.216	8.275	0.059
35	101	8.294	8.270	-0.024
35	102	8.286	8.279	-0.007
35	103	8.294	8.201	-0.093
35	104	8.299	8.291	-0.008
35	105	8.320	8.261	-0.059
50	106	8.289	8.232	-0.057
50	107	8.306	8.233	-0.083
50	108	8.312	8.230	-0.082
50	109	8.231	8.170	-0.061
50	110	8.305	8.252	-0.053
50	111	8.279	8.243	-0.036
55	113	8.300	8.239	-0.061
55	114	8.261	8.219	-0.042
55	115	8.262	8.235	-0.027
55	116	8.258	8.254	-0.004
55	126	8.364	8.232	-0.132
55	128	8.302	8.186	-0.116
100	129	8.336	8.303	-0.033
100	131	8.322	8.240	-0.082
100	132	8.284	8.244	-0.040
100	133	8.342	8.267	-0.075
100	134	8.330	8.269	-0.061
100	135	8.277	8.270	-0.007
100	138	8.314	8.225	-0.089
100	139	8.288	8.233	-0.055
100	140	8.314	8.261	-0.053
100	141	8.307	8.180	-0.127
100	142	8.331	8.241	-0.090
100	143	8.288	8.236	-0.052
100	144	8.320	8.203	-0.117
100	220	8.291	8.202	-0.089
100	221	8.282	8.156	-0.126
100	224	8.323	8.250	-0.073
100	225	8.405	8.301	-0.104
100	226	8.340	8.265	-0.075
100	228	8.291	8.252	-0.039
100	232	8.304	8.218	-0.086
100	233	8.316	8.222	-0.094
100	236	8.304	8.227	-0.077
100	238	8.289	8.251	-0.038
100	243	8.333	8.238	-0.095
105	244	8.345	8.220	-0.125
105	246	8.277	8.162	-0.115
105	248	8.323	8.236	-0.087
105	374	8.325	8.280	-0.045
105	376	8.300	8.274	-0.026
Max	8.405	8.367	0.073	
Average	8.298	8.259	-0.039	
Min	8.216	8.156	-0.132	
Std Dev	0.031	0.042	0.047	



9.78_T_SYNC_FALL_200kHz	
Test Site	
Tester	
Test Number	
Max Limit	17
Min Limit	ns

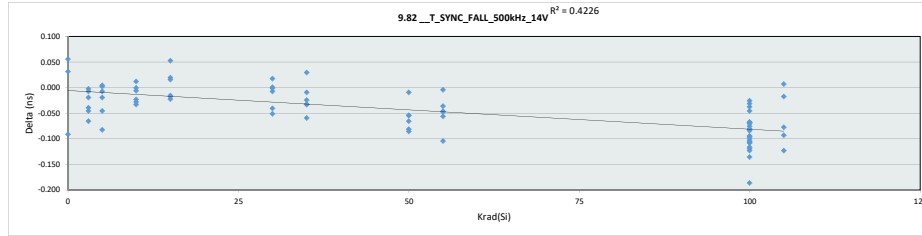
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.290	8.234	8.252	8.240	8.257	8.252	8.201	8.170	8.186	8.156	8.162
Average	8.308	8.286	8.290	8.291	8.296	8.269	8.263	8.225	8.228	8.240	8.234
Max	8.328	8.340	8.354	8.335	8.367	8.310	8.291	8.252	8.254	8.303	8.280
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



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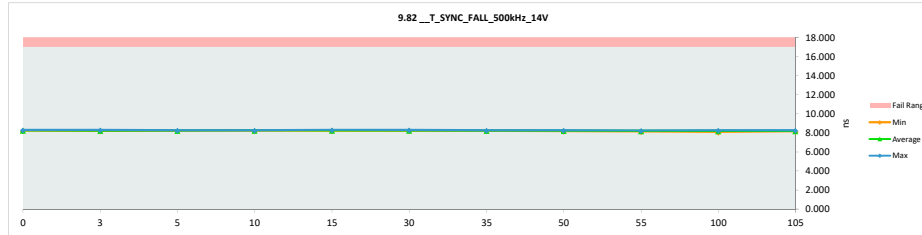
9.82_T_SYNC_FALL_500kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.320	8.230	-0.090
0	118	8.244	8.277	0.033
0	122	8.228	8.285	0.057
3	61	8.265	8.227	-0.038
3	62	8.275	8.211	-0.064
3	63	8.218	8.200	-0.018
3	64	8.252	8.246	-0.006
3	65	8.310	8.309	-0.001
3	66	8.283	8.239	-0.044
5	67	8.240	8.222	-0.018
5	68	8.264	8.270	0.006
5	69	8.260	8.254	-0.006
5	70	8.220	8.223	0.003
5	71	8.304	8.223	-0.081
5	72	8.291	8.247	-0.044
10	73	8.265	8.266	0.001
10	74	8.275	8.248	-0.027
10	75	8.218	8.196	-0.022
10	76	8.252	8.265	0.013
10	77	8.310	8.278	-0.032
10	78	8.283	8.278	-0.005
15	79	8.240	8.226	-0.014
15	80	8.264	8.285	0.021
15	81	8.260	8.314	0.054
15	82	8.220	8.237	0.017
15	83	8.304	8.283	-0.021
15	84	8.291	8.275	-0.016
30	86	8.265	8.284	0.019
30	89	8.275	8.236	-0.039
30	91	8.218	8.218	0.000
30	92	8.252	8.246	-0.006
30	94	8.310	8.312	0.002
30	95	8.283	8.233	-0.050
35	100	8.240	8.232	-0.008
35	101	8.264	8.233	-0.031
35	102	8.260	8.237	-0.023
35	103	8.220	8.251	0.031
35	104	8.304	8.246	-0.058
35	105	8.291	8.259	-0.032
50	106	8.294	8.210	-0.084
50	107	8.248	8.240	-0.008
50	108	8.353	8.273	-0.080
50	109	8.205	8.152	-0.053
50	110	8.296	8.243	-0.053
50	111	8.243	8.179	-0.064
55	113	8.291	8.236	-0.055
55	114	8.245	8.142	-0.103
55	115	8.216	8.171	-0.045
55	116	8.247	8.212	-0.035
55	126	8.248	8.245	-0.003
55	128	8.231	8.185	-0.046
100	129	8.343	8.237	-0.106
100	131	8.306	8.213	-0.093
100	132	8.300	8.231	-0.069
100	133	8.354	8.253	-0.101
100	134	8.352	8.218	-0.134
100	135	8.252	8.145	-0.107
100	138	8.247	8.173	-0.074
100	139	8.253	8.149	-0.104
100	140	8.301	8.277	-0.024
100	141	8.264	8.079	-0.185
100	142	8.225	8.189	-0.036
100	143	8.223	8.179	-0.044
100	144	8.227	8.197	-0.030
100	220	8.261	8.182	-0.079
100	221	8.269	8.147	-0.122
100	224	8.290	8.210	-0.080
100	225	8.351	8.257	-0.094
100	226	8.296	8.178	-0.118
100	228	8.294	8.228	-0.066
100	232	8.315	8.218	-0.097
100	233	8.289	8.222	-0.067
100	236	8.293	8.210	-0.083
100	238	8.274	8.208	-0.066
100	243	8.323	8.208	-0.115
105	244	8.276	8.200	-0.076
105	246	8.297	8.175	-0.122
105	248	8.261	8.245	-0.016
105	374	8.245	8.253	0.008
105	376	8.291	8.199	-0.092
	Max	8.354	8.314	0.057
	Average	8.272	8.227	-0.044
	Min	8.205	8.079	-0.185
	Std Dev	0.036	0.043	0.047



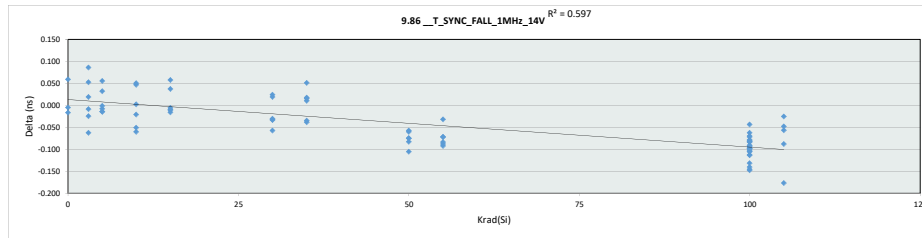
9.82_T_SYNC_FALL_500kHz	
Test Site	
Tester	
Test Number	
Max Limit	17
Min Limit	ns

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.230	8.200	8.222	8.196	8.226	8.218	8.232	8.152	8.142	8.079	8.175
Average	8.264	8.239	8.240	8.255	8.270	8.255	8.243	8.216	8.199	8.200	8.214
Max	8.285	8.309	8.270	8.278	8.314	8.312	8.259	8.273	8.245	8.277	8.253
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

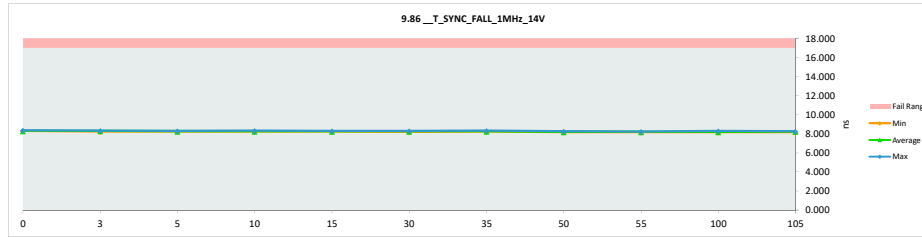


LDR TID Report
TPS7H5001-SP

9.86_T_SYNC_FALL_1MHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.288	8.285	-0.003
0	118	8.302	8.363	0.061
0	122	8.306	8.291	-0.015
3	61	8.256	8.310	0.054
3	62	8.215	8.303	0.088
3	63	8.246	8.223	-0.023
3	64	8.318	8.257	-0.061
3	65	8.318	8.339	0.021
3	66	8.314	8.307	-0.007
5	67	8.242	8.242	0.000
5	68	8.294	8.288	-0.006
5	69	8.274	8.261	-0.013
5	70	8.266	8.253	-0.013
5	71	8.252	8.286	0.034
5	72	8.224	8.281	0.057
10	73	8.256	8.304	0.048
10	74	8.215	8.267	0.052
10	75	8.246	8.227	-0.019
10	76	8.318	8.259	-0.059
10	77	8.318	8.322	0.004
10	78	8.314	8.265	-0.049
15	79	8.242	8.233	-0.009
15	80	8.294	8.286	-0.008
15	81	8.274	8.270	-0.004
15	82	8.266	8.252	-0.014
15	83	8.252	8.291	0.039
15	84	8.224	8.283	0.059
30	86	8.256	8.282	0.026
30	89	8.215	8.236	0.021
30	91	8.246	8.214	-0.032
30	92	8.318	8.289	-0.029
30	94	8.318	8.287	-0.031
30	95	8.314	8.258	-0.056
35	100	8.242	8.254	0.012
35	101	8.294	8.347	0.053
35	102	8.274	8.241	-0.033
35	103	8.266	8.229	-0.037
35	104	8.252	8.269	0.017
35	105	8.224	8.243	0.019
50	106	8.288	8.229	-0.059
50	107	8.305	8.232	-0.073
50	108	8.287	8.231	-0.056
50	109	8.238	8.157	-0.081
50	110	8.364	8.260	-0.104
50	111	8.253	8.180	-0.073
55	113	8.311	8.241	-0.070
55	114	8.295	8.213	-0.082
55	115	8.263	8.177	-0.086
55	116	8.303	8.212	-0.091
55	126	8.288	8.218	-0.070
55	128	8.236	8.206	-0.030
100	129	8.332	8.250	-0.082
100	131	8.332	8.228	-0.104
100	132	8.333	8.194	-0.139
100	133	8.361	8.281	-0.080
100	134	8.321	8.210	-0.111
100	135	8.260	8.193	-0.067
100	138	8.271	8.174	-0.097
100	139	8.296	8.228	-0.068
100	140	8.336	8.255	-0.081
100	141	8.310	8.219	-0.091
100	142	8.253	8.183	-0.070
100	143	8.262	8.165	-0.097
100	144	8.289	8.177	-0.112
100	220	8.280	8.177	-0.103
100	221	8.252	8.163	-0.089
100	224	8.324	8.228	-0.096
100	225	8.366	8.305	-0.061
100	226	8.323	8.231	-0.092
100	228	8.280	8.238	-0.042
100	232	8.323	8.223	-0.100
100	233	8.361	8.215	-0.146
100	236	8.328	8.198	-0.130
100	238	8.349	8.205	-0.144
100	243	8.287	8.208	-0.079
105	244	8.289	8.234	-0.055
105	246	8.272	8.226	-0.046
105	248	8.334	8.159	-0.175
105	374	8.286	8.262	-0.024
105	376	8.321	8.235	-0.086
Max	8.366	8.363	0.088	
Average	8.286	8.244	-0.043	
Min	8.215	8.157	-0.175	
Std Dev	0.038	0.045	0.057	

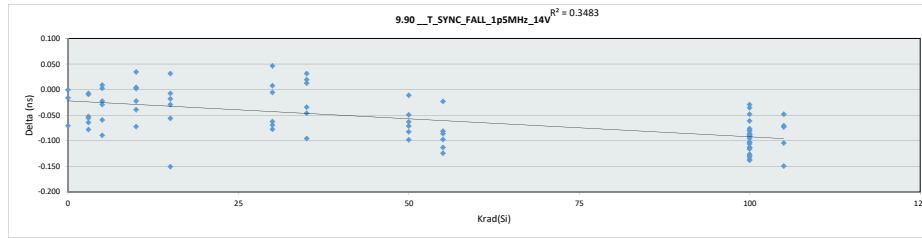


9.86_T_SYNC_FALL_1MHz_14											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.285	8.223	8.242	8.227	8.233	8.214	8.229	8.157	8.177	8.163	8.159
Average	8.313	8.290	8.269	8.274	8.269	8.261	8.264	8.215	8.211	8.214	8.223
Max	8.363	8.339	8.288	8.322	8.291	8.289	8.347	8.260	8.241	8.305	8.262
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

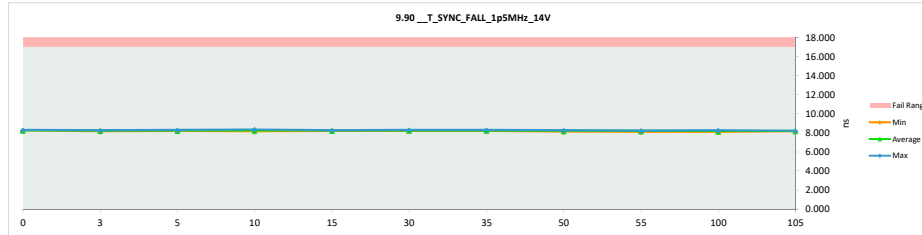


LDR TID Report
TPS7H5001-SP

9.90_T_SYNC_FALL_1p5MHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.243	8.244	0.001
0	118	8.306	8.291	-0.015
0	122	8.330	8.261	-0.069
3	61	8.316	8.262	-0.054
3	62	8.314	8.251	-0.063
3	63	8.166	8.160	-0.006
3	64	8.230	8.222	-0.008
3	65	8.315	8.264	-0.051
3	66	8.316	8.239	-0.077
5	67	8.260	8.202	-0.058
5	68	8.274	8.284	0.010
5	69	8.217	8.196	-0.021
5	70	8.354	8.266	-0.088
5	71	8.235	8.239	0.004
5	72	8.284	8.256	-0.028
10	73	8.316	8.245	-0.071
10	74	8.314	8.293	-0.021
10	75	8.166	8.172	0.006
10	76	8.230	8.233	0.003
10	77	8.315	8.351	0.036
10	78	8.316	8.278	-0.038
15	79	8.260	8.205	-0.055
15	80	8.274	8.268	-0.006
15	81	8.217	8.200	-0.017
15	82	8.354	8.205	-0.149
15	83	8.235	8.268	0.033
15	84	8.284	8.256	-0.028
30	86	8.316	8.240	-0.076
30	89	8.314	8.253	-0.061
30	91	8.166	8.214	0.048
30	92	8.230	8.239	0.009
30	94	8.315	8.311	-0.004
30	95	8.316	8.248	-0.068
35	100	8.260	8.215	-0.045
35	101	8.274	8.307	0.033
35	102	8.217	8.231	0.014
35	103	8.354	8.260	-0.094
35	104	8.235	8.202	-0.033
35	105	8.284	8.305	0.021
50	106	8.281	8.200	-0.081
50	107	8.300	8.252	-0.048
50	108	8.304	8.207	-0.097
50	109	8.217	8.207	-0.010
50	110	8.326	8.264	-0.062
50	111	8.215	8.145	-0.070
55	113	8.232	8.210	-0.022
55	114	8.211	8.099	-0.112
55	115	8.269	8.173	-0.096
55	116	8.256	8.176	-0.080
55	126	8.361	8.238	-0.123
55	128	8.285	8.200	-0.085
100	129	8.343	8.213	-0.130
100	131	8.301	8.207	-0.094
100	132	8.322	8.197	-0.125
100	133	8.350	8.238	-0.112
100	134	8.320	8.234	-0.086
100	135	8.224	8.134	-0.090
100	138	8.330	8.193	-0.137
100	139	8.205	8.130	-0.075
100	140	8.266	8.187	-0.079
100	141	8.216	8.104	-0.112
100	142	8.281	8.197	-0.084
100	143	8.218	8.184	-0.034
100	144	8.264	8.129	-0.135
100	220	8.283	8.155	-0.128
100	221	8.204	8.099	-0.105
100	224	8.260	8.172	-0.088
100	225	8.314	8.267	-0.047
100	226	8.289	8.174	-0.115
100	228	8.266	8.238	-0.028
100	232	8.254	8.152	-0.102
100	233	8.250	8.190	-0.060
100	236	8.245	8.155	-0.090
100	238	8.243	8.142	-0.101
100	243	8.247	8.169	-0.078
105	244	8.258	8.189	-0.069
105	246	8.223	8.176	-0.047
105	248	8.338	8.190	-0.148
105	374	8.287	8.215	-0.072
105	376	8.275	8.172	-0.103
Max		8.361	8.351	0.048
Average		8.273	8.215	-0.058
Min		8.166	8.099	-0.149
Std Dev		0.047	0.051	0.048

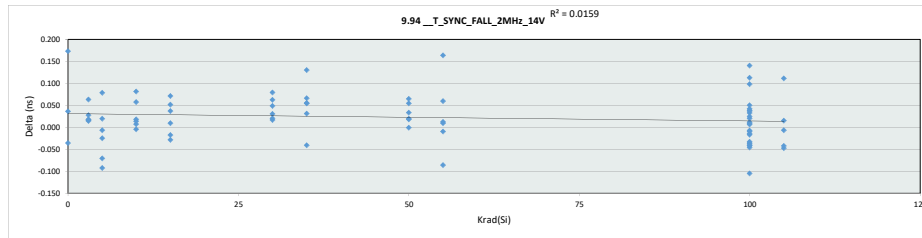


9.90_T_SYNC_FALL_1p5MHz											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.244	8.160	8.196	8.172	8.200	8.214	8.202	8.145	8.099	8.099	8.172
Average	8.265	8.233	8.241	8.262	8.234	8.251	8.253	8.213	8.183	8.178	8.188
Max	8.291	8.264	8.284	8.351	8.268	8.311	8.307	8.264	8.238	8.267	8.215
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

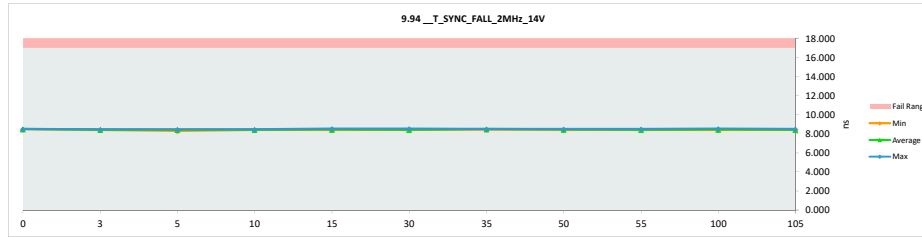


LDR TID Report
TPS7H5001-SP

9.94_T_SYNC_FALL_2MHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.330	8.505	0.175
0	118	8.535	8.501	-0.034
0	122	8.444	8.482	0.038
3	61	8.448	8.464	0.016
3	62	8.432	8.461	0.029
3	63	8.373	8.391	0.018
3	64	8.366	8.386	0.020
3	65	8.376	8.441	0.065
3	66	8.397	8.415	0.018
5	67	8.448	8.425	-0.023
5	68	8.539	8.470	-0.069
5	69	8.410	8.405	-0.005
5	70	8.357	8.437	0.080
5	71	8.395	8.304	-0.091
5	72	8.446	8.467	0.021
10	73	8.448	8.463	0.015
10	74	8.432	8.429	-0.003
10	75	8.373	8.393	0.020
10	76	8.366	8.425	0.059
10	77	8.376	8.459	0.083
10	78	8.397	8.406	0.009
15	79	8.448	8.459	0.011
15	80	8.539	8.523	-0.016
15	81	8.410	8.383	-0.027
15	82	8.357	8.430	0.073
15	83	8.395	8.434	0.039
15	84	8.446	8.499	0.053
30	86	8.448	8.529	0.081
30	89	8.432	8.450	0.018
30	91	8.373	8.437	0.064
30	92	8.366	8.416	0.050
30	94	8.376	8.398	0.022
30	95	8.397	8.429	0.032
35	100	8.448	8.505	0.057
35	101	8.539	8.500	-0.039
35	102	8.410	8.443	0.033
35	103	8.357	8.489	0.132
35	104	8.395	8.451	0.056
35	105	8.446	8.514	0.068
50	106	8.484	8.505	0.021
50	107	8.449	8.505	0.056
50	108	8.395	8.461	0.066
50	109	8.430	8.431	0.001
50	110	8.490	8.510	0.020
50	111	8.351	8.386	0.035
55	113	8.330	8.495	0.165
55	114	8.385	8.396	0.011
55	115	8.394	8.455	0.061
55	116	8.442	8.456	0.014
55	126	8.531	8.447	-0.084
55	128	8.431	8.423	-0.008
100	129	8.552	8.512	-0.040
100	131	8.414	8.448	0.034
100	132	8.429	8.543	0.114
100	133	8.535	8.548	0.013
100	134	8.470	8.492	0.022
100	135	8.375	8.401	0.026
100	138	8.488	8.528	0.040
100	139	8.491	8.451	-0.040
100	140	8.448	8.458	0.010
100	141	8.387	8.395	0.008
100	142	8.559	8.456	-0.103
100	143	8.458	8.422	-0.036
100	144	8.462	8.454	-0.008
100	220	8.483	8.449	-0.034
100	221	8.426	8.413	-0.013
100	224	8.457	8.451	-0.006
100	225	8.515	8.500	-0.015
100	226	8.478	8.522	0.044
100	228	8.510	8.549	0.039
100	232	8.537	8.506	-0.031
100	233	8.348	8.448	0.100
100	236	8.400	8.452	0.052
100	238	8.351	8.493	0.142
100	243	8.453	8.409	-0.044
105	244	8.502	8.461	-0.041
105	246	8.389	8.406	0.017
105	248	8.383	8.496	0.113
105	374	8.451	8.446	-0.005
105	376	8.432	8.386	-0.046
Max		8.559	8.549	0.175
Average		8.432	8.455	0.023
Min		8.330	8.304	-0.103
Std Dev		0.058	0.046	0.053

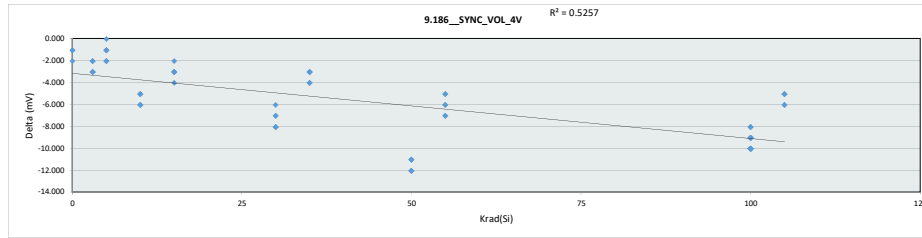


9.94_T_SYNC_FALL_2MHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.482	8.386	8.304	8.393	8.383	8.398	8.443	8.386	8.396	8.395	8.386
Average	8.496	8.426	8.418	8.429	8.455	8.443	8.484	8.466	8.445	8.471	8.439
Max	8.505	8.464	8.470	8.463	8.523	8.529	8.514	8.510	8.495	8.549	8.496
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

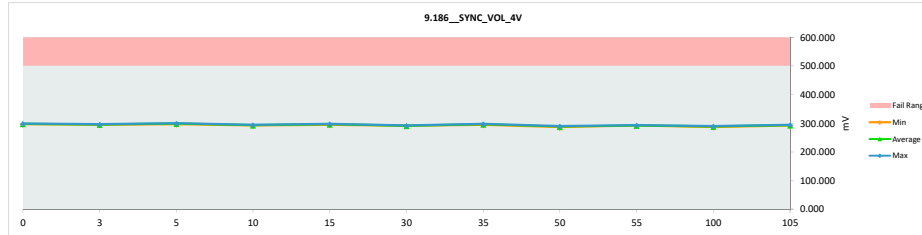


LDR TID Report
TPS7H5001-SP

9.186_SYNC_VOL_4V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	500	500		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	300.000	299.000	-1.000
0	118	300.000	299.000	-1.000
0	122	299.000	297.000	-2.000
3	61	299.000	296.000	-3.000
3	62	300.000	297.000	-3.000
3	63	299.000	296.000	-3.000
3	64	299.000	296.000	-3.000
3	65	297.000	295.000	-2.000
3	66	299.000	297.000	-2.000
5	67	300.000	300.000	0.000
5	68	301.000	300.000	-1.000
5	69	299.000	298.000	-1.000
5	70	301.000	299.000	-2.000
5	71	298.000	297.000	-1.000
5	72	302.000	300.000	-2.000
10	73	299.000	293.000	-6.000
10	74	300.000	295.000	-5.000
10	75	299.000	293.000	-6.000
10	76	299.000	294.000	-5.000
10	77	297.000	292.000	-5.000
10	78	299.000	293.000	-6.000
15	79	300.000	298.000	-2.000
15	80	301.000	298.000	-3.000
15	81	299.000	296.000	-3.000
15	82	301.000	298.000	-3.000
15	83	298.000	295.000	-3.000
15	84	302.000	298.000	-4.000
30	86	299.000	291.000	-8.000
30	89	300.000	292.000	-8.000
30	91	299.000	292.000	-7.000
30	92	299.000	292.000	-7.000
30	94	297.000	291.000	-6.000
30	95	299.000	291.000	-8.000
35	100	300.000	297.000	-3.000
35	101	301.000	298.000	-3.000
35	102	299.000	296.000	-3.000
35	103	301.000	298.000	-3.000
35	104	298.000	294.000	-4.000
35	105	302.000	298.000	-4.000
50	106	298.000	286.000	-12.000
50	107	300.000	289.000	-11.000
50	108	301.000	290.000	-11.000
50	109	299.000	288.000	-11.000
50	110	301.000	289.000	-12.000
50	111	299.000	287.000	-12.000
55	113	299.000	293.000	-6.000
55	114	299.000	292.000	-7.000
55	115	298.000	292.000	-6.000
55	116	299.000	292.000	-7.000
55	125	297.000	292.000	-5.000
55	128	298.000	293.000	-5.000
100	129	296.000	287.000	-9.000
100	131	299.000	289.000	-10.000
100	132	299.000	290.000	-9.000
100	133	298.000	288.000	-10.000
100	134	299.000	290.000	-9.000
100	135	297.000	287.000	-10.000
100	138	297.000	288.000	-9.000
100	139	298.000	289.000	-9.000
100	140	299.000	289.000	-10.000
100	141	298.000	288.000	-10.000
100	142	298.000	288.000	-10.000
100	143	297.000	289.000	-8.000
100	144	297.000	289.000	-8.000
100	220	300.000	290.000	-10.000
100	221	298.000	288.000	-10.000
100	224	298.000	290.000	-8.000
100	225	299.000	290.000	-9.000
100	226	298.000	288.000	-10.000
100	228	298.000	288.000	-10.000
100	232	299.000	290.000	-9.000
100	233	300.000	290.000	-10.000
100	236	297.000	287.000	-10.000
100	238	298.000	289.000	-9.000
100	243	298.000	288.000	-10.000
105	244	299.000	293.000	-6.000
105	246	300.000	295.000	-5.000
105	248	299.000	294.000	-5.000
105	374	297.000	292.000	-5.000
105	376	301.000	295.000	-6.000
Max		302.000	300.000	0.000
Average		299.000	292.750	-6.250
Min		296.000	286.000	-12.000
Std Dev		1.331	3.957	3.320

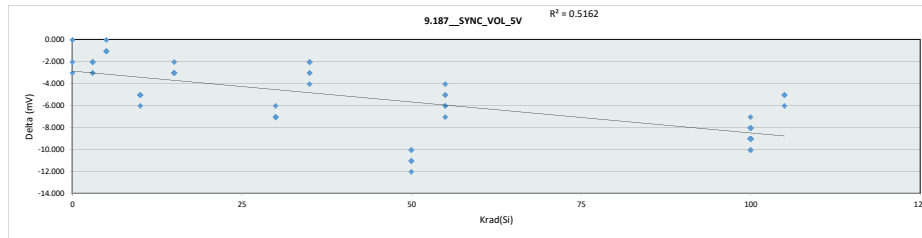


9.186_SYNC_VOL_4V											
Test Site											
Tester											
Test Number											
Max Limit	500	mV									
Min Limit		mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	297.000	295.000	297.000	292.000	295.000	291.000	294.000	286.000	292.000	287.000	292.000
Average	298.333	296.167	299.000	293.333	297.167	291.500	296.833	288.167	292.333	288.708	293.800
Max	299.000	297.000	300.000	295.000	298.000	292.000	298.000	290.000	293.000	290.000	295.000
UL	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000

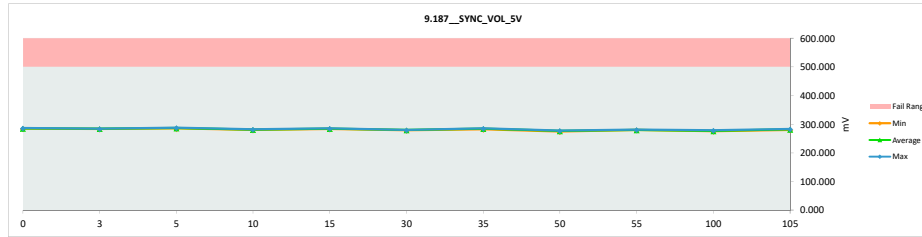


LDR TID Report
TPS7H5001-SP

9.187_SYNC_VOL_5V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	500	500		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	288.000	286.000	-2.000
0	118	287.000	287.000	0.000
0	122	287.000	284.000	-3.000
3	61	286.000	284.000	-2.000
3	62	287.000	285.000	-2.000
3	63	287.000	284.000	-3.000
3	64	287.000	284.000	-3.000
3	65	285.000	283.000	-2.000
3	66	286.000	284.000	-2.000
5	67	288.000	287.000	-1.000
5	68	288.000	287.000	-1.000
5	69	286.000	286.000	0.000
5	70	288.000	287.000	-1.000
5	71	286.000	285.000	-1.000
5	72	289.000	288.000	-1.000
10	73	286.000	281.000	-5.000
10	74	287.000	282.000	-5.000
10	75	287.000	281.000	-6.000
10	76	287.000	282.000	-5.000
10	77	285.000	280.000	-5.000
10	78	286.000	281.000	-5.000
15	79	288.000	285.000	-3.000
15	80	288.000	285.000	-3.000
15	81	286.000	284.000	-2.000
15	82	288.000	285.000	-3.000
15	83	286.000	283.000	-3.000
15	84	289.000	286.000	-3.000
30	86	286.000	279.000	-7.000
30	89	287.000	280.000	-7.000
30	91	287.000	280.000	-7.000
30	92	287.000	280.000	-7.000
30	94	285.000	279.000	-6.000
30	95	286.000	279.000	-7.000
35	100	288.000	286.000	-2.000
35	101	288.000	285.000	-3.000
35	102	286.000	284.000	-2.000
35	103	288.000	286.000	-2.000
35	104	286.000	282.000	-4.000
35	105	289.000	286.000	-3.000
50	106	286.000	274.000	-12.000
50	107	288.000	278.000	-10.000
50	108	289.000	278.000	-11.000
50	109	287.000	276.000	-11.000
50	110	288.000	277.000	-11.000
50	111	286.000	276.000	-10.000
55	113	286.000	281.000	-5.000
55	114	286.000	280.000	-6.000
55	115	286.000	280.000	-6.000
55	116	287.000	280.000	-7.000
55	126	285.000	280.000	-5.000
55	128	285.000	281.000	-4.000
100	129	284.000	276.000	-8.000
100	131	286.000	278.000	-8.000
100	132	286.000	278.000	-8.000
100	133	285.000	276.000	-9.000
100	134	287.000	278.000	-9.000
100	135	284.000	275.000	-9.000
100	138	284.000	276.000	-8.000
100	139	286.000	278.000	-8.000
100	140	286.000	277.000	-9.000
100	141	286.000	277.000	-9.000
100	142	286.000	276.000	-10.000
100	143	285.000	277.000	-8.000
100	144	285.000	277.000	-8.000
100	220	288.000	279.000	-9.000
100	221	286.000	276.000	-10.000
100	224	286.000	279.000	-7.000
100	225	287.000	278.000	-9.000
100	226	285.000	276.000	-9.000
100	228	286.000	277.000	-9.000
100	232	287.000	278.000	-9.000
100	233	288.000	279.000	-9.000
100	236	285.000	275.000	-10.000
100	238	286.000	277.000	-9.000
100	243	286.000	277.000	-9.000
105	244	287.000	281.000	-6.000
105	246	288.000	283.000	-5.000
105	248	287.000	282.000	-5.000
105	374	285.000	280.000	-5.000
105	376	288.000	283.000	-5.000
Max		289.000	288.000	0.000
Average		286.563	280.775	-5.788
Min		284.000	274.000	-12.000
Std Dev		1.210	3.684	3.161

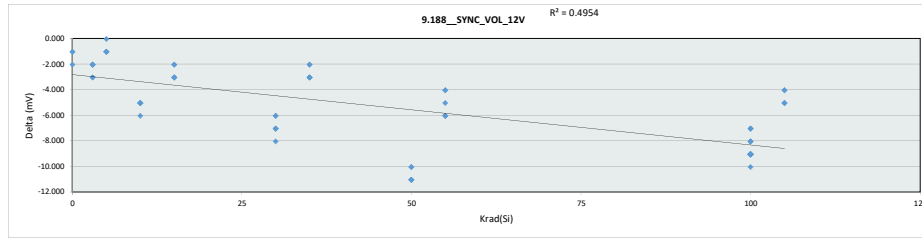


9.187_SYNC_VOL_5V											
Test Site											
Tester											
Test Number											
Max Limit	500	mV									
Min Limit		mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	284.000	283.000	285.000	280.000	283.000	279.000	282.000	274.000	280.000	275.000	280.000
Average	285.667	284.000	286.667	281.167	284.667	279.500	284.833	276.500	280.333	277.083	281.800
Max	287.000	285.000	288.000	282.000	286.000	280.000	286.000	278.000	281.000	279.000	283.000
UL	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000

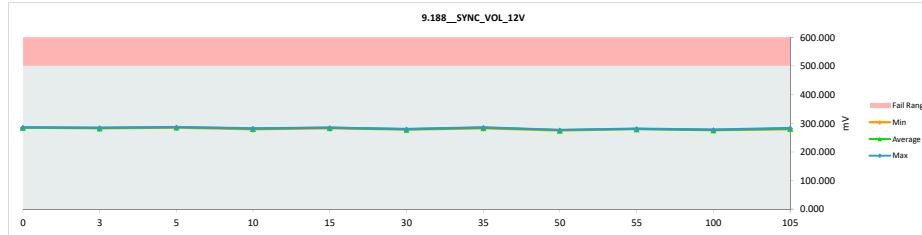


LDR TID Report
TPS7H5001-SP

9.188_SYNC_VOL_12V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	500	500		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	287.000	286.000	-1.000
0	118	287.000	286.000	-1.000
0	122	286.000	284.000	-2.000
3	61	286.000	284.000	-2.000
3	62	287.000	284.000	-3.000
3	63	286.000	283.000	-3.000
3	64	286.000	284.000	-2.000
3	65	284.000	282.000	-2.000
3	66	286.000	284.000	-2.000
5	67	287.000	287.000	0.000
5	68	288.000	287.000	-1.000
5	69	286.000	285.000	-1.000
5	70	288.000	287.000	-1.000
5	71	285.000	284.000	-1.000
5	72	288.000	287.000	-1.000
10	73	286.000	280.000	-6.000
10	74	287.000	282.000	-5.000
10	75	286.000	281.000	-5.000
10	76	286.000	281.000	-5.000
10	77	284.000	279.000	-5.000
10	78	286.000	281.000	-5.000
15	79	287.000	285.000	-2.000
15	80	288.000	285.000	-3.000
15	81	286.000	284.000	-2.000
15	82	288.000	285.000	-3.000
15	83	285.000	282.000	-3.000
15	84	288.000	285.000	-3.000
30	86	286.000	279.000	-7.000
30	89	287.000	280.000	-7.000
30	91	286.000	280.000	-6.000
30	92	286.000	279.000	-7.000
30	94	284.000	278.000	-6.000
30	95	286.000	278.000	-8.000
35	100	287.000	285.000	-2.000
35	101	288.000	285.000	-3.000
35	102	286.000	283.000	-3.000
35	103	288.000	286.000	-2.000
35	104	285.000	282.000	-3.000
35	105	288.000	285.000	-3.000
50	106	285.000	274.000	-11.000
50	107	287.000	277.000	-10.000
50	108	288.000	277.000	-11.000
50	109	286.000	276.000	-10.000
50	110	288.000	277.000	-11.000
50	111	286.000	275.000	-11.000
55	113	285.000	280.000	-5.000
55	114	286.000	280.000	-6.000
55	115	286.000	280.000	-6.000
55	116	286.000	280.000	-6.000
55	125	284.000	280.000	-4.000
55	128	285.000	281.000	-4.000
100	129	283.000	275.000	-8.000
100	131	286.000	277.000	-9.000
100	132	286.000	277.000	-9.000
100	133	285.000	276.000	-9.000
100	134	286.000	277.000	-9.000
100	135	284.000	275.000	-9.000
100	138	284.000	276.000	-8.000
100	139	286.000	277.000	-9.000
100	140	286.000	277.000	-9.000
100	141	285.000	276.000	-9.000
100	142	286.000	276.000	-10.000
100	143	284.000	277.000	-7.000
100	144	284.000	277.000	-7.000
100	220	287.000	278.000	-9.000
100	221	285.000	276.000	-9.000
100	224	285.000	278.000	-7.000
100	225	286.000	277.000	-9.000
100	226	285.000	276.000	-9.000
100	228	285.000	276.000	-9.000
100	232	286.000	278.000	-8.000
100	233	287.000	278.000	-9.000
100	236	284.000	275.000	-9.000
100	238	286.000	277.000	-9.000
100	243	285.000	276.000	-9.000
105	244	286.000	281.000	-5.000
105	246	287.000	283.000	-4.000
105	248	286.000	282.000	-4.000
105	374	284.000	279.000	-5.000
105	376	287.000	282.000	-5.000
	Max	288.000	287.000	0.000
	Average	285.988	280.325	-5.663
	Min	283.000	274.000	-11.000
	Std Dev	1.227	3.697	3.150

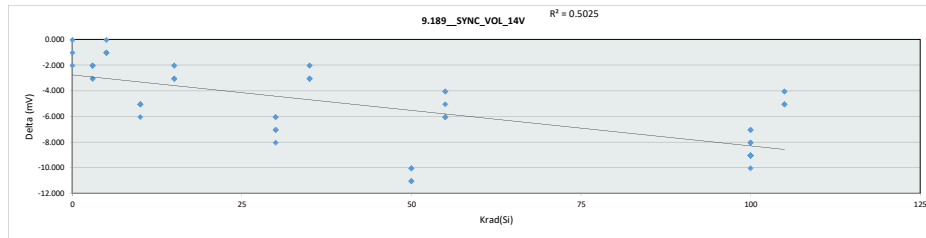


9.188_SYNC_VOL_12V											
Test Site											
Tester											
Test Number											
Max Limit	500	mV									
Min Limit		mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	284.000	282.000	284.000	279.000	282.000	278.000	282.000	274.000	280.000	275.000	279.000
Average	285.333	283.500	286.167	280.667	284.333	279.000	284.333	276.000	280.167	276.583	281.400
Max	286.000	284.000	287.000	282.000	285.000	280.000	286.000	277.000	281.000	278.000	283.000
UL	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000

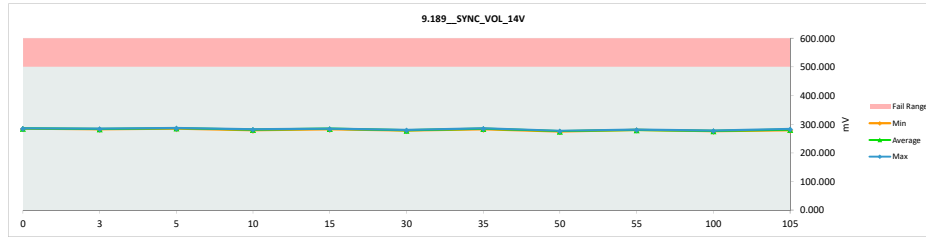


LDR TID Report
TPS7H5001-SP

9.189_SYNC_VOL_14V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	500	500		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	287.000	286.000	-1.000
0	118	286.000	286.000	0.000
0	122	286.000	284.000	-2.000
3	61	286.000	284.000	-2.000
3	62	287.000	284.000	-3.000
3	63	286.000	283.000	-3.000
3	64	286.000	284.000	-2.000
3	65	284.000	282.000	-2.000
3	66	286.000	284.000	-2.000
5	67	287.000	287.000	0.000
5	68	288.000	287.000	-1.000
5	69	286.000	285.000	-1.000
5	70	288.000	287.000	-1.000
5	71	285.000	284.000	-1.000
5	72	288.000	287.000	-1.000
10	73	286.000	280.000	-6.000
10	74	287.000	282.000	-5.000
10	75	286.000	281.000	-5.000
10	76	286.000	281.000	-5.000
10	77	284.000	279.000	-5.000
10	78	286.000	281.000	-5.000
15	79	287.000	285.000	-2.000
15	80	288.000	285.000	-3.000
15	81	286.000	284.000	-2.000
15	82	288.000	285.000	-3.000
15	83	285.000	282.000	-3.000
15	84	288.000	285.000	-3.000
30	86	286.000	279.000	-7.000
30	89	287.000	280.000	-7.000
30	91	286.000	280.000	-6.000
30	92	286.000	279.000	-7.000
30	94	284.000	278.000	-6.000
30	95	286.000	278.000	-8.000
35	100	287.000	285.000	-2.000
35	101	288.000	285.000	-3.000
35	102	286.000	283.000	-3.000
35	103	288.000	286.000	-2.000
35	104	285.000	282.000	-3.000
35	105	288.000	285.000	-3.000
50	106	285.000	274.000	-11.000
50	107	287.000	277.000	-10.000
50	108	288.000	277.000	-11.000
50	109	286.000	276.000	-10.000
50	110	287.000	277.000	-10.000
50	111	286.000	275.000	-11.000
55	113	285.000	280.000	-5.000
55	114	286.000	280.000	-6.000
55	115	286.000	280.000	-6.000
55	116	286.000	280.000	-6.000
55	126	284.000	280.000	-4.000
55	128	285.000	281.000	-4.000
100	129	283.000	275.000	-8.000
100	131	286.000	277.000	-9.000
100	132	286.000	277.000	-9.000
100	133	285.000	276.000	-9.000
100	134	286.000	277.000	-9.000
100	135	284.000	275.000	-9.000
100	138	284.000	276.000	-8.000
100	139	286.000	277.000	-9.000
100	140	286.000	277.000	-9.000
100	141	285.000	276.000	-9.000
100	142	286.000	276.000	-10.000
100	143	284.000	277.000	-7.000
100	144	284.000	277.000	-7.000
100	220	287.000	278.000	-9.000
100	221	285.000	276.000	-9.000
100	224	285.000	278.000	-7.000
100	225	286.000	277.000	-9.000
100	226	285.000	276.000	-9.000
100	228	285.000	276.000	-9.000
100	232	286.000	278.000	-8.000
100	233	287.000	278.000	-9.000
100	236	284.000	275.000	-9.000
100	238	286.000	277.000	-9.000
100	243	285.000	276.000	-9.000
105	244	286.000	281.000	-5.000
105	246	287.000	283.000	-4.000
105	248	286.000	282.000	-4.000
105	374	284.000	279.000	-5.000
105	376	287.000	282.000	-5.000
	Max	288.000	287.000	0.000
	Average	285.963	280.325	-5.638
	Min	283.000	274.000	-11.000
	Std Dev	1.206	3.697	3.151

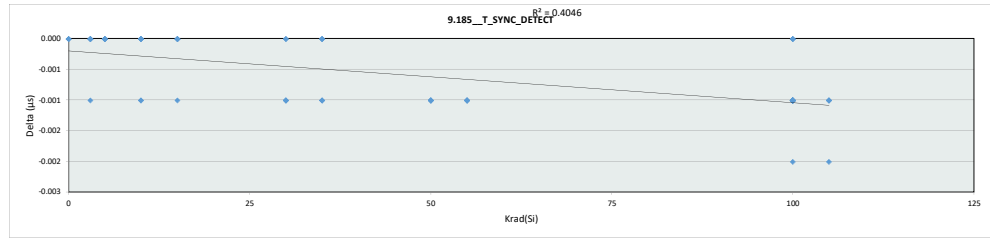


9.189_SYNC_VOL_14V											
Test Site											
Tester											
Test Number											
Max Limit	500	mV									
Min Limit		mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	284.000	282.000	284.000	279.000	282.000	278.000	282.000	274.000	280.000	275.000	279.000
Average	285.333	283.500	286.167	280.667	284.333	279.000	284.333	276.000	280.167	276.583	281.400
Max	286.000	284.000	287.000	282.000	285.000	280.000	286.000	277.000	281.000	278.000	283.000
UL	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000

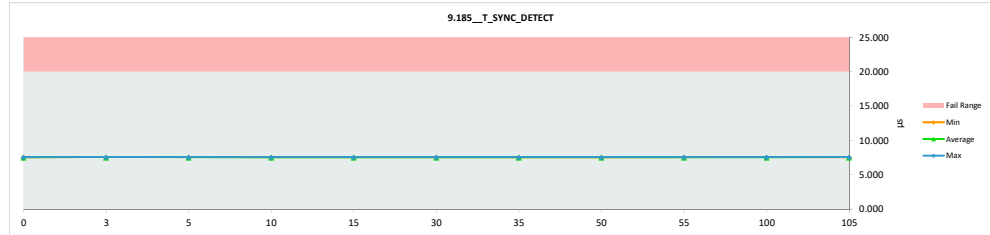


LDR TID Report
TPS7H5001-SP

9.185_T_SYNC_DETECT				
Test Site				
Tester				
Test Number				
Unit	µs	µs		
Max Limit	20	20		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	7.587	7.587	0.000
0	118	7.586	7.586	0.000
0	122	7.587	7.587	0.000
3	61	7.588	7.588	0.000
3	62	7.587	7.587	0.000
3	63	7.587	7.587	0.000
3	64	7.588	7.587	-0.001
3	65	7.587	7.587	0.000
3	66	7.588	7.588	0.000
5	67	7.587	7.587	0.000
5	68	7.586	7.586	0.000
5	69	7.587	7.587	0.000
5	70	7.587	7.587	0.000
5	71	7.588	7.588	0.000
5	72	7.588	7.588	0.000
10	73	7.588	7.588	0.000
10	74	7.587	7.587	0.000
10	75	7.587	7.587	0.000
10	76	7.588	7.587	-0.001
10	77	7.587	7.587	0.000
10	78	7.588	7.587	-0.001
15	79	7.587	7.587	0.000
15	80	7.586	7.586	0.000
15	81	7.587	7.587	0.000
15	82	7.587	7.587	0.000
15	83	7.588	7.588	0.000
15	84	7.588	7.587	-0.001
30	86	7.588	7.587	-0.001
30	89	7.587	7.587	0.000
30	91	7.587	7.586	-0.001
30	92	7.588	7.587	-0.001
30	94	7.587	7.586	-0.001
30	95	7.588	7.588	0.000
35	100	7.587	7.587	0.000
35	101	7.586	7.586	0.000
35	102	7.587	7.586	-0.001
35	103	7.587	7.587	0.000
35	104	7.588	7.587	-0.001
35	105	7.588	7.587	-0.001
50	106	7.588	7.587	-0.001
50	107	7.588	7.587	-0.001
50	108	7.586	7.585	-0.001
50	109	7.587	7.586	-0.001
50	110	7.587	7.586	-0.001
50	111	7.588	7.587	-0.001
55	113	7.588	7.587	-0.001
55	114	7.587	7.586	-0.001
55	115	7.588	7.587	-0.001
55	116	7.588	7.587	-0.001
55	126	7.586	7.585	-0.001
55	128	7.588	7.587	-0.001
100	129	7.587	7.587	0.000
100	131	7.586	7.585	-0.001
100	132	7.587	7.586	-0.001
100	133	7.586	7.585	-0.001
100	134	7.588	7.587	-0.001
100	135	7.587	7.586	-0.001
100	138	7.588	7.587	-0.001
100	139	7.585	7.584	-0.001
100	140	7.586	7.585	-0.001
100	141	7.588	7.587	-0.001
100	142	7.586	7.585	-0.001
100	143	7.587	7.585	-0.002
100	144	7.587	7.586	-0.001
100	220	7.587	7.586	-0.001
100	221	7.587	7.586	-0.001
100	224	7.586	7.585	-0.001
100	225	7.587	7.586	-0.001
100	226	7.588	7.587	-0.001
100	228	7.588	7.587	-0.001
100	232	7.586	7.586	0.000
100	233	7.587	7.586	-0.001
100	236	7.586	7.585	-0.001
100	238	7.588	7.588	0.000
100	243	7.587	7.586	-0.001
105	244	7.587	7.585	-0.002
105	246	7.586	7.585	-0.001
105	248	7.587	7.586	-0.001
105	374	7.586	7.585	-0.001
105	376	7.588	7.587	-0.001
	Max	7.588	7.588	0.000
	Average	7.587	7.587	-0.001
	Min	7.585	7.584	-0.002
	Std Dev	0.001	0.001	0.001

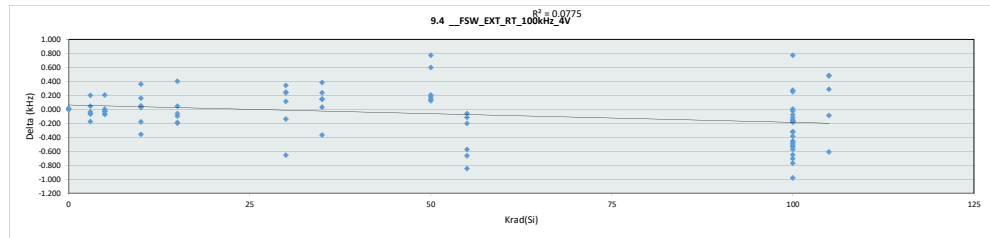


9.185_T_SYNC_DETECT											
Test Site											
Tester											
Test Number											
Max Limit	20	µs									
Min Limit		µs									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.586	7.587	7.586	7.587	7.586	7.586	7.586	7.585	7.585	7.584	7.585
Average	7.587	7.587	7.587	7.587	7.587	7.587	7.587	7.586	7.587	7.586	7.586
Max	7.587	7.588	7.588	7.588	7.588	7.588	7.587	7.587	7.587	7.588	7.587
UL	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000

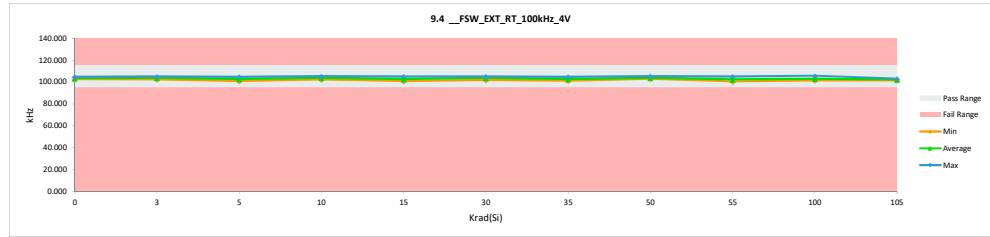


LDR TID Report
TPS7H5001-SP

9.4_FSW_EXT_RT_100kHz_4V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	115	115		
Min Limit	95	95		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	104.640	104.650	0.010
0	118	103.382	103.390	0.008
0	122	102.764	102.792	0.028
3	61	104.230	104.175	-0.055
3	62	104.811	105.021	0.210
3	63	102.449	102.285	-0.164
3	64	103.076	103.047	-0.029
3	65	104.387	104.331	-0.056
3	66	103.906	103.960	0.054
5	67	102.778	102.792	0.014
5	68	102.320	102.304	-0.016
5	69	102.636	102.574	-0.062
5	70	104.512	104.725	0.213
5	71	102.929	102.904	-0.025
5	72	100.854	100.796	-0.058
10	73	104.230	104.287	0.057
10	74	104.811	105.182	0.371
10	75	102.449	102.277	-0.172
10	76	103.076	102.728	-0.348
10	77	104.387	104.556	0.169
10	78	103.906	103.940	0.034
15	79	102.778	102.831	0.053
15	80	102.320	102.132	-0.188
15	81	102.636	102.550	-0.086
15	82	104.512	104.925	0.413
15	83	102.929	102.752	-0.177
15	84	100.854	100.805	-0.049
30	86	104.230	104.475	0.245
30	89	104.811	105.066	0.255
30	91	102.449	101.804	-0.645
30	92	103.076	102.948	-0.128
30	94	104.387	104.740	0.353
30	95	103.906	104.030	0.124
35	100	102.778	102.931	0.153
35	101	102.320	101.963	-0.357
35	102	102.636	103.027	0.391
35	103	104.512	104.668	0.156
35	104	102.929	102.970	0.041
35	105	100.854	101.101	0.247
50	106	103.512	103.728	0.216
50	107	103.243	103.399	0.156
50	108	105.201	105.387	0.186
50	109	103.760	104.367	0.607
50	110	102.999	103.132	0.133
50	111	102.147	102.930	0.783
55	113	101.779	100.939	-0.840
55	114	102.137	101.576	-0.561
55	115	102.736	102.079	-0.657
55	116	100.590	100.483	-0.107
55	126	105.037	104.985	-0.052
55	128	103.920	103.727	-0.193
100	129	102.274	102.533	0.259
100	131	104.163	103.706	-0.457
100	132	104.663	104.022	-0.641
100	133	102.170	102.154	-0.016
100	134	102.823	102.713	-0.110
100	135	102.850	102.476	-0.374
100	138	101.988	102.768	0.780
100	139	102.710	102.012	-0.698
100	140	102.917	102.743	-0.174
100	141	104.554	103.792	-0.762
100	142	102.335	102.619	0.284
100	143	102.129	101.645	-0.484
100	144	101.800	101.487	-0.313
100	220	103.914	102.945	-0.969
100	221	103.711	103.644	-0.067
100	224	102.665	102.102	-0.563
100	225	104.684	104.170	-0.514
100	226	105.891	105.746	-0.145
100	228	102.169	102.182	0.013
100	232	102.740	102.570	-0.170
100	233	101.931	101.782	-0.149
100	236	102.864	102.551	-0.313
100	238	104.019	103.493	-0.526
100	243	103.319	102.874	-0.445
105	244	102.036	101.436	-0.600
105	246	102.537	102.458	-0.079
105	248	102.469	102.957	0.488
105	374	101.129	101.623	0.494
105	376	102.782	103.077	0.295
Max		105.891	105.746	0.783
Average		103.134	103.068	-0.066
Min		100.590	100.483	-0.969
Std Dev		1.122	1.189	0.358

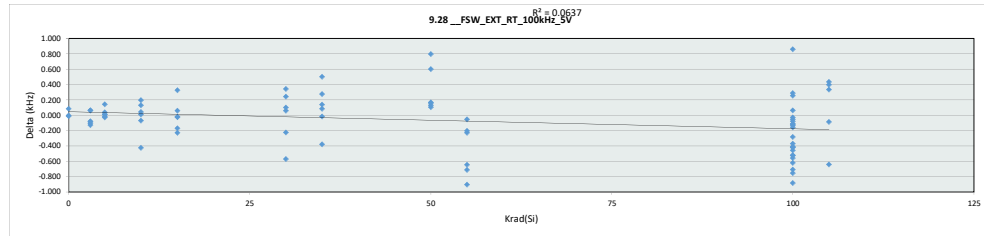


9.4_FSW_EXT_RT_100kHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	115	kHz									
Min Limit	95	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000
Min	102.792	102.285	100.796	102.277	100.805	101.804	101.101	102.930	100.483	101.487	101.436
Average	103.611	103.803	102.683	103.828	102.666	103.844	102.777	103.824	102.298	102.864	103.310
Max	104.650	105.021	104.725	105.182	104.925	105.066	104.668	105.387	104.985	105.746	103.077
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000

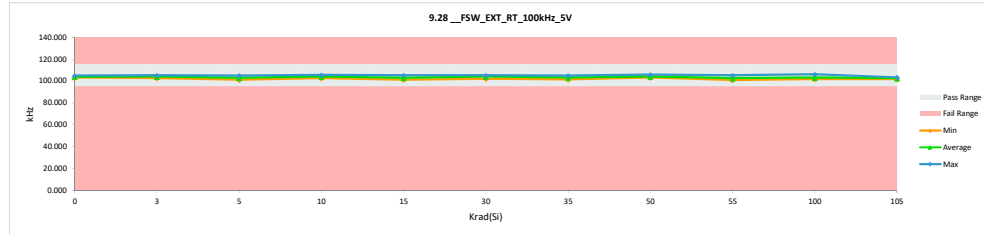


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9.28_FSW_EXT_RT_100kHz_5V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	115	115		
Min Limit	95	95		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	105.035	105.031	-0.004
0	118	103.714	103.720	0.006
0	122	103.134	103.226	0.092
3	61	104.589	104.520	-0.069
3	62	105.327	105.400	0.073
3	63	102.728	102.610	-0.118
3	64	103.503	103.403	-0.100
3	65	104.779	104.702	-0.077
3	66	104.331	104.404	0.073
5	67	103.126	103.145	0.019
5	68	102.663	102.683	0.020
5	69	102.892	102.941	0.049
5	70	104.968	105.120	0.152
5	71	103.389	103.380	-0.009
5	72	101.198	101.178	-0.020
10	73	104.589	104.640	0.051
10	74	105.327	105.534	0.207
10	75	102.728	102.668	-0.060
10	76	103.503	103.086	-0.417
10	77	104.779	104.919	0.140
10	78	104.331	104.349	0.018
15	79	103.126	103.193	0.067
15	80	102.663	102.503	-0.160
15	81	102.892	102.860	-0.032
15	82	104.968	105.303	0.335
15	83	103.389	103.169	-0.220
15	84	101.198	101.184	-0.014
30	86	104.589	104.839	0.250
30	89	105.327	105.437	0.110
30	91	102.728	102.168	-0.560
30	92	103.503	103.289	-0.214
30	94	104.779	105.130	0.351
30	95	104.331	104.399	0.068
35	100	103.126	103.273	0.147
35	101	102.663	102.292	-0.371
35	102	102.892	103.403	0.511
35	103	104.968	105.060	0.092
35	104	103.389	103.380	-0.009
35	105	101.198	101.483	0.285
50	106	104.031	104.143	0.112
50	107	103.647	103.787	0.140
50	108	105.593	105.761	0.168
50	109	104.066	104.675	0.609
50	110	103.360	103.538	0.178
50	111	102.476	103.281	0.805
55	113	102.250	101.353	-0.897
55	114	102.543	101.906	-0.637
55	115	103.130	102.427	-0.703
55	116	101.027	100.834	-0.193
55	125	105.394	105.349	-0.045
55	128	104.320	104.100	-0.220
100	129	102.686	102.949	0.263
100	131	104.487	104.077	-0.410
100	132	104.993	104.383	-0.610
100	133	102.535	102.515	-0.020
100	134	103.186	103.117	-0.069
100	135	103.215	102.815	-0.401
100	138	102.297	103.165	0.868
100	139	103.081	102.382	-0.699
100	140	103.240	103.115	-0.125
100	141	104.932	104.188	-0.744
100	142	102.746	103.043	0.297
100	143	102.472	102.024	-0.448
100	144	102.165	101.890	-0.275
100	220	104.235	103.361	-0.874
100	221	104.091	104.046	-0.045
100	224	102.983	102.468	-0.515
100	225	105.071	104.523	-0.548
100	226	106.325	106.177	-0.148
100	228	102.540	102.612	0.072
100	232	103.117	103.009	-0.108
100	233	102.255	102.152	-0.103
100	236	103.291	102.931	-0.360
100	238	104.399	103.887	-0.512
100	243	103.672	103.264	-0.408
105	244	102.495	101.863	-0.632
105	246	102.852	102.775	-0.077
105	248	102.837	103.244	0.407
105	374	101.500	101.942	0.442
105	376	103.099	103.441	0.342
	Max	106.325	106.177	0.868
	Average	103.512	103.444	-0.068
	Min	101.027	100.834	-0.897
	Std Dev	1.143	1.191	0.354

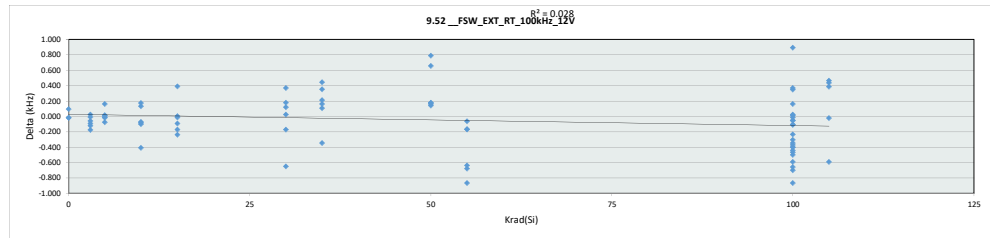


9.28_FSW_EXT_RT_100kHz_5											
Test Site											
Tester											
Test Number											
Max Limit	115	kHz									
Min Limit	95	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000
Min	103.226	102.610	101.178	102.668	101.184	102.168	101.483	103.281	100.834	101.890	101.863
Average	103.992	104.173	103.075	104.199	103.039	104.210	103.149	104.198	102.662	103.254	103.653
Max	105.031	105.400	105.120	105.534	105.303	105.437	105.060	105.761	105.349	106.177	103.441
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000

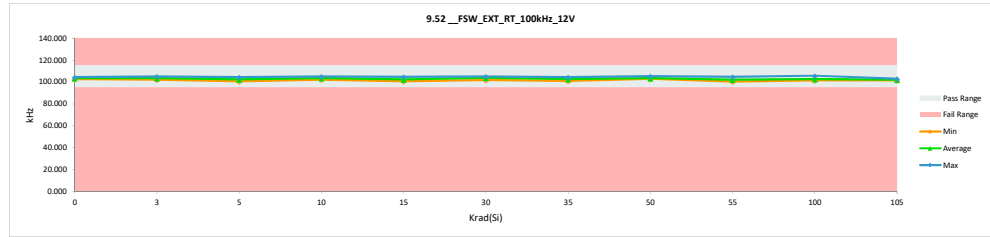


LDR TID Report
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9.52_FSW_EXT_RT_100kHz_12V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	115	115		
Min Limit	95	95		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	104.472	104.461	-0.011
0	118	103.196	103.188	-0.008
0	122	102.578	102.681	0.103
3	61	104.152	104.038	-0.114
3	62	104.842	104.873	0.031
3	63	102.303	102.136	-0.167
3	64	103.005	102.919	-0.086
3	65	104.219	104.169	-0.050
3	66	103.858	103.856	-0.002
5	67	102.620	102.637	0.017
5	68	102.169	102.160	-0.009
5	69	102.449	102.381	-0.068
5	70	104.327	104.495	0.168
5	71	102.790	102.790	0.000
5	72	100.669	100.694	0.025
10	73	104.152	104.089	-0.063
10	74	104.842	105.024	0.182
10	75	102.303	102.211	-0.092
10	76	103.005	102.606	-0.399
10	77	104.219	104.361	0.142
10	78	103.858	103.781	-0.077
15	79	102.620	102.632	0.012
15	80	102.169	101.940	-0.229
15	81	102.449	102.365	-0.084
15	82	104.327	104.726	0.399
15	83	102.790	102.628	-0.162
15	84	100.669	100.663	-0.006
30	86	104.152	104.340	0.188
30	89	104.842	104.969	0.127
30	91	102.303	101.663	-0.640
30	92	103.005	102.840	-0.165
30	94	104.219	104.597	0.378
30	95	103.858	103.889	0.031
35	100	102.620	102.789	0.169
35	101	102.169	101.832	-0.337
35	102	102.449	102.903	0.454
35	103	104.327	104.548	0.221
35	104	102.790	102.906	0.116
35	105	100.669	101.030	0.361
50	106	103.432	103.603	0.171
50	107	103.111	103.298	0.187
50	108	105.073	105.254	0.181
50	109	103.530	104.193	0.663
50	110	102.882	103.029	0.147
50	111	101.992	102.788	0.796
55	113	101.731	100.875	-0.856
55	114	102.079	101.448	-0.631
55	115	102.578	101.908	-0.670
55	116	100.514	100.356	-0.158
55	126	104.874	104.819	-0.055
55	128	103.756	103.598	-0.158
100	129	102.130	102.487	0.357
100	131	103.957	103.595	-0.362
100	132	104.489	103.906	-0.583
100	133	102.005	102.038	0.033
100	134	102.627	102.621	-0.006
100	135	102.717	102.380	-0.337
100	138	101.808	102.708	0.900
100	139	102.561	101.911	-0.650
100	140	102.740	102.698	-0.042
100	141	104.401	103.711	-0.690
100	142	102.166	102.543	0.377
100	143	102.004	101.544	-0.460
100	144	101.686	101.459	-0.227
100	220	103.742	102.886	-0.856
100	221	103.558	103.579	0.021
100	224	102.436	102.002	-0.434
100	225	104.508	104.015	-0.493
100	226	105.824	105.729	-0.095
100	228	101.962	102.131	0.169
100	232	102.611	102.509	-0.102
100	233	101.724	101.679	-0.045
100	236	102.749	102.452	-0.297
100	238	103.803	103.411	-0.392
100	243	103.169	102.779	-0.390
105	244	101.976	101.394	-0.582
105	246	102.336	102.321	-0.015
105	248	102.354	102.826	0.472
105	374	101.013	101.457	0.444
105	376	102.592	102.985	0.393
Max		105.824	105.729	0.900
Average		102.996	102.947	-0.049
Min		100.514	100.356	-0.856
Std Dev		1.135	1.177	0.351

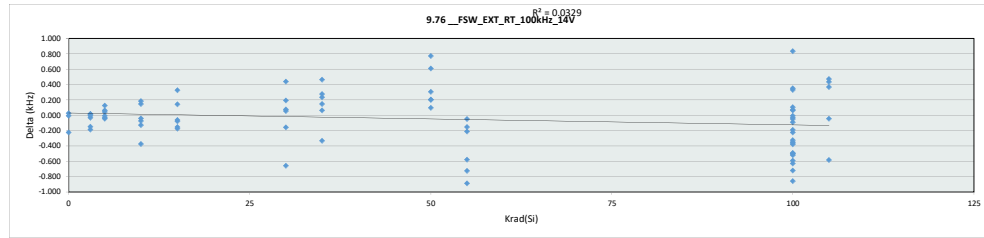


9.52_FSW_EXT_RT_100kHz_1											
Test Site											
Tester											
Test Number											
Max Limit	115	kHz									
Min Limit	95	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000
Min	102.681	102.136	100.694	102.211	100.663	101.663	101.030	102.788	100.356	101.459	101.394
Average	103.443	103.665	102.526	103.679	102.492	103.716	102.668	103.694	102.167	102.782	102.197
Max	104.461	104.873	104.495	105.024	104.726	104.969	104.548	105.254	104.819	105.729	102.985
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000

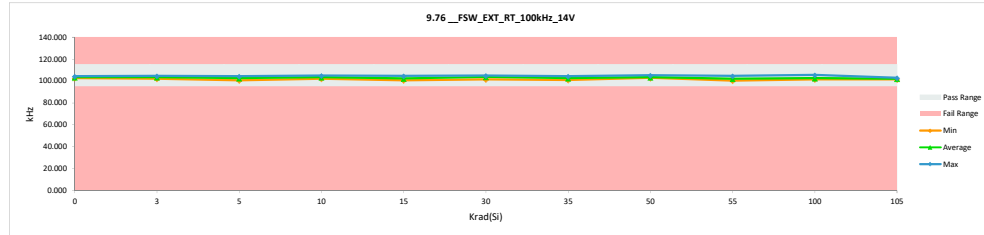


LDR TID Report
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9.76_FSW_EXT_RT_100kHz_14V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	115	115		
Min Limit	95	95		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	104.456	104.490	0.034
0	118	103.396	103.180	-0.216
0	122	102.660	102.661	0.001
3	61	104.141	103.999	-0.142
3	62	104.859	104.860	0.001
3	63	102.293	102.115	-0.178
3	64	102.961	102.938	-0.023
3	65	104.157	104.182	0.025
3	66	103.812	103.835	0.023
5	67	102.543	102.617	0.074
5	68	102.110	102.112	0.002
5	69	102.412	102.390	-0.022
5	70	104.391	104.526	0.135
5	71	102.808	102.860	0.052
5	72	100.731	100.693	-0.038
10	73	104.141	104.077	-0.064
10	74	104.859	105.014	0.155
10	75	102.293	102.173	-0.120
10	76	102.961	102.594	-0.367
10	77	104.157	104.350	0.193
10	78	103.812	103.779	-0.033
15	79	102.543	102.696	0.153
15	80	102.110	101.965	-0.145
15	81	102.412	102.358	-0.054
15	82	104.391	104.727	0.336
15	83	102.808	102.643	-0.165
15	84	100.731	100.660	-0.071
30	86	104.141	104.343	0.202
30	89	104.859	104.922	0.063
30	91	102.293	101.642	-0.651
30	92	102.961	102.814	-0.147
30	94	104.157	104.605	0.448
30	95	103.812	103.895	0.083
35	100	102.543	102.786	0.243
35	101	102.110	101.788	-0.322
35	102	102.412	102.886	0.474
35	103	104.391	104.548	0.157
35	104	102.808	102.881	0.073
35	105	100.731	101.017	0.286
50	106	103.357	103.571	0.214
50	107	103.102	103.209	0.107
50	108	105.032	105.242	0.210
50	109	103.557	104.174	0.617
50	110	102.724	103.037	0.313
50	111	101.987	102.768	0.781
55	113	101.718	100.841	-0.877
55	114	102.047	101.478	-0.569
55	115	102.611	101.897	-0.714
55	116	100.554	100.351	-0.203
55	126	104.852	104.810	-0.042
55	128	103.748	103.604	-0.144
100	129	102.125	102.462	0.337
100	131	103.949	103.579	-0.370
100	132	104.460	103.878	-0.582
100	133	101.983	102.053	0.070
100	134	102.648	102.647	-0.001
100	135	102.709	102.367	-0.342
100	138	101.842	102.687	0.845
100	139	102.527	101.905	-0.622
100	140	102.708	102.683	-0.025
100	141	104.401	103.690	-0.711
100	142	102.162	102.521	0.359
100	143	102.028	101.515	-0.513
100	144	101.666	101.452	-0.214
100	220	103.735	102.887	-0.848
100	221	103.480	103.562	0.082
100	224	102.447	101.963	-0.484
100	225	104.509	104.012	-0.497
100	226	105.783	105.602	-0.181
100	228	102.016	102.131	0.115
100	232	102.589	102.508	-0.081
100	233	101.740	101.698	-0.042
100	236	102.754	102.438	-0.316
100	238	103.890	103.400	-0.490
100	243	103.141	102.789	-0.352
105	244	101.981	101.405	-0.576
105	246	102.330	102.295	-0.035
105	248	102.316	102.798	0.482
105	374	101.006	101.448	0.442
105	376	102.595	102.970	0.375
	Max	105.783	105.602	0.845
	Average	102.987	102.937	-0.050
	Min	100.554	100.351	-0.877
	Std Dev	1.129	1.173	0.352

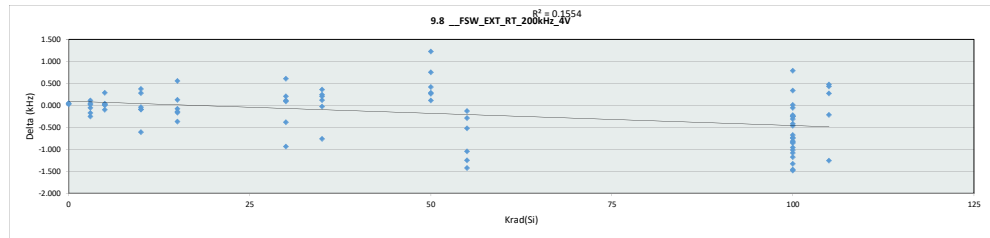


9.76_FSW_EXT_RT_100kHz_1											
Test Site											
Tester											
Test Number											
Max Limit	115	kHz									
Min Limit	95	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000
Min	102.661	102.115	100.693	102.173	100.660	101.642	101.017	102.768	100.351	101.452	101.405
Average	103.444	103.655	102.533	103.665	102.508	103.704	102.651	103.667	102.164	102.768	102.183
Max	104.490	104.860	104.526	105.014	104.727	104.922	104.548	105.242	104.810	105.602	102.970
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000

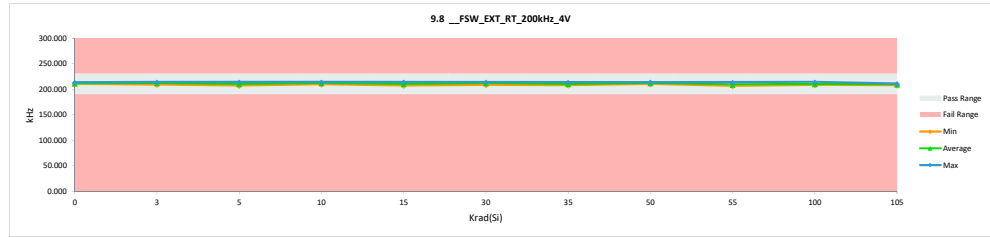


LDR TID Report
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9.8_FSW_EXT_RT_200kHz_4V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	230	230		
Min Limit	190	190		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	213.421	213.463	0.042
0	118	211.339	211.402	0.063
0	122	210.901	210.964	0.063
3	61	212.744	212.588	-0.156
3	62	214.187	214.311	0.124
3	63	209.805	209.571	-0.234
3	64	211.436	211.395	-0.041
3	65	212.923	212.956	0.033
3	66	212.524	212.582	0.058
5	67	210.526	210.565	0.039
5	68	210.165	210.221	0.056
5	69	209.898	209.919	0.021
5	70	213.848	214.153	0.305
5	71	210.860	210.894	0.034
5	72	207.661	207.580	-0.081
10	73	212.744	212.718	-0.026
10	74	214.187	214.483	0.296
10	75	209.805	209.730	-0.075
10	76	211.436	210.842	-0.594
10	77	212.923	213.309	0.386
10	78	212.524	212.450	-0.074
15	79	210.526	210.668	0.142
15	80	210.165	209.811	-0.354
15	81	209.898	209.751	-0.147
15	82	213.848	214.416	0.568
15	83	210.860	210.730	-0.130
15	84	207.661	207.597	-0.064
30	86	212.744	212.966	0.222
30	89	214.187	214.315	0.128
30	91	209.805	208.885	-0.920
30	92	211.436	211.071	-0.365
30	94	212.923	213.542	0.619
30	95	212.524	212.631	0.107
35	100	210.526	210.744	0.218
35	101	210.165	209.415	-0.750
35	102	209.898	210.273	0.375
35	103	213.848	213.840	-0.008
35	104	210.860	210.994	0.134
35	105	207.661	207.919	0.258
50	106	211.872	212.307	0.435
50	107	211.244	211.523	0.279
50	108	214.334	214.637	0.303
50	109	212.645	213.410	0.765
50	110	211.133	211.263	0.130
50	111	209.385	210.625	1.240
55	113	209.110	207.702	-1.408
55	114	209.297	208.263	-1.034
55	115	210.432	209.198	-1.234
55	116	207.142	206.871	-0.271
55	126	214.090	213.979	-0.111
55	128	212.584	212.075	-0.509
100	129	209.878	209.901	0.023
100	131	212.831	212.033	-0.798
100	132	213.772	212.611	-1.161
100	133	209.795	209.555	-0.240
100	134	210.997	210.596	-0.401
100	135	211.036	210.320	-0.716
100	138	209.658	210.462	0.804
100	139	210.994	209.683	-1.311
100	140	211.072	210.626	-0.446
100	141	213.150	211.705	-1.445
100	142	210.387	210.738	0.351
100	143	209.516	208.694	-0.822
100	144	209.757	209.025	-0.732
100	220	212.146	210.682	-1.464
100	221	212.042	211.835	-0.207
100	224	210.437	209.497	-0.940
100	225	213.663	212.665	-0.998
100	226	214.902	214.664	-0.238
100	228	209.741	209.697	-0.044
100	232	210.375	210.076	-0.299
100	233	209.668	209.438	-0.230
100	236	210.633	209.973	-0.660
100	238	212.774	211.705	-1.069
100	243	211.106	210.262	-0.844
105	244	209.543	208.298	-1.245
105	246	210.424	210.223	-0.201
105	248	210.848	211.336	0.488
105	374	207.830	208.276	0.446
105	376	211.083	211.368	0.285
Max		214.902	214.664	1.240
Average		211.234	211.043	-0.191
Min		207.142	206.871	-1.464
Std Dev		1.773	1.892	0.568

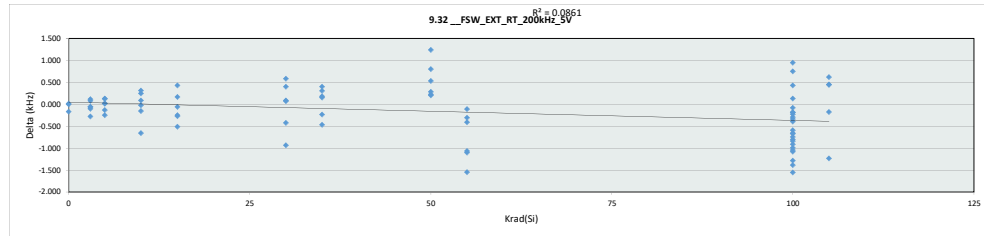


9.8_FSW_EXT_RT_200kHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	230	kHz									
Min Limit	190	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min	210.964	209.571	207.580	209.730	207.597	208.885	207.919	210.625	206.871	208.694	208.276
Average	211.943	212.234	210.555	212.255	210.496	212.235	210.531	212.294	209.681	210.685	209.900
Max	213.463	214.311	214.153	214.483	214.416	214.315	213.840	214.637	213.979	214.664	211.368
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000

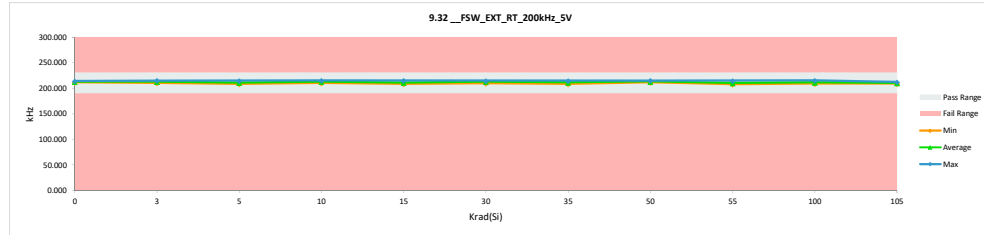


LDR TID Report
TPS7H5001-SP

9.32_FSW_EXT_RT_200kHz_5V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	230	230		
Min Limit	190	190		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	214.455	214.472	0.017
0	118	212.473	212.321	-0.152
0	122	211.885	211.917	0.032
3	61	213.621	213.574	-0.047
3	62	215.194	215.322	0.128
3	63	210.776	210.516	-0.260
3	64	212.470	212.379	-0.091
3	65	214.025	213.981	-0.044
3	66	213.538	213.631	0.093
5	67	211.528	211.558	0.030
5	68	211.007	211.042	0.035
5	69	210.888	210.770	-0.118
5	70	215.030	215.178	0.148
5	71	212.178	211.944	-0.234
5	72	208.620	208.758	0.138
10	73	213.621	213.723	0.102
10	74	215.194	215.521	0.327
10	75	210.776	210.640	-0.136
10	76	212.470	211.827	-0.643
10	77	214.025	214.291	0.266
10	78	213.538	213.528	-0.010
15	79	211.528	211.707	0.179
15	80	211.007	210.752	-0.255
15	81	210.888	210.656	-0.232
15	82	215.030	215.473	0.443
15	83	212.178	211.679	-0.499
15	84	208.620	208.576	-0.044
30	86	213.621	214.035	0.414
30	89	215.194	215.299	0.105
30	91	210.776	209.854	-0.922
30	92	212.470	212.062	-0.408
30	94	214.025	214.626	0.601
30	95	213.538	213.624	0.086
35	100	211.528	211.722	0.194
35	101	211.007	210.555	-0.452
35	102	210.888	211.303	0.415
35	103	215.030	215.196	0.166
35	104	212.178	211.958	-0.220
35	105	208.620	208.939	0.319
50	106	213.012	213.556	0.544
50	107	212.328	212.546	0.218
50	108	215.417	215.714	0.297
50	109	213.535	214.350	0.815
50	110	212.063	212.304	0.241
50	111	210.406	211.658	1.252
55	113	210.256	208.722	-1.534
55	114	210.324	209.234	-1.090
55	115	211.427	210.380	-1.047
55	116	208.126	207.835	-0.291
55	126	215.130	215.032	-0.098
55	128	213.633	213.237	-0.396
100	129	210.654	211.099	0.445
100	131	213.770	213.038	-0.732
100	132	214.703	213.636	-1.067
100	133	210.810	210.602	-0.208
100	134	211.994	211.671	-0.323
100	135	212.002	211.346	-0.656
100	138	210.517	211.483	0.966
100	139	211.997	210.726	-1.271
100	140	212.060	211.681	-0.379
100	141	214.148	212.780	-1.368
100	142	211.376	212.144	0.768
100	143	210.435	209.609	-0.826
100	144	210.774	210.130	-0.644
100	220	213.166	211.628	-1.538
100	221	213.062	212.999	-0.063
100	224	211.411	210.516	-0.895
100	225	214.699	213.723	-0.976
100	226	215.937	215.770	-0.167
100	228	210.677	210.824	0.147
100	232	211.451	211.174	-0.277
100	233	210.604	210.437	-0.167
100	236	211.639	211.063	-0.576
100	238	213.791	212.764	-1.027
100	243	212.101	211.314	-0.787
105	244	210.578	209.358	-1.220
105	246	211.300	211.138	-0.162
105	248	211.711	212.342	0.631
105	374	208.731	209.198	0.467
105	376	211.964	212.420	0.456
	Max	215.937	215.770	1.252
	Average	212.239	212.076	-0.163
	Min	208.126	207.835	-1.538
	Std Dev	1.802	1.910	0.578

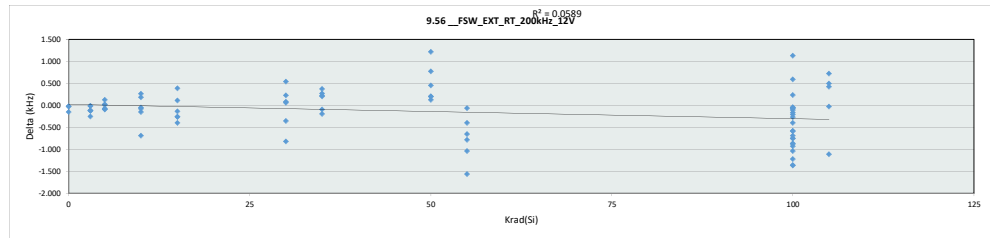


9.32_FSW_EXT_RT_200kHz_5											
Test Site											
Tester											
Test Number											
Max Limit	230	kHz									
Min Limit	190	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min	211.917	210.516	208.758	210.640	208.576	209.854	208.939	211.658	207.835	209.609	209.198
Average	212.903	213.234	211.542	213.255	211.474	213.250	211.612	213.355	210.740	211.757	210.891
Max	214.472	215.322	215.178	215.521	215.473	215.299	215.196	215.714	215.032	215.770	212.420
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000

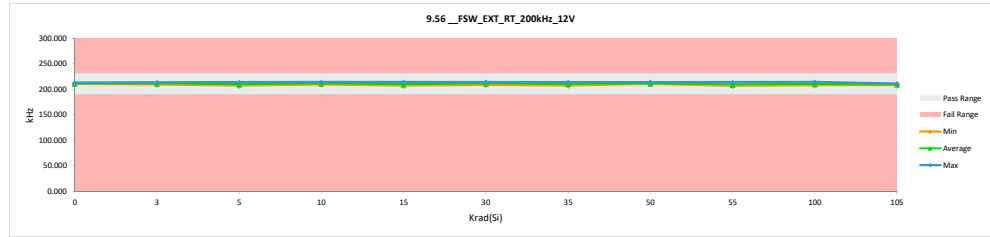


LDR TID Report
TPS7H5001-SP

9.56_FSW_EXT_RT_200kHz_12V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	230	230		
Min Limit	190	190		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	213.135	213.133	-0.002
0	118	211.168	211.155	-0.013
0	122	210.703	210.568	-0.135
3	61	212.508	212.400	-0.108
3	62	214.067	214.067	0.000
3	63	209.594	209.358	-0.236
3	64	211.123	211.019	-0.104
3	65	212.731	212.629	-0.102
3	66	212.295	212.300	0.005
5	67	210.260	210.301	0.041
5	68	209.634	209.776	0.142
5	69	209.701	209.634	-0.067
5	70	213.677	213.699	0.022
5	71	210.805	210.729	-0.076
5	72	207.495	207.441	-0.054
10	73	212.508	212.475	-0.033
10	74	214.067	214.263	0.196
10	75	209.594	209.458	-0.136
10	76	211.123	210.446	-0.677
10	77	212.731	213.008	0.277
10	78	212.295	212.236	-0.059
15	79	210.260	210.385	0.125
15	80	209.634	209.513	-0.121
15	81	209.701	209.459	-0.242
15	82	213.677	214.084	0.407
15	83	210.805	210.419	-0.386
15	84	207.495	207.251	-0.244
30	86	212.508	212.752	0.244
30	89	214.067	214.164	0.097
30	91	209.594	208.788	-0.806
30	92	211.123	210.781	-0.342
30	94	212.731	213.287	0.556
30	95	212.295	212.374	0.079
35	100	210.260	210.544	0.284
35	101	209.634	209.557	-0.077
35	102	209.701	210.089	0.388
35	103	213.677	213.896	0.219
35	104	210.805	210.624	-0.181
35	105	207.495	207.734	0.239
50	106	211.830	212.296	0.466
50	107	210.955	211.177	0.222
50	108	214.121	214.334	0.213
50	109	212.191	212.981	0.790
50	110	210.867	211.005	0.138
50	111	209.152	210.384	1.232
55	113	209.039	207.495	-1.544
55	114	209.122	208.102	-1.020
55	115	210.132	209.361	-0.771
55	116	206.964	206.579	-0.385
55	126	213.795	213.748	-0.047
55	128	212.721	212.086	-0.635
100	129	209.553	209.802	0.249
100	131	212.484	211.811	-0.673
100	132	213.423	212.397	-1.026
100	133	209.509	209.444	-0.065
100	134	210.695	210.545	-0.150
100	135	210.750	210.182	-0.568
100	138	209.251	210.398	1.147
100	139	210.741	209.533	-1.208
100	140	210.751	210.499	-0.252
100	141	212.897	211.551	-1.346
100	142	210.145	210.752	0.607
100	143	209.218	208.305	-0.913
100	144	209.435	209.049	-0.386
100	220	211.892	210.540	-1.352
100	238	212.556	211.691	-0.865
100	243	210.885	210.147	-0.738
105	244	209.382	208.289	-1.093
105	246	210.011	209.997	-0.014
105	248	210.485	211.222	0.737
105	374	207.704	208.216	0.512
105	376	210.740	211.183	0.443
	Max	214.766	214.675	1.232
	Average	210.992	210.846	-0.147
	Min	206.964	206.579	-1.544
	Std Dev	1.786	1.873	0.539

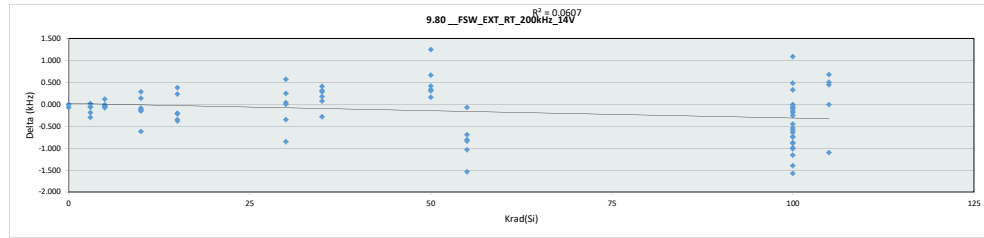


9.56_FSW_EXT_RT_200kHz_1											
Test Site											
Tester											
Test Number											
Max Limit	230	kHz									
Min Limit	190	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min	210.568	209.358	207.441	209.458	207.251	208.788	207.734	210.384	206.579	208.305	208.216
Average	211.619	211.962	210.263	211.981	210.185	212.024	210.407	212.030	209.562	210.559	209.781
Max	213.133	214.067	213.699	214.263	214.084	214.164	213.896	214.334	213.748	214.675	211.222
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000

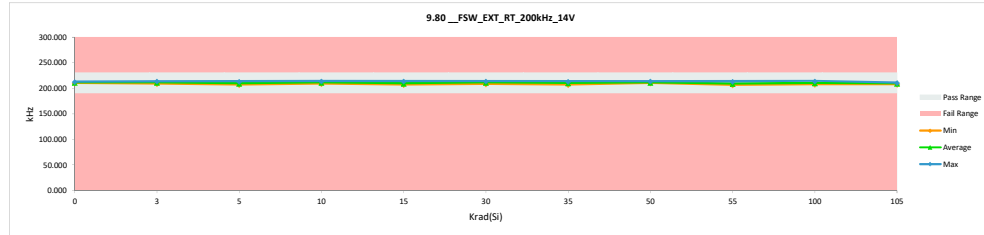


LDR TID Report
TPS7H5001-SP

9.80_FSW_EXT_RT_200kHz_14V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	230	230		
Min Limit	190	190		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	213.132	213.134	0.002
0	118	211.047	211.059	0.012
0	122	210.668	210.607	-0.061
3	61	212.505	212.333	-0.172
3	62	214.048	214.071	0.023
3	63	209.588	209.307	-0.281
3	64	211.074	211.021	-0.053
3	65	212.690	212.649	-0.041
3	66	212.308	212.339	0.031
5	67	210.179	210.311	0.132
5	68	209.794	209.765	-0.029
5	69	209.651	209.628	-0.023
5	70	213.681	213.614	-0.067
5	71	210.755	210.721	-0.034
5	72	207.408	207.412	0.004
10	73	212.505	212.434	-0.071
10	74	214.048	214.202	0.154
10	75	209.588	209.449	-0.139
10	76	211.074	210.472	-0.602
10	77	212.690	212.986	0.296
10	78	212.308	212.196	-0.112
15	79	210.179	210.424	0.245
15	80	209.794	209.460	-0.334
15	81	209.651	209.448	-0.203
15	82	213.681	214.078	0.397
15	83	210.755	210.390	-0.365
15	84	207.408	207.217	-0.191
30	86	212.505	212.768	0.263
30	89	214.048	214.098	0.050
30	91	209.588	208.751	-0.837
30	92	211.074	210.737	-0.337
30	94	212.690	213.274	0.584
30	95	212.308	212.321	0.013
35	100	210.179	210.478	0.299
35	101	209.794	209.524	-0.270
35	102	209.651	210.072	0.421
35	103	213.681	213.770	0.089
35	104	210.755	210.945	0.190
35	105	207.408	207.735	0.327
50	106	211.825	212.252	0.427
50	107	210.826	211.175	0.349
50	108	214.049	214.371	0.322
50	109	212.311	212.987	0.676
50	110	210.836	211.008	0.172
50	111	209.136	210.396	1.260
55	113	209.001	207.477	-1.524
55	114	209.122	208.104	-1.018
55	115	210.026	209.230	-0.796
55	116	207.314	206.637	-0.677
55	126	213.766	213.706	-0.060
55	128	212.869	212.046	-0.823
100	129	209.320	209.660	0.340
100	131	212.448	211.818	-0.630
100	132	213.388	212.414	-0.974
100	133	209.508	209.425	-0.083
100	134	210.663	210.488	-0.175
100	135	210.722	210.145	-0.577
100	138	209.224	210.323	1.099
100	139	210.688	209.541	-1.147
100	140	210.696	210.456	-0.240
100	141	212.900	211.511	-1.389
100	142	210.133	210.632	0.499
100	143	209.236	208.237	-0.999
100	144	209.477	209.041	-0.436
100	220	211.880	210.318	-1.562
100	221	211.750	211.758	0.008
100	224	210.073	209.343	-0.730
100	225	213.306	212.442	-0.864
100	226	214.730	214.660	-0.070
100	228	209.436	209.437	0.001
100	232	210.120	209.978	-0.142
100	233	209.299	209.250	-0.049
100	236	210.416	209.892	-0.524
100	238	212.556	211.684	-0.872
100	243	210.897	210.174	-0.723
105	244	209.386	208.302	-1.084
105	246	209.997	210.001	0.004
105	248	210.497	211.189	0.692
105	374	207.640	208.156	0.516
105	376	210.678	211.137	0.459
	Max	214.730	214.660	1.260
	Average	210.975	210.825	-0.150
	Min	207.314	206.637	-1.562
	Std Dev	1.784	1.871	0.550

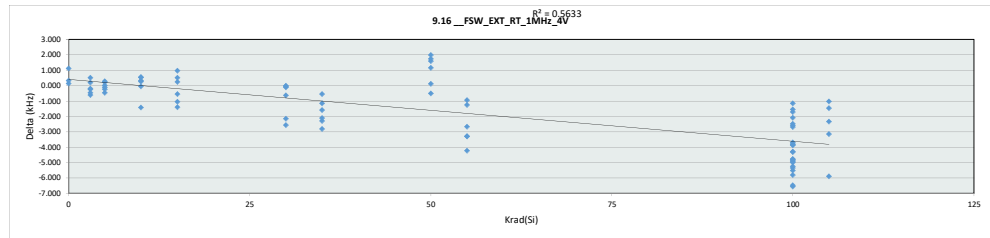


9.80_FSW_EXT_RT_200kHz_1											
Test Site											
Tester											
Test Number											
Max Limit	230	kHz									
Min Limit	190	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min	210.607	209.307	207.412	209.449	207.217	208.751	207.735	210.396	206.637	208.237	208.156
Average	211.600	211.953	210.242	211.957	210.170	211.992	210.421	212.032	209.533	210.526	209.757
Max	213.134	214.071	213.614	214.202	214.078	214.098	213.770	214.371	213.706	214.660	211.189
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000

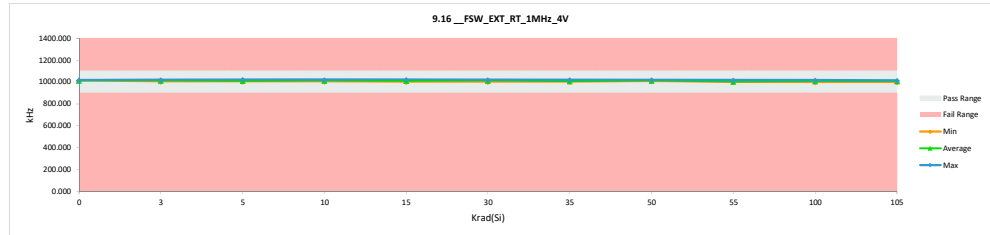


LDR TID Report
TPS7H5001-SP

9.16_FSW_EXT_RT_1MHz_4V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	1100	1100		
Min Limit	900	900		
Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1016.625	1017.001	0.376
0	118	1013.508	1013.677	0.169
0	122	1014.885	1016.047	1.162
3	61	1014.566	1014.006	-0.560
3	62	1021.207	1021.760	0.553
3	63	1005.657	1005.223	-0.434
3	64	1016.414	1016.219	-0.195
3	65	1014.372	1014.213	-0.159
3	66	1016.652	1016.883	0.231
5	67	1010.625	1010.194	-0.431
5	68	1013.291	1013.554	0.263
5	69	1005.295	1005.100	-0.195
5	70	1023.880	1024.197	0.317
5	71	1010.413	1010.344	-0.069
5	72	1007.923	1007.986	0.063
10	73	1014.566	1014.933	0.367
10	74	1021.207	1021.790	0.583
10	75	1005.657	1005.962	0.305
10	76	1016.414	1015.032	-1.382
10	77	1014.372	1014.959	0.587
10	78	1016.652	1016.634	-0.018
15	79	1010.625	1011.188	0.563
15	80	1013.291	1012.789	-0.502
15	81	1005.295	1003.927	-1.368
15	82	1023.880	1024.882	1.002
15	83	1010.413	1009.396	-1.017
15	84	1007.923	1008.198	0.275
30	86	1014.566	1014.497	-0.069
30	89	1021.207	1021.203	-0.004
30	91	1005.657	1003.123	-2.534
30	92	1016.414	1014.298	-2.116
30	94	1014.372	1014.408	0.036
30	95	1016.652	1016.052	-0.600
35	100	1010.625	1010.111	-0.514
35	101	1013.291	1011.221	-2.070
35	102	1005.295	1003.035	-2.260
35	103	1023.880	1021.109	-2.771
35	104	1010.413	1008.871	-1.542
35	105	1007.923	1006.807	-1.116
50	106	1015.560	1016.560	1.200
50	107	1013.268	1013.429	0.161
50	108	1017.042	1018.829	1.787
50	109	1018.953	1020.584	1.631
50	110	1014.793	1014.317	-0.476
50	111	1005.771	1007.808	2.037
55	113	1007.197	1002.999	-4.198
55	114	1002.511	999.264	-3.247
55	115	1009.986	1007.351	-2.635
55	116	1000.700	999.798	-0.902
55	126	1019.883	1018.665	-1.218
55	128	1017.985	1014.734	-3.251
100	129	1010.195	1006.548	-3.647
100	131	1018.490	1013.585	-4.905
100	132	1022.562	1017.759	-4.803
100	133	1012.650	1008.866	-3.784
100	134	1016.399	1012.134	-4.265
100	135	1011.502	1006.778	-4.724
100	138	1011.230	1009.551	-1.679
100	139	1015.098	1009.319	-5.779
100	140	1014.474	1010.639	-3.835
100	141	1013.734	1007.206	-6.528
100	142	1015.802	1014.301	-1.501
100	143	1007.293	1001.808	-5.485
100	144	1015.989	1010.685	-5.304
100	220	1014.570	1008.139	-6.431
100	221	1013.342	1011.303	-2.039
100	224	1011.951	1007.201	-4.750
100	225	1018.230	1013.269	-4.961
100	226	1015.057	1012.508	-2.549
100	228	1010.427	1009.322	-1.105
100	232	1011.534	1009.099	-2.435
100	233	1013.168	1010.525	-2.643
100	236	1005.189	1001.439	-3.750
100	238	1016.712	1011.511	-5.201
100	243	1007.856	1003.596	-4.260
105	244	1007.450	1001.602	-5.848
105	246	1012.746	1011.321	-1.425
105	248	1016.178	1015.196	-0.982
105	374	1002.257	999.959	-2.298
105	376	1016.223	1013.118	-3.105
Max		1023.880	1024.882	2.037
Average		1013.096	1011.418	-1.678
Min		1000.700	999.264	-1.435
Std Dev		5.180	5.796	2.167

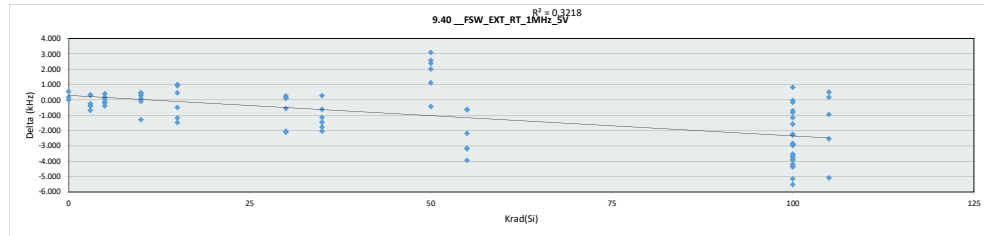


9.16_FSW_EXT_RT_1MHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	1100	kHz									
Min Limit	900	kHz									
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1013.677	1005.223	1005.100	1005.962	1003.927	1003.123	1003.035	1007.808	999.264	1001.439	999.959
Average	1015.575	1014.717	1011.896	1014.885	1011.730	1013.930	1010.192	1015.255	1007.135	1009.462	1008.239
Max	1017.001	1021.760	1024.197	1021.790	1024.882	1021.203	1021.109	1020.584	1018.665	1017.759	1015.196
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000

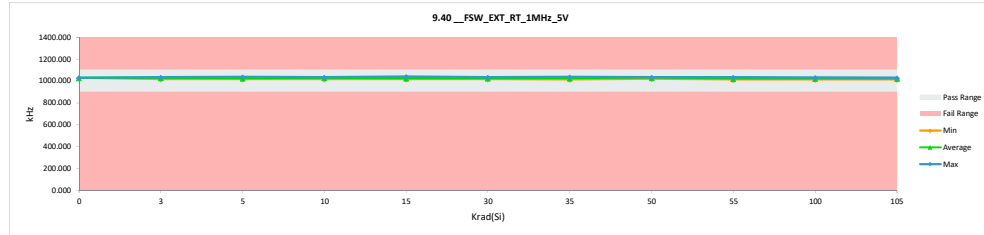


LDR TID Report
TPS7H5001-SP

9.40_FSW_EXT_RT_1MHz_5V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	1100	1100		
Min Limit	900	900		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1029.916	1030.121	0.205
0	118	1028.590	1028.630	0.040
0	122	1030.407	1030.987	0.580
3	61	1029.874	1029.227	-0.647
3	62	1036.045	1036.408	0.363
3	63	1018.793	1018.433	-0.356
3	64	1030.200	1029.993	-0.207
3	65	1028.048	1027.739	-0.309
3	66	1030.224	1030.579	0.355
5	67	1025.484	1025.145	-0.339
5	68	1027.840	1028.276	0.436
5	69	1018.230	1018.157	-0.073
5	70	1039.545	1039.790	0.245
5	71	1025.306	1025.141	-0.165
5	72	1022.378	1022.464	0.086
10	73	1029.874	1030.005	0.131
10	74	1036.045	1036.367	0.322
10	75	1018.789	1019.254	0.465
10	76	1030.200	1028.959	-1.241
10	77	1028.048	1028.557	0.509
10	78	1030.224	1030.177	-0.047
15	79	1025.484	1026.447	0.963
15	80	1027.840	1027.388	-0.452
15	81	1018.230	1016.804	-1.426
15	82	1039.545	1040.592	1.047
15	83	1025.306	1024.161	-1.145
15	84	1022.378	1022.884	0.506
30	86	1029.874	1030.044	0.170
30	89	1036.045	1036.335	0.290
30	91	1018.789	1016.729	-2.060
30	92	1030.200	1028.210	-1.990
30	94	1028.048	1028.205	0.157
30	95	1030.224	1029.715	-0.509
35	100	1025.484	1025.800	0.316
35	101	1027.840	1026.429	-1.411
35	102	1018.230	1016.513	-1.717
35	103	1039.545	1037.550	-1.995
35	104	1025.306	1024.215	-1.091
35	105	1022.378	1021.798	-0.580
50	106	1036.758	1032.802	-3.956
50	107	1028.423	1029.585	1.162
50	108	1029.567	1032.691	3.124
50	109	1034.527	1036.949	2.422
50	110	1030.313	1029.933	-0.380
50	111	1019.126	1021.745	2.619
55	113	1021.368	1017.473	-3.895
55	114	1016.476	1013.368	-3.108
55	115	1024.572	1022.441	-2.131
55	116	1016.054	1015.451	-0.603
55	126	1035.414	1034.827	-0.587
55	128	1032.844	1029.697	-3.147
100	129	1025.279	1023.077	-2.202
100	131	1032.519	1028.261	-4.258
100	132	1037.206	1033.543	-3.663
100	133	1027.861	1025.065	-2.796
100	134	1031.544	1028.629	-2.915
100	135	1023.547	1019.701	-3.846
100	138	1025.798	1025.674	-0.124
100	139	1028.561	1024.225	-4.336
100	140	1028.938	1026.076	-2.862
100	141	1027.002	1021.904	-5.098
100	142	1030.534	1030.542	0.008
100	143	1021.890	1017.752	-4.138
100	144	1030.712	1027.019	-3.693
100	220	1028.749	1025.284	-3.465
100	221	1027.657	1027.005	-0.652
100	224	1026.219	1022.732	-3.487
100	225	1031.446	1027.924	-3.522
100	226	1029.761	1028.639	-1.122
100	228	1024.312	1025.174	0.862
100	232	1026.460	1025.677	-0.783
100	233	1026.759	1025.224	-1.535
100	236	1018.095	1015.850	-2.245
100	238	1031.033	1027.154	-3.879
100	243	1021.927	1019.030	-2.897
105	244	1022.117	1017.082	-5.035
105	246	1026.084	1026.298	0.214
105	248	1029.882	1030.424	0.542
105	374	1016.573	1015.670	-0.903
105	376	1029.804	1027.317	-2.487
	Max	1039.545	1040.592	3.124
	Average	1027.406	1026.339	-1.067
	Min	1016.054	1013.368	-2.686
	Std Dev	5.510	5.998	1.876

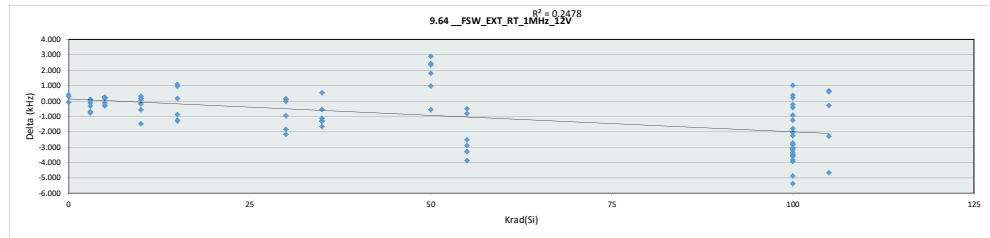


9.40_FSW_EXT_RT_1MHz_5V											
Test Site											
Tester											
Test Number											
Max Limit	1100	kHz									
Min Limit	900	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1028.630	1018.433	1018.157	1019.254	1016.804	1016.729	1016.513	1021.745	1013.368	1015.850	1015.670
Average	1029.913	1028.730	1026.496	1028.887	1026.379	1028.206	1025.384	1030.618	1022.210	1024.965	1023.358
Max	1030.987	1036.408	1039.790	1036.367	1040.592	1036.335	1037.550	1036.949	1034.827	1033.543	1030.424
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000

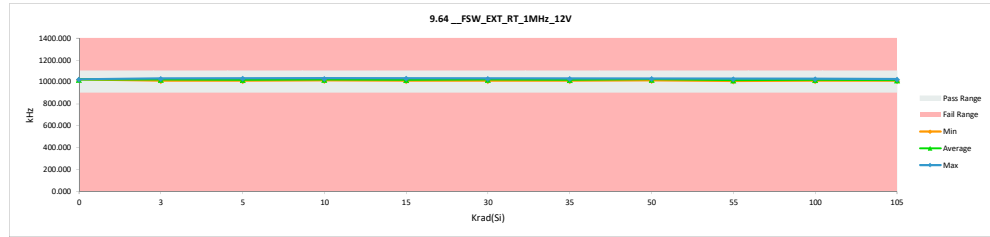


LDR TID Report
TPS7H5001-SP

9.64_FSW_EXT_RT_1MHz_12V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	1100	1100		
Min Limit	900	900		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1024.335	1024.688	0.353
0	118	1023.558	1023.517	-0.041
0	122	1025.143	1025.587	0.444
3	61	1024.929	1024.194	-0.735
3	62	1031.106	1031.201	0.095
3	63	1013.840	1013.187	-0.653
3	64	1024.646	1024.558	-0.088
3	65	1022.912	1022.607	-0.305
3	66	1025.168	1025.310	0.142
5	67	1020.286	1020.206	-0.080
5	68	1022.943	1023.222	0.279
5	69	1012.705	1012.985	0.280
5	70	1034.288	1034.499	0.211
5	71	1020.404	1020.135	-0.269
5	72	1017.515	1017.279	-0.236
10	73	1024.929	1024.767	-0.162
10	74	1031.106	1031.196	0.090
10	75	1013.840	1014.048	0.208
10	76	1024.646	1023.203	-1.443
10	77	1022.912	1023.261	0.349
10	78	1025.168	1024.637	-0.531
15	79	1020.286	1021.392	1.106
15	80	1022.943	1022.108	-0.835
15	81	1012.705	1011.493	-1.212
15	82	1034.288	1035.285	0.997
15	83	1020.404	1019.154	-1.250
15	84	1017.515	1017.717	0.202
30	86	1024.929	1024.947	0.018
30	89	1031.106	1031.236	0.130
30	91	1013.840	1011.719	-2.121
30	92	1024.646	1022.841	-1.805
30	94	1022.912	1023.087	0.175
30	95	1025.168	1024.250	-0.918
35	100	1020.286	1020.852	0.566
35	101	1022.943	1021.668	-1.275
35	102	1012.705	1011.447	-1.258
35	103	1034.288	1032.661	-1.627
35	104	1020.404	1019.322	-1.082
35	105	1017.515	1017.017	-0.498
50	106	1025.569	1027.413	1.844
50	107	1022.929	1023.939	1.010
50	108	1024.224	1027.180	2.956
50	109	1028.888	1031.358	2.470
50	110	1025.526	1025.001	-0.525
50	111	1013.970	1016.355	2.385
55	113	1016.250	1012.416	-3.834
55	114	1011.376	1008.131	-3.245
55	115	1019.639	1017.160	-2.479
55	116	1011.238	1010.470	-0.768
55	126	1030.121	1029.650	-0.471
55	128	1027.253	1024.388	-2.865
100	129	1020.295	1018.541	-1.754
100	131	1026.917	1023.131	-3.786
100	132	1031.566	1028.528	-3.038
100	133	1022.548	1020.338	-2.210
100	134	1026.367	1023.587	-2.780
100	135	1018.068	1014.571	-3.497
100	138	1020.583	1020.827	0.244
100	139	1023.121	1019.229	-3.892
100	140	1023.679	1020.902	-2.777
100	141	1021.707	1016.867	-4.840
100	142	1025.232	1025.636	0.404
100	143	1016.769	1013.196	-3.573
100	144	1025.703	1022.269	-3.434
100	220	1023.530	1018.201	-5.329
100	221	1022.242	1021.868	-0.374
100	224	1020.964	1017.954	-3.010
100	225	1026.029	1022.712	-3.317
100	226	1024.782	1023.904	-0.878
100	228	1019.163	1020.211	1.048
100	232	1020.997	1020.806	-0.191
100	233	1021.469	1020.247	-1.222
100	236	1013.068	1011.059	-2.009
100	238	1025.352	1022.213	-3.139
100	243	1017.043	1014.355	-2.688
105	244	1017.237	1012.622	-4.615
105	246	1020.770	1021.416	0.646
105	248	1024.936	1025.645	0.709
105	374	1011.764	1011.506	-0.258
105	376	1024.516	1022.258	-2.258
Max		1034.288	1035.285	2.956
Average		1022.233	1021.257	-0.976
Min		1011.238	1008.131	-3.107
Std Dev		5.447	5.895	1.739

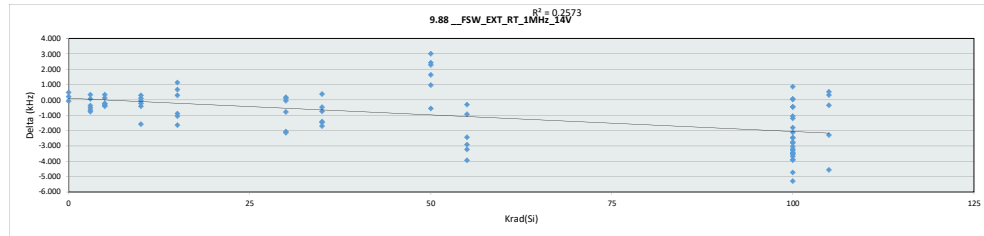


9.64_FSW_EXT_RT_1MHz_12V										
Test Site										
Tester										
Test Number										
Max Limit	1100	kHz								
Min Limit	900	kHz								
Krad(Si)	0	3	5	10	15	30	35	50	55	100
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1023.517	1013.187	1012.985	1014.048	1011.493	1011.719	1011.447	1016.355	1008.131	1011.059
Average	1024.597	1023.510	1021.388	1023.519	1021.192	1023.013	1020.495	1025.208	1017.036	1020.048
Max	1025.587	1031.201	1034.499	1031.196	1035.285	1031.236	1032.661	1031.358	1029.650	1028.528
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000

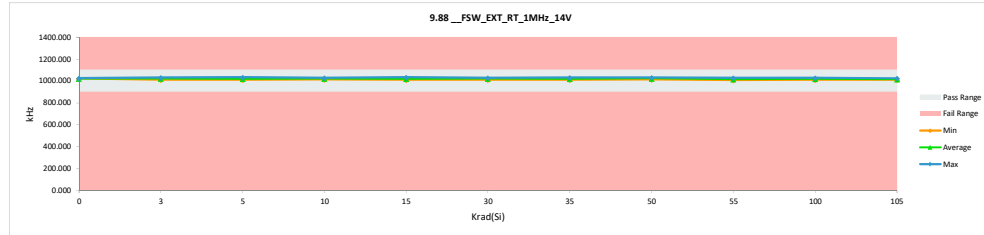


LDR TID Report
TPS7H5001-SP

9.88_FSW_EXT_RT_1MHz_14V				
Test Site	Tester	Unit	Max Limit	Min Limit
Test Number				
Unit		kHz	kHz	
Max Limit		1100	1100	
Min Limit		900	900	
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1024.104	1024.354	0.250
0	118	1023.471	1023.429	-0.042
0	122	1025.067	1025.586	0.519
3	61	1024.771	1024.049	-0.722
3	62	1031.050	1031.164	0.114
3	63	1013.696	1013.085	-0.611
3	64	1024.752	1024.422	-0.330
3	65	1022.833	1022.363	-0.470
3	66	1024.908	1025.297	0.389
5	67	1020.381	1020.002	-0.379
5	68	1022.923	1023.096	0.173
5	69	1013.022	1012.725	-0.297
5	70	1033.903	1034.276	0.373
5	71	1020.063	1019.890	-0.173
5	72	1017.332	1017.117	-0.215
10	73	1024.771	1024.591	-0.180
10	74	1031.050	1031.032	-0.018
10	75	1013.696	1013.858	0.162
10	76	1024.752	1023.209	-1.543
10	77	1022.833	1023.181	0.348
10	78	1024.908	1024.544	-0.364
15	79	1020.381	1021.090	0.709
15	80	1022.923	1022.065	-0.858
15	81	1013.022	1011.425	-1.597
15	82	1033.903	1035.074	1.171
15	83	1020.063	1019.049	-1.014
15	84	1017.332	1017.674	0.342
30	86	1024.771	1024.916	0.145
30	89	1031.050	1031.041	-0.009
30	91	1013.696	1011.597	-2.099
30	92	1024.752	1022.736	-2.016
30	94	1022.833	1023.040	0.207
30	95	1024.908	1024.158	-0.750
35	100	1020.381	1020.807	0.426
35	101	1022.923	1021.485	-1.438
35	102	1013.022	1011.352	-1.670
35	103	1033.903	1032.531	-1.372
35	104	1020.063	1019.367	-0.696
35	105	1017.332	1016.890	-0.442
50	106	1025.488	1027.166	1.678
50	107	1023.029	1024.034	1.005
50	108	1024.036	1027.076	3.040
50	109	1028.759	1031.230	2.471
50	110	1025.228	1024.710	-0.518
50	111	1013.966	1016.292	2.326
55	113	1016.182	1012.290	-3.892
55	114	1011.301	1008.118	-3.183
55	115	1019.525	1017.140	-2.385
55	116	1011.108	1010.226	-0.882
55	126	1029.869	1029.597	-0.272
55	128	1027.167	1024.299	-2.868
100	129	1020.155	1018.383	-1.772
100	131	1026.874	1023.006	-3.868
100	132	1031.613	1028.450	-3.163
100	133	1022.526	1020.095	-2.431
100	134	1026.188	1023.490	-2.698
100	135	1017.974	1014.342	-3.632
100	138	1020.473	1020.537	0.064
100	139	1023.022	1019.178	-3.844
100	140	1023.600	1020.842	-2.758
100	141	1021.522	1016.841	-4.681
100	142	1025.215	1025.331	0.116
100	143	1016.637	1013.129	-3.508
100	144	1025.610	1022.180	-3.430
100	220	1023.406	1018.168	-5.238
100	221	1022.230	1021.823	-0.407
100	224	1020.772	1017.786	-2.986
100	225	1025.859	1022.604	-3.255
100	226	1024.774	1023.767	-1.007
100	228	1019.152	1020.061	0.909
100	232	1020.994	1020.583	-0.411
100	233	1021.294	1020.130	-1.164
100	236	1012.926	1010.841	-2.085
100	238	1025.336	1021.940	-3.396
100	243	1016.798	1014.376	-2.422
105	244	1017.013	1012.488	-4.525
105	246	1020.772	1021.134	0.362
105	248	1024.827	1025.404	0.577
105	374	1011.664	1011.362	-0.302
105	376	1024.416	1022.174	-2.242
Max		1033.903	1035.074	3.040
Average		1022.136	1021.127	-1.008
Min		1011.108	1008.118	-2.986
Std Dev		5.406	5.888	1.716

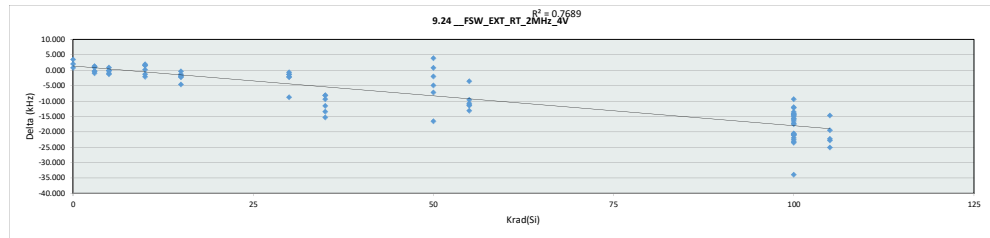


9.88_FSW_EXT_RT_1MHz_14V											
Test Site	Tester	Unit	Max Limit	Min Limit							
Test Number											
Unit		kHz	kHz								
Max Limit		1100	1100								
Min Limit		900	900								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1023.429	1013.085	1012.725	1013.858	1011.425	1011.597	1011.352	1016.292	1008.118	1010.841	1011.362
Average	1024.456	1023.397	1021.184	1023.403	1021.063	1022.915	1020.405	1025.085	1016.945	1019.912	1018.512
Max	1025.586	1031.164	1034.276	1031.032	1035.074	1031.041	1032.531	1031.230	1029.597	1028.450	1025.404
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000

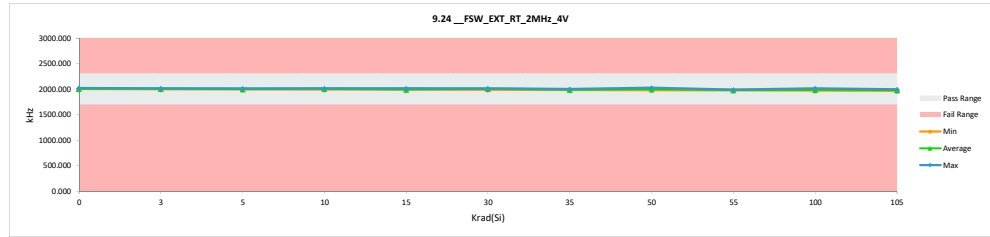


LDR TID Report
TPS7H5001-SP

9.24_FSW_EXT_RT_2MHz_4V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	2300	2300		
Min Limit	1700	1700		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	2023.508	2025.746	2.238
0	118	2001.757	2002.727	0.970
0	122	2005.692	2009.369	3.677
3	61	1997.022	1996.747	-0.275
3	62	2013.080	2014.639	1.559
3	63	2010.961	2010.901	-0.060
3	64	2016.920	2018.351	1.431
3	65	2019.328	2020.417	1.089
3	66	2016.633	2015.897	-0.736
5	67	1995.582	1995.654	0.072
5	68	2004.039	2002.902	-1.137
5	69	2012.047	2011.293	-0.754
5	70	2019.587	2019.647	0.060
5	71	2003.940	2004.122	0.182
5	72	1991.611	1992.695	1.084
10	73	1997.022	1998.701	1.679
10	74	2013.080	2014.936	1.856
10	75	2010.961	2009.877	-1.084
10	76	2016.920	2014.992	-1.928
10	77	2019.328	2019.735	0.407
10	78	2016.633	2018.816	2.183
15	79	1995.582	1993.568	-2.014
15	80	2004.039	2003.892	-0.147
15	81	2012.047	2007.637	-4.410
15	82	2019.587	2018.466	-1.121
15	83	2003.940	2002.380	-1.560
15	84	1991.611	1989.642	-1.969
30	86	1997.022	1995.971	-1.051
30	89	2013.080	2011.052	-2.028
30	91	2010.961	2002.385	-8.576
30	92	2016.920	2014.966	-1.954
30	94	2019.328	2018.092	-1.236
30	95	2016.633	2016.217	-0.416
35	100	1995.582	1986.376	-9.206
35	101	2004.039	1992.634	-11.405
35	102	2012.047	1998.761	-13.286
35	103	2019.587	2004.438	-15.149
35	104	2003.940	1995.936	-8.004
35	105	1991.611	1983.649	-7.962
50	106	2002.471	2000.658	-1.813
50	107	1993.519	1986.541	-6.978
50	108	2048.079	2031.658	-16.421
50	109	2004.684	1999.961	-4.723
50	110	1996.240	1997.218	0.978
50	111	2012.861	2016.961	4.100
55	113	2005.858	1992.934	-12.924
55	114	1996.174	1986.794	-9.380
55	115	1992.387	1981.278	-11.109
55	116	1977.691	1974.263	-3.428
55	126	2002.051	1991.503	-10.548
55	128	2005.270	1993.957	-11.313
100	129	1992.369	1978.430	-13.939
100	131	2019.744	2007.774	-11.970
100	132	2031.054	2009.250	-21.804
100	133	1995.935	1981.008	-14.927
100	134	2008.763	1995.391	-13.372
100	135	2042.157	2008.398	-33.759
100	138	1993.894	1976.597	-17.297
100	139	2032.074	2008.725	-23.349
100	140	2013.620	2004.424	-9.196
100	141	2009.016	1988.297	-20.719
100	142	2005.849	1990.283	-15.566
100	143	1993.605	1973.296	-20.309
100	144	2012.828	1991.854	-20.974
100	220	2011.347	1994.343	-17.004
100	221	2005.736	1991.468	-14.268
100	224	2010.572	1994.287	-16.285
100	225	2037.975	2017.328	-20.647
100	226	2001.795	1987.793	-14.002
100	228	2000.855	1986.439	-14.416
100	232	2009.142	1993.478	-15.664
100	233	2019.858	2008.059	-11.799
100	236	2017.787	1995.362	-22.425
100	238	2022.068	1999.018	-23.050
100	243	1993.908	1973.359	-20.549
105	244	1992.496	1969.833	-22.663
105	246	2013.902	1991.842	-22.060
105	248	2018.358	1999.112	-19.246
105	374	1991.605	1966.720	-24.885
105	376	2015.153	2000.609	-14.544
	Max	2048.079	2031.658	4.100
	Average	2008.549	1999.884	-8.665
	Min	1977.691	1966.720	-33.759
	Std Dev	12.518	13.926	8.952

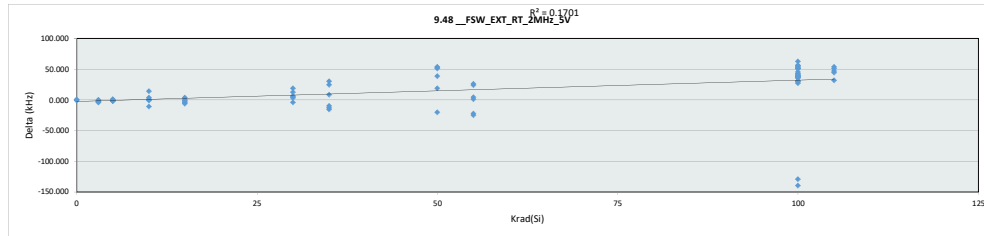


9.24_FSW_EXT_RT_2MHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	2300	kHz									
Min Limit	1700	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min	2002.727	1996.747	1992.695	1998.701	1989.642	1995.971	1983.649	1986.541	1974.263	1973.296	1966.720
Average	2012.614	2012.825	2004.386	2012.843	2002.598	2009.781	1993.632	2005.500	1986.788	1993.944	1985.623
Max	2025.746	2020.417	2019.647	2019.735	2018.466	2018.092	2004.438	2031.658	1993.957	2017.328	2000.609
UL	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000

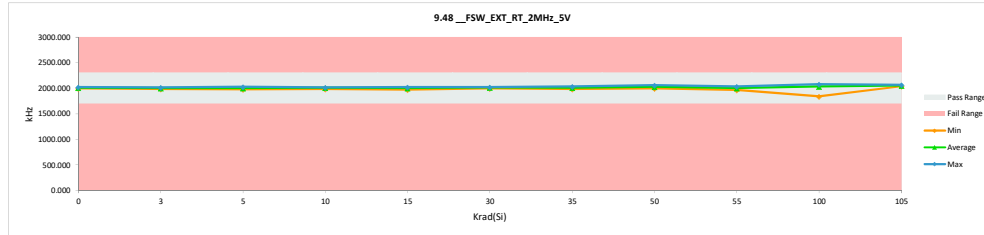


LDR TID Report
TPS7H5001-SP

9.48_FSW_EXT_RT_2MHz_5V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	2300	2300		
Min Limit	1700	1700		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	2001.204	2002.346	1.142
0	118	2007.489	2007.550	0.061
0	122	2020.877	2022.769	1.892
3	61	2010.382	2007.273	-3.109
3	62	2015.456	2012.991	-2.465
3	63	1988.663	1987.751	-0.912
3	64	2017.967	2018.290	0.323
3	65	2003.998	2002.594	-1.404
3	66	2012.483	2013.544	1.061
5	67	2006.492	2008.817	2.325
5	68	2007.270	2006.816	-0.454
5	69	1979.653	1979.388	-0.265
5	70	2030.783	2031.872	1.089
5	71	2009.942	2009.148	-0.794
5	72	2002.110	2002.872	0.762
10	73	2010.382	2000.543	-9.839
10	74	2015.456	2017.434	1.978
10	75	1988.663	1989.950	1.287
10	76	2017.967	2018.155	0.188
10	77	2003.998	2019.107	15.109
10	78	2012.483	2017.217	4.734
15	79	2006.492	2011.449	4.957
15	80	2007.270	2007.280	0.010
15	81	1979.653	1976.148	-3.505
15	82	2030.783	2025.672	-5.111
15	83	2009.942	2012.020	2.078
15	84	2002.110	2000.011	-2.099
30	86	2010.382	2007.488	-2.894
30	89	2015.456	2021.360	5.904
30	91	1988.663	2002.414	13.751
30	92	2017.967	2022.343	4.376
30	94	2003.998	2023.847	19.849
30	95	2012.483	2019.848	7.365
35	100	2006.492	2037.831	31.339
35	101	2007.270	1998.648	-8.622
35	102	1979.653	2005.197	25.544
35	103	2030.783	2018.958	-11.825
35	104	2009.942	2019.213	9.271
35	105	2002.110	1987.463	-14.647
50	106	2008.332	2060.823	52.491
50	107	1990.873	2010.659	19.786
50	108	1997.606	2052.268	54.662
50	109	2015.657	1996.780	-18.877
50	110	2005.850	2059.907	54.057
50	111	1981.006	2020.625	39.619
55	113	2009.724	2035.006	25.282
55	114	1998.642	2026.052	27.410
55	115	2001.947	2003.939	1.992
55	116	1989.818	1968.767	-21.051
55	126	2007.673	1984.087	-23.586
55	128	2008.646	2014.259	5.613
100	129	2008.291	2060.763	52.472
100	131	2026.050	2065.102	39.052
100	132	2040.527	2080.941	40.414
100	133	2008.754	2066.027	57.273
100	134	2022.495	2074.495	52.000
100	135	1982.894	1844.614	-138.280
100	138	2002.880	2059.730	56.850
100	139	2011.298	2047.868	36.570
100	140	2017.494	2061.103	43.609
100	141	2001.669	2033.442	31.773
100	142	2018.753	1890.593	-128.160
100	143	1996.672	2043.197	46.525
100	144	2015.989	2067.735	51.746
100	220	2023.464	2053.778	30.314
100	221	2010.524	2049.897	39.373
100	224	2007.539	2050.753	43.214
100	225	2007.875	2047.913	40.038
100	226	2004.278	2057.932	53.654
100	228	2008.405	2071.860	63.455
100	232	2018.803	2074.394	55.591
100	233	2021.978	2053.737	31.759
100	236	1978.872	2007.011	28.139
100	238	2024.573	2065.979	41.406
100	243	2001.873	2039.521	37.648
105	244	1993.588	2041.423	47.835
105	246	2009.189	2063.965	54.776
105	248	2020.812	2066.145	45.333
105	374	1992.688	2045.139	52.451
105	376	2016.309	2049.182	32.873
	Max	2040.527	2080.941	63.455
	Average	2007.443	2023.000	15.557
	Min	1978.872	1844.614	-138.280
	Std Dev	12.705	36.868	33.580

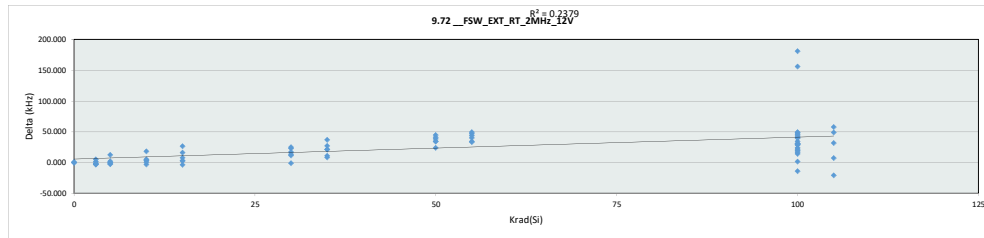


9.48_FSW_EXT_RT_2MHz_5V											
Test Site											
Tester											
Test Number											
Max Limit	2300	kHz									
Min Limit	1700	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min	2002.346	1987.751	1979.388	1989.950	1976.148	2002.414	1987.463	1996.780	1968.767	1844.614	2041.423
Average	2010.888	2007.074	2006.486	2010.401	2005.430	2016.217	2011.218	2033.510	2005.352	2040.308	2053.171
Max	2022.769	2018.290	2031.872	2019.107	2025.672	2023.847	2037.831	2060.823	2035.006	2080.941	2066.145
UL	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000

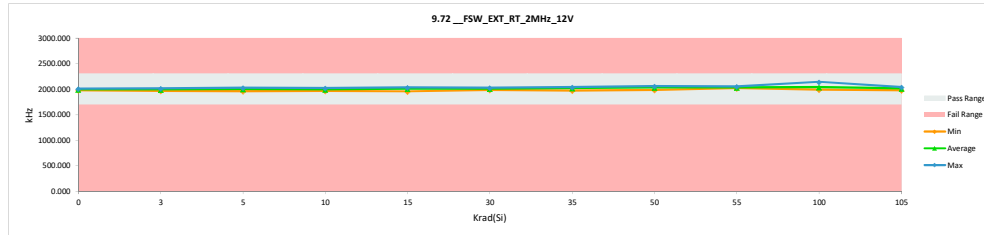


LDR TID Report
TPS7H5001-SP

9.72_FSW_EXT_RT_2MHz_12V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	2300	2300		
Min Limit	1700	1700		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1982.972	1983.936	0.964
0	118	2012.898	2013.201	0.303
0	122	2009.199	2011.075	1.876
3	61	2016.957	2017.047	0.090
3	62	2018.609	2020.975	2.366
3	63	1969.194	1968.078	-1.116
3	64	1993.494	1991.170	-2.324
3	65	1988.109	1986.109	-2.000
3	66	1984.093	1990.191	6.098
5	67	2016.338	2017.737	1.399
5	68	2005.131	2004.018	-1.113
5	69	1961.563	1962.149	0.586
5	70	2017.945	2031.567	13.622
5	71	1994.290	1992.924	-1.366
5	72	2007.119	2010.362	3.243
10	73	2016.957	2021.209	4.252
10	74	2018.609	2023.868	5.259
10	75	1969.194	1970.733	1.539
10	76	1993.494	2012.656	19.162
10	77	1988.109	1986.278	-1.831
10	78	1984.093	1990.308	6.215
15	79	2016.338	2025.148	8.810
15	80	2005.131	2009.045	3.914
15	81	1961.563	1958.882	-2.681
15	82	2017.945	2035.169	17.224
15	83	1994.290	2021.934	27.644
15	84	2007.119	2010.504	3.385
30	86	2016.957	2031.595	14.638
30	89	2018.609	2031.243	12.634
30	91	1969.194	1987.203	18.009
30	92	1993.494	2017.649	24.155
30	94	1988.109	1988.056	-0.053
30	95	1984.093	2009.972	25.879
35	100	2016.338	2038.931	22.593
35	101	2005.131	2014.623	9.492
35	102	1961.563	1973.280	11.717
35	103	2017.945	2040.540	22.595
35	104	1994.290	2032.140	37.850
35	105	2007.119	2034.998	27.879
50	106	2018.758	2054.307	35.549
50	107	2002.646	2041.934	39.288
50	108	1980.379	2022.524	42.145
50	109	2017.433	2062.966	45.533
50	110	2017.314	2052.605	35.291
50	111	1963.476	1988.531	25.055
55	113	1984.658	2032.242	47.584
55	114	1970.891	2021.534	50.643
55	115	2000.666	2035.190	34.524
55	116	1995.892	2036.713	40.821
55	126	2011.177	2055.867	44.690
55	128	2000.420	2035.838	35.418
100	129	2013.590	2045.331	31.741
100	131	2008.828	2040.530	31.702
100	132	2023.213	2070.709	47.496
100	133	2012.165	2047.537	35.372
100	134	2017.629	2058.166	40.537
100	135	1966.018	2123.195	157.177
100	138	1999.090	2044.697	45.607
100	139	1992.131	2022.298	30.167
100	140	2001.269	2043.002	41.733
100	141	1973.350	1993.066	19.716
100	142	2014.042	2063.237	49.195
100	143	2004.535	2021.567	17.032
100	144	2021.518	2042.424	20.906
100	220	2033.885	2020.960	-12.925
100	221	2013.798	2016.355	2.557
100	224	1991.006	2034.479	43.473
100	225	1990.429	2020.017	29.588
100	226	2008.332	2031.310	22.978
100	228	2018.925	2050.271	31.346
100	232	2031.257	2056.421	25.164
100	233	1990.509	2021.887	31.378
100	236	1964.552	2146.612	182.060
100	238	1997.122	2047.768	50.646
100	243	1996.059	2011.087	15.028
105	244	2000.347	2008.708	8.361
105	246	1983.611	2042.111	58.500
105	248	1994.498	2027.238	32.740
105	374	1977.431	2027.464	50.033
105	376	2001.693	1981.810	-19.883
	Max	2033.885	2146.612	182.060
	Average	1999.102	2023.338	24.236
	Min	1961.563	1958.882	-19.883
	Std Dev	18.120	30.893	29.635

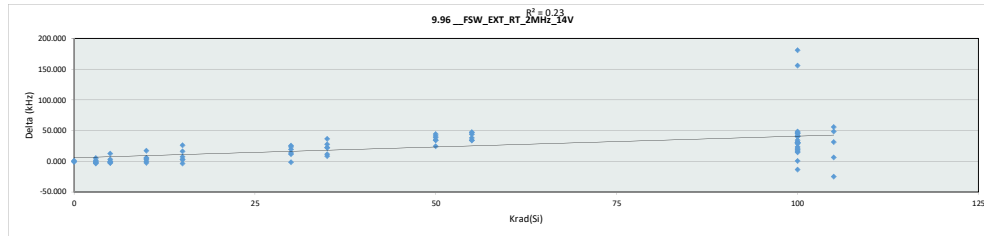


9.72_FSW_EXT_RT_2MHz_12V											
Test Site											
Tester											
Test Number											
Max Limit	2300	kHz									
Min Limit	1700	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min	1983.936	1968.078	1962.149	1970.733	1958.882	1987.203	1973.280	1988.531	2021.534	1993.066	1981.810
Average	2002.737	1995.595	2003.126	2000.842	2010.114	2010.953	2022.419	2037.145	2036.231	2044.705	2017.466
Max	2013.201	2020.975	2031.567	2023.868	2035.169	2031.595	2040.540	2062.966	2055.867	2146.612	2042.111
UL	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000

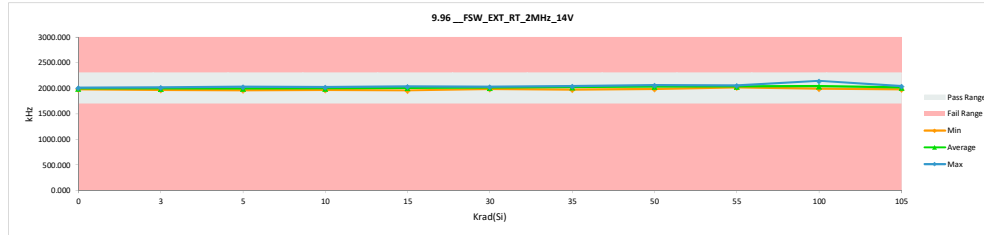


LDR TID Report
TPS7H5001-SP

9.96_FSW_EXT_RT_2MHz_14V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	2300	2300		
Min Limit	1700	1700		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1982.915	1983.758	0.843
0	118	2012.703	2013.065	0.362
0	122	2008.769	2010.867	2.098
3	61	2016.400	2016.911	0.511
3	62	2018.336	2020.942	2.606
3	63	1968.601	1967.894	-0.707
3	64	1993.417	1990.784	-2.633
3	65	1987.840	1986.178	-1.662
3	66	1983.859	1990.128	6.269
5	67	2016.495	2017.640	1.145
5	68	2004.941	2003.702	-1.239
5	69	1961.210	1961.465	0.255
5	70	2017.511	2031.112	13.601
5	71	1994.259	1992.658	-1.601
5	72	2005.896	2010.342	4.446
10	73	2016.400	2020.934	4.534
10	74	2018.336	2024.191	5.855
10	75	1968.601	1969.966	1.365
10	76	1993.417	2011.901	18.484
10	77	1987.840	1986.262	-1.578
10	78	1983.859	1990.940	7.081
15	79	2016.495	2025.165	8.670
15	80	2004.941	2008.771	3.830
15	81	1961.210	1958.626	-2.584
15	82	2017.511	2035.030	17.519
15	83	1994.259	2021.622	27.363
15	84	2005.896	2010.244	4.348
30	86	2016.400	2031.131	14.731
30	89	2018.336	2031.018	12.682
30	91	1968.601	1988.839	20.238
30	92	1993.417	2017.909	24.492
30	94	1987.840	1987.523	-0.317
30	95	1983.859	2010.561	26.702
35	100	2016.495	2039.613	23.118
35	101	2004.941	2014.262	9.321
35	102	1961.210	1973.660	12.450
35	103	2017.511	2040.823	23.312
35	104	1994.259	2032.102	37.843
35	105	2005.896	2034.843	28.947
50	106	2018.246	2053.996	35.750
50	107	2002.005	2041.689	39.684
50	108	1980.000	2022.495	42.495
50	109	2017.093	2062.471	45.378
50	110	2016.963	2052.620	35.657
50	111	1963.190	1988.857	25.667
55	113	1984.471	2032.220	47.749
55	114	1970.548	2019.123	48.575
55	115	2000.165	2034.944	34.779
55	116	1997.635	2036.696	39.061
55	126	2010.903	2055.873	44.970
55	128	2000.000	2035.353	35.353
100	129	2013.097	2044.789	31.692
100	131	2008.324	2039.623	31.299
100	132	2023.025	2070.243	47.218
100	133	2011.535	2047.279	35.744
100	134	2016.881	2058.132	41.251
100	135	1965.645	2122.393	156.748
100	138	1998.038	2044.642	46.604
100	139	1991.795	2022.204	30.409
100	140	2000.955	2042.900	41.945
100	141	1973.312	1992.532	19.220
100	142	2013.510	2063.287	49.777
100	143	2004.422	2020.986	16.564
100	144	2021.420	2041.882	20.462
100	220	2033.181	2020.658	-12.523
100	221	2014.090	2015.660	1.570
100	224	1990.482	2033.903	43.421
100	225	1990.083	2019.742	29.659
100	226	2009.138	2031.444	22.306
100	228	2018.378	2050.173	31.795
100	232	2031.208	2055.711	24.503
100	233	1990.180	2022.159	31.979
100	236	1964.300	2146.471	182.171
100	238	1996.626	2044.182	47.556
100	243	1996.188	2011.719	15.531
105	244	2000.241	2007.634	7.393
105	246	1983.747	2040.881	57.134
105	248	1994.573	2026.946	32.373
105	374	1977.505	2027.338	49.833
105	376	2004.640	1980.654	-23.986
	Max	2033.181	2146.471	182.171
	Average	1998.855	2023.074	24.218
	Min	1961.210	1958.626	-23.986
	Std Dev	18.107	30.817	29.615



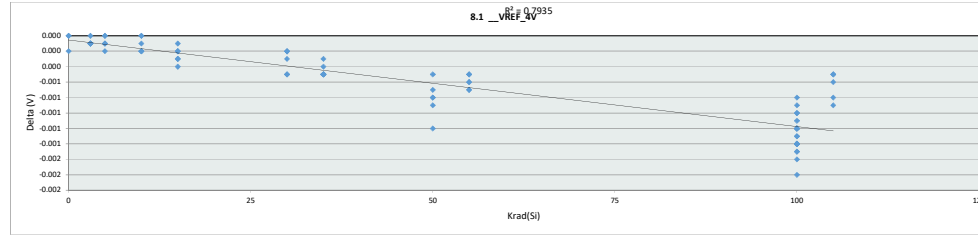
9.96_FSW_EXT_RT_2MHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	2300	kHz									
Min Limit	1700	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min	1983.758	1967.894	1961.465	1969.966	1958.626	1987.523	1973.660	1988.857	2019.123	1992.532	1980.654
Average	2002.563	1995.473	2002.820	2000.699	2009.910	2011.164	2022.551	2037.021	2035.702	2044.280	2016.691
Max	2013.065	2020.942	2031.112	2024.191	2035.030	2031.131	2040.823	2062.471	2055.873	2146.471	2040.881
UL	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000



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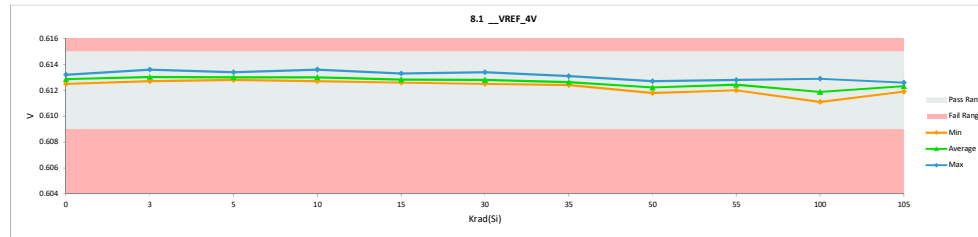
		8.1_VREF_4V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	0.615	0.615	
Min Limit	0.609	0.609	

Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	0.613	0.613	0.000
0	118	0.613	0.613	0.000
0	122	0.613	0.613	0.000
3	61	0.613	0.613	0.000
3	62	0.613	0.613	0.000
3	63	0.613	0.613	0.000
3	64	0.613	0.613	0.000
3	65	0.613	0.613	0.000
3	66	0.614	0.614	0.000
5	67	0.613	0.613	0.000
5	68	0.613	0.613	0.000
5	69	0.613	0.613	0.000
5	70	0.613	0.613	0.000
5	71	0.613	0.613	0.000
5	72	0.613	0.613	0.000
10	73	0.613	0.613	0.000
10	74	0.613	0.613	0.000
10	75	0.613	0.613	0.000
10	76	0.613	0.613	0.000
10	77	0.613	0.613	0.000
10	78	0.614	0.614	0.000
15	79	0.613	0.613	0.000
15	80	0.613	0.613	0.000
15	81	0.613	0.613	0.000
15	82	0.613	0.613	0.000
15	83	0.613	0.613	0.000
15	84	0.613	0.613	0.000
30	86	0.613	0.613	0.000
30	89	0.613	0.613	0.000
30	91	0.613	0.613	0.000
30	92	0.613	0.613	0.000
30	94	0.613	0.613	0.000
30	95	0.614	0.613	0.000
35	100	0.613	0.613	0.000
35	101	0.613	0.612	-0.001
35	102	0.613	0.612	-0.001
35	103	0.613	0.613	-0.001
35	104	0.613	0.613	0.000
35	105	0.613	0.613	0.000
50	106	0.613	0.613	-0.001
50	107	0.613	0.612	-0.001
50	108	0.613	0.612	-0.001
50	109	0.613	0.612	-0.001
50	110	0.613	0.613	-0.001
50	111	0.613	0.612	-0.001
55	113	0.613	0.613	-0.001
55	114	0.613	0.612	-0.001
55	115	0.613	0.612	-0.001
55	116	0.613	0.613	-0.001
55	126	0.613	0.612	-0.001
55	128	0.613	0.613	-0.001
100	129	0.613	0.612	-0.001
100	131	0.613	0.612	-0.001
100	132	0.614	0.612	-0.001
100	133	0.613	0.612	-0.001
100	134	0.613	0.612	-0.001
100	135	0.613	0.611	-0.002
100	138	0.613	0.612	-0.001
100	139	0.613	0.612	-0.001
100	140	0.613	0.612	-0.001
100	141	0.613	0.611	-0.002
100	142	0.613	0.612	-0.001
100	143	0.613	0.612	-0.001
100	144	0.613	0.612	-0.001
100	220	0.613	0.611	-0.001
100	221	0.613	0.612	-0.001
100	224	0.613	0.612	-0.002
100	225	0.614	0.613	-0.001
100	226	0.613	0.612	-0.001
100	228	0.613	0.612	-0.001
100	232	0.613	0.611	-0.001
100	233	0.613	0.612	-0.001
100	236	0.613	0.612	-0.001
100	238	0.613	0.612	-0.001
100	243	0.613	0.612	-0.001
105	244	0.613	0.612	-0.001
105	246	0.613	0.613	0.000
105	248	0.613	0.613	-0.001
105	374	0.613	0.612	-0.001
105	376	0.613	0.612	-0.001
Max		0.614	0.614	0.000
Average		0.613	0.612	-0.001
Min		0.613	0.611	-0.002
Std Dev		0.000	0.001	0.001



		8.1_VREF_4V	
Test Site			
Tester			
Test Number			
Max Limit	0.615	V	
Min Limit	0.609	V	

Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609
Min	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.611	0.612
Average	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.612
Max	0.613	0.614	0.613	0.614	0.613	0.613	0.613	0.613	0.613	0.613	0.613
UL	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615

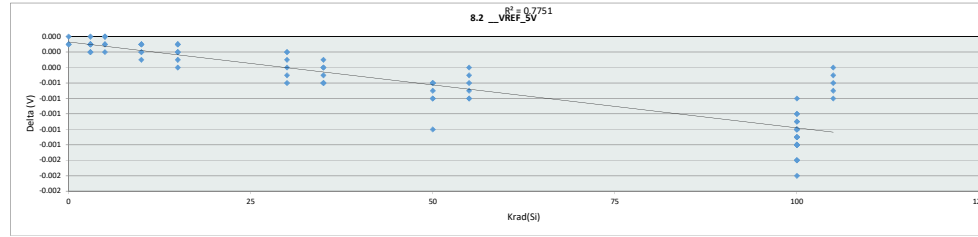


LDR TID Report
TPS7H5001-SP

		8.2_VREF_5V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	0.615	0.615	
Min Limit	0.609	0.609	

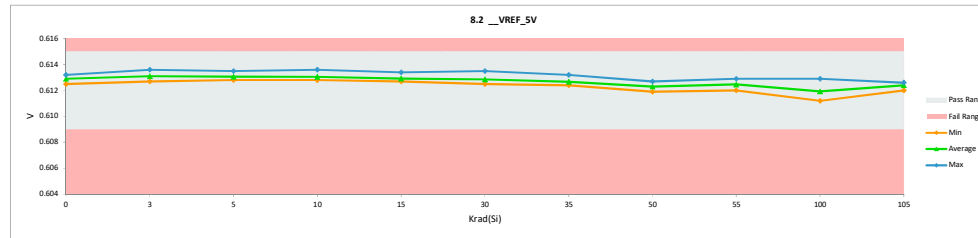
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	0.613	0.613	0.000
0	118	0.613	0.613	0.000
0	122	0.613	0.613	0.000
3	61	0.613	0.613	0.000
3	62	0.613	0.613	0.000
3	63	0.613	0.613	0.000
3	64	0.613	0.613	0.000
3	65	0.613	0.613	0.000
3	66	0.614	0.614	0.000
5	67	0.613	0.613	0.000
5	68	0.613	0.613	0.000
5	69	0.613	0.613	0.000
5	70	0.613	0.613	0.000
5	71	0.613	0.613	0.000
5	72	0.613	0.613	0.000
10	73	0.613	0.613	0.000
10	74	0.613	0.613	0.000
10	75	0.613	0.613	0.000
10	76	0.613	0.613	0.000
10	77	0.613	0.613	0.000
10	78	0.614	0.614	0.000
15	79	0.613	0.613	0.000
15	80	0.613	0.613	0.000
15	81	0.613	0.613	0.000
15	82	0.613	0.613	0.000
15	83	0.613	0.613	0.000
15	84	0.613	0.613	0.000
30	86	0.613	0.613	-0.001
30	89	0.613	0.613	0.000
30	91	0.613	0.613	0.000
30	92	0.613	0.613	0.000
30	94	0.613	0.613	0.000
30	95	0.614	0.613	0.000
35	100	0.613	0.613	-0.001
35	101	0.613	0.612	-0.001
35	102	0.613	0.613	0.000
35	103	0.613	0.613	0.000
35	104	0.613	0.613	0.000
35	105	0.613	0.613	0.000
50	106	0.613	0.613	-0.001
50	107	0.613	0.612	-0.001
50	108	0.613	0.612	-0.001
50	109	0.613	0.612	-0.001
50	110	0.613	0.613	-0.001
50	111	0.613	0.612	-0.001
55	113	0.613	0.613	-0.001
55	114	0.613	0.612	-0.001
55	115	0.613	0.612	-0.001
55	116	0.613	0.613	-0.001
55	126	0.613	0.612	-0.001
55	128	0.613	0.613	0.000
100	129	0.613	0.612	-0.001
100	131	0.613	0.612	-0.001
100	132	0.614	0.612	-0.002
100	133	0.614	0.613	-0.001
100	134	0.613	0.612	-0.001
100	135	0.613	0.611	-0.002
100	138	0.613	0.612	-0.001
100	139	0.613	0.612	-0.001
100	140	0.613	0.612	-0.001
100	141	0.613	0.612	-0.002
100	142	0.613	0.612	-0.001
100	143	0.613	0.612	-0.001
100	144	0.613	0.612	-0.001
100	220	0.613	0.612	-0.001
100	221	0.613	0.612	-0.001
100	224	0.613	0.612	-0.002
100	225	0.614	0.613	-0.001
100	226	0.613	0.612	-0.001
100	228	0.613	0.612	-0.001
100	232	0.613	0.611	-0.001
100	233	0.613	0.612	-0.001
100	236	0.614	0.612	-0.001
100	238	0.613	0.612	-0.001
100	243	0.613	0.612	-0.001
105	244	0.613	0.612	-0.001
105	246	0.613	0.613	-0.001
105	248	0.613	0.613	0.000
105	374	0.613	0.613	0.000
105	376	0.613	0.612	-0.001

Max	0.614	0.614	0.000
Average	0.613	0.613	-0.001
Min	0.613	0.611	-0.002
Std Dev	0.000	0.001	0.001



		8.2_VREF_5V	
Test Site			
Tester			
Test Number			
Max Limit	0.615	V	
Min Limit	0.609	V	

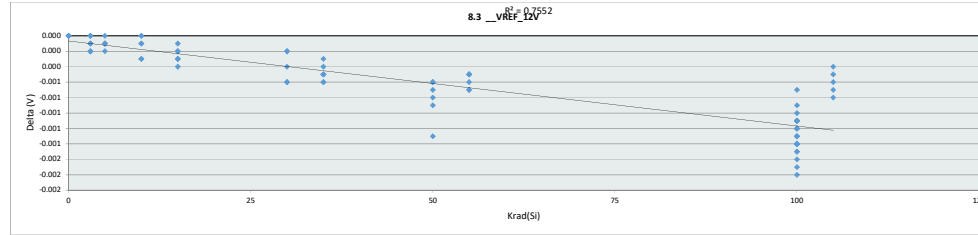
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609
Min	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.611	0.612
Average	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.612
Max	0.613	0.614	0.614	0.614	0.613	0.614	0.613	0.613	0.613	0.613	0.613
UL	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615



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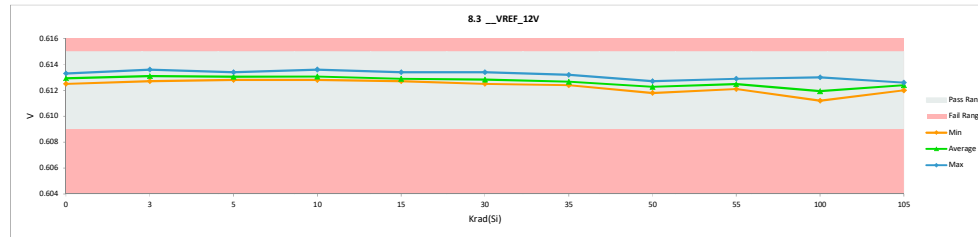
		8.3_VREF_12V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	0.615	0.615	
Min Limit	0.609	0.609	

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	0.613	0.613	0.000
0	118	0.613	0.613	0.000
0	122	0.613	0.613	0.000
3	61	0.613	0.613	0.000
3	62	0.613	0.613	0.000
3	63	0.613	0.613	0.000
3	64	0.613	0.613	0.000
3	65	0.613	0.613	0.000
3	66	0.614	0.614	0.000
5	67	0.613	0.613	0.000
5	68	0.613	0.613	0.000
5	69	0.613	0.613	0.000
5	70	0.613	0.613	0.000
5	71	0.613	0.613	0.000
5	72	0.613	0.613	0.000
10	73	0.613	0.613	0.000
10	74	0.613	0.613	0.000
10	75	0.613	0.613	0.000
10	76	0.613	0.613	0.000
10	77	0.613	0.613	0.000
10	78	0.614	0.614	0.000
15	79	0.613	0.613	0.000
15	80	0.613	0.613	0.000
15	81	0.613	0.613	0.000
15	82	0.613	0.613	0.000
15	83	0.613	0.613	0.000
15	84	0.613	0.613	0.000
30	86	0.613	0.613	-0.001
30	89	0.613	0.613	-0.001
30	91	0.613	0.613	0.000
30	92	0.613	0.613	0.000
30	94	0.613	0.613	0.000
30	95	0.614	0.613	0.000
35	100	0.613	0.613	-0.001
35	101	0.613	0.612	-0.001
35	102	0.613	0.613	0.000
35	103	0.613	0.613	-0.001
35	104	0.613	0.613	-0.001
35	105	0.613	0.613	0.000
50	106	0.613	0.613	-0.001
50	107	0.613	0.612	-0.001
50	108	0.613	0.612	-0.001
50	109	0.613	0.612	-0.001
50	110	0.613	0.613	-0.001
50	111	0.613	0.612	-0.001
55	113	0.613	0.613	-0.001
55	114	0.613	0.612	-0.001
55	115	0.613	0.612	-0.001
55	116	0.613	0.613	0.000
55	126	0.613	0.612	-0.001
55	128	0.613	0.613	0.000
100	129	0.613	0.612	-0.001
100	131	0.613	0.612	-0.001
100	132	0.614	0.612	-0.002
100	133	0.614	0.613	-0.001
100	134	0.613	0.612	-0.001
100	135	0.613	0.611	-0.002
100	138	0.613	0.612	-0.002
100	139	0.613	0.612	-0.001
100	140	0.613	0.612	-0.001
100	141	0.613	0.612	-0.002
100	142	0.613	0.612	-0.001
100	143	0.613	0.612	-0.001
100	144	0.613	0.612	-0.001
100	220	0.613	0.611	-0.001
100	221	0.613	0.612	-0.001
100	224	0.613	0.612	-0.002
100	225	0.614	0.613	-0.001
100	226	0.613	0.612	-0.001
100	228	0.613	0.612	-0.001
100	232	0.613	0.611	-0.001
100	233	0.613	0.612	-0.001
100	236	0.614	0.613	-0.001
100	238	0.613	0.612	-0.001
100	243	0.613	0.612	-0.001
105	244	0.613	0.612	-0.001
105	246	0.613	0.613	-0.001
105	248	0.613	0.613	0.000
105	374	0.613	0.613	0.000
105	376	0.613	0.612	-0.001
Max		0.614	0.614	0.000
Average		0.613	0.613	-0.001
Min		0.613	0.611	-0.002
Std Dev		0.000	0.001	0.001



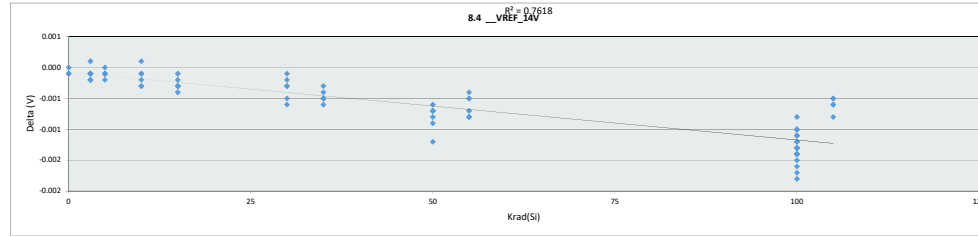
		8.3_VREF_12V	
Test Site			
Tester			
Test Number			
Max Limit	0.615	V	
Min Limit	0.609	V	

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609
Min	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.611	0.612
Average	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.612
Max	0.613	0.614	0.613	0.614	0.613	0.613	0.613	0.613	0.613	0.613	0.613
UL	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615

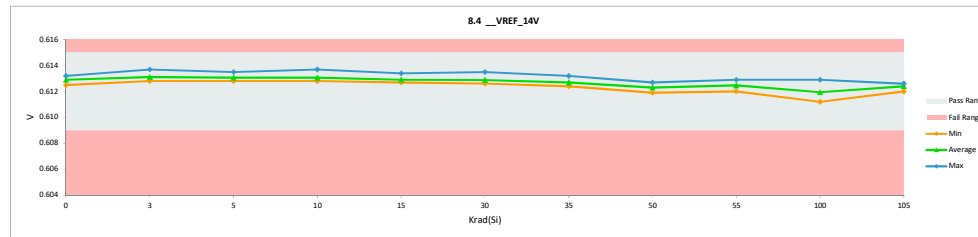


LDR TID Report
TPS7H5001-SP

		8.4_VREF_14V		
Test Site	Tester			
Test Number	Unit	V	V	
Max Limit	Min Limit	0.615	0.615	
		0.609	0.609	
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	0.613	0.613	0.000
0	118	0.613	0.613	0.000
0	122	0.613	0.613	0.000
3	61	0.613	0.613	0.000
3	62	0.613	0.613	0.000
3	63	0.613	0.613	0.000
3	64	0.613	0.613	0.000
3	65	0.613	0.613	0.000
3	66	0.614	0.614	0.000
5	67	0.613	0.613	0.000
5	68	0.613	0.613	0.000
5	69	0.613	0.613	0.000
5	70	0.613	0.613	0.000
5	71	0.613	0.613	0.000
5	72	0.613	0.613	0.000
10	73	0.613	0.613	0.000
10	74	0.613	0.613	0.000
10	75	0.613	0.613	0.000
10	76	0.613	0.613	0.000
10	77	0.613	0.613	0.000
10	78	0.614	0.614	0.000
15	79	0.613	0.613	0.000
15	80	0.613	0.613	0.000
15	81	0.613	0.613	0.000
15	82	0.613	0.613	0.000
15	83	0.613	0.613	0.000
15	84	0.613	0.613	0.000
30	86	0.613	0.613	-0.001
30	89	0.613	0.613	-0.001
30	91	0.613	0.613	0.000
30	92	0.613	0.613	0.000
30	94	0.613	0.613	0.000
30	95	0.614	0.613	0.000
35	100	0.613	0.613	0.000
35	101	0.613	0.612	-0.001
35	102	0.613	0.613	0.000
35	103	0.613	0.613	-0.001
35	104	0.613	0.613	-0.001
35	105	0.613	0.613	0.000
50	106	0.613	0.613	-0.001
50	107	0.613	0.612	-0.001
50	108	0.613	0.612	-0.001
50	109	0.613	0.612	-0.001
50	110	0.613	0.613	-0.001
50	111	0.613	0.612	-0.001
55	113	0.613	0.613	-0.001
55	114	0.613	0.612	-0.001
55	115	0.613	0.612	-0.001
55	116	0.613	0.613	0.000
55	126	0.613	0.612	-0.001
55	128	0.613	0.613	0.000
100	129	0.613	0.612	-0.001
100	131	0.613	0.612	-0.001
100	132	0.614	0.612	-0.002
100	133	0.614	0.613	-0.001
100	134	0.613	0.612	-0.001
100	135	0.613	0.611	-0.002
100	138	0.613	0.612	-0.001
100	139	0.613	0.612	-0.001
100	140	0.613	0.612	-0.001
100	141	0.613	0.612	-0.002
100	142	0.613	0.612	-0.001
100	143	0.613	0.612	-0.001
100	144	0.613	0.612	-0.001
100	220	0.613	0.611	-0.001
100	221	0.613	0.612	-0.001
100	224	0.613	0.612	-0.002
100	225	0.614	0.613	-0.001
100	226	0.613	0.612	-0.001
100	228	0.613	0.612	-0.001
100	232	0.613	0.611	-0.001
100	233	0.613	0.612	-0.001
100	236	0.614	0.613	-0.001
100	238	0.613	0.612	-0.001
100	243	0.613	0.612	-0.001
105	244	0.613	0.612	-0.001
105	246	0.613	0.613	-0.001
105	248	0.613	0.613	0.000
105	374	0.613	0.613	0.000
105	376	0.613	0.612	-0.001
Max		0.614	0.614	0.000
Average		0.613	0.613	-0.001
Min		0.613	0.611	-0.002
Std Dev		0.000	0.001	0.001



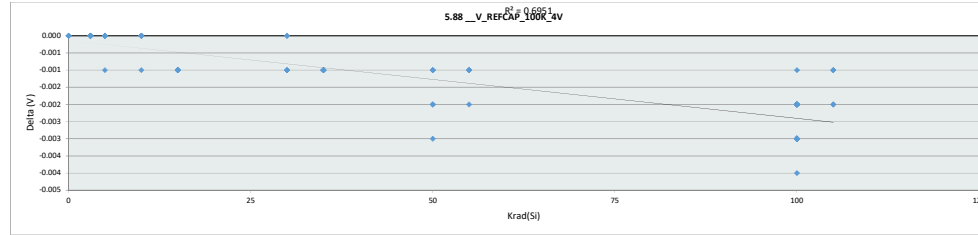
		8.4_VREF_14V									
Test Site	Tester										
Test Number	Unit	V	V								
Max Limit	Min Limit	0.615	0.609								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609
Min	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.611	0.612
Average	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.612
Max	0.613	0.614	0.614	0.614	0.613	0.614	0.613	0.613	0.613	0.613	0.613
UL	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615



LDR TID Report
TPS7H5001-SP

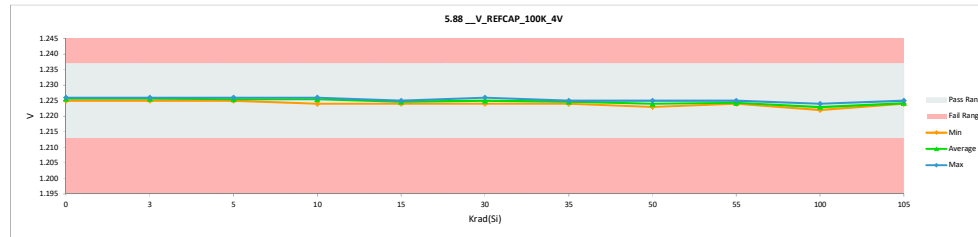
5.88_V_REFCAP_100K_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.225	-0.001
10	73	1.225	1.224	-0.001
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.224	-0.001
15	80	1.225	1.224	-0.001
15	81	1.226	1.225	-0.001
15	82	1.226	1.225	-0.001
15	83	1.226	1.225	-0.001
15	84	1.226	1.225	-0.001
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.225	-0.001
30	92	1.225	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.225	1.224	-0.001
55	115	1.225	1.224	-0.001
55	116	1.226	1.224	-0.002
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.225	1.223	-0.002
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.225	1.223	-0.002
100	143	1.225	1.223	-0.002
100	144	1.225	1.224	-0.001
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.223	-0.003
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.225	1.222	-0.003
100	243	1.225	1.223	-0.002
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.225	1.224	-0.001
105	374	1.225	1.224	-0.001
105	376	1.226	1.224	-0.002
Max		1.226	1.226	0.000
Average		1.226	1.224	-0.001
Min		1.225	1.222	-0.004
Std Dev		0.000	0.001	0.001



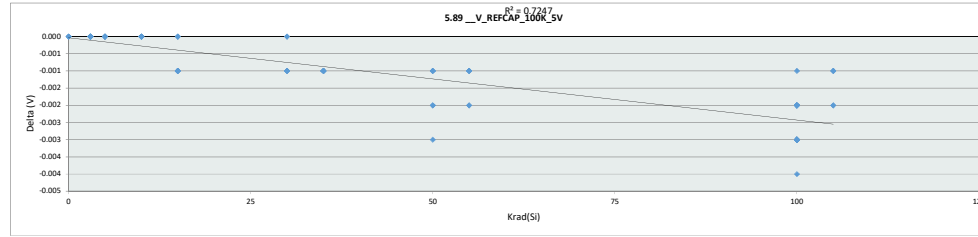
5.88_V_REFCAP_100K_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.224	1.224	1.224	1.224	1.223	1.224	1.222	1.224
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.224	1.223	1.224
Max	1.226	1.226	1.226	1.226	1.225	1.226	1.225	1.225	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

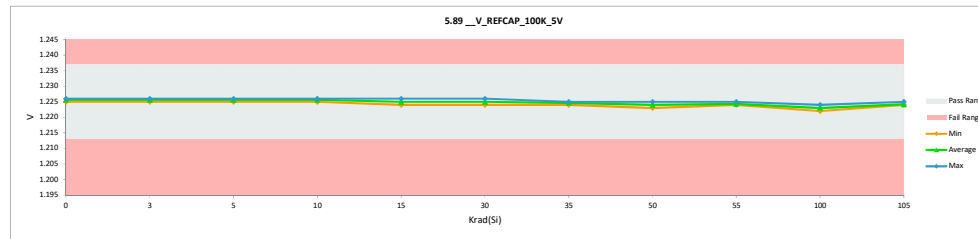


LDR TID Report
TPS7H5001-SP

5.89_V_REFCAP_100K_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.237	1.237		
Min Limit	1.213	1.213		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.225	0.000
15	80	1.225	1.224	-0.001
15	81	1.226	1.226	0.000
15	82	1.226	1.225	-0.001
15	83	1.226	1.225	-0.001
15	84	1.226	1.225	-0.001
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.225	-0.001
30	92	1.225	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.225	1.224	-0.001
55	115	1.226	1.224	-0.002
55	116	1.226	1.224	-0.002
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.223	-0.003
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.225	1.223	-0.002
100	143	1.225	1.223	-0.002
100	144	1.225	1.224	-0.001
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.223	-0.003
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.225	1.223	-0.002
100	243	1.225	1.223	-0.002
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.225	1.224	-0.001
105	374	1.225	1.224	-0.001
105	376	1.226	1.224	-0.002
Max		1.226	1.226	0.000
Average		1.226	1.224	-0.001
Min		1.225	1.222	-0.004
Std Dev		0.000	0.001	0.001



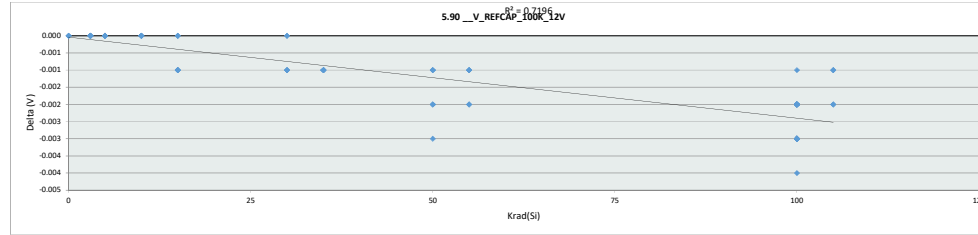
5.89_V_REFCAP_100K_5V											
Test Site											
Tester											
Test Number											
Max Limit	1.237 V										
Min Limit	1.213 V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.223	1.224	1.222	1.224
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.224	1.223	1.224
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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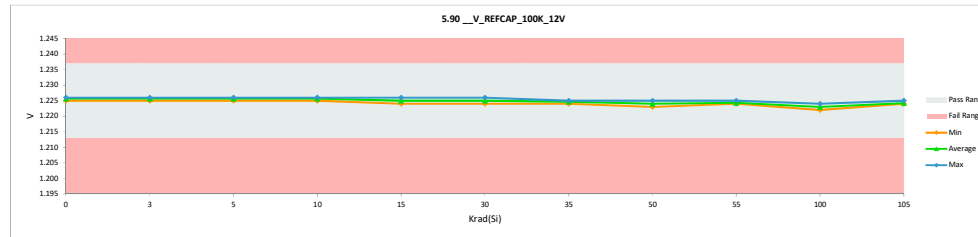
5.90_V_REFCAP_100K_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.225	0.000
15	80	1.225	1.224	-0.001
15	81	1.226	1.226	0.000
15	82	1.226	1.225	-0.001
15	83	1.226	1.225	-0.001
15	84	1.226	1.225	-0.001
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.225	-0.001
30	92	1.225	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.225	1.224	-0.001
55	115	1.226	1.224	-0.002
55	116	1.226	1.224	-0.002
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.225	1.223	-0.002
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.225	1.223	-0.002
100	143	1.225	1.223	-0.002
100	144	1.225	1.224	-0.001
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.223	-0.003
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.225	1.223	-0.002
100	243	1.225	1.223	-0.002
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.225	1.224	-0.001
105	374	1.225	1.224	-0.001
105	376	1.226	1.224	-0.002
Max		1.226	1.226	0.000
Average		1.226	1.224	-0.001
Min		1.225	1.222	-0.004
Std Dev		0.000	0.001	0.001



5.90_V_REFCAP_100K_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

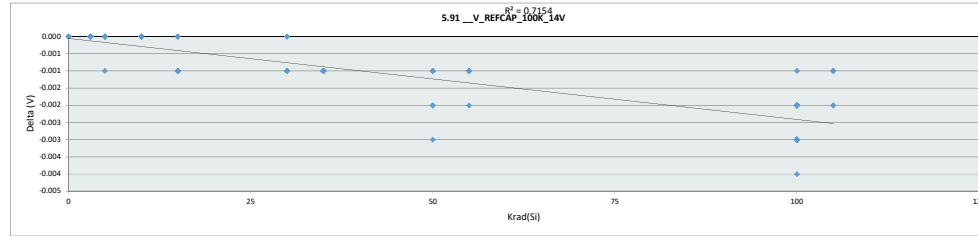
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.223	1.224	1.222	1.224
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.224	1.223	1.224
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



LDR TID Report
TPS7H5001-SP

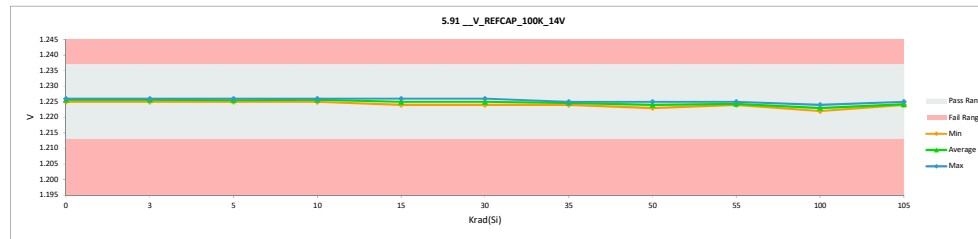
5.91_V_REFCAP_100K_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.225	-0.001
10	73	1.225	1.225	0.000
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.225	0.000
15	80	1.225	1.224	-0.001
15	81	1.226	1.226	0.000
15	82	1.226	1.225	-0.001
15	83	1.226	1.225	-0.001
15	84	1.226	1.225	-0.001
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.225	-0.001
30	92	1.225	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.225	1.224	-0.001
55	115	1.225	1.224	-0.001
55	116	1.226	1.224	-0.002
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.223	-0.003
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.225	1.223	-0.002
100	143	1.225	1.223	-0.002
100	144	1.225	1.224	-0.001
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.223	-0.003
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.225	1.223	-0.002
100	243	1.225	1.223	-0.002
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.225	1.224	-0.001
105	374	1.225	1.224	-0.001
105	376	1.226	1.224	-0.002
Max		1.226	1.226	0.000
Average		1.226	1.224	-0.001
Min		1.225	1.222	-0.004
Std Dev		0.000	0.001	0.001



5.91_V_REFCAP_100K_14V	
Test Site	
Tester	
Test Number	
Max Limit	1.237 V
Min Limit	1.213 V

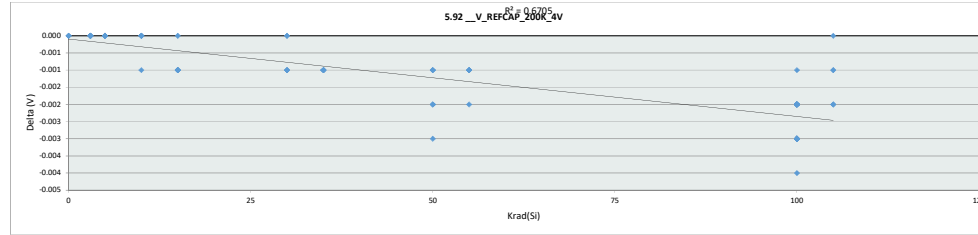
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.223	1.224	1.222	1.224
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.224	1.223	1.224
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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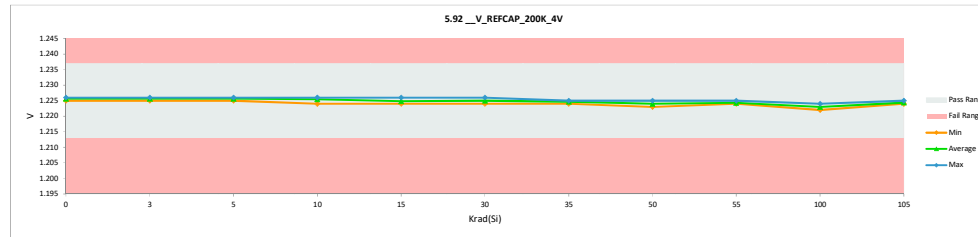
5.92_V_REFCAP_200K_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.224	-0.001
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.224	-0.001
15	80	1.225	1.224	-0.001
15	81	1.226	1.226	0.000
15	82	1.226	1.225	-0.001
15	83	1.226	1.225	-0.001
15	84	1.226	1.225	-0.001
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.225	-0.001
30	92	1.225	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.225	1.224	-0.001
55	115	1.225	1.224	-0.001
55	116	1.226	1.224	-0.002
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.225	1.223	-0.002
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.225	1.223	-0.002
100	143	1.225	1.223	-0.002
100	144	1.225	1.224	-0.001
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.223	-0.003
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.225	1.223	-0.002
100	243	1.225	1.223	-0.002
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.225	1.224	-0.001
105	374	1.225	1.225	0.000
105	376	1.226	1.224	-0.002
Max		1.226	1.226	0.000
Average		1.226	1.224	-0.001
Min		1.225	1.222	-0.004
Std Dev		0.000	0.001	0.001



5.92_V_REFCAP_200K_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

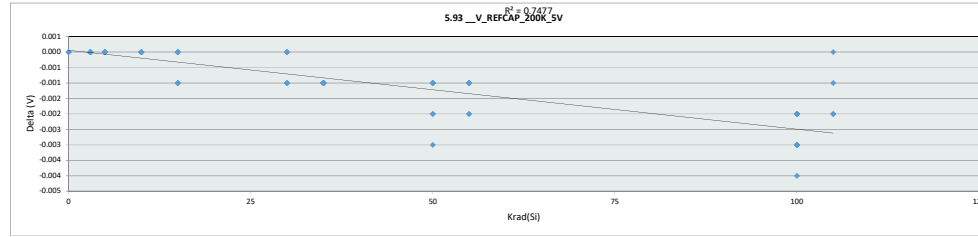
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.224	1.224	1.224	1.224	1.223	1.224	1.222	1.224
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.224	1.223	1.224
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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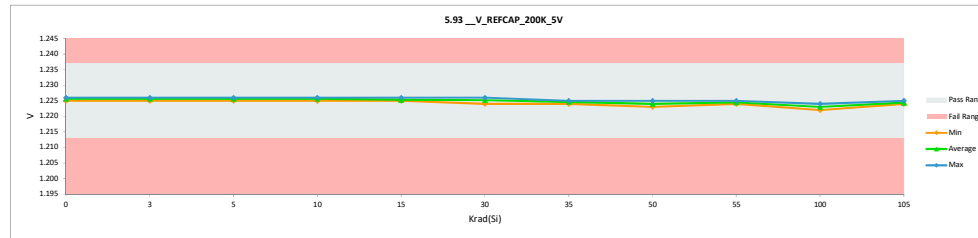
		5.93_V_REFCAP_200K_5V	
Test Site	Tester		
Test Number	Unit	V	V
Max Limit	Min Limit	1.237	1.213
		1.213	1.213

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.225	0.000
15	80	1.225	1.225	0.000
15	81	1.226	1.226	0.000
15	82	1.226	1.225	-0.001
15	83	1.226	1.226	0.000
15	84	1.226	1.225	-0.001
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.226	0.000
30	92	1.225	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.226	1.224	-0.002
55	115	1.226	1.224	-0.002
55	116	1.226	1.225	-0.001
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.223	-0.003
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.225	1.223	-0.002
100	143	1.225	1.223	-0.002
100	144	1.226	1.224	-0.002
100	220	1.225	1.223	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.224	-0.002
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.226	1.224	-0.002
105	374	1.225	1.225	0.000
105	376	1.226	1.224	-0.002
Max		1.226	1.226	0.000
Average		1.226	1.224	-0.001
Min		1.225	1.222	-0.004
Std Dev		0.000	0.001	0.001



		5.93_V_REFCAP_200K_5V	
Test Site	Tester		
Test Number	Unit	V	V
Max Limit	Min Limit	1.237	1.213
		1.213	1.213

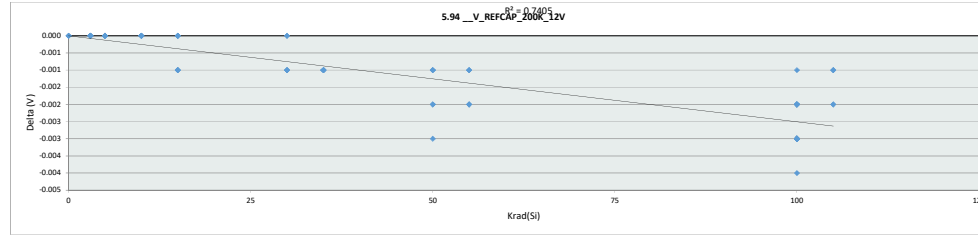
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.224	1.222	1.224
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.225	1.224	1.225	1.224
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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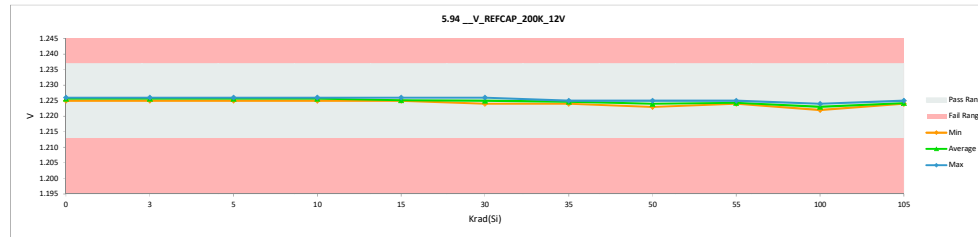
		5.94_V_REFCAP_200K_12V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.237	1.237	
Min Limit	1.213	1.213	

Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.225	0.000
15	80	1.225	1.225	0.000
15	81	1.226	1.226	0.000
15	82	1.226	1.225	-0.001
15	83	1.226	1.225	-0.001
15	84	1.226	1.225	-0.001
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.225	-0.001
30	92	1.225	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.226	1.224	-0.002
55	115	1.226	1.224	-0.002
55	116	1.226	1.224	-0.002
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.223	-0.003
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.226	1.223	-0.003
100	143	1.225	1.223	-0.002
100	144	1.225	1.224	-0.001
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.224	-0.002
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.225	1.224	-0.001
105	374	1.225	1.224	-0.001
105	376	1.226	1.224	-0.002
Max		1.226	1.226	0.000
Average		1.226	1.224	-0.001
Min		1.225	1.222	-0.004
Std Dev		0.000	0.001	0.001



		5.94_V_REFCAP_200K_12V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.237	1.237	
Min Limit	1.213	1.213	

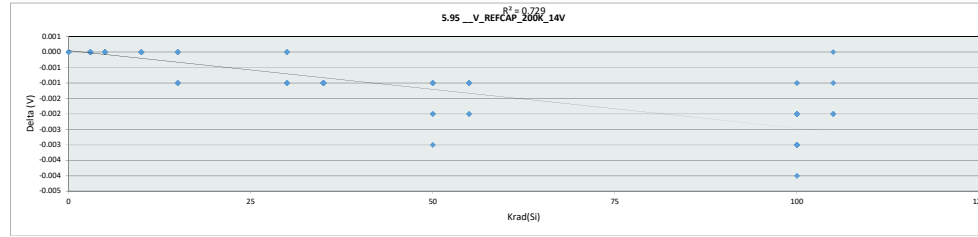
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.224	1.222	1.224
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.224	1.223	1.224
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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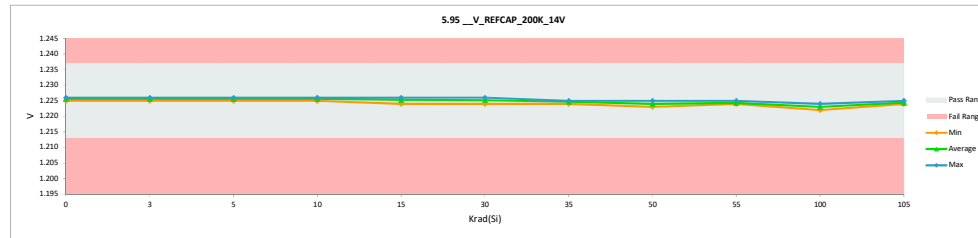
		5.95_V_REFCAP_200K_14V	
Test Site	Tester		
Test Number	Unit	V	V
Max Limit	Min Limit	1.237	1.237
		1.213	1.213

Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.225	0.000
15	80	1.225	1.224	-0.001
15	81	1.226	1.226	0.000
15	82	1.226	1.225	-0.001
15	83	1.226	1.226	0.000
15	84	1.226	1.226	0.000
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.226	0.000
30	92	1.225	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.225	1.224	-0.001
55	115	1.226	1.224	-0.002
55	116	1.226	1.224	-0.002
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.223	-0.003
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.225	1.223	-0.002
100	143	1.225	1.223	-0.002
100	144	1.225	1.224	-0.001
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.224	-0.002
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.226	1.224	-0.002
105	374	1.225	1.225	0.000
105	376	1.226	1.224	-0.002
Max		1.226	1.226	0.000
Average		1.226	1.224	-0.001
Min		1.225	1.222	-0.004
Std Dev		0.000	0.001	0.001



		5.95_V_REFCAP_200K_14V	
Test Site	Tester		
Test Number	Unit	V	V
Max Limit	Min Limit	1.237	1.237
		1.213	1.213

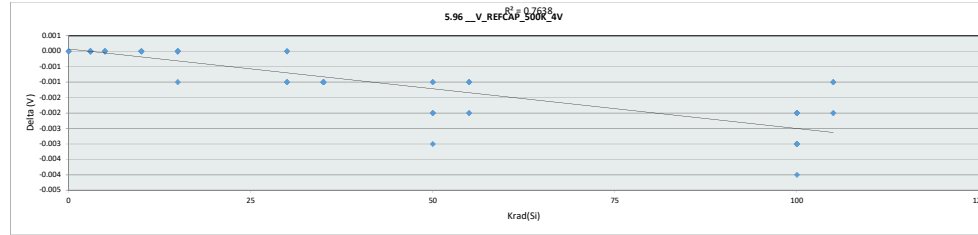
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.223	1.224	1.222	1.224
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.224	1.223	1.224
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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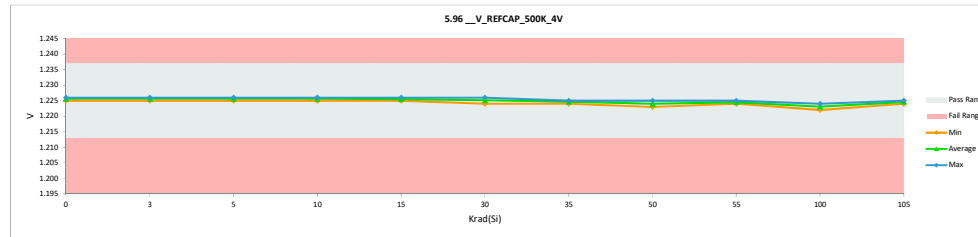
5.96_V_REFCAP_500K_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.225	0.000
15	80	1.225	1.225	0.000
15	81	1.226	1.226	0.000
15	82	1.226	1.225	-0.001
15	83	1.226	1.226	0.000
15	84	1.226	1.226	0.000
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.226	0.000
30	92	1.225	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.226	1.224	-0.002
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.226	1.224	-0.002
55	115	1.226	1.224	-0.002
55	116	1.226	1.225	-0.001
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.226	1.223	-0.003
100	143	1.225	1.223	-0.002
100	144	1.226	1.224	-0.002
100	220	1.225	1.223	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.224	-0.002
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.225	-0.001
105	246	1.226	1.225	-0.001
105	248	1.226	1.224	-0.002
105	374	1.226	1.225	-0.001
105	376	1.226	1.224	-0.002
Max		1.226	1.226	0.000
Average		1.226	1.224	-0.001
Min		1.225	1.222	-0.004
Std Dev		0.000	0.001	0.001



5.96_V_REFCAP_500K_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

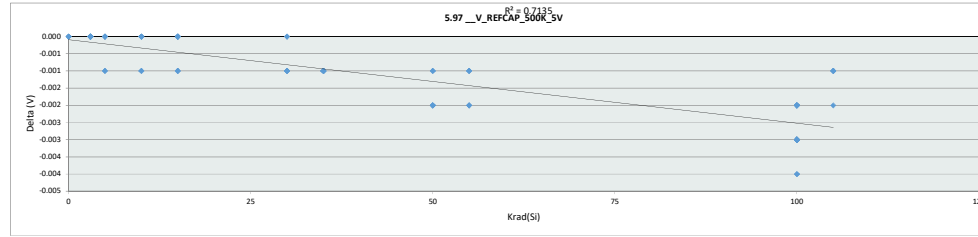
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.224	1.222	1.224
Average	1.226	1.226	1.226	1.226	1.226	1.225	1.224	1.224	1.225	1.223	1.225
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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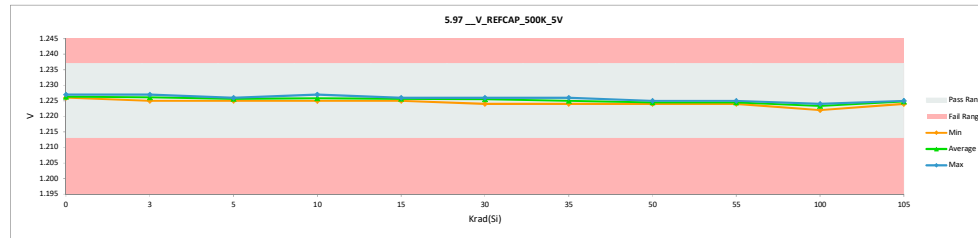
5.97_V_REFCAP_500K_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.227	1.227	0.000
3	63	1.226	1.226	0.000
3	64	1.226	1.226	0.000
3	65	1.226	1.226	0.000
3	66	1.227	1.227	0.000
5	67	1.226	1.225	-0.001
5	68	1.225	1.225	0.000
5	69	1.227	1.226	-0.001
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.227	1.226	-0.001
10	75	1.226	1.226	0.000
10	76	1.226	1.225	-0.001
10	77	1.226	1.226	0.000
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.225	1.225	0.000
15	81	1.227	1.226	-0.001
15	82	1.226	1.226	0.000
15	83	1.226	1.226	0.000
15	84	1.226	1.226	0.000
30	86	1.225	1.224	-0.001
30	89	1.227	1.226	-0.001
30	91	1.226	1.226	0.000
30	92	1.226	1.225	-0.001
30	94	1.226	1.226	0.000
30	95	1.227	1.226	-0.001
35	100	1.226	1.225	-0.001
35	101	1.225	1.224	-0.001
35	102	1.227	1.226	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.226	1.225	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.224	-0.002
50	109	1.226	1.225	-0.001
50	110	1.227	1.225	-0.002
50	111	1.226	1.224	-0.002
55	113	1.226	1.224	-0.002
55	114	1.226	1.224	-0.002
55	115	1.226	1.224	-0.002
55	116	1.226	1.225	-0.001
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.226	1.223	-0.003
100	131	1.226	1.224	-0.002
100	132	1.227	1.223	-0.004
100	133	1.227	1.224	-0.003
100	134	1.226	1.223	-0.003
100	135	1.226	1.223	-0.003
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.226	1.224	-0.002
100	141	1.226	1.222	-0.004
100	142	1.226	1.223	-0.003
100	143	1.226	1.223	-0.003
100	144	1.226	1.224	-0.002
100	220	1.225	1.223	-0.002
100	221	1.226	1.223	-0.003
100	224	1.226	1.223	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.224	-0.002
100	228	1.226	1.224	-0.002
100	232	1.226	1.223	-0.003
100	233	1.227	1.224	-0.003
100	236	1.227	1.224	-0.003
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.225	-0.001
105	246	1.226	1.225	-0.001
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	376	1.226	1.224	-0.002
Max		1.227	1.227	0.000
Average		1.226	1.225	-0.001
Min		1.225	1.222	-0.004
Std Dev		0.001	0.001	0.001



5.97_V_REFCAP_500K_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

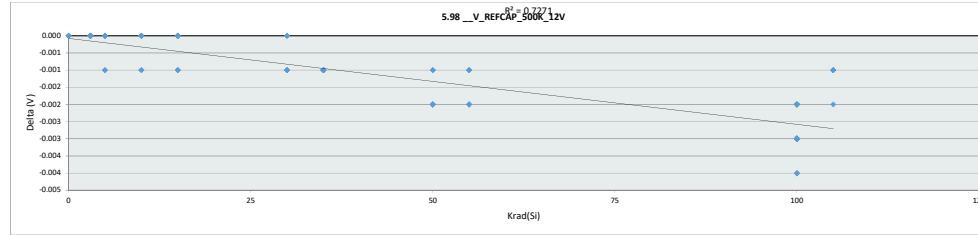
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.224	1.222	1.224
Average	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.225
Max	1.227	1.227	1.226	1.227	1.226	1.226	1.226	1.225	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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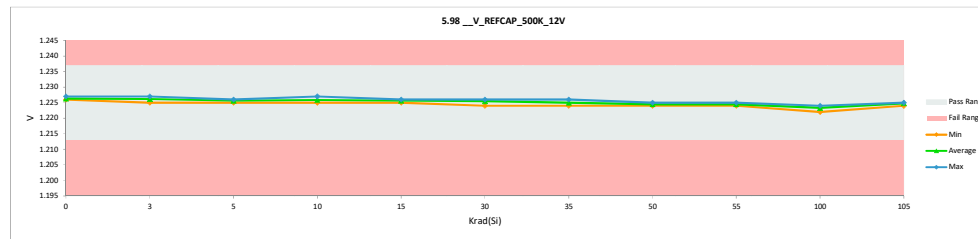
		5.98_V_REFCAP_500K_12V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.237	1.237	
Min Limit	1.213	1.213	

Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.227	1.227	0.000
3	63	1.226	1.226	0.000
3	64	1.226	1.226	0.000
3	65	1.226	1.226	0.000
3	66	1.227	1.227	0.000
5	67	1.226	1.225	-0.001
5	68	1.225	1.225	0.000
5	69	1.227	1.226	-0.001
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.227	1.226	-0.001
10	75	1.226	1.226	0.000
10	76	1.226	1.225	-0.001
10	77	1.226	1.226	0.000
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.225	1.225	0.000
15	81	1.227	1.226	-0.001
15	82	1.226	1.226	0.000
15	83	1.226	1.226	0.000
15	84	1.226	1.226	0.000
30	86	1.225	1.224	-0.001
30	89	1.227	1.226	-0.001
30	91	1.226	1.226	0.000
30	92	1.226	1.225	-0.001
30	94	1.226	1.226	0.000
30	95	1.227	1.226	-0.001
35	100	1.226	1.225	-0.001
35	101	1.225	1.224	-0.001
35	102	1.227	1.226	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.226	1.225	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.224	-0.002
50	109	1.226	1.225	-0.001
50	110	1.227	1.225	-0.002
50	111	1.226	1.224	-0.002
55	113	1.226	1.224	-0.002
55	114	1.226	1.224	-0.002
55	115	1.226	1.224	-0.002
55	116	1.226	1.225	-0.001
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.226	1.223	-0.003
100	131	1.226	1.224	-0.002
100	132	1.227	1.223	-0.004
100	133	1.227	1.224	-0.003
100	134	1.226	1.223	-0.003
100	135	1.226	1.223	-0.003
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.226	1.224	-0.002
100	141	1.226	1.222	-0.004
100	142	1.226	1.223	-0.003
100	143	1.226	1.223	-0.003
100	144	1.226	1.224	-0.002
100	220	1.225	1.223	-0.003
100	221	1.226	1.223	-0.003
100	224	1.226	1.223	-0.003
100	225	1.227	1.224	-0.003
100	226	1.226	1.224	-0.002
100	228	1.227	1.224	-0.003
100	232	1.226	1.223	-0.003
100	233	1.227	1.224	-0.003
100	236	1.227	1.224	-0.003
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.225	-0.001
105	246	1.226	1.225	-0.001
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	376	1.226	1.224	-0.002
Max		1.227	1.227	0.000
Average		1.226	1.225	-0.001
Min		1.225	1.222	-0.004
Std Dev		0.001	0.001	0.001



		5.98_V_REFCAP_500K_12V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.237	1.237	
Min Limit	1.213	1.213	

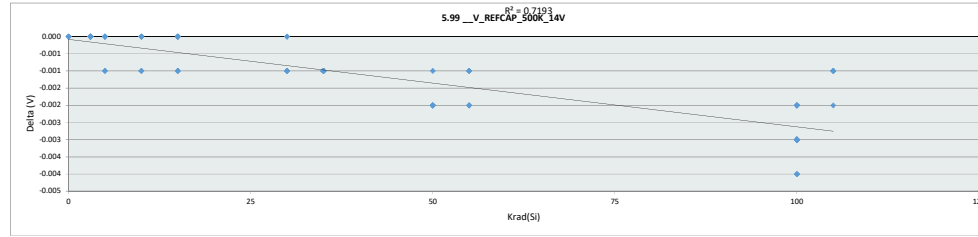
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.224	1.222	1.224
Average	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.223	1.225
Max	1.227	1.227	1.226	1.227	1.226	1.226	1.226	1.225	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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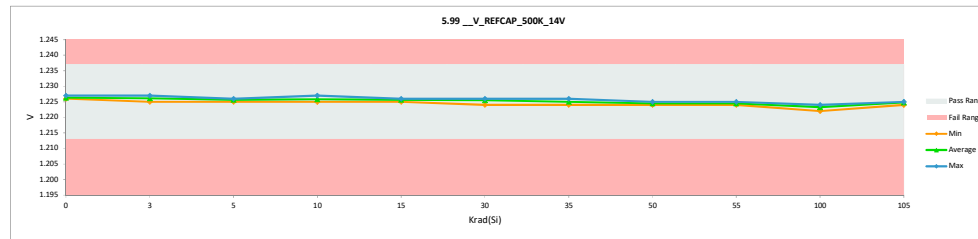
5.99_V_REFCAP_500K_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.227	1.227	0.000
3	63	1.226	1.226	0.000
3	64	1.226	1.226	0.000
3	65	1.226	1.226	0.000
3	66	1.227	1.227	0.000
5	67	1.226	1.225	-0.001
5	68	1.225	1.225	0.000
5	69	1.227	1.226	-0.001
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.227	1.226	-0.001
10	75	1.226	1.226	0.000
10	76	1.226	1.225	-0.001
10	77	1.226	1.226	0.000
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.225	1.225	0.000
15	81	1.227	1.226	-0.001
15	82	1.226	1.226	0.000
15	83	1.226	1.226	0.000
15	84	1.226	1.226	0.000
30	86	1.225	1.224	-0.001
30	89	1.227	1.226	-0.001
30	91	1.226	1.226	0.000
30	92	1.226	1.225	-0.001
30	94	1.226	1.226	0.000
30	95	1.227	1.226	-0.001
35	100	1.226	1.225	-0.001
35	101	1.225	1.224	-0.001
35	102	1.227	1.226	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.226	1.225	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.224	-0.002
50	109	1.227	1.225	-0.002
50	110	1.227	1.225	-0.002
50	111	1.226	1.224	-0.002
55	113	1.226	1.224	-0.002
55	114	1.226	1.224	-0.002
55	115	1.226	1.224	-0.002
55	116	1.226	1.225	-0.001
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.226	1.223	-0.003
100	131	1.226	1.224	-0.002
100	132	1.227	1.223	-0.004
100	133	1.227	1.224	-0.003
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.226	1.224	-0.002
100	141	1.226	1.222	-0.004
100	142	1.226	1.223	-0.003
100	143	1.226	1.223	-0.003
100	144	1.226	1.224	-0.002
100	220	1.225	1.223	-0.002
100	221	1.226	1.223	-0.003
100	224	1.226	1.223	-0.003
100	225	1.227	1.224	-0.003
100	226	1.226	1.224	-0.002
100	228	1.227	1.224	-0.003
100	232	1.226	1.223	-0.003
100	233	1.227	1.224	-0.003
100	236	1.227	1.224	-0.003
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.225	-0.001
105	246	1.226	1.225	-0.001
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	376	1.226	1.224	-0.002
Max		1.227	1.227	0.000
Average		1.226	1.225	-0.001
Min		1.225	1.222	-0.004
Std Dev		0.001	0.001	0.001



5.99_V_REFCAP_500K_14V	
Test Site	
Tester	
Test Number	
Max Limit	1.237 V
Min Limit	1.213 V

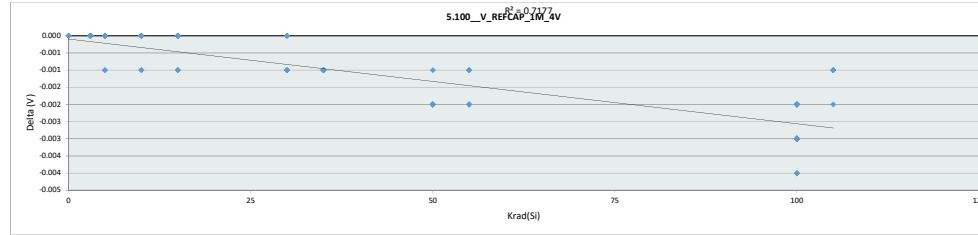
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.224	1.222	1.224
Average	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.223	1.225
Max	1.227	1.227	1.226	1.227	1.226	1.226	1.226	1.225	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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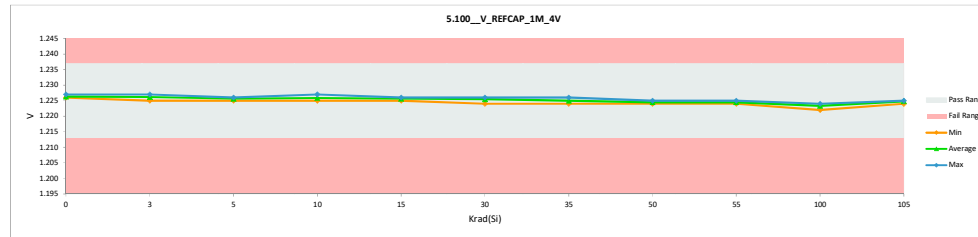
		5.100_V_REFCAP_1M_4V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.237	1.237	
Min Limit	1.213	1.213	

Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.227	1.227	0.000
3	63	1.226	1.226	0.000
3	64	1.226	1.226	0.000
3	65	1.226	1.226	0.000
3	66	1.227	1.227	0.000
5	67	1.226	1.225	-0.001
5	68	1.225	1.225	0.000
5	69	1.227	1.226	-0.001
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.227	1.226	-0.001
10	75	1.226	1.226	0.000
10	76	1.226	1.225	-0.001
10	77	1.226	1.226	0.000
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.225	1.225	0.000
15	81	1.227	1.226	-0.001
15	82	1.226	1.226	0.000
15	83	1.226	1.226	0.000
15	84	1.226	1.226	0.000
30	86	1.225	1.224	-0.001
30	89	1.227	1.226	-0.001
30	91	1.226	1.226	0.000
30	92	1.226	1.225	-0.001
30	94	1.226	1.226	0.000
30	95	1.227	1.226	-0.001
35	100	1.226	1.225	-0.001
35	101	1.225	1.224	-0.001
35	102	1.227	1.226	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.226	1.225	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.224	-0.002
50	109	1.227	1.225	-0.002
50	110	1.227	1.225	-0.002
50	111	1.226	1.224	-0.002
55	113	1.226	1.224	-0.002
55	114	1.226	1.224	-0.002
55	115	1.226	1.224	-0.002
55	116	1.226	1.225	-0.001
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.226	1.223	-0.003
100	131	1.226	1.224	-0.002
100	132	1.227	1.223	-0.004
100	133	1.227	1.224	-0.003
100	134	1.226	1.223	-0.003
100	135	1.226	1.223	-0.003
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.226	1.224	-0.002
100	141	1.226	1.222	-0.004
100	142	1.226	1.223	-0.003
100	143	1.226	1.223	-0.003
100	144	1.226	1.224	-0.002
100	220	1.226	1.223	-0.003
100	221	1.226	1.223	-0.003
100	224	1.226	1.223	-0.003
100	225	1.227	1.224	-0.003
100	226	1.226	1.224	-0.002
100	228	1.226	1.224	-0.002
100	232	1.226	1.223	-0.003
100	233	1.227	1.224	-0.003
100	236	1.227	1.224	-0.003
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.225	-0.001
105	246	1.226	1.225	-0.001
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	376	1.226	1.224	-0.002
Max		1.227	1.227	0.000
Average		1.226	1.225	-0.001
Min		1.225	1.222	-0.004
Std Dev		0.001	0.001	0.001



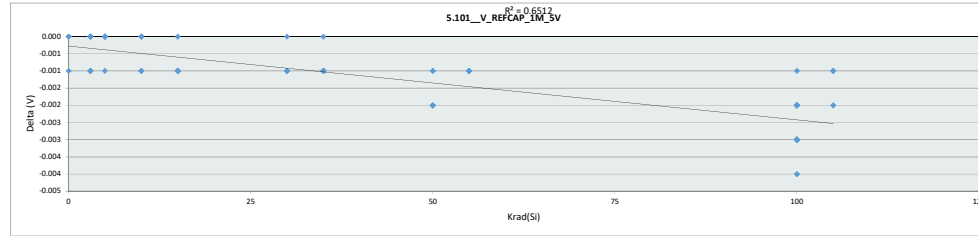
		5.100_V_REFCAP_1M_4V	
Test Site			
Tester			
Test Number			
Max Limit	1.237	V	
Min Limit	1.213	V	

Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.224	1.222	1.224
Average	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.223	1.225
Max	1.227	1.227	1.226	1.227	1.226	1.226	1.226	1.225	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

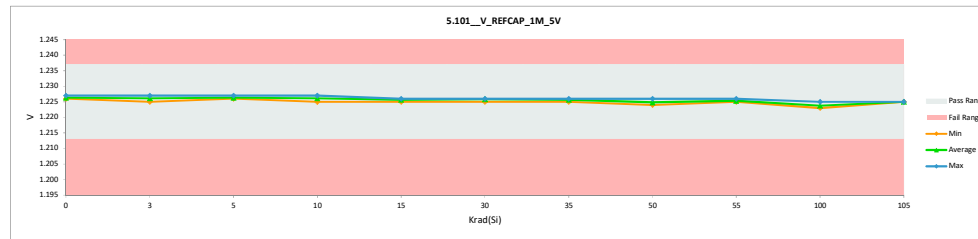


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5.101_V_REFCAP_1M_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.237	1.237		
Min Limit	1.213	1.213		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.227	1.226	-0.001
3	61	1.226	1.225	-0.001
3	62	1.227	1.227	0.000
3	63	1.227	1.226	-0.001
3	64	1.226	1.226	0.000
3	65	1.227	1.226	-0.001
3	66	1.227	1.227	0.000
5	67	1.226	1.226	0.000
5	68	1.226	1.226	0.000
5	69	1.227	1.227	0.000
5	70	1.227	1.227	0.000
5	71	1.227	1.226	-0.001
5	72	1.226	1.226	0.000
10	73	1.226	1.225	-0.001
10	74	1.227	1.227	0.000
10	75	1.227	1.226	-0.001
10	76	1.226	1.226	0.000
10	77	1.227	1.226	-0.001
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.226	1.225	-0.001
15	81	1.227	1.226	-0.001
15	82	1.227	1.226	-0.001
15	83	1.227	1.226	-0.001
15	84	1.226	1.226	0.000
30	86	1.226	1.225	-0.001
30	89	1.227	1.226	-0.001
30	91	1.227	1.226	-0.001
30	92	1.226	1.226	0.000
30	94	1.227	1.226	-0.001
30	95	1.227	1.226	-0.001
35	100	1.226	1.225	-0.001
35	101	1.226	1.225	-0.001
35	102	1.227	1.226	-0.001
35	103	1.227	1.226	-0.001
35	104	1.227	1.226	-0.001
35	105	1.226	1.226	0.000
50	106	1.226	1.225	-0.001
50	107	1.227	1.225	-0.002
50	108	1.226	1.224	-0.002
50	109	1.227	1.225	-0.002
50	110	1.227	1.226	-0.001
50	111	1.226	1.224	-0.002
55	113	1.226	1.225	-0.001
55	114	1.226	1.225	-0.001
55	115	1.226	1.225	-0.001
55	116	1.226	1.225	-0.001
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.226	1.223	-0.003
100	131	1.227	1.224	-0.003
100	132	1.227	1.224	-0.003
100	133	1.227	1.225	-0.002
100	134	1.227	1.223	-0.004
100	135	1.227	1.223	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.227	1.224	-0.003
100	141	1.226	1.223	-0.003
100	142	1.226	1.223	-0.003
100	143	1.226	1.224	-0.002
100	144	1.226	1.225	-0.001
100	220	1.226	1.223	-0.003
100	221	1.226	1.224	-0.002
100	224	1.226	1.223	-0.003
100	225	1.227	1.225	-0.002
100	226	1.227	1.224	-0.003
100	228	1.227	1.224	-0.003
100	232	1.226	1.224	-0.002
100	233	1.227	1.224	-0.003
100	236	1.227	1.225	-0.002
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.227	1.225	-0.002
105	246	1.227	1.225	-0.002
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	376	1.226	1.225	-0.001
Max		1.227	1.227	0.000
Average		1.227	1.225	-0.001
Min		1.226	1.223	-0.004
Std Dev		0.001	0.001	0.001



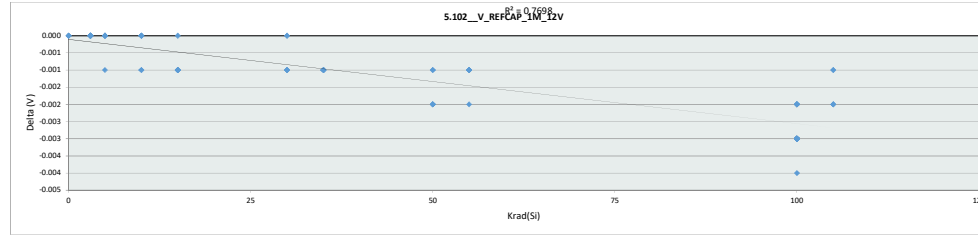
5.101_V_REFCAP_1M_5V											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.226	1.225	1.225	1.225	1.225	1.224	1.225	1.223	1.225
Average	1.226	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.225
Max	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.226	1.226	1.225	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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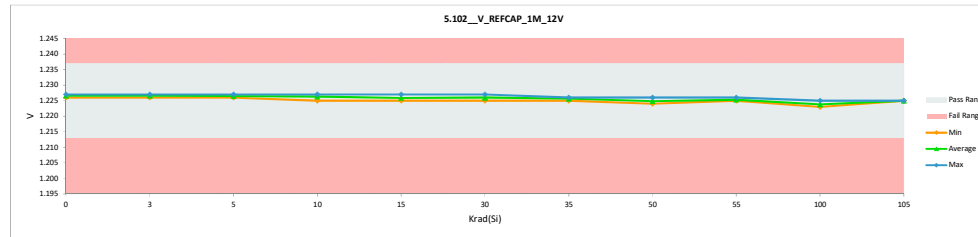
		5.102_V_REFCAP_1M_12V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.237	1.237	
Min Limit	1.213	1.213	

Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.227	1.227	0.000
3	61	1.226	1.226	0.000
3	62	1.227	1.227	0.000
3	63	1.227	1.227	0.000
3	64	1.226	1.226	0.000
3	65	1.227	1.227	0.000
3	66	1.227	1.227	0.000
5	67	1.226	1.226	0.000
5	68	1.226	1.226	0.000
5	69	1.227	1.227	0.000
5	70	1.227	1.227	0.000
5	71	1.227	1.227	0.000
5	72	1.227	1.226	-0.001
10	73	1.226	1.225	-0.001
10	74	1.227	1.227	0.000
10	75	1.227	1.227	0.000
10	76	1.226	1.226	0.000
10	77	1.227	1.226	-0.001
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.226	1.225	-0.001
15	81	1.227	1.227	0.000
15	82	1.227	1.226	-0.001
15	83	1.227	1.226	-0.001
15	84	1.227	1.226	-0.001
30	86	1.226	1.225	-0.001
30	89	1.227	1.226	-0.001
30	91	1.227	1.226	-0.001
30	92	1.226	1.226	0.000
30	94	1.227	1.226	-0.001
30	95	1.227	1.227	0.000
35	100	1.226	1.225	-0.001
35	101	1.226	1.225	-0.001
35	102	1.227	1.226	-0.001
35	103	1.227	1.226	-0.001
35	104	1.227	1.226	-0.001
35	105	1.227	1.226	-0.001
50	106	1.226	1.225	-0.001
50	107	1.227	1.225	-0.002
50	108	1.226	1.224	-0.002
50	109	1.227	1.225	-0.002
50	110	1.227	1.226	-0.001
50	111	1.226	1.224	-0.002
55	113	1.226	1.225	-0.001
55	114	1.226	1.225	-0.001
55	115	1.226	1.225	-0.001
55	116	1.227	1.225	-0.002
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.226	1.223	-0.003
100	131	1.227	1.224	-0.003
100	132	1.227	1.224	-0.003
100	133	1.227	1.225	-0.002
100	134	1.227	1.224	-0.003
100	135	1.227	1.223	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.227	1.224	-0.003
100	141	1.226	1.223	-0.003
100	142	1.226	1.223	-0.003
100	143	1.226	1.224	-0.002
100	144	1.227	1.225	-0.002
100	220	1.226	1.223	-0.003
100	221	1.227	1.224	-0.003
100	224	1.226	1.223	-0.003
100	225	1.227	1.225	-0.002
100	226	1.227	1.224	-0.003
100	228	1.227	1.224	-0.003
100	232	1.227	1.224	-0.003
100	233	1.227	1.224	-0.003
100	236	1.227	1.225	-0.002
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.227	1.225	-0.002
105	246	1.227	1.225	-0.002
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	376	1.227	1.225	-0.002
Max		1.227	1.227	0.000
Average		1.227	1.225	-0.001
Min		1.226	1.223	-0.004
Std Dev		0.000	0.001	0.001



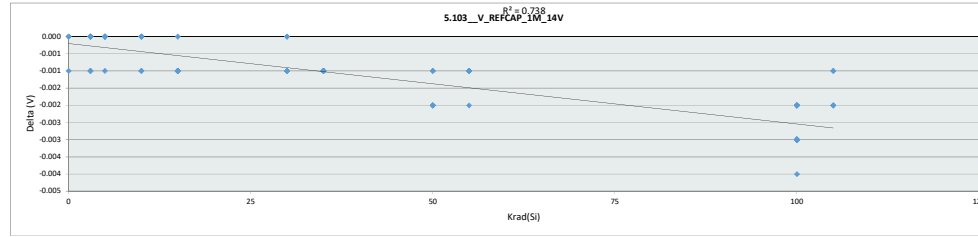
		5.102_V_REFCAP_1M_12V	
Test Site			
Tester			
Test Number			
Max Limit	1.237	V	
Min Limit	1.213	V	

Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.224	1.225	1.223	1.225
Average	1.227	1.227	1.227	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.225
Max	1.227	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.225	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

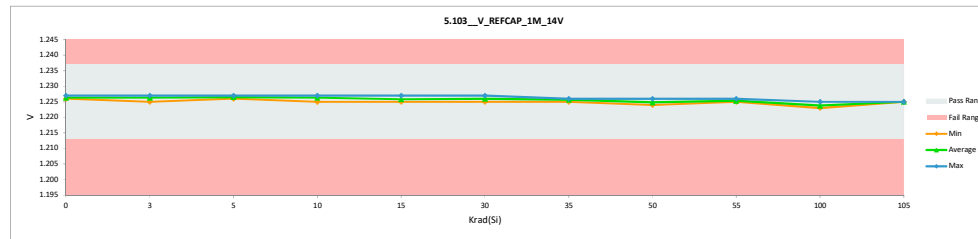


LDR TID Report
TPS7H5001-SP

5.103_V_REFCAP_1M_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.237	1.237		
Min Limit	1.213	1.213		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.227	1.226	-0.001
3	61	1.226	1.225	-0.001
3	62	1.227	1.227	0.000
3	63	1.227	1.227	0.000
3	64	1.226	1.226	0.000
3	65	1.227	1.226	-0.001
3	66	1.227	1.227	0.000
5	67	1.226	1.226	0.000
5	68	1.226	1.226	0.000
5	69	1.227	1.227	0.000
5	70	1.227	1.227	0.000
5	71	1.227	1.227	0.000
5	72	1.227	1.226	-0.001
10	73	1.226	1.225	-0.001
10	74	1.227	1.227	0.000
10	75	1.227	1.227	0.000
10	76	1.226	1.226	0.000
10	77	1.227	1.226	-0.001
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.226	1.225	-0.001
15	81	1.227	1.227	0.000
15	82	1.227	1.226	-0.001
15	83	1.227	1.226	-0.001
15	84	1.227	1.226	-0.001
30	86	1.226	1.225	-0.001
30	89	1.227	1.226	-0.001
30	91	1.227	1.226	-0.001
30	92	1.226	1.226	0.000
30	94	1.227	1.226	-0.001
30	95	1.227	1.227	0.000
35	100	1.226	1.225	-0.001
35	101	1.226	1.225	-0.001
35	102	1.227	1.226	-0.001
35	103	1.227	1.226	-0.001
35	104	1.227	1.226	-0.001
35	105	1.227	1.226	-0.001
50	106	1.226	1.225	-0.001
50	107	1.227	1.225	-0.002
50	108	1.226	1.224	-0.002
50	109	1.227	1.225	-0.002
50	110	1.227	1.226	-0.001
50	111	1.226	1.224	-0.002
55	113	1.226	1.225	-0.001
55	114	1.226	1.225	-0.001
55	115	1.226	1.225	-0.001
55	116	1.227	1.225	-0.002
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.226	1.223	-0.003
100	131	1.227	1.224	-0.003
100	132	1.227	1.224	-0.003
100	133	1.227	1.225	-0.002
100	134	1.227	1.224	-0.003
100	135	1.227	1.223	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.227	1.224	-0.003
100	141	1.226	1.223	-0.003
100	142	1.226	1.223	-0.003
100	143	1.226	1.224	-0.002
100	144	1.227	1.225	-0.002
100	220	1.226	1.223	-0.003
100	221	1.227	1.224	-0.003
100	224	1.226	1.223	-0.003
100	225	1.227	1.225	-0.002
100	226	1.227	1.224	-0.003
100	228	1.227	1.224	-0.003
100	232	1.227	1.224	-0.003
100	233	1.227	1.224	-0.003
100	236	1.227	1.225	-0.002
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.227	1.225	-0.002
105	246	1.227	1.225	-0.002
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	376	1.227	1.225	-0.002
Max		1.227	1.227	0.000
Average		1.227	1.225	-0.001
Min		1.226	1.223	-0.004
Std Dev		0.000	0.001	0.001



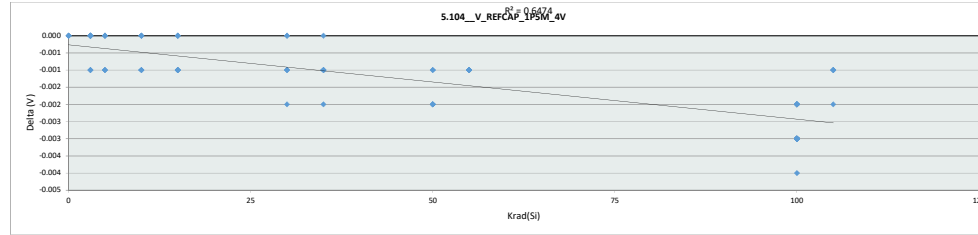
5.103_V_REFCAP_1M_14V											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.226	1.225	1.225	1.225	1.225	1.224	1.225	1.223	1.225
Average	1.226	1.226	1.227	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.225
Max	1.227	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.225	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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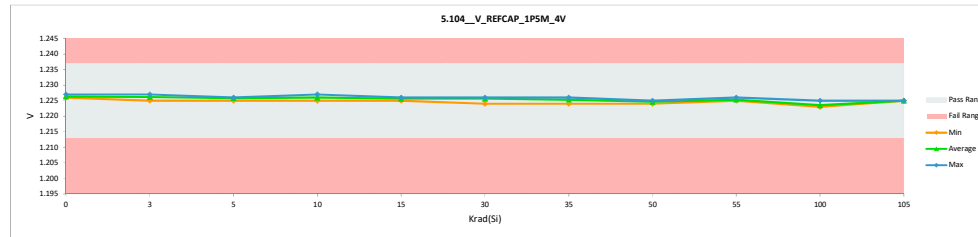
		5.104_V_REFCAP_1P5M_4V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.237	1.237	
Min Limit	1.213	1.213	

Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.226	1.226	0.000
3	61	1.226	1.225	-0.001
3	62	1.227	1.227	0.000
3	63	1.226	1.226	0.000
3	64	1.226	1.226	0.000
3	65	1.227	1.226	-0.001
3	66	1.227	1.227	0.000
5	67	1.226	1.226	0.000
5	68	1.226	1.225	-0.001
5	69	1.227	1.226	-0.001
5	70	1.226	1.226	0.000
5	71	1.227	1.226	-0.001
5	72	1.226	1.226	0.000
10	73	1.226	1.225	-0.001
10	74	1.227	1.226	-0.001
10	75	1.226	1.226	0.000
10	76	1.226	1.226	0.000
10	77	1.227	1.226	-0.001
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.226	1.225	-0.001
15	81	1.227	1.226	-0.001
15	82	1.226	1.226	0.000
15	83	1.227	1.226	-0.001
15	84	1.226	1.226	0.000
30	86	1.226	1.224	-0.002
30	89	1.227	1.226	-0.001
30	91	1.226	1.226	0.000
30	92	1.226	1.226	0.000
30	94	1.227	1.226	-0.001
30	95	1.227	1.226	-0.001
35	100	1.226	1.225	-0.001
35	101	1.226	1.224	-0.002
35	102	1.227	1.226	-0.001
35	103	1.226	1.225	-0.001
35	104	1.227	1.226	-0.001
35	105	1.226	1.226	0.000
50	106	1.226	1.225	-0.001
50	107	1.226	1.225	-0.001
50	108	1.226	1.224	-0.002
50	109	1.227	1.225	-0.002
50	110	1.227	1.225	-0.002
50	111	1.226	1.224	-0.002
55	113	1.226	1.225	-0.001
55	114	1.226	1.225	-0.001
55	115	1.226	1.225	-0.001
55	116	1.226	1.225	-0.001
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.226	1.223	-0.003
100	131	1.226	1.224	-0.002
100	132	1.227	1.223	-0.004
100	133	1.227	1.225	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.223	-0.003
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.226	1.224	-0.002
100	141	1.226	1.223	-0.003
100	142	1.226	1.223	-0.003
100	143	1.226	1.224	-0.002
100	144	1.226	1.224	-0.002
100	220	1.226	1.223	-0.003
100	221	1.226	1.223	-0.003
100	224	1.226	1.223	-0.003
100	225	1.227	1.225	-0.002
100	226	1.227	1.224	-0.003
100	228	1.227	1.224	-0.003
100	232	1.226	1.223	-0.003
100	233	1.227	1.224	-0.003
100	236	1.227	1.224	-0.003
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.225	-0.001
105	246	1.227	1.225	-0.002
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	376	1.226	1.225	-0.001
Max		1.227	1.227	0.000
Average		1.226	1.225	-0.001
Min		1.226	1.223	-0.004
Std Dev		0.000	0.001	0.001



		5.104_V_REFCAP_1P5M_4V	
Test Site			
Tester			
Test Number			
Max Limit	1.237	V	
Min Limit	1.213	V	

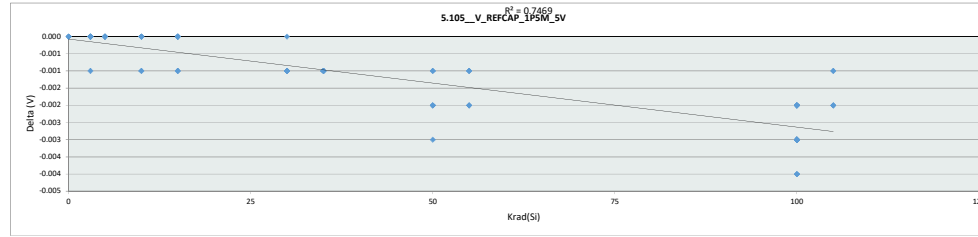
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.225	1.223	1.225
Average	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.225
Max	1.227	1.227	1.226	1.227	1.226	1.226	1.226	1.225	1.226	1.225	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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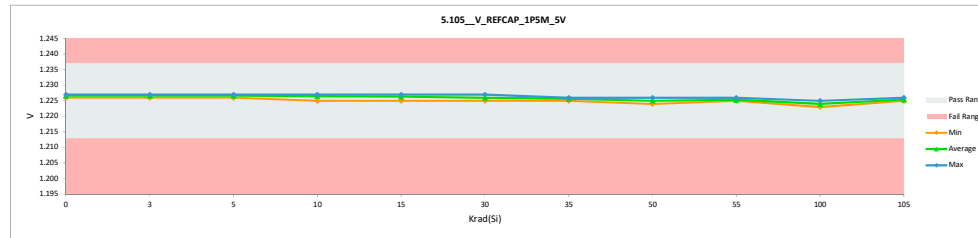
		5.105_V_REFCAP_1P5M_5V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.237	1.237	
Min Limit	1.213	1.213	

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.227	1.227	0.000
3	61	1.226	1.226	0.000
3	62	1.227	1.227	0.000
3	63	1.227	1.227	0.000
3	64	1.227	1.226	-0.001
3	65	1.227	1.227	0.000
3	66	1.227	1.227	0.000
5	67	1.226	1.226	0.000
5	68	1.226	1.226	0.000
5	69	1.227	1.227	0.000
5	70	1.227	1.227	0.000
5	71	1.227	1.227	0.000
5	72	1.227	1.227	0.000
10	73	1.226	1.225	-0.001
10	74	1.227	1.227	0.000
10	75	1.227	1.227	0.000
10	76	1.227	1.226	-0.001
10	77	1.227	1.227	0.000
10	78	1.227	1.227	0.000
15	79	1.226	1.226	0.000
15	80	1.226	1.225	-0.001
15	81	1.227	1.227	0.000
15	82	1.227	1.226	-0.001
15	83	1.227	1.227	0.000
15	84	1.227	1.227	0.000
30	86	1.226	1.225	-0.001
30	89	1.227	1.226	-0.001
30	91	1.227	1.226	-0.001
30	92	1.227	1.226	-0.001
30	94	1.227	1.226	-0.001
30	95	1.227	1.227	0.000
35	100	1.226	1.225	-0.001
35	101	1.226	1.225	-0.001
35	102	1.227	1.226	-0.001
35	103	1.227	1.226	-0.001
35	104	1.227	1.226	-0.001
35	105	1.227	1.226	-0.001
50	106	1.227	1.225	-0.002
50	107	1.227	1.225	-0.002
50	108	1.227	1.224	-0.003
50	109	1.227	1.225	-0.002
50	110	1.227	1.226	-0.001
50	111	1.226	1.225	-0.001
55	113	1.226	1.225	-0.001
55	114	1.227	1.225	-0.002
55	115	1.227	1.225	-0.002
55	116	1.227	1.225	-0.002
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.226	1.223	-0.003
100	131	1.227	1.224	-0.003
100	132	1.227	1.224	-0.003
100	133	1.227	1.225	-0.002
100	134	1.227	1.224	-0.003
100	135	1.227	1.223	-0.004
100	138	1.227	1.224	-0.003
100	139	1.227	1.224	-0.003
100	140	1.227	1.224	-0.003
100	141	1.227	1.223	-0.004
100	142	1.227	1.224	-0.003
100	143	1.226	1.224	-0.002
100	144	1.227	1.225	-0.002
100	220	1.226	1.223	-0.003
100	221	1.227	1.224	-0.003
100	224	1.227	1.223	-0.004
100	225	1.227	1.225	-0.002
100	226	1.227	1.224	-0.003
100	228	1.227	1.224	-0.003
100	232	1.227	1.224	-0.003
100	233	1.227	1.225	-0.002
100	236	1.227	1.225	-0.002
100	238	1.227	1.224	-0.003
100	243	1.226	1.224	-0.002
105	244	1.227	1.225	-0.002
105	246	1.227	1.226	-0.001
105	248	1.227	1.225	-0.002
105	374	1.227	1.226	-0.001
105	376	1.227	1.225	-0.002
Max		1.227	1.227	0.000
Average		1.227	1.225	-0.001
Min		1.226	1.223	-0.004
Std Dev		0.000	0.001	0.001



		5.105_V_REFCAP_1P5M_5V	
Test Site			
Tester			
Test Number			
Max Limit	1.237	V	
Min Limit	1.213	V	

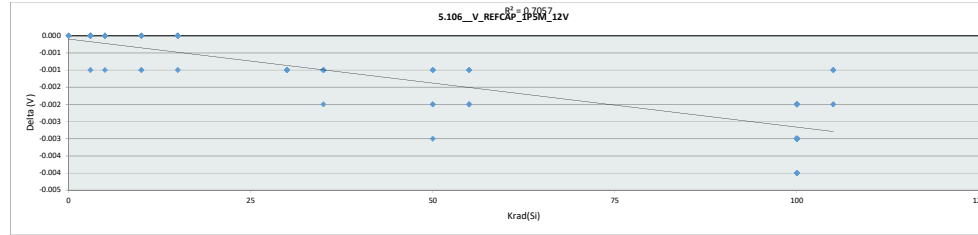
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.224	1.225	1.223	1.225
Average	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.225	1.225	1.224	1.225
Max	1.227	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.225	1.226
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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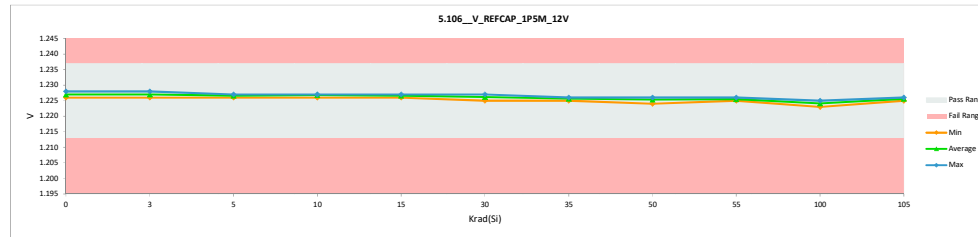
		5.106_V_REFCAP_1P5M_12V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.237	1.237	
Min Limit	1.213	1.213	

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.226	1.226	0.000
0	118	1.228	1.228	0.000
0	122	1.227	1.227	0.000
3	61	1.226	1.226	0.000
3	62	1.228	1.228	0.000
3	63	1.227	1.227	0.000
3	64	1.227	1.227	0.000
3	65	1.227	1.227	0.000
3	66	1.228	1.227	-0.001
5	67	1.227	1.226	-0.001
5	68	1.226	1.226	0.000
5	69	1.227	1.227	0.000
5	70	1.227	1.227	0.000
5	71	1.227	1.227	0.000
5	72	1.227	1.227	0.000
10	73	1.226	1.226	0.000
10	74	1.228	1.227	-0.001
10	75	1.227	1.227	0.000
10	76	1.227	1.227	0.000
10	77	1.227	1.227	0.000
10	78	1.228	1.227	-0.001
15	79	1.227	1.226	-0.001
15	80	1.226	1.226	0.000
15	81	1.227	1.227	0.000
15	82	1.227	1.227	0.000
15	83	1.227	1.227	0.000
15	84	1.227	1.227	0.000
30	86	1.226	1.225	-0.001
30	89	1.228	1.227	-0.001
30	91	1.227	1.226	-0.001
30	92	1.227	1.226	-0.001
30	94	1.227	1.226	-0.001
30	95	1.228	1.227	-0.001
35	100	1.227	1.225	-0.002
35	101	1.226	1.225	-0.001
35	102	1.227	1.226	-0.001
35	103	1.227	1.226	-0.001
35	104	1.227	1.226	-0.001
35	105	1.227	1.226	-0.001
50	106	1.227	1.226	-0.001
50	107	1.227	1.225	-0.002
50	108	1.227	1.224	-0.003
50	109	1.227	1.226	-0.001
50	110	1.228	1.226	-0.002
50	111	1.226	1.225	-0.001
55	113	1.227	1.225	-0.002
55	114	1.227	1.225	-0.002
55	115	1.227	1.225	-0.002
55	116	1.227	1.226	-0.001
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.227	1.224	-0.003
100	131	1.227	1.224	-0.003
100	132	1.228	1.224	-0.004
100	133	1.228	1.225	-0.003
100	134	1.227	1.224	-0.003
100	135	1.227	1.223	-0.004
100	138	1.227	1.224	-0.003
100	139	1.227	1.225	-0.002
100	140	1.227	1.224	-0.003
100	141	1.227	1.223	-0.004
100	142	1.227	1.224	-0.003
100	143	1.226	1.224	-0.002
100	144	1.227	1.225	-0.002
100	220	1.227	1.223	-0.004
100	221	1.227	1.224	-0.003
100	224	1.227	1.223	-0.004
100	225	1.227	1.225	-0.002
100	226	1.227	1.225	-0.002
100	228	1.227	1.224	-0.003
100	232	1.227	1.224	-0.003
100	233	1.227	1.225	-0.002
100	236	1.227	1.225	-0.002
100	238	1.227	1.224	-0.003
100	243	1.227	1.224	-0.003
105	244	1.227	1.225	-0.002
105	246	1.227	1.226	-0.001
105	248	1.227	1.226	-0.001
105	374	1.227	1.226	-0.001
105	376	1.227	1.225	-0.002
Max		1.228	1.228	0.000
Average		1.227	1.226	-0.001
Min		1.226	1.223	-0.004
Std Dev		0.000	0.001	0.001



		5.106_V_REFCAP_1P5M_12V	
Test Site			
Tester			
Test Number			
Max Limit	1.237	V	
Min Limit	1.213	V	

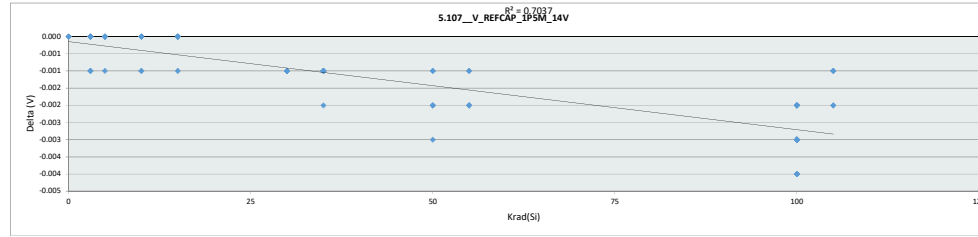
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.225	1.223	1.225
Average	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.225	1.226	1.224	1.226
Max	1.228	1.228	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.225	1.226
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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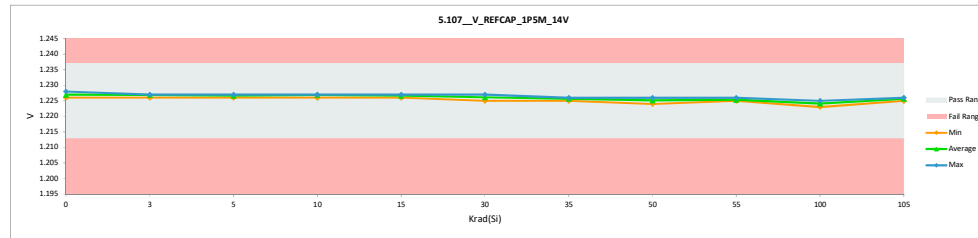
		5.107_V_REFCAP_1P5M_14V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.237	1.237	
Min Limit	1.213	1.213	

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.226	1.226	0.000
0	118	1.228	1.228	0.000
0	122	1.227	1.227	0.000
3	61	1.226	1.226	0.000
3	62	1.228	1.227	-0.001
3	63	1.227	1.227	0.000
3	64	1.227	1.227	0.000
3	65	1.227	1.227	0.000
3	66	1.228	1.227	-0.001
5	67	1.227	1.226	-0.001
5	68	1.226	1.226	0.000
5	69	1.227	1.227	0.000
5	70	1.227	1.227	0.000
5	71	1.227	1.227	0.000
5	72	1.227	1.227	0.000
10	73	1.226	1.226	0.000
10	74	1.228	1.227	-0.001
10	75	1.227	1.227	0.000
10	76	1.227	1.227	0.000
10	77	1.227	1.227	0.000
10	78	1.228	1.227	-0.001
15	79	1.227	1.226	-0.001
15	80	1.226	1.226	0.000
15	81	1.227	1.227	0.000
15	82	1.227	1.227	0.000
15	83	1.227	1.227	0.000
15	84	1.227	1.227	0.000
30	86	1.226	1.225	-0.001
30	89	1.228	1.227	-0.001
30	91	1.227	1.226	-0.001
30	92	1.227	1.226	-0.001
30	94	1.227	1.226	-0.001
30	95	1.228	1.227	-0.001
35	100	1.227	1.225	-0.002
35	101	1.226	1.225	-0.001
35	102	1.227	1.226	-0.001
35	103	1.227	1.226	-0.001
35	104	1.227	1.226	-0.001
35	105	1.227	1.226	-0.001
50	106	1.227	1.226	-0.001
50	107	1.227	1.225	-0.002
50	108	1.227	1.224	-0.003
50	109	1.227	1.225	-0.002
50	110	1.228	1.226	-0.002
50	111	1.226	1.225	-0.001
55	113	1.227	1.225	-0.002
55	114	1.227	1.225	-0.002
55	115	1.227	1.225	-0.002
55	116	1.227	1.225	-0.002
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.227	1.224	-0.003
100	131	1.227	1.224	-0.003
100	132	1.228	1.224	-0.004
100	133	1.228	1.225	-0.003
100	134	1.227	1.224	-0.003
100	135	1.227	1.223	-0.004
100	138	1.227	1.224	-0.003
100	139	1.227	1.225	-0.002
100	140	1.227	1.224	-0.003
100	141	1.227	1.223	-0.004
100	142	1.227	1.224	-0.003
100	143	1.226	1.224	-0.002
100	144	1.227	1.225	-0.002
100	220	1.227	1.223	-0.004
100	221	1.227	1.224	-0.003
100	224	1.227	1.223	-0.004
100	225	1.227	1.225	-0.002
100	226	1.227	1.225	-0.002
100	228	1.227	1.224	-0.003
100	232	1.227	1.224	-0.003
100	233	1.228	1.225	-0.003
100	236	1.227	1.225	-0.002
100	238	1.227	1.224	-0.003
100	243	1.227	1.224	-0.003
105	244	1.227	1.225	-0.002
105	246	1.227	1.226	-0.001
105	248	1.227	1.226	-0.001
105	374	1.227	1.226	-0.001
105	376	1.227	1.225	-0.002
Max		1.228	1.228	0.000
Average		1.227	1.226	-0.001
Min		1.226	1.223	-0.004
Std Dev		0.001	0.001	0.001



		5.107_V_REFCAP_1P5M_14V	
Test Site			
Tester			
Test Number			
Max Limit	1.237	V	
Min Limit	1.213	V	

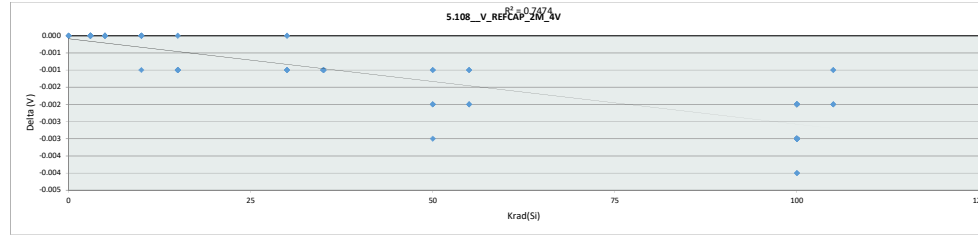
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.225	1.223	1.225
Average	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.225	1.225	1.224	1.226
Max	1.228	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.225	1.226
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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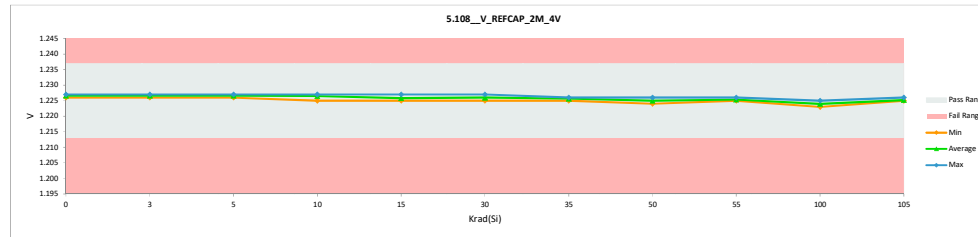
		5.108_V_REFCAP_2M_4V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.237	1.237	
Min Limit	1.213	1.213	

Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.227	1.227	0.000
3	61	1.226	1.226	0.000
3	62	1.227	1.227	0.000
3	63	1.227	1.227	0.000
3	64	1.226	1.226	0.000
3	65	1.227	1.227	0.000
3	66	1.227	1.227	0.000
5	67	1.226	1.226	0.000
5	68	1.226	1.226	0.000
5	69	1.227	1.227	0.000
5	70	1.227	1.227	0.000
5	71	1.227	1.227	0.000
5	72	1.227	1.227	0.000
10	73	1.226	1.225	-0.001
10	74	1.227	1.227	0.000
10	75	1.227	1.227	0.000
10	76	1.226	1.226	0.000
10	77	1.227	1.227	0.000
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.226	1.225	-0.001
15	81	1.227	1.227	0.000
15	82	1.227	1.226	-0.001
15	83	1.227	1.226	-0.001
15	84	1.227	1.226	-0.001
30	86	1.226	1.225	-0.001
30	89	1.227	1.226	-0.001
30	91	1.227	1.226	-0.001
30	92	1.226	1.226	0.000
30	94	1.227	1.226	-0.001
30	95	1.227	1.227	0.000
35	100	1.226	1.225	-0.001
35	101	1.226	1.225	-0.001
35	102	1.227	1.226	-0.001
35	103	1.227	1.226	-0.001
35	104	1.227	1.226	-0.001
35	105	1.227	1.226	-0.001
50	106	1.227	1.225	-0.002
50	107	1.227	1.225	-0.002
50	108	1.227	1.224	-0.003
50	109	1.227	1.225	-0.002
50	110	1.227	1.226	-0.001
50	111	1.226	1.225	-0.001
55	113	1.226	1.225	-0.001
55	114	1.227	1.225	-0.002
55	115	1.227	1.225	-0.002
55	116	1.227	1.225	-0.002
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.226	1.223	-0.003
100	131	1.227	1.224	-0.003
100	132	1.227	1.224	-0.003
100	133	1.227	1.225	-0.002
100	134	1.227	1.224	-0.003
100	135	1.227	1.223	-0.004
100	138	1.227	1.224	-0.003
100	139	1.227	1.224	-0.003
100	140	1.227	1.224	-0.003
100	141	1.226	1.223	-0.003
100	142	1.226	1.224	-0.002
100	143	1.226	1.224	-0.002
100	144	1.227	1.225	-0.002
100	220	1.226	1.223	-0.003
100	221	1.227	1.224	-0.003
100	224	1.226	1.223	-0.003
100	225	1.227	1.225	-0.002
100	226	1.227	1.224	-0.003
100	228	1.227	1.224	-0.003
100	232	1.227	1.224	-0.003
100	233	1.227	1.225	-0.002
100	236	1.227	1.225	-0.002
100	238	1.227	1.223	-0.004
100	243	1.226	1.224	-0.002
105	244	1.227	1.225	-0.002
105	246	1.227	1.226	-0.001
105	248	1.227	1.225	-0.002
105	374	1.226	1.225	-0.001
105	376	1.227	1.225	-0.002
Max		1.227	1.227	0.000
Average		1.227	1.225	-0.001
Min		1.226	1.223	-0.004
Std Dev		0.000	0.001	0.001



		5.108_V_REFCAP_2M_4V	
Test Site			
Tester			
Test Number			
Max Limit	1.237	V	
Min Limit	1.213	V	

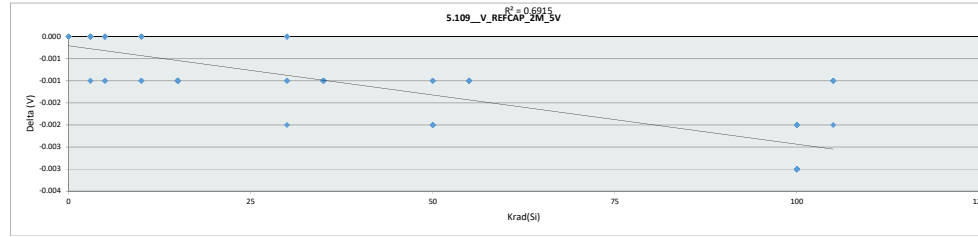
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.224	1.225	1.223	1.225
Average	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.225	1.225	1.224	1.225
Max	1.227	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.225	1.226
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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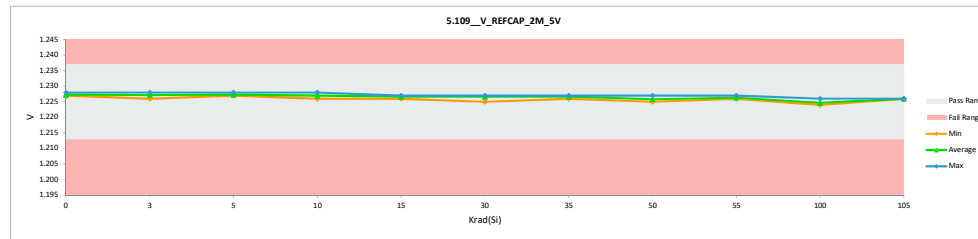
		5.109_V_REFCAP_2M_5V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.237	1.237	
Min Limit	1.213	1.213	

Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.227	1.227	0.000
0	118	1.228	1.228	0.000
0	122	1.227	1.227	0.000
3	61	1.227	1.226	-0.001
3	62	1.228	1.228	0.000
3	63	1.227	1.227	0.000
3	64	1.227	1.227	0.000
3	65	1.227	1.227	0.000
3	66	1.228	1.228	0.000
5	67	1.227	1.227	0.000
5	68	1.227	1.227	0.000
5	69	1.228	1.228	0.000
5	70	1.228	1.228	0.000
5	71	1.228	1.227	-0.001
5	72	1.228	1.227	-0.001
10	73	1.227	1.226	-0.001
10	74	1.228	1.227	-0.001
10	75	1.227	1.227	0.000
10	76	1.227	1.227	0.000
10	77	1.227	1.227	0.000
10	78	1.228	1.228	0.000
15	79	1.227	1.226	-0.001
15	80	1.227	1.226	-0.001
15	81	1.228	1.227	-0.001
15	82	1.228	1.227	-0.001
15	83	1.228	1.227	-0.001
15	84	1.228	1.227	-0.001
30	86	1.227	1.225	-0.002
30	89	1.228	1.227	-0.001
30	91	1.227	1.227	0.000
30	92	1.227	1.227	0.000
30	94	1.227	1.227	0.000
30	95	1.228	1.227	-0.001
35	100	1.227	1.226	-0.001
35	101	1.227	1.226	-0.001
35	102	1.228	1.227	-0.001
35	103	1.228	1.227	-0.001
35	104	1.228	1.227	-0.001
35	105	1.228	1.227	-0.001
50	106	1.227	1.226	-0.001
50	107	1.228	1.226	-0.002
50	108	1.227	1.225	-0.002
50	109	1.228	1.226	-0.002
50	110	1.228	1.227	-0.001
50	111	1.227	1.225	-0.002
55	113	1.227	1.226	-0.001
55	114	1.227	1.226	-0.001
55	115	1.227	1.226	-0.001
55	116	1.227	1.226	-0.001
55	126	1.228	1.227	-0.001
55	128	1.228	1.227	-0.001
100	129	1.227	1.224	-0.003
100	131	1.227	1.225	-0.002
100	132	1.228	1.225	-0.003
100	133	1.228	1.226	-0.002
100	134	1.228	1.225	-0.003
100	135	1.227	1.224	-0.003
100	138	1.227	1.224	-0.003
100	139	1.227	1.225	-0.002
100	140	1.228	1.225	-0.003
100	141	1.227	1.224	-0.003
100	142	1.227	1.224	-0.003
100	143	1.227	1.225	-0.002
100	144	1.228	1.226	-0.002
100	220	1.227	1.224	-0.003
100	221	1.227	1.225	-0.002
100	224	1.227	1.224	-0.003
100	225	1.228	1.225	-0.003
100	226	1.228	1.225	-0.003
100	228	1.228	1.225	-0.003
100	232	1.228	1.225	-0.003
100	233	1.228	1.225	-0.003
100	236	1.228	1.225	-0.003
100	238	1.227	1.224	-0.003
100	243	1.227	1.224	-0.003
105	244	1.227	1.226	-0.001
105	246	1.228	1.226	-0.002
105	248	1.227	1.226	-0.001
105	374	1.227	1.226	-0.001
105	376	1.227	1.226	-0.001
Max		1.228	1.228	0.000
Average		1.227	1.226	-0.001
Min		1.227	1.224	-0.003
Std Dev		0.001	0.001	0.001



		5.109_V_REFCAP_2M_5V	
Test Site			
Tester			
Test Number			
Max Limit	1.237	V	
Min Limit	1.213	V	

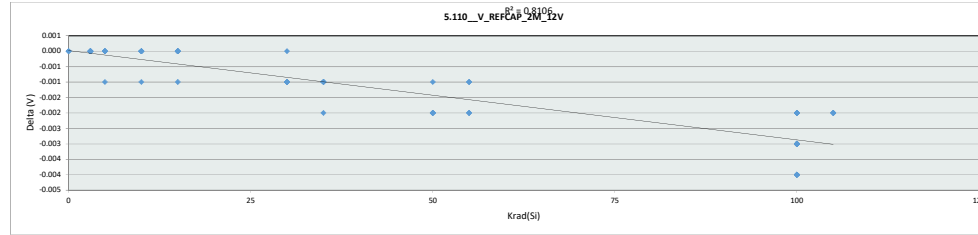
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.227	1.226	1.227	1.226	1.226	1.225	1.226	1.225	1.226	1.224	1.226
Average	1.227	1.227	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.225	1.226
Max	1.228	1.228	1.228	1.228	1.227	1.227	1.227	1.227	1.227	1.226	1.226
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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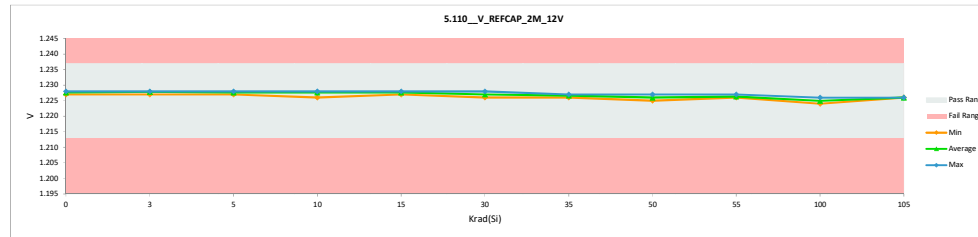
		5.110_V_REFCAP_2M_12V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.237	1.237	
Min Limit	1.213	1.213	

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.227	1.227	0.000
0	118	1.228	1.228	0.000
0	122	1.228	1.228	0.000
3	61	1.227	1.227	0.000
3	62	1.228	1.228	0.000
3	63	1.228	1.228	0.000
3	64	1.228	1.228	0.000
3	65	1.228	1.228	0.000
3	66	1.228	1.228	0.000
5	67	1.228	1.227	-0.001
5	68	1.227	1.227	0.000
5	69	1.228	1.228	0.000
5	70	1.228	1.228	0.000
5	71	1.228	1.228	0.000
5	72	1.228	1.228	0.000
10	73	1.227	1.226	-0.001
10	74	1.228	1.228	0.000
10	75	1.228	1.228	0.000
10	76	1.228	1.228	0.000
10	77	1.228	1.228	0.000
10	78	1.228	1.228	0.000
15	79	1.228	1.227	-0.001
15	80	1.227	1.227	0.000
15	81	1.228	1.228	0.000
15	82	1.228	1.228	0.000
15	83	1.228	1.228	0.000
15	84	1.228	1.228	0.000
30	86	1.227	1.226	-0.001
30	89	1.228	1.227	-0.001
30	91	1.228	1.227	-0.001
30	92	1.228	1.227	-0.001
30	94	1.228	1.227	-0.001
30	95	1.228	1.228	0.000
35	100	1.228	1.226	-0.002
35	101	1.227	1.226	-0.001
35	102	1.228	1.227	-0.001
35	103	1.228	1.227	-0.001
35	104	1.228	1.227	-0.001
35	105	1.228	1.227	-0.001
50	106	1.228	1.226	-0.002
50	107	1.228	1.226	-0.002
50	108	1.227	1.225	-0.002
50	109	1.228	1.226	-0.002
50	110	1.229	1.227	-0.002
50	111	1.227	1.226	-0.001
55	113	1.227	1.226	-0.001
55	114	1.228	1.226	-0.002
55	115	1.228	1.226	-0.002
55	116	1.228	1.226	-0.002
55	126	1.228	1.227	-0.001
55	128	1.228	1.227	-0.001
100	129	1.228	1.225	-0.003
100	131	1.228	1.225	-0.003
100	132	1.229	1.225	-0.004
100	133	1.229	1.226	-0.003
100	134	1.228	1.225	-0.003
100	135	1.228	1.224	-0.004
100	138	1.228	1.225	-0.003
100	139	1.228	1.225	-0.003
100	140	1.228	1.225	-0.003
100	141	1.228	1.224	-0.004
100	142	1.228	1.225	-0.003
100	143	1.227	1.225	-0.002
100	144	1.228	1.226	-0.002
100	220	1.228	1.224	-0.004
100	221	1.228	1.225	-0.003
100	224	1.228	1.224	-0.004
100	225	1.228	1.226	-0.002
100	226	1.228	1.225	-0.003
100	228	1.228	1.225	-0.003
100	232	1.228	1.225	-0.003
100	233	1.228	1.226	-0.002
100	236	1.228	1.226	-0.002
100	238	1.228	1.224	-0.004
100	243	1.227	1.224	-0.003
105	244	1.228	1.226	-0.002
105	246	1.228	1.226	-0.002
105	248	1.228	1.226	-0.002
105	374	1.228	1.226	-0.002
105	376	1.228	1.226	-0.002
Max		1.229	1.228	0.000
Average		1.228	1.226	-0.001
Min		1.227	1.224	-0.004
Std Dev		0.000	0.001	0.001



		5.110_V_REFCAP_2M_12V	
Test Site			
Tester			
Test Number			
Max Limit	1.237	V	
Min Limit	1.213	V	

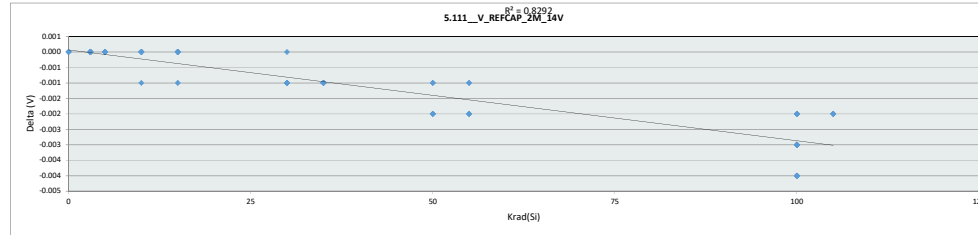
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.227	1.227	1.227	1.226	1.227	1.226	1.226	1.225	1.226	1.224	1.226
Average	1.228	1.228	1.228	1.228	1.228	1.228	1.227	1.226	1.226	1.225	1.226
Max	1.228	1.228	1.228	1.228	1.228	1.228	1.227	1.227	1.227	1.226	1.226
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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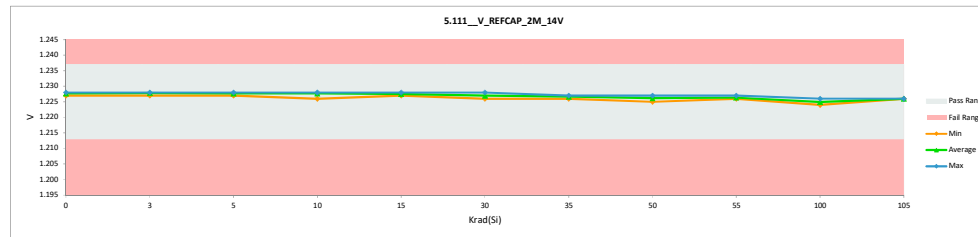
5.111_V_REFCAP_2M_14V		
Test Site		
Tester		
Test Number		
Unit	V	V
Max Limit	1.237	1.237
Min Limit	1.213	1.213

Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.227	1.227	0.000
0	118	1.228	1.228	0.000
0	122	1.228	1.228	0.000
3	61	1.227	1.227	0.000
3	62	1.228	1.228	0.000
3	63	1.228	1.228	0.000
3	64	1.228	1.228	0.000
3	65	1.228	1.228	0.000
3	66	1.228	1.228	0.000
5	67	1.227	1.227	0.000
5	68	1.227	1.227	0.000
5	69	1.228	1.228	0.000
5	70	1.228	1.228	0.000
5	71	1.228	1.228	0.000
5	72	1.228	1.228	0.000
10	73	1.227	1.226	-0.001
10	74	1.228	1.228	0.000
10	75	1.228	1.228	0.000
10	76	1.228	1.228	0.000
10	77	1.228	1.228	0.000
10	78	1.228	1.228	0.000
15	79	1.227	1.227	0.000
15	80	1.227	1.227	0.000
15	81	1.228	1.228	0.000
15	82	1.228	1.228	0.000
15	83	1.228	1.227	-0.001
15	84	1.228	1.228	0.000
30	86	1.227	1.226	-0.001
30	89	1.228	1.227	-0.001
30	91	1.228	1.227	-0.001
30	92	1.228	1.227	-0.001
30	94	1.228	1.227	-0.001
30	95	1.228	1.228	0.000
35	100	1.227	1.226	-0.001
35	101	1.227	1.226	-0.001
35	102	1.228	1.227	-0.001
35	103	1.228	1.227	-0.001
35	104	1.228	1.227	-0.001
35	105	1.228	1.227	-0.001
50	106	1.228	1.227	-0.001
50	107	1.228	1.226	-0.002
50	108	1.227	1.225	-0.002
50	109	1.228	1.226	-0.002
50	110	1.229	1.227	-0.002
50	111	1.227	1.226	-0.001
55	113	1.228	1.226	-0.002
55	114	1.228	1.226	-0.002
55	115	1.228	1.226	-0.002
55	116	1.228	1.226	-0.002
55	126	1.228	1.227	-0.001
55	128	1.228	1.227	-0.001
100	129	1.228	1.225	-0.003
100	131	1.228	1.225	-0.003
100	132	1.229	1.225	-0.004
100	133	1.229	1.226	-0.003
100	134	1.228	1.225	-0.003
100	135	1.228	1.224	-0.004
100	138	1.228	1.225	-0.003
100	139	1.228	1.225	-0.003
100	140	1.228	1.225	-0.003
100	141	1.228	1.224	-0.004
100	142	1.228	1.225	-0.003
100	143	1.227	1.225	-0.002
100	144	1.228	1.226	-0.002
100	220	1.228	1.224	-0.004
100	221	1.228	1.225	-0.003
100	224	1.228	1.224	-0.004
100	225	1.228	1.226	-0.002
100	226	1.228	1.225	-0.003
100	228	1.228	1.225	-0.003
100	232	1.228	1.225	-0.003
100	233	1.228	1.226	-0.002
100	236	1.228	1.226	-0.002
100	238	1.228	1.224	-0.004
100	243	1.227	1.224	-0.003
105	244	1.228	1.226	-0.002
105	246	1.228	1.226	-0.002
105	248	1.228	1.226	-0.002
105	374	1.228	1.226	-0.002
105	376	1.228	1.226	-0.002
Max		1.229	1.228	0.000
Average		1.228	1.226	-0.001
Min		1.227	1.224	-0.004
Std Dev		0.000	0.001	



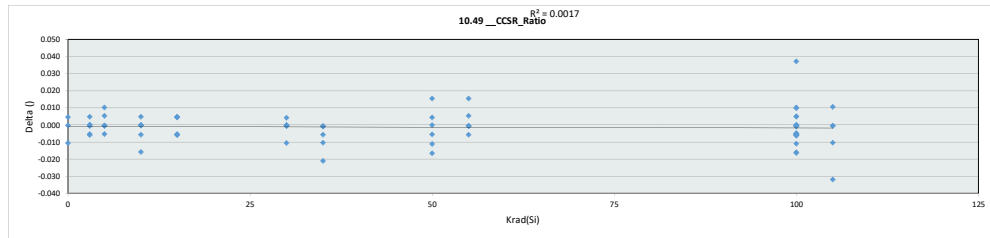
5.111_V_REFCAP_2M_14V		
Test Site		
Tester		
Test Number		
Max Limit	1.237	V
Min Limit	1.213	V

Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.227	1.227	1.227	1.226	1.227	1.226	1.226	1.225	1.226	1.224	1.226
Average	1.228	1.228	1.228	1.228	1.228	1.228	1.227	1.226	1.226	1.225	1.226
Max	1.228	1.228	1.228	1.228	1.228	1.228	1.227	1.227	1.227	1.226	1.226
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

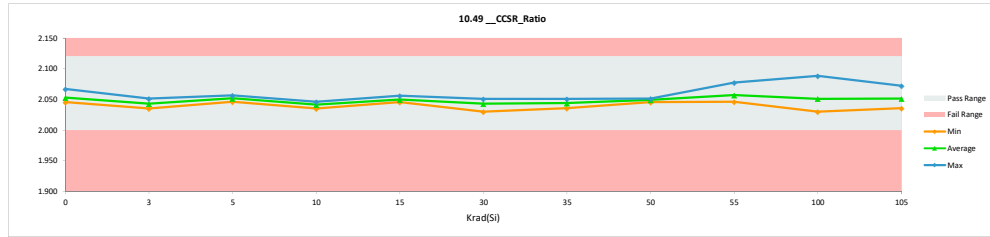


LDR TID Report
TPS7H5001-SP

		10.49_CCSR_Ratio		
Test Site				
Tester				
Test Number				
Unit				
Max Limit	2.12	2.12		
Min Limit	2	2		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	2.041	2.046	0.005
0	118	2.067	2.067	0.000
0	122	2.056	2.046	-0.010
3	61	2.041	2.046	0.005
3	62	2.046	2.041	-0.006
3	63	2.046	2.046	0.000
3	64	2.051	2.051	0.000
3	65	2.040	2.036	-0.005
3	66	2.041	2.041	0.000
5	67	2.041	2.046	0.006
5	68	2.056	2.056	0.000
5	69	2.056	2.051	-0.005
5	70	2.046	2.057	0.011
5	71	2.051	2.051	0.000
5	72	2.051	2.051	0.000
10	73	2.041	2.046	0.005
10	74	2.046	2.041	-0.005
10	75	2.046	2.046	0.000
10	76	2.051	2.035	-0.016
10	77	2.040	2.041	0.000
10	78	2.041	2.041	0.000
15	79	2.041	2.046	0.005
15	80	2.056	2.051	-0.005
15	81	2.056	2.051	-0.005
15	82	2.046	2.051	0.005
15	83	2.051	2.046	-0.005
15	84	2.051	2.056	0.005
30	86	2.041	2.041	0.000
30	89	2.046	2.046	0.000
30	91	2.046	2.046	0.000
30	92	2.051	2.051	0.000
30	94	2.040	2.030	-0.010
30	95	2.041	2.046	0.005
35	100	2.041	2.040	0.000
35	101	2.056	2.036	-0.021
35	102	2.056	2.046	-0.010
35	103	2.046	2.046	0.000
35	104	2.051	2.046	-0.005
35	105	2.051	2.051	0.000
50	106	2.062	2.051	-0.011
50	107	2.041	2.046	0.005
50	108	2.051	2.046	-0.005
50	109	2.035	2.051	0.016
50	110	2.067	2.051	-0.016
50	111	2.051	2.051	0.000
55	113	2.051	2.051	0.000
55	114	2.062	2.062	0.000
55	115	2.051	2.046	-0.005
55	116	2.051	2.051	0.000
55	126	2.062	2.078	0.016
55	128	2.051	2.057	0.006
100	129	2.035	2.046	0.010
100	131	2.062	2.056	-0.006
100	132	2.051	2.046	-0.005
100	133	2.041	2.046	0.005
100	134	2.041	2.041	0.000
100	135	2.046	2.030	-0.016
100	138	2.056	2.057	0.000
100	139	2.062	2.046	-0.016
100	140	2.041	2.040	0.000
100	141	2.072	2.067	-0.005
100	142	2.067	2.061	-0.006
100	143	2.041	2.041	0.000
100	144	2.088	2.078	-0.010
100	220	2.046	2.046	0.000
100	221	2.051	2.046	-0.005
100	224	2.056	2.057	0.001
100	225	2.035	2.041	0.005
100	226	2.067	2.062	-0.005
100	228	2.051	2.046	-0.004
100	232	2.051	2.089	0.037
100	233	2.046	2.041	-0.005
100	236	2.046	2.041	-0.005
100	238	2.051	2.062	0.011
100	243	2.046	2.046	0.000
105	244	2.072	2.072	0.000
105	246	2.078	2.046	-0.032
105	248	2.046	2.036	-0.010
105	374	2.051	2.062	0.011
105	376	2.041	2.041	0.000
	Max	2.088	2.089	0.037
	Average	2.050	2.049	-0.001
	Min	2.035	2.030	-0.032
	Std Dev	0.010	0.010	0.009

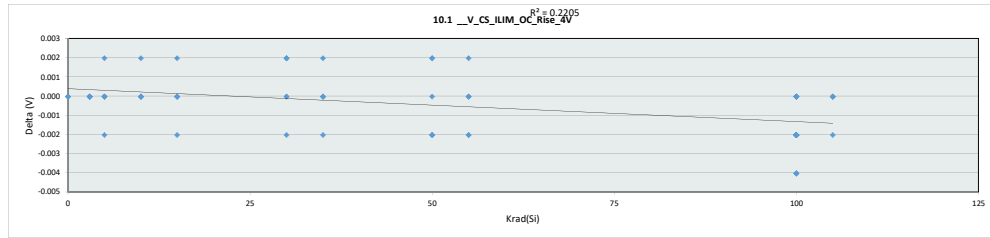


		10.49_CCSR_Ratio										
Test Site												
Tester												
Test Number												
Max Limit	2.12											
Min Limit	2											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	
Min	2.046	2.036	2.046	2.035	2.046	2.030	2.036	2.046	2.046	2.030	2.036	
Average	2.053	2.043	2.052	2.042	2.050	2.043	2.044	2.049	2.057	2.051	2.051	
Max	2.067	2.051	2.057	2.046	2.056	2.051	2.051	2.078	2.089	2.072		
UL	2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120	

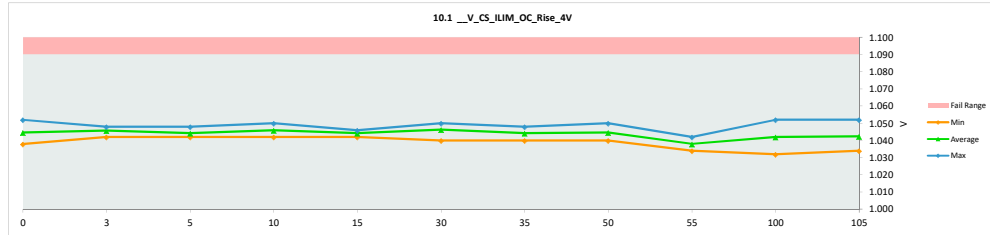


LDR TID Report
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10.1 _V_CS_ILIM_OC_Rise_4V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.09	1.09		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.052	1.052	0.000
0	118	1.038	1.038	0.000
0	122	1.044	1.044	0.000
3	61	1.048	1.048	0.000
3	62	1.046	1.046	0.000
3	63	1.046	1.046	0.000
3	64	1.044	1.044	0.000
3	65	1.042	1.042	0.000
3	66	1.048	1.048	0.000
5	67	1.048	1.048	0.000
5	68	1.046	1.046	0.000
5	69	1.042	1.044	0.002
5	70	1.044	1.044	0.000
5	71	1.042	1.042	0.000
5	72	1.044	1.042	-0.002
10	73	1.048	1.050	0.002
10	74	1.046	1.046	0.000
10	75	1.046	1.046	0.000
10	76	1.044	1.044	0.000
10	77	1.042	1.042	0.000
10	78	1.048	1.048	0.000
15	79	1.048	1.046	-0.002
15	80	1.046	1.046	0.000
15	81	1.042	1.044	0.002
15	82	1.044	1.044	0.000
15	83	1.042	1.042	0.000
15	84	1.044	1.044	0.000
30	86	1.048	1.050	0.002
30	89	1.046	1.046	0.000
30	91	1.046	1.046	0.000
30	92	1.044	1.046	0.002
30	94	1.042	1.040	-0.002
30	95	1.048	1.050	0.002
35	100	1.048	1.048	0.000
35	101	1.046	1.046	0.000
35	102	1.042	1.044	0.002
35	103	1.044	1.044	0.000
35	104	1.042	1.040	-0.002
35	105	1.044	1.044	0.000
50	106	1.050	1.050	0.000
50	107	1.046	1.044	-0.002
50	108	1.046	1.044	-0.002
50	109	1.044	1.042	-0.002
50	110	1.046	1.048	0.002
50	111	1.038	1.040	0.002
55	113	1.036	1.034	-0.002
55	114	1.036	1.036	0.000
55	115	1.038	1.036	-0.002
55	116	1.040	1.040	0.000
55	125	1.042	1.042	0.000
55	128	1.038	1.040	0.002
100	129	1.046	1.044	-0.002
100	131	1.034	1.034	0.000
100	132	1.038	1.036	-0.002
100	133	1.038	1.038	0.000
100	134	1.044	1.044	0.000
100	135	1.046	1.042	-0.004
100	138	1.052	1.050	-0.002
100	139	1.040	1.036	-0.004
100	140	1.040	1.040	0.000
100	141	1.044	1.040	-0.004
100	142	1.034	1.032	-0.002
100	143	1.046	1.044	-0.002
100	144	1.052	1.052	0.000
100	220	1.048	1.046	-0.002
100	221	1.050	1.048	-0.002
100	224	1.042	1.042	0.000
100	225	1.042	1.040	-0.002
100	226	1.042	1.040	-0.002
100	228	1.048	1.044	-0.004
100	232	1.042	1.042	0.000
100	233	1.050	1.048	-0.002
100	236	1.040	1.038	-0.002
100	238	1.042	1.042	0.000
100	243	1.046	1.044	-0.002
105	244	1.038	1.038	0.000
105	246	1.034	1.034	0.000
105	248	1.052	1.052	0.000
105	374	1.040	1.040	0.000
105	376	1.050	1.048	-0.002
Max		1.052	1.052	0.002
Average		1.044	1.043	-0.001
Min		1.034	1.032	-0.004
Std Dev		0.004	0.004	0.001

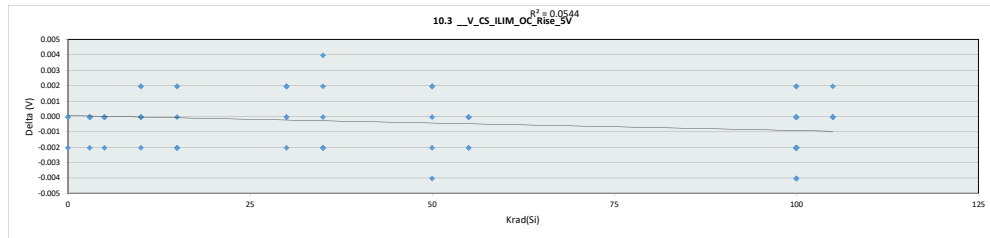


10.1 _V_CS_ILIM_OC_Rise_4V											
Test Site											
Tester											
Test Number											
Max Limit	1.09	V									
Min Limit	V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.038	1.042	1.042	1.042	1.042	1.040	1.040	1.040	1.034	1.032	1.034
Average	1.045	1.046	1.044	1.046	1.044	1.046	1.044	1.045	1.038	1.042	1.042
Max	1.052	1.048	1.048	1.050	1.046	1.050	1.048	1.050	1.042	1.052	1.052
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090

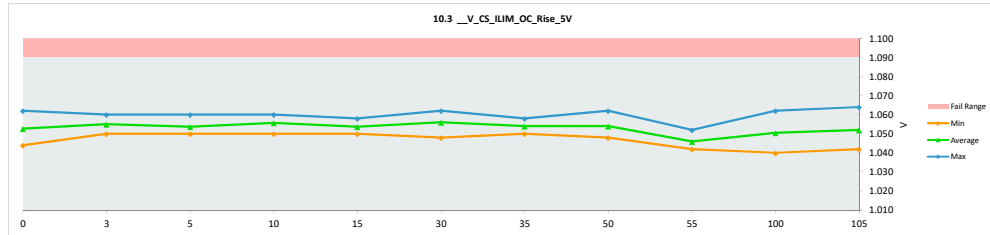


LDR TID Report
TPS7H5001-SP

10.3 _V_CS_ILIM_OC_Rise_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.09	1.09		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.062	1.062	0.000
0	118	1.046	1.044	-0.002
0	122	1.052	1.052	0.000
3	61	1.058	1.058	0.000
3	62	1.056	1.054	-0.002
3	63	1.054	1.054	0.000
3	64	1.054	1.054	0.000
3	65	1.050	1.050	0.000
3	66	1.060	1.060	0.000
5	67	1.060	1.060	0.000
5	68	1.054	1.054	0.000
5	69	1.052	1.052	0.000
5	70	1.056	1.056	0.000
5	71	1.052	1.050	-0.002
5	72	1.050	1.050	0.000
10	73	1.058	1.060	0.002
10	74	1.056	1.054	-0.002
10	75	1.054	1.054	0.000
10	76	1.054	1.056	0.002
10	77	1.050	1.050	0.000
10	78	1.060	1.060	0.000
15	79	1.060	1.058	-0.002
15	80	1.054	1.052	-0.002
15	81	1.052	1.054	0.002
15	82	1.056	1.058	0.002
15	83	1.052	1.050	-0.002
15	84	1.050	1.050	0.000
30	86	1.058	1.060	0.002
30	89	1.056	1.056	0.000
30	91	1.054	1.054	0.000
30	92	1.054	1.056	0.002
30	94	1.050	1.048	-0.002
30	95	1.060	1.062	0.002
35	100	1.060	1.058	-0.002
35	101	1.054	1.052	-0.002
35	102	1.052	1.054	0.002
35	103	1.056	1.056	0.000
35	104	1.052	1.050	-0.002
35	105	1.050	1.054	0.004
50	106	1.060	1.062	0.002
50	107	1.054	1.054	0.000
50	108	1.054	1.052	-0.002
50	109	1.056	1.052	-0.004
50	110	1.054	1.056	0.002
50	111	1.046	1.048	0.002
55	113	1.044	1.042	-0.002
55	114	1.042	1.042	0.000
55	115	1.048	1.046	-0.002
55	116	1.048	1.048	0.000
55	126	1.052	1.052	0.000
55	128	1.046	1.046	0.000
100	129	1.054	1.052	-0.002
100	131	1.044	1.042	-0.002
100	132	1.044	1.042	-0.002
100	133	1.046	1.046	0.000
100	134	1.050	1.052	0.002
100	135	1.054	1.052	-0.002
100	138	1.060	1.058	-0.002
100	139	1.048	1.044	-0.004
100	140	1.050	1.048	-0.002
100	141	1.050	1.048	-0.002
100	142	1.044	1.040	-0.004
100	143	1.054	1.054	0.000
100	144	1.062	1.062	0.000
100	220	1.058	1.058	0.000
100	221	1.062	1.060	-0.002
100	224	1.048	1.048	0.000
100	225	1.050	1.048	-0.002
100	226	1.050	1.048	-0.002
100	228	1.058	1.054	-0.004
100	232	1.050	1.052	0.002
100	233	1.058	1.056	-0.002
100	236	1.046	1.046	0.000
100	238	1.050	1.050	0.000
100	243	1.054	1.052	-0.002
105	244	1.046	1.048	0.002
105	246	1.042	1.042	0.000
105	248	1.064	1.064	0.000
105	374	1.048	1.048	0.000
105	376	1.058	1.058	0.000
Max		1.064	1.064	0.004
Average		1.053	1.052	0.000
Min		1.042	1.040	-0.004
Std Dev		0.005	0.006	0.002

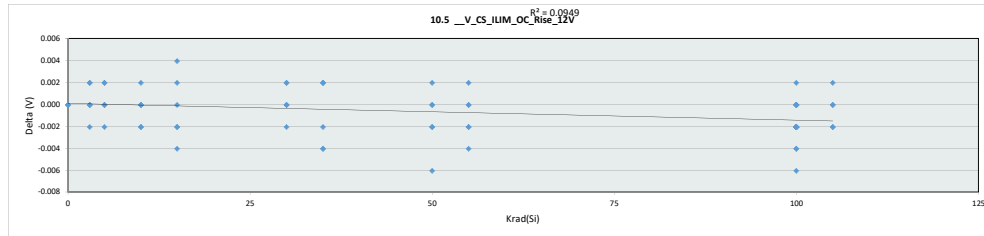


10.3 _V_CS_ILIM_OC_Rise_5V											
Test Site											
Tester											
Test Number											
Max Limit	1.09	V									
Min Limit	V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.044	1.050	1.050	1.050	1.050	1.048	1.050	1.048	1.042	1.040	1.042
Average	1.053	1.055	1.054	1.056	1.054	1.056	1.054	1.054	1.046	1.051	1.052
Max	1.062	1.060	1.060	1.060	1.058	1.062	1.058	1.062	1.052	1.062	1.064
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090

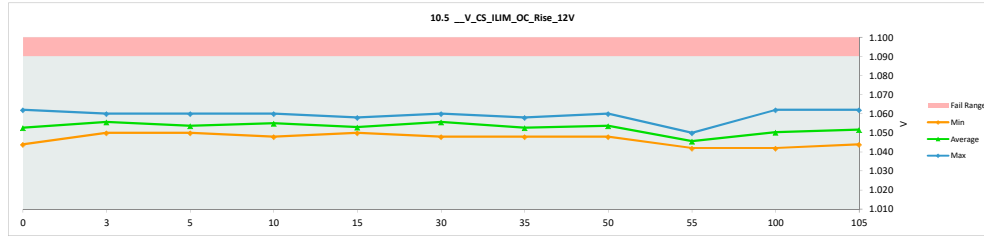


LDR TID Report
TPS7H5001-SP

10.5 _V_CS_ILIM_OC_Rise_12V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.09	1.09		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.062	1.062	0.000
0	118	1.044	1.044	0.000
0	122	1.052	1.052	0.000
3	61	1.058	1.060	0.002
3	62	1.056	1.054	-0.002
3	63	1.054	1.054	0.000
3	64	1.056	1.056	0.000
3	65	1.048	1.050	0.002
3	66	1.060	1.060	0.000
5	67	1.060	1.060	0.000
5	68	1.054	1.052	-0.002
5	69	1.050	1.052	0.002
5	70	1.054	1.056	0.002
5	71	1.052	1.052	0.000
5	72	1.050	1.050	0.000
10	73	1.058	1.060	0.002
10	74	1.056	1.054	-0.002
10	75	1.054	1.054	0.000
10	76	1.056	1.054	-0.002
10	77	1.048	1.048	0.000
10	78	1.060	1.060	0.000
15	79	1.060	1.056	-0.004
15	80	1.054	1.052	-0.002
15	81	1.050	1.052	0.002
15	82	1.054	1.058	0.004
15	83	1.052	1.050	-0.002
15	84	1.050	1.050	0.000
30	86	1.058	1.060	0.002
30	89	1.056	1.054	-0.002
30	91	1.054	1.054	0.000
30	92	1.056	1.058	0.002
30	94	1.048	1.048	0.000
30	95	1.060	1.060	0.000
35	100	1.060	1.058	-0.002
35	101	1.054	1.050	-0.004
35	102	1.050	1.052	0.002
35	103	1.054	1.056	0.002
35	104	1.052	1.048	-0.004
35	105	1.050	1.052	0.002
50	106	1.062	1.060	-0.002
50	107	1.054	1.054	0.000
50	108	1.054	1.054	0.000
50	109	1.056	1.050	-0.006
50	110	1.056	1.056	0.000
50	111	1.046	1.048	0.002
55	113	1.046	1.042	-0.004
55	114	1.040	1.042	0.002
55	115	1.048	1.046	-0.002
55	116	1.048	1.048	0.000
55	126	1.052	1.050	-0.002
55	128	1.046	1.046	0.000
100	129	1.054	1.054	0.000
100	131	1.042	1.042	0.000
100	132	1.044	1.042	-0.002
100	133	1.046	1.044	-0.002
100	134	1.052	1.050	-0.002
100	135	1.054	1.052	-0.002
100	138	1.060	1.058	-0.002
100	139	1.048	1.044	-0.004
100	140	1.050	1.048	-0.002
100	141	1.052	1.048	-0.004
100	142	1.042	1.042	0.000
100	143	1.056	1.054	-0.002
100	144	1.060	1.062	0.002
100	220	1.056	1.056	0.000
100	221	1.060	1.058	-0.002
100	224	1.048	1.046	-0.002
100	225	1.050	1.050	0.000
100	226	1.050	1.048	-0.002
100	228	1.058	1.052	-0.006
100	232	1.052	1.052	0.000
100	233	1.058	1.056	-0.002
100	236	1.048	1.046	-0.002
100	238	1.050	1.048	-0.002
100	243	1.054	1.054	0.000
105	244	1.048	1.046	-0.002
105	246	1.042	1.044	0.002
105	248	1.064	1.062	-0.002
105	374	1.048	1.048	0.000
105	376	1.058	1.058	0.000
Max		1.064	1.062	0.004
Average		1.053	1.052	-0.001
Min		1.040	1.042	-0.006
Std Dev		0.005	0.005	0.002

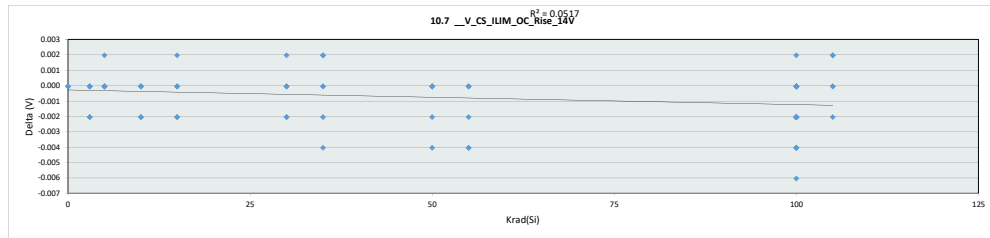


10.5 _V_CS_ILIM_OC_Rise_12V											
Test Site											
Tester											
Test Number											
Max Limit	1.09	V									
Min Limit	V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.044	1.050	1.050	1.048	1.050	1.048	1.048	1.048	1.042	1.042	1.044
Average	1.053	1.056	1.054	1.055	1.053	1.056	1.053	1.054	1.046	1.050	1.052
Max	1.062	1.060	1.060	1.060	1.058	1.060	1.058	1.060	1.060	1.050	1.062
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090

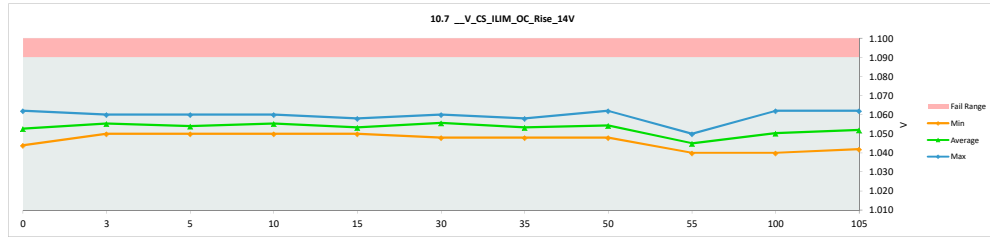


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10.7 _V_CS_ILIM_OC_Rise_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.09	1.09		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	1.062	1.062	0.000
0	118	1.044	1.044	0.000
0	122	1.052	1.052	0.000
3	61	1.060	1.060	0.000
3	62	1.056	1.054	-0.002
3	63	1.054	1.054	0.000
3	64	1.056	1.054	-0.002
3	65	1.050	1.050	0.000
3	66	1.060	1.060	0.000
5	67	1.060	1.060	0.000
5	68	1.052	1.054	0.002
5	69	1.052	1.052	0.000
5	70	1.056	1.056	0.000
5	71	1.052	1.052	0.000
5	72	1.050	1.050	0.000
10	73	1.060	1.060	0.000
10	74	1.056	1.054	-0.002
10	75	1.054	1.054	0.000
10	76	1.056	1.054	-0.002
10	77	1.050	1.050	0.000
10	78	1.060	1.060	0.000
15	79	1.060	1.058	-0.002
15	80	1.052	1.052	0.000
15	81	1.052	1.052	0.000
15	82	1.056	1.056	0.000
15	83	1.052	1.050	-0.002
15	84	1.050	1.052	0.002
30	86	1.060	1.060	0.000
30	89	1.056	1.054	-0.002
30	91	1.054	1.054	0.000
30	92	1.056	1.058	0.002
30	94	1.050	1.048	-0.002
30	95	1.060	1.060	0.000
35	100	1.060	1.058	-0.002
35	101	1.052	1.052	0.000
35	102	1.052	1.054	0.002
35	103	1.056	1.056	0.000
35	104	1.052	1.048	-0.004
35	105	1.050	1.052	0.002
50	106	1.062	1.062	0.000
50	107	1.054	1.054	-0.002
50	108	1.054	1.054	0.000
50	109	1.056	1.052	-0.004
50	110	1.056	1.056	0.000
50	111	1.048	1.048	0.000
55	113	1.046	1.042	-0.004
55	114	1.040	1.040	0.000
55	115	1.048	1.044	-0.004
55	116	1.048	1.048	0.000
55	126	1.052	1.050	-0.002
55	128	1.046	1.046	0.000
100	129	1.054	1.052	-0.002
100	131	1.042	1.042	0.000
100	132	1.044	1.044	0.000
100	133	1.044	1.044	0.000
100	134	1.052	1.050	-0.002
100	135	1.054	1.052	-0.002
100	138	1.060	1.058	-0.002
100	139	1.048	1.044	-0.004
100	140	1.048	1.048	0.000
100	141	1.052	1.046	-0.006
100	142	1.044	1.040	-0.004
100	143	1.054	1.054	0.000
100	144	1.062	1.062	0.000
100	220	1.056	1.056	0.000
100	221	1.062	1.060	-0.002
100	224	1.048	1.048	0.000
100	225	1.050	1.050	0.000
100	226	1.050	1.048	-0.002
100	228	1.058	1.054	-0.004
100	232	1.052	1.052	0.000
100	233	1.058	1.056	-0.002
100	236	1.048	1.044	-0.004
100	238	1.048	1.050	0.002
100	243	1.056	1.054	-0.002
105	244	1.046	1.048	0.002
105	246	1.042	1.042	0.000
105	248	1.064	1.062	-0.002
105	374	1.048	1.050	0.002
105	376	1.058	1.058	0.000
Max		1.064	1.062	0.002
Average		1.053	1.052	-0.001
Min		1.040	1.040	-0.006
Std Dev		0.005	0.006	0.002



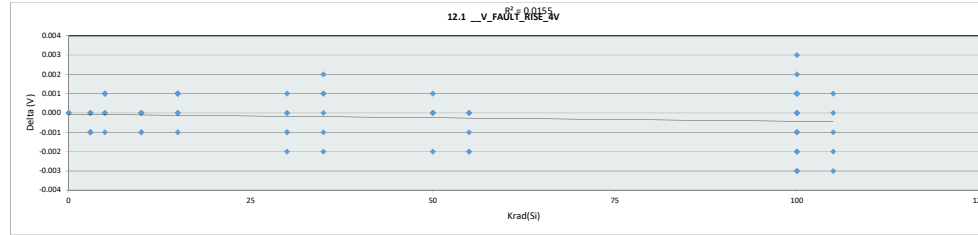
10.7 _V_CS_ILIM_OC_Rise_14V											
Test Site											
Tester											
Test Number											
Max Limit	1.09	V									
Min Limit	V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.044	1.050	1.050	1.050	1.050	1.050	1.048	1.048	1.048	1.040	1.042
Average	1.053	1.055	1.054	1.055	1.053	1.056	1.053	1.054	1.045	1.050	1.052
Max	1.062	1.060	1.060	1.060	1.058	1.060	1.058	1.062	1.050	1.062	1.062
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090



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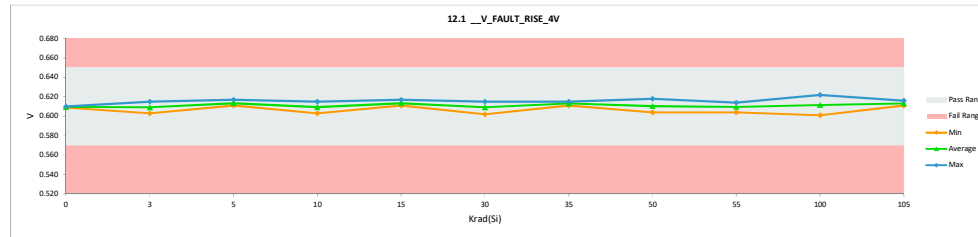
		12.1 _V_FAULT_RISE_4V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	0.65	0.65	
Min Limit	0.57	0.57	

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	0.609	0.609	0.000
0	118	0.610	0.610	0.000
0	122	0.610	0.610	0.000
3	61	0.605	0.604	-0.001
3	62	0.614	0.614	0.000
3	63	0.603	0.603	0.000
3	64	0.615	0.614	-0.001
3	65	0.606	0.605	-0.001
3	66	0.615	0.615	0.000
5	67	0.616	0.615	-0.001
5	68	0.610	0.611	0.001
5	69	0.610	0.611	0.001
5	70	0.616	0.617	0.001
5	71	0.611	0.611	0.000
5	72	0.615	0.615	0.000
10	73	0.605	0.605	0.000
10	74	0.614	0.614	0.000
10	75	0.603	0.603	0.000
10	76	0.615	0.614	-0.001
10	77	0.606	0.605	-0.001
10	78	0.615	0.615	0.000
15	79	0.616	0.615	-0.001
15	80	0.610	0.611	0.001
15	81	0.610	0.611	0.001
15	82	0.616	0.617	0.001
15	83	0.611	0.611	0.000
15	84	0.615	0.615	0.000
30	86	0.605	0.604	-0.001
30	89	0.614	0.615	0.001
30	91	0.603	0.602	-0.001
30	92	0.615	0.613	-0.002
30	94	0.606	0.606	0.000
30	95	0.615	0.615	0.000
35	100	0.616	0.614	-0.002
35	101	0.610	0.612	0.002
35	102	0.610	0.611	0.001
35	103	0.616	0.615	-0.001
35	104	0.611	0.612	0.001
35	105	0.615	0.615	0.000
50	106	0.614	0.614	0.000
50	107	0.612	0.613	0.001
50	108	0.606	0.606	0.000
50	109	0.618	0.618	0.000
50	110	0.606	0.604	-0.002
50	111	0.607	0.607	0.000
55	113	0.606	0.606	0.000
55	114	0.606	0.604	-0.002
55	115	0.612	0.611	-0.001
55	116	0.616	0.614	-0.002
55	126	0.613	0.613	0.000
55	128	0.609	0.609	0.000
100	129	0.610	0.611	0.001
100	131	0.604	0.603	-0.001
100	132	0.606	0.606	0.000
100	133	0.611	0.611	0.000
100	134	0.609	0.606	-0.003
100	135	0.614	0.613	-0.001
100	138	0.611	0.611	0.000
100	139	0.603	0.601	-0.002
100	140	0.607	0.608	0.001
100	141	0.617	0.614	-0.003
100	142	0.616	0.616	0.000
100	143	0.609	0.609	0.000
100	144	0.616	0.614	-0.002
100	220	0.613	0.613	0.000
100	221	0.616	0.616	0.000
100	224	0.602	0.605	0.003
100	225	0.610	0.610	0.000
100	226	0.611	0.610	-0.001
100	228	0.614	0.615	0.001
100	232	0.619	0.621	0.002
100	233	0.622	0.622	0.000
100	236	0.614	0.615	0.001
100	238	0.612	0.610	-0.002
100	243	0.617	0.617	0.000
105	244	0.612	0.611	-0.001
105	246	0.610	0.611	0.001
105	248	0.617	0.615	-0.002
105	374	0.613	0.613	0.000
105	376	0.619	0.616	-0.003
Max		0.622	0.622	0.003
Average		0.611	0.611	0.000
Min		0.602	0.601	-0.003
Std Dev		0.005	0.005	0.001



		12.1 _V_FAULT_RISE_4V	
Test Site			
Tester			
Test Number			
Max Limit	0.65	V	
Min Limit	0.57	V	

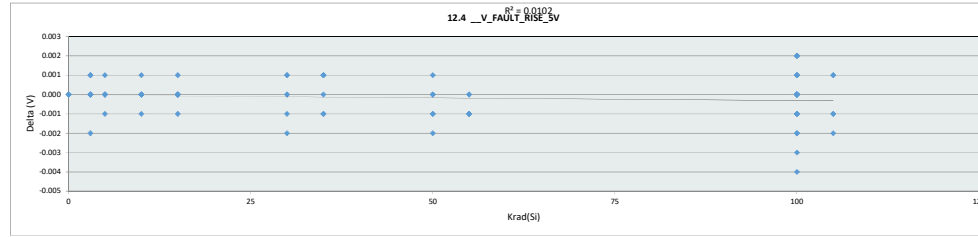
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.609	0.603	0.611	0.603	0.611	0.602	0.611	0.604	0.604	0.601	0.611
Average	0.610	0.609	0.613	0.609	0.613	0.609	0.613	0.610	0.610	0.612	0.613
Max	0.610	0.615	0.617	0.615	0.617	0.615	0.615	0.618	0.614	0.622	0.616
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



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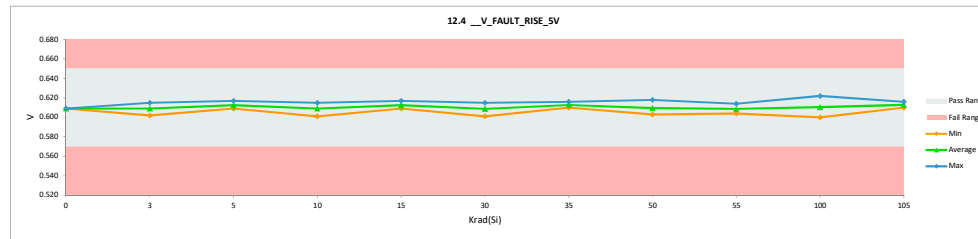
		12.4 _V_FAULT_RISE_5V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	0.65	0.65	
Min Limit	0.57	0.57	

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	0.609	0.609	0.000
0	118	0.609	0.609	0.000
0	122	0.609	0.609	0.000
3	61	0.606	0.604	-0.002
3	62	0.613	0.613	0.000
3	63	0.601	0.602	0.001
3	64	0.615	0.615	0.000
3	65	0.605	0.605	0.000
3	66	0.614	0.615	0.001
5	67	0.616	0.615	-0.001
5	68	0.609	0.609	0.000
5	69	0.609	0.610	0.001
5	70	0.617	0.617	0.000
5	71	0.610	0.610	0.000
5	72	0.614	0.614	0.000
10	73	0.606	0.605	-0.001
10	74	0.613	0.613	0.000
10	75	0.601	0.601	0.000
10	76	0.615	0.615	0.000
10	77	0.605	0.605	0.000
10	78	0.614	0.615	0.001
15	79	0.616	0.616	0.000
15	80	0.609	0.609	0.000
15	81	0.609	0.610	0.001
15	82	0.617	0.617	0.000
15	83	0.610	0.610	0.000
15	84	0.614	0.613	-0.001
30	86	0.606	0.604	-0.002
30	89	0.613	0.614	0.001
30	91	0.601	0.601	0.000
30	92	0.615	0.614	-0.001
30	94	0.605	0.605	0.000
30	95	0.614	0.615	0.001
35	100	0.616	0.615	-0.001
35	101	0.609	0.610	0.001
35	102	0.609	0.610	0.001
35	103	0.617	0.616	-0.001
35	104	0.610	0.611	0.001
35	105	0.614	0.614	0.000
50	106	0.615	0.614	-0.001
50	107	0.612	0.613	0.001
50	108	0.604	0.604	0.000
50	109	0.619	0.618	-0.001
50	110	0.605	0.603	-0.002
50	111	0.605	0.605	0.000
55	113	0.605	0.604	-0.001
55	114	0.605	0.604	-0.001
55	115	0.611	0.610	-0.001
55	116	0.615	0.614	-0.001
55	126	0.612	0.612	0.000
55	128	0.608	0.608	0.000
100	129	0.609	0.610	0.001
100	131	0.601	0.600	-0.001
100	132	0.604	0.604	0.000
100	133	0.611	0.611	0.000
100	134	0.608	0.605	-0.003
100	135	0.613	0.612	-0.001
100	138	0.610	0.610	0.000
100	139	0.602	0.600	-0.002
100	140	0.605	0.606	0.001
100	141	0.617	0.613	-0.004
100	142	0.614	0.614	0.000
100	143	0.608	0.608	0.000
100	144	0.615	0.613	-0.002
100	220	0.613	0.614	0.001
100	221	0.615	0.615	0.000
100	224	0.601	0.603	0.002
100	225	0.610	0.609	-0.001
100	226	0.610	0.610	0.000
100	228	0.614	0.615	0.001
100	232	0.619	0.621	0.002
100	233	0.622	0.622	0.000
100	236	0.612	0.614	0.002
100	238	0.611	0.610	-0.001
100	243	0.616	0.616	0.000
105	244	0.611	0.610	-0.001
105	246	0.609	0.610	0.001
105	248	0.616	0.615	-0.001
105	374	0.612	0.613	0.001
105	376	0.618	0.616	-0.002
Max		0.622	0.622	0.002
Average		0.611	0.611	0.000
Min		0.601	0.600	-0.004
Std Dev		0.005	0.005	0.001



		12.4 _V_FAULT_RISE_5V	
Test Site			
Tester			
Test Number			
Max Limit	0.65	V	
Min Limit	0.57	V	

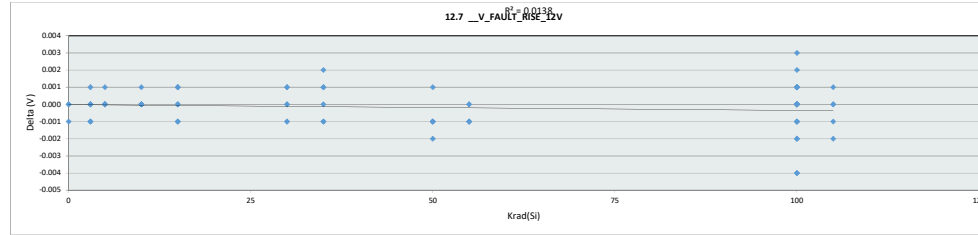
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.609	0.602	0.609	0.601	0.609	0.601	0.610	0.603	0.604	0.600	0.610
Average	0.609	0.609	0.613	0.609	0.613	0.617	0.615	0.618	0.614	0.622	0.616
Max	0.609	0.615	0.617	0.615	0.617	0.615	0.616	0.618	0.614	0.622	0.616
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



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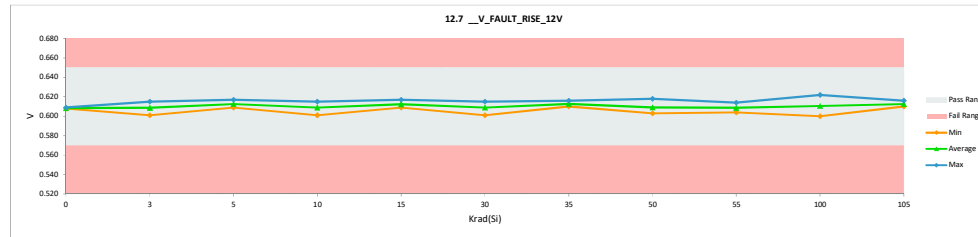
		12.7 __V_FAULT_RISE_12V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	0.65	0.65	
Min Limit	0.57	0.57	

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	0.609	0.609	0.000
0	118	0.609	0.608	-0.001
0	122	0.608	0.608	0.000
3	61	0.605	0.604	-0.001
3	62	0.613	0.613	0.000
3	63	0.601	0.601	0.000
3	64	0.615	0.615	0.000
3	65	0.605	0.604	-0.001
3	66	0.614	0.615	0.001
5	67	0.616	0.616	0.000
5	68	0.608	0.609	0.001
5	69	0.609	0.609	0.000
5	70	0.617	0.617	0.000
5	71	0.610	0.610	0.000
5	72	0.614	0.614	0.000
10	73	0.605	0.605	0.000
10	74	0.613	0.613	0.000
10	75	0.601	0.601	0.000
10	76	0.615	0.615	0.000
10	77	0.605	0.605	0.000
10	78	0.614	0.615	0.001
15	79	0.616	0.615	-0.001
15	80	0.608	0.609	0.001
15	81	0.609	0.610	0.001
15	82	0.617	0.617	0.000
15	83	0.610	0.610	0.000
15	84	0.614	0.613	-0.001
30	86	0.605	0.604	-0.001
30	89	0.613	0.614	0.001
30	91	0.601	0.601	0.000
30	92	0.615	0.614	-0.001
30	94	0.605	0.605	0.000
30	95	0.614	0.615	0.001
35	100	0.616	0.615	-0.001
35	101	0.608	0.610	0.002
35	102	0.609	0.610	0.001
35	103	0.617	0.616	-0.001
35	104	0.610	0.611	0.001
35	105	0.614	0.614	0.000
50	106	0.615	0.614	-0.001
50	107	0.612	0.613	0.001
50	108	0.604	0.603	-0.001
50	109	0.619	0.618	-0.001
50	110	0.605	0.603	-0.002
50	111	0.605	0.604	-0.001
55	113	0.605	0.604	-0.001
55	114	0.605	0.604	-0.001
55	115	0.611	0.610	-0.001
55	116	0.615	0.614	-0.001
55	126	0.612	0.612	0.000
55	128	0.608	0.608	0.000
100	129	0.609	0.610	0.001
100	131	0.601	0.600	-0.001
100	132	0.604	0.604	0.000
100	133	0.611	0.611	0.000
100	134	0.608	0.604	-0.004
100	135	0.613	0.612	-0.001
100	138	0.610	0.609	-0.001
100	139	0.601	0.600	-0.001
100	140	0.605	0.606	0.001
100	141	0.617	0.613	-0.004
100	142	0.614	0.614	0.000
100	143	0.608	0.608	0.000
100	144	0.615	0.613	-0.002
100	220	0.614	0.615	0.001
100	221	0.615	0.615	0.000
100	224	0.600	0.603	0.003
100	225	0.609	0.609	0.000
100	226	0.610	0.610	0.000
100	228	0.614	0.615	0.001
100	232	0.619	0.621	0.002
100	233	0.622	0.622	0.000
100	236	0.612	0.613	0.001
100	238	0.611	0.609	-0.002
100	243	0.616	0.616	0.000
105	244	0.610	0.610	0.000
105	246	0.609	0.610	0.001
105	248	0.616	0.615	-0.001
105	374	0.612	0.612	0.000
105	376	0.618	0.616	-0.002
Max		0.622	0.622	0.003
Average		0.611	0.610	0.000
Min		0.600	0.600	-0.004
Std Dev		0.005	0.005	0.001



		12.7 __V_FAULT_RISE_12V	
Test Site			
Tester			
Test Number			
Max Limit	0.65	V	
Min Limit	0.57	V	

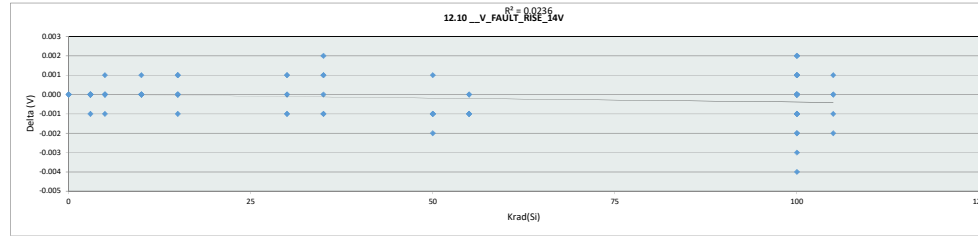
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.608	0.601	0.609	0.601	0.609	0.601	0.610	0.603	0.604	0.600	0.610
Average	0.608	0.609	0.613	0.609	0.612	0.609	0.613	0.609	0.609	0.611	0.613
Max	0.609	0.615	0.617	0.615	0.617	0.615	0.616	0.618	0.614	0.622	0.616
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



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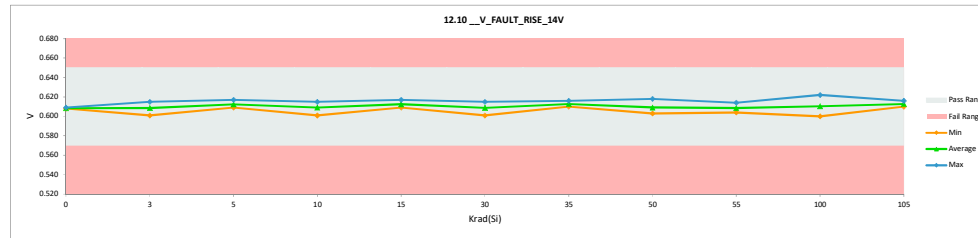
		12.10_V_FAULT_RISE_14V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	0.65	0.65	
Min Limit	0.57	0.57	

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	0.609	0.609	0.000
0	118	0.608	0.608	0.000
0	122	0.608	0.608	0.000
3	61	0.605	0.604	-0.001
3	62	0.613	0.613	0.000
3	63	0.601	0.601	0.000
3	64	0.615	0.615	0.000
3	65	0.605	0.605	0.000
3	66	0.614	0.614	0.000
5	67	0.616	0.615	-0.001
5	68	0.608	0.609	0.001
5	69	0.609	0.609	0.000
5	70	0.617	0.617	0.000
5	71	0.610	0.610	0.000
5	72	0.614	0.614	0.000
10	73	0.605	0.605	0.000
10	74	0.613	0.613	0.000
10	75	0.601	0.601	0.000
10	76	0.615	0.615	0.000
10	77	0.605	0.605	0.000
10	78	0.614	0.615	0.001
15	79	0.616	0.616	0.000
15	80	0.608	0.609	0.001
15	81	0.609	0.610	0.001
15	82	0.617	0.617	0.000
15	83	0.610	0.610	0.000
15	84	0.614	0.613	-0.001
30	86	0.605	0.604	-0.001
30	89	0.613	0.614	0.001
30	91	0.601	0.601	0.000
30	92	0.615	0.614	-0.001
30	94	0.605	0.605	0.000
30	95	0.614	0.615	0.001
35	100	0.616	0.615	-0.001
35	101	0.608	0.610	0.002
35	102	0.609	0.610	0.001
35	103	0.617	0.616	-0.001
35	104	0.610	0.611	0.001
35	105	0.614	0.614	0.000
50	106	0.615	0.614	-0.001
50	107	0.612	0.613	0.001
50	108	0.604	0.603	-0.001
50	109	0.619	0.618	-0.001
50	110	0.605	0.603	-0.002
50	111	0.605	0.604	-0.001
55	113	0.605	0.604	-0.001
55	114	0.605	0.604	-0.001
55	115	0.611	0.610	-0.001
55	116	0.615	0.614	-0.001
55	126	0.612	0.612	0.000
55	128	0.608	0.608	0.000
100	129	0.609	0.610	0.001
100	131	0.601	0.600	-0.001
100	132	0.604	0.604	0.000
100	133	0.611	0.611	0.000
100	134	0.608	0.604	-0.004
100	135	0.613	0.612	-0.001
100	138	0.610	0.609	-0.001
100	139	0.602	0.600	-0.002
100	140	0.605	0.606	0.001
100	141	0.617	0.614	-0.003
100	142	0.614	0.614	0.000
100	143	0.608	0.608	0.000
100	144	0.615	0.613	-0.002
100	220	0.614	0.615	0.001
100	221	0.615	0.615	0.000
100	224	0.601	0.603	0.002
100	225	0.609	0.608	-0.001
100	226	0.610	0.609	-0.001
100	228	0.614	0.615	0.001
100	232	0.619	0.621	0.002
100	233	0.622	0.622	0.000
100	236	0.612	0.613	0.001
100	238	0.610	0.610	0.000
100	243	0.616	0.616	0.000
105	244	0.610	0.610	0.000
105	246	0.609	0.610	0.001
105	248	0.616	0.615	-0.001
105	374	0.612	0.612	0.000
105	376	0.618	0.616	-0.002
Max		0.622	0.622	0.002
Average		0.611	0.610	0.000
Min		0.601	0.600	-0.004
Std Dev		0.005	0.005	0.001



		12.10_V_FAULT_RISE_14V	
Test Site			
Tester			
Test Number			
Max Limit	0.65	V	
Min Limit	0.57	V	

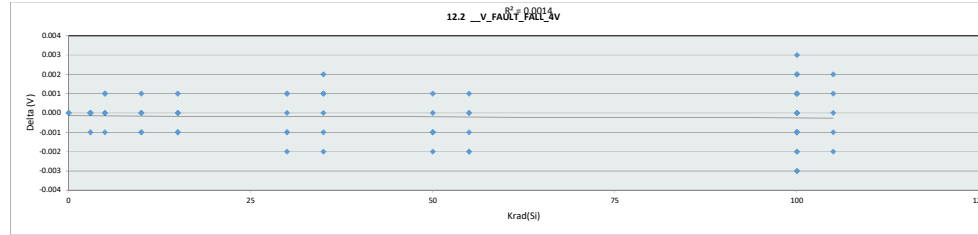
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.608	0.601	0.609	0.601	0.609	0.601	0.610	0.603	0.604	0.600	0.610
Average	0.608	0.609	0.612	0.609	0.613	0.609	0.613	0.609	0.609	0.611	0.613
Max	0.609	0.615	0.617	0.615	0.617	0.615	0.616	0.618	0.614	0.622	0.616
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



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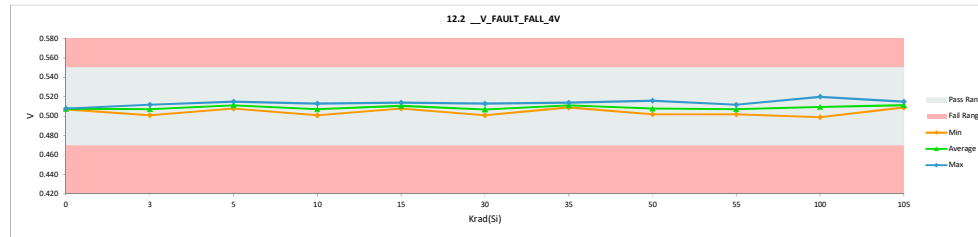
		12.2 _V_FAULT_FALL_4V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	0.55	0.55	
Min Limit	0.47	0.47	

Krad(S)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	0.507	0.507	0.000
0	118	0.508	0.508	0.000
0	122	0.508	0.508	0.000
3	61	0.503	0.503	0.000
3	62	0.512	0.512	0.000
3	63	0.501	0.501	0.000
3	64	0.512	0.512	0.000
3	65	0.504	0.503	-0.001
3	66	0.512	0.512	0.000
5	67	0.514	0.513	-0.001
5	68	0.508	0.509	0.001
5	69	0.508	0.508	0.000
5	70	0.514	0.515	0.001
5	71	0.509	0.509	0.000
5	72	0.513	0.513	0.000
10	73	0.503	0.503	0.000
10	74	0.512	0.511	-0.001
10	75	0.501	0.501	0.000
10	76	0.512	0.512	0.000
10	77	0.504	0.503	-0.001
10	78	0.512	0.513	0.001
15	79	0.514	0.513	-0.001
15	80	0.508	0.509	0.001
15	81	0.508	0.508	0.000
15	82	0.514	0.514	0.000
15	83	0.509	0.509	0.000
15	84	0.513	0.512	-0.001
30	86	0.503	0.501	-0.002
30	89	0.512	0.513	0.001
30	91	0.501	0.501	0.000
30	92	0.512	0.511	-0.001
30	94	0.504	0.503	-0.001
30	95	0.512	0.513	0.001
35	100	0.514	0.512	-0.002
35	101	0.508	0.510	0.002
35	102	0.508	0.509	0.001
35	103	0.514	0.514	0.000
35	104	0.509	0.510	0.001
35	105	0.513	0.512	-0.001
50	106	0.512	0.512	0.000
50	107	0.510	0.511	0.001
50	108	0.504	0.503	-0.001
50	109	0.517	0.516	-0.001
50	110	0.504	0.502	-0.002
50	111	0.505	0.504	-0.001
55	113	0.504	0.503	-0.001
55	114	0.504	0.502	-0.002
55	115	0.509	0.509	0.000
55	116	0.514	0.512	-0.002
55	126	0.511	0.511	0.000
55	128	0.506	0.507	0.001
100	129	0.507	0.509	0.002
100	131	0.502	0.501	-0.001
100	132	0.504	0.504	0.000
100	133	0.509	0.509	0.000
100	134	0.507	0.504	-0.003
100	135	0.512	0.511	-0.001
100	138	0.510	0.509	-0.001
100	139	0.501	0.499	-0.002
100	140	0.504	0.505	0.001
100	141	0.515	0.512	-0.003
100	142	0.514	0.513	-0.001
100	143	0.507	0.507	0.000
100	144	0.514	0.512	-0.002
100	220	0.511	0.512	0.001
100	221	0.514	0.514	0.000
100	224	0.500	0.503	0.003
100	225	0.508	0.507	-0.001
100	226	0.508	0.508	0.000
100	228	0.512	0.513	0.001
100	232	0.517	0.519	0.002
100	233	0.520	0.520	0.000
100	236	0.512	0.513	0.001
100	238	0.510	0.509	-0.001
100	243	0.515	0.515	0.000
105	244	0.509	0.509	0.000
105	246	0.507	0.509	0.002
105	248	0.514	0.513	-0.001
105	374	0.510	0.511	0.001
105	376	0.517	0.515	-0.002
Max		0.520	0.520	0.003
Average		0.509	0.509	0.000
Min		0.500	0.499	-0.003
Std Dev		0.005	0.005	0.001



		12.2 _V_FAULT_FALL_4V	
Test Site			
Tester			
Test Number			
Max Limit	0.55	V	
Min Limit	0.47	V	

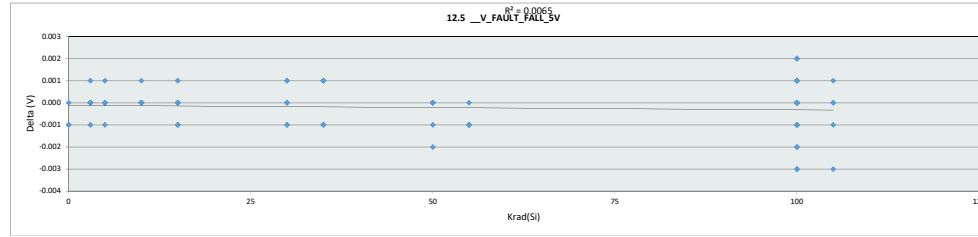
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.507	0.501	0.508	0.501	0.508	0.501	0.509	0.502	0.502	0.499	0.509
Average	0.508	0.507	0.511	0.507	0.511	0.507	0.511	0.508	0.507	0.510	0.511
Max	0.508	0.512	0.515	0.513	0.514	0.513	0.514	0.516	0.512	0.520	0.515
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



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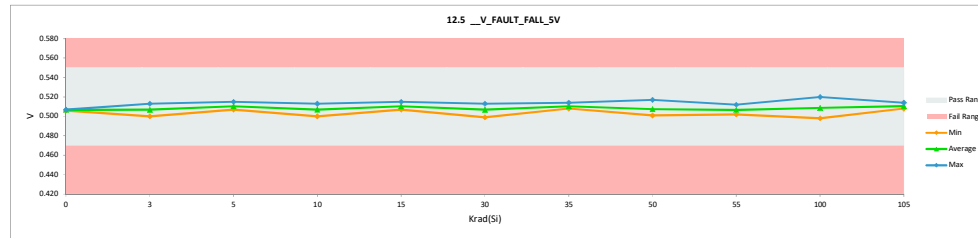
		12.5 _V_FAULT_FALL_5V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	0.55	0.55	
Min Limit	0.47	0.47	

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	0.508	0.507	-0.001
0	118	0.507	0.506	-0.001
0	122	0.507	0.507	0.000
3	61	0.503	0.502	-0.001
3	62	0.511	0.511	0.000
3	63	0.499	0.500	0.001
3	64	0.513	0.513	0.000
3	65	0.503	0.503	0.000
3	66	0.512	0.512	0.000
5	67	0.514	0.513	-0.001
5	68	0.507	0.507	0.000
5	69	0.507	0.508	0.001
5	70	0.515	0.515	0.000
5	71	0.508	0.508	0.000
5	72	0.512	0.512	0.000
10	73	0.503	0.503	0.000
10	74	0.511	0.511	0.000
10	75	0.499	0.500	0.001
10	76	0.513	0.513	0.000
10	77	0.503	0.503	0.000
10	78	0.512	0.512	0.000
15	79	0.514	0.513	-0.001
15	80	0.507	0.507	0.000
15	81	0.507	0.507	0.000
15	82	0.515	0.515	0.000
15	83	0.508	0.509	0.001
15	84	0.512	0.511	-0.001
30	86	0.503	0.502	-0.001
30	89	0.511	0.512	0.001
30	91	0.499	0.499	0.000
30	92	0.513	0.512	-0.001
30	94	0.503	0.503	0.000
30	95	0.512	0.513	0.001
35	100	0.514	0.513	-0.001
35	101	0.507	0.508	0.001
35	102	0.507	0.508	0.001
35	103	0.515	0.514	-0.001
35	104	0.508	0.509	0.001
35	105	0.512	0.511	-0.001
50	106	0.512	0.512	0.000
50	107	0.511	0.511	0.000
50	108	0.502	0.502	0.000
50	109	0.517	0.517	0.000
50	110	0.503	0.501	-0.002
50	111	0.503	0.502	-0.001
55	113	0.503	0.502	-0.001
55	114	0.503	0.502	-0.001
55	115	0.509	0.508	-0.001
55	116	0.513	0.512	-0.001
55	126	0.511	0.510	-0.001
55	128	0.506	0.506	0.000
100	129	0.508	0.509	0.001
100	131	0.499	0.498	-0.001
100	132	0.502	0.502	0.000
100	133	0.509	0.509	0.000
100	134	0.506	0.503	-0.003
100	135	0.511	0.510	-0.001
100	138	0.508	0.508	0.000
100	139	0.500	0.498	-0.002
100	140	0.503	0.504	0.001
100	141	0.515	0.512	-0.003
100	142	0.512	0.512	0.000
100	143	0.507	0.506	-0.001
100	144	0.513	0.511	-0.002
100	220	0.512	0.513	0.001
100	221	0.513	0.513	0.000
100	224	0.499	0.501	0.002
100	225	0.507	0.507	0.000
100	226	0.508	0.508	0.000
100	228	0.512	0.514	0.002
100	232	0.517	0.519	0.002
100	233	0.520	0.520	0.000
100	236	0.510	0.511	0.001
100	238	0.509	0.508	-0.001
100	243	0.514	0.514	0.000
105	244	0.509	0.508	-0.001
105	246	0.507	0.508	0.001
105	248	0.513	0.513	0.000
105	374	0.510	0.510	0.000
105	376	0.517	0.514	-0.003
Max		0.520	0.520	0.002
Average		0.509	0.509	0.000
Min		0.498	0.498	-0.003
Std Dev		0.005	0.005	0.001



		12.5 _V_FAULT_FALL_5V	
Test Site			
Tester			
Test Number			
Max Limit	0.55	V	
Min Limit	0.47	V	

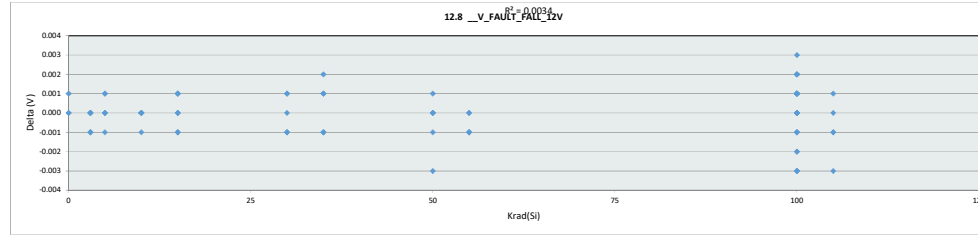
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.506	0.500	0.507	0.500	0.507	0.499	0.508	0.501	0.502	0.498	0.508
Average	0.507	0.507	0.511	0.507	0.510	0.507	0.511	0.508	0.507	0.509	0.511
Max	0.507	0.513	0.515	0.513	0.515	0.513	0.514	0.517	0.512	0.520	0.514
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



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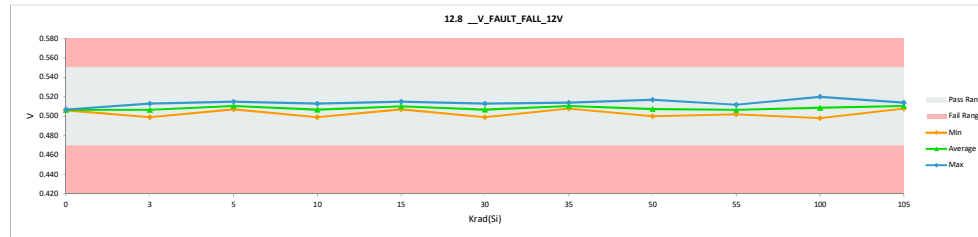
12.8 __V_FAULT_FALL_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.55
Min Limit	0.47

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	0.507	0.507	0.000
0	118	0.506	0.506	0.000
0	122	0.506	0.507	0.001
3	61	0.503	0.502	-0.001
3	62	0.511	0.511	0.000
3	63	0.500	0.499	-0.001
3	64	0.513	0.513	0.000
3	65	0.503	0.503	0.000
3	66	0.512	0.512	0.000
5	67	0.514	0.513	-0.001
5	68	0.506	0.507	0.001
5	69	0.507	0.508	0.001
5	70	0.515	0.515	0.000
5	71	0.508	0.508	0.000
5	72	0.512	0.512	0.000
10	73	0.503	0.503	0.000
10	74	0.511	0.511	0.000
10	75	0.500	0.499	-0.001
10	76	0.513	0.513	0.000
10	77	0.503	0.503	0.000
10	78	0.512	0.512	0.000
15	79	0.514	0.513	-0.001
15	80	0.506	0.507	0.001
15	81	0.507	0.508	0.001
15	82	0.515	0.515	0.000
15	83	0.508	0.508	0.000
15	84	0.512	0.511	-0.001
30	86	0.503	0.502	-0.001
30	89	0.511	0.512	0.001
30	91	0.500	0.499	-0.001
30	92	0.513	0.512	-0.001
30	94	0.503	0.503	0.000
30	95	0.512	0.513	0.001
35	100	0.514	0.513	-0.001
35	101	0.506	0.508	0.002
35	102	0.507	0.508	0.001
35	103	0.515	0.514	-0.001
35	104	0.508	0.509	0.001
35	105	0.512	0.511	-0.001
50	106	0.512	0.512	0.000
50	107	0.511	0.512	0.001
50	108	0.502	0.501	-0.001
50	109	0.517	0.517	0.000
50	110	0.503	0.500	-0.003
50	111	0.503	0.503	0.000
55	113	0.503	0.502	-0.001
55	114	0.503	0.502	-0.001
55	115	0.509	0.508	-0.001
55	116	0.513	0.512	-0.001
55	126	0.510	0.510	0.000
55	128	0.506	0.506	0.000
100	129	0.507	0.509	0.002
100	131	0.499	0.498	-0.001
100	132	0.502	0.502	0.000
100	133	0.509	0.510	0.001
100	134	0.506	0.503	-0.003
100	135	0.511	0.510	-0.001
100	138	0.508	0.508	0.000
100	139	0.500	0.498	-0.002
100	140	0.503	0.504	0.001
100	141	0.515	0.512	-0.003
100	142	0.511	0.512	0.001
100	143	0.507	0.506	-0.001
100	144	0.513	0.510	-0.003
100	220	0.512	0.513	0.001
100	221	0.513	0.513	0.000
100	224	0.498	0.501	0.003
100	225	0.508	0.506	-0.002
100	226	0.508	0.508	0.000
100	228	0.512	0.513	0.001
100	232	0.517	0.519	0.002
100	233	0.519	0.520	0.001
100	236	0.509	0.511	0.002
100	238	0.508	0.508	0.000
100	243	0.514	0.514	0.000
105	244	0.508	0.508	0.000
105	246	0.507	0.508	0.001
105	248	0.514	0.513	-0.001
105	374	0.511	0.510	-0.001
105	376	0.517	0.514	-0.003
Max		0.519	0.520	0.003
Average		0.509	0.508	0.000
Min		0.498	0.498	-0.003
Std Dev		0.005	0.005	0.001



12.8 __V_FAULT_FALL_12V	
Test Site	
Tester	
Test Number	
Max Limit	0.55 V
Min Limit	0.47 V

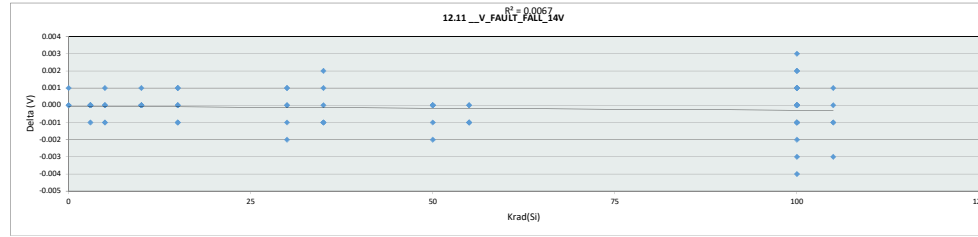
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.506	0.499	0.507	0.499	0.507	0.499	0.508	0.500	0.502	0.498	0.508
Average	0.507	0.507	0.511	0.507	0.510	0.507	0.511	0.508	0.507	0.509	0.511
Max	0.507	0.513	0.515	0.513	0.515	0.513	0.514	0.517	0.512	0.520	0.514
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



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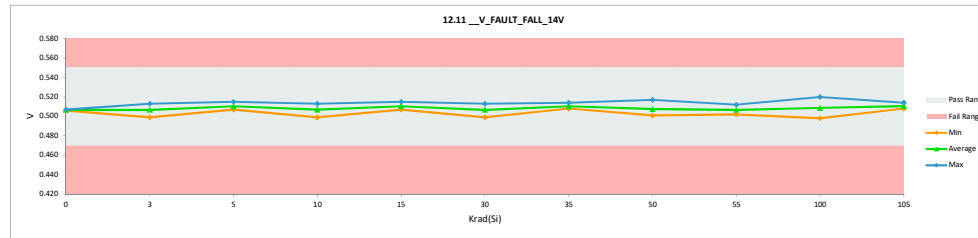
		12.11_V_FAULT_FALL_14V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	0.55	0.55	
Min Limit	0.47	0.47	

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	0.507	0.507	0.000
0	118	0.506	0.507	0.001
0	122	0.506	0.506	0.000
3	61	0.503	0.502	-0.001
3	62	0.511	0.511	0.000
3	63	0.499	0.499	0.000
3	64	0.513	0.513	0.000
3	65	0.503	0.503	0.000
3	66	0.512	0.512	0.000
5	67	0.514	0.513	-0.001
5	68	0.506	0.507	0.001
5	69	0.507	0.507	0.000
5	70	0.515	0.515	0.000
5	71	0.509	0.508	-0.001
5	72	0.512	0.512	0.000
10	73	0.503	0.503	0.000
10	74	0.511	0.511	0.000
10	75	0.499	0.499	0.000
10	76	0.513	0.513	0.000
10	77	0.503	0.503	0.000
10	78	0.512	0.513	0.001
15	79	0.514	0.513	-0.001
15	80	0.506	0.507	0.001
15	81	0.507	0.508	0.001
15	82	0.515	0.515	0.000
15	83	0.509	0.509	0.000
15	84	0.512	0.511	-0.001
30	86	0.503	0.501	-0.002
30	89	0.511	0.512	0.001
30	91	0.499	0.499	0.000
30	92	0.513	0.512	-0.001
30	94	0.503	0.503	0.000
30	95	0.512	0.513	0.001
35	100	0.514	0.513	-0.001
35	101	0.506	0.508	0.002
35	102	0.507	0.508	0.001
35	103	0.515	0.514	-0.001
35	104	0.509	0.509	0.000
35	105	0.512	0.511	-0.001
50	106	0.512	0.512	0.000
50	107	0.511	0.511	0.000
50	108	0.502	0.502	0.000
50	109	0.517	0.517	0.000
50	110	0.503	0.501	-0.002
50	111	0.503	0.502	-0.001
55	113	0.503	0.502	-0.001
55	114	0.503	0.502	-0.001
55	115	0.509	0.508	-0.001
55	116	0.513	0.512	-0.001
55	126	0.510	0.510	0.000
55	128	0.506	0.506	0.000
100	129	0.507	0.508	0.001
100	131	0.499	0.498	-0.001
100	132	0.502	0.501	-0.001
100	133	0.508	0.509	0.001
100	134	0.506	0.502	-0.004
100	135	0.511	0.510	-0.001
100	138	0.508	0.508	0.000
100	139	0.500	0.498	-0.002
100	140	0.503	0.504	0.001
100	141	0.515	0.512	-0.003
100	142	0.512	0.512	0.000
100	143	0.506	0.506	0.000
100	144	0.512	0.511	-0.001
100	220	0.512	0.513	0.001
100	221	0.513	0.513	0.000
100	224	0.498	0.501	0.003
100	225	0.507	0.507	0.000
100	226	0.508	0.508	0.000
100	228	0.512	0.514	0.002
100	232	0.517	0.519	0.002
100	233	0.520	0.520	0.000
100	236	0.510	0.511	0.001
100	238	0.509	0.508	-0.001
100	243	0.514	0.514	0.000
105	244	0.509	0.508	-0.001
105	246	0.507	0.508	0.001
105	248	0.514	0.513	-0.001
105	374	0.510	0.510	0.000
105	376	0.517	0.514	-0.003
Max		0.520	0.520	0.003
Average		0.509	0.508	0.000
Min		0.498	0.498	-0.004
Std Dev		0.005	0.005	0.001



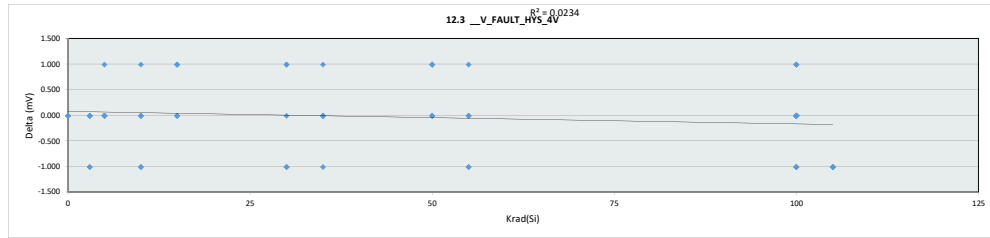
		12.11_V_FAULT_FALL_14V	
Test Site			
Tester			
Test Number			
Max Limit	0.55	V	
Min Limit	0.47	V	

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.506	0.499	0.507	0.499	0.507	0.499	0.508	0.501	0.502	0.498	0.508
Average	0.507	0.507	0.510	0.507	0.511	0.507	0.511	0.508	0.507	0.509	0.511
Max	0.507	0.513	0.515	0.513	0.515	0.513	0.514	0.517	0.512	0.520	0.514
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550

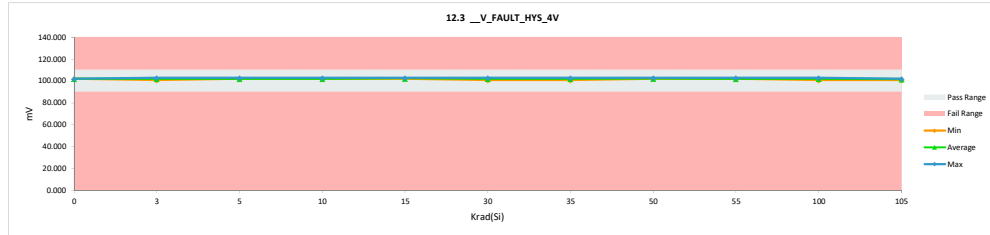


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		12.3 __V_FAULT_HYS_4V		
Test Site	Tester			
Test Number	Unit	mV	mV	
Max Limit		110	110	
Min Limit		90	90	
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	102.000	102.000	0.000
0	118	102.000	102.000	0.000
0	122	102.000	102.000	0.000
3	61	102.000	101.000	-1.000
3	62	102.000	102.000	0.000
3	63	102.000	102.000	0.000
3	64	103.000	102.000	-1.000
3	65	102.000	102.000	0.000
3	66	103.000	103.000	0.000
5	67	102.000	102.000	0.000
5	68	102.000	102.000	0.000
5	69	102.000	103.000	1.000
5	70	102.000	102.000	0.000
5	71	102.000	102.000	0.000
5	72	102.000	102.000	0.000
10	73	102.000	102.000	0.000
10	74	102.000	103.000	1.000
10	75	102.000	102.000	0.000
10	76	103.000	102.000	-1.000
10	77	102.000	102.000	0.000
10	78	103.000	102.000	-1.000
15	79	102.000	102.000	0.000
15	80	102.000	102.000	0.000
15	81	102.000	103.000	1.000
15	82	102.000	103.000	1.000
15	83	102.000	102.000	0.000
15	84	102.000	103.000	1.000
30	86	102.000	103.000	1.000
30	89	102.000	102.000	0.000
30	91	102.000	101.000	-1.000
30	92	103.000	102.000	-1.000
30	94	102.000	103.000	1.000
30	95	103.000	102.000	-1.000
35	100	102.000	102.000	0.000
35	101	102.000	102.000	0.000
35	102	102.000	102.000	0.000
35	103	102.000	101.000	-1.000
35	104	102.000	102.000	0.000
35	105	102.000	103.000	1.000
50	106	102.000	102.000	0.000
50	107	102.000	102.000	0.000
50	108	102.000	103.000	1.000
50	109	101.000	102.000	1.000
50	110	102.000	102.000	0.000
50	111	102.000	103.000	1.000
55	113	102.000	103.000	1.000
55	114	102.000	102.000	0.000
55	115	103.000	102.000	-1.000
55	116	102.000	102.000	0.000
55	125	102.000	102.000	0.000
55	128	103.000	102.000	-1.000
100	129	103.000	102.000	-1.000
100	131	102.000	102.000	0.000
100	132	102.000	102.000	0.000
100	133	102.000	102.000	0.000
100	134	102.000	102.000	0.000
100	135	102.000	102.000	0.000
100	138	101.000	102.000	1.000
100	139	102.000	102.000	0.000
100	140	103.000	103.000	0.000
100	141	102.000	102.000	0.000
100	142	102.000	103.000	1.000
100	143	102.000	102.000	0.000
100	144	102.000	102.000	0.000
100	220	102.000	101.000	-1.000
100	221	102.000	102.000	0.000
100	224	102.000	102.000	0.000
100	225	102.000	103.000	1.000
100	226	103.000	102.000	-1.000
100	228	102.000	102.000	0.000
100	232	102.000	102.000	0.000
100	233	102.000	102.000	0.000
100	236	102.000	102.000	0.000
100	238	102.000	101.000	-1.000
100	243	102.000	102.000	0.000
105	244	103.000	102.000	-1.000
105	246	103.000	102.000	-1.000
105	248	103.000	102.000	-1.000
105	374	103.000	102.000	-1.000
105	376	102.000	101.000	-1.000
	Max	103.000	103.000	1.000
	Average	102.163	102.113	-0.050
	Min	101.000	101.000	-1.000
	Std Dev	0.434	0.503	0.654

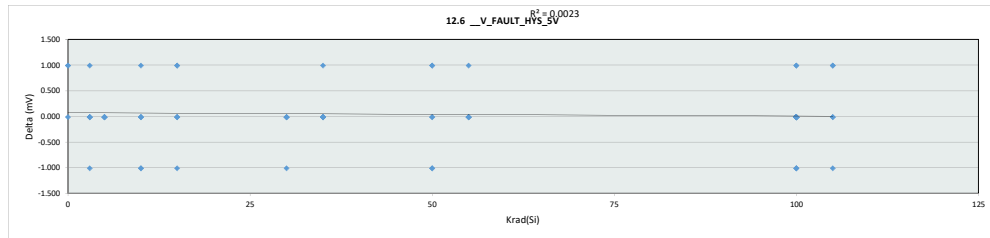


		12.3 __V_FAULT_HYS_4V										
Test Site	Tester											
Test Number	Unit											
Max Limit		110	mV									
Min Limit		90	mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	
Min	102.000	101.000	102.000	102.000	102.000	101.000	101.000	102.000	101.000	101.000	101.000	
Average	102.000	102.000	102.167	102.167	102.500	102.167	102.000	102.333	102.167	102.042	101.800	
Max	102.000	103.000	103.000	103.000	103.000	103.000	103.000	103.000	103.000	103.000	102.000	
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	

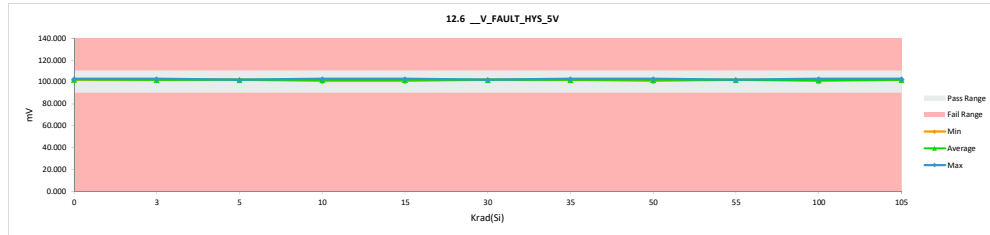


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12.6 __V_FAULT_HYS_5V				
Test Site	Tester	Unit	Max Limit	Min Limit
12.6 __V_FAULT_HYS_5V		mV	110	90
0	117	101.000	102.000	1.000
0	118	102.000	103.000	1.000
0	122	102.000	102.000	0.000
3	61	103.000	102.000	-1.000
3	62	102.000	102.000	0.000
3	63	102.000	102.000	0.000
3	64	102.000	102.000	0.000
3	65	102.000	102.000	0.000
3	66	102.000	103.000	1.000
5	67	102.000	102.000	0.000
5	68	102.000	102.000	0.000
5	69	102.000	102.000	0.000
5	70	102.000	102.000	0.000
5	71	102.000	102.000	0.000
5	72	102.000	102.000	0.000
10	73	103.000	102.000	-1.000
10	74	102.000	102.000	0.000
10	75	102.000	101.000	-1.000
10	76	102.000	102.000	0.000
10	77	102.000	102.000	0.000
10	78	102.000	103.000	1.000
15	79	102.000	103.000	1.000
15	80	102.000	102.000	0.000
15	81	102.000	103.000	1.000
15	82	102.000	102.000	0.000
15	83	102.000	101.000	-1.000
15	84	102.000	102.000	0.000
30	86	103.000	102.000	-1.000
30	89	102.000	102.000	0.000
30	91	102.000	102.000	0.000
30	92	102.000	102.000	0.000
30	94	102.000	102.000	0.000
30	95	102.000	102.000	0.000
35	100	102.000	102.000	0.000
35	101	102.000	102.000	0.000
35	102	102.000	102.000	0.000
35	103	102.000	102.000	0.000
35	104	102.000	102.000	0.000
35	105	102.000	103.000	1.000
50	106	103.000	102.000	-1.000
50	107	101.000	102.000	1.000
50	108	102.000	102.000	0.000
50	109	102.000	101.000	-1.000
50	110	102.000	102.000	0.000
50	111	102.000	103.000	1.000
55	113	102.000	102.000	0.000
55	114	102.000	102.000	0.000
55	115	102.000	102.000	0.000
55	116	102.000	102.000	0.000
55	126	101.000	102.000	1.000
55	128	102.000	102.000	0.000
100	129	101.000	101.000	0.000
100	131	102.000	102.000	0.000
100	132	102.000	102.000	0.000
100	133	102.000	102.000	0.000
100	134	102.000	102.000	0.000
100	135	102.000	102.000	0.000
100	138	102.000	102.000	0.000
100	139	102.000	102.000	0.000
100	140	102.000	102.000	0.000
100	141	102.000	101.000	-1.000
100	142	102.000	102.000	0.000
100	143	101.000	102.000	1.000
100	144	102.000	102.000	0.000
100	220	101.000	101.000	0.000
100	221	102.000	102.000	0.000
100	224	102.000	102.000	0.000
100	225	103.000	102.000	-1.000
100	226	102.000	102.000	0.000
100	228	102.000	101.000	-1.000
100	232	102.000	102.000	0.000
100	233	102.000	102.000	0.000
100	236	102.000	103.000	1.000
100	238	102.000	102.000	0.000
100	243	102.000	102.000	0.000
105	244	102.000	102.000	0.000
105	246	102.000	102.000	0.000
105	248	103.000	102.000	-1.000
105	374	102.000	103.000	1.000
105	376	101.000	102.000	1.000
Max		103.000	103.000	1.000
Average		101.988	102.025	0.038
Min		101.000	101.000	-1.000
Std Dev		0.405	0.449	0.561

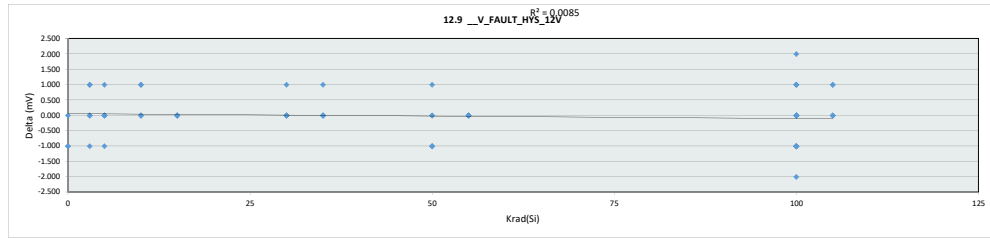


12.6 __V_FAULT_HYS_5V											
Test Site	Tester	Unit	Max Limit	Min Limit							
12.6 __V_FAULT_HYS_5V		mV	110	90							
0	3	5	10	15	30	35	50	55	100	105	
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	
Min	102.000	102.000	102.000	101.000	101.000	102.000	102.000	101.000	102.000	102.000	
Average	102.333	102.167	102.000	102.000	102.167	102.000	102.167	102.000	102.000	101.875	
Max	103.000	103.000	102.000	103.000	103.000	102.000	103.000	103.000	102.000	103.000	
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	

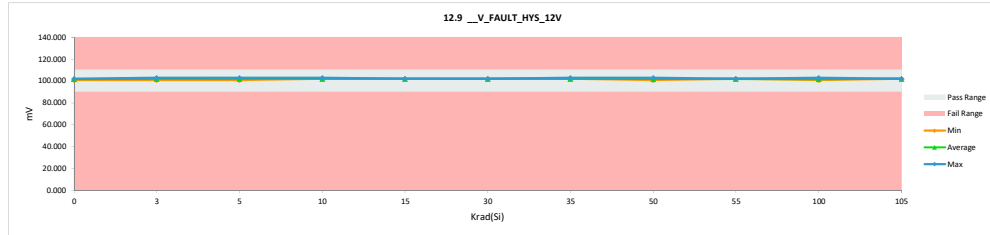


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12.9 _V_FAULT_HYS_12V				
Test Site				
Tester				
Test Number				
Unit	mV		mV	
Max Limit	110		110	
Min Limit	90		90	
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	102.000	102.000	0.000
0	118	103.000	102.000	-1.000
0	122	102.000	101.000	-1.000
3	61	102.000	102.000	0.000
3	62	102.000	102.000	0.000
3	63	101.000	102.000	1.000
3	64	102.000	102.000	0.000
3	65	102.000	101.000	-1.000
3	66	102.000	103.000	1.000
5	67	102.000	103.000	1.000
5	68	102.000	102.000	0.000
5	69	102.000	101.000	-1.000
5	70	102.000	102.000	0.000
5	71	102.000	102.000	0.000
5	72	102.000	102.000	0.000
10	73	102.000	102.000	0.000
10	74	102.000	102.000	0.000
10	75	101.000	102.000	1.000
10	76	102.000	102.000	0.000
10	77	102.000	102.000	0.000
10	78	102.000	103.000	1.000
15	79	102.000	102.000	0.000
15	80	102.000	102.000	0.000
15	81	102.000	102.000	0.000
15	82	102.000	102.000	0.000
15	83	102.000	102.000	0.000
15	84	102.000	102.000	0.000
30	86	102.000	102.000	0.000
30	89	102.000	102.000	0.000
30	91	101.000	102.000	1.000
30	92	102.000	102.000	0.000
30	94	102.000	102.000	0.000
30	95	102.000	102.000	0.000
35	100	102.000	102.000	0.000
35	101	102.000	102.000	0.000
35	102	102.000	102.000	0.000
35	103	102.000	102.000	0.000
35	104	102.000	102.000	0.000
35	105	102.000	103.000	1.000
50	106	103.000	102.000	-1.000
50	107	101.000	101.000	0.000
50	108	102.000	102.000	0.000
50	109	102.000	101.000	-1.000
50	110	102.000	103.000	1.000
50	111	102.000	101.000	-1.000
55	113	102.000	102.000	0.000
55	114	102.000	102.000	0.000
55	115	102.000	102.000	0.000
55	116	102.000	102.000	0.000
55	125	102.000	102.000	0.000
55	128	102.000	102.000	0.000
100	129	102.000	101.000	-1.000
100	131	102.000	102.000	0.000
100	132	102.000	102.000	0.000
100	133	102.000	101.000	-1.000
100	134	102.000	101.000	-1.000
100	135	102.000	102.000	0.000
100	138	102.000	101.000	-1.000
100	139	101.000	102.000	1.000
100	140	102.000	102.000	0.000
100	141	102.000	101.000	-1.000
100	142	103.000	102.000	-1.000
100	143	101.000	102.000	1.000
100	144	102.000	103.000	1.000
100	220	102.000	102.000	0.000
100	221	102.000	102.000	0.000
100	224	102.000	102.000	0.000
100	225	101.000	103.000	2.000
100	226	102.000	102.000	0.000
100	228	102.000	102.000	0.000
100	232	102.000	102.000	0.000
100	233	103.000	102.000	-1.000
100	236	103.000	102.000	-1.000
100	238	103.000	101.000	-2.000
100	243	102.000	102.000	0.000
105	244	102.000	102.000	0.000
105	246	102.000	102.000	0.000
105	248	102.000	102.000	0.000
105	374	101.000	102.000	1.000
105	376	101.000	102.000	1.000
	Max	103.000	103.000	2.000
	Average	101.963	101.938	-0.025
	Min	101.000	101.000	-2.000
	Std Dev	0.434	0.486	0.675

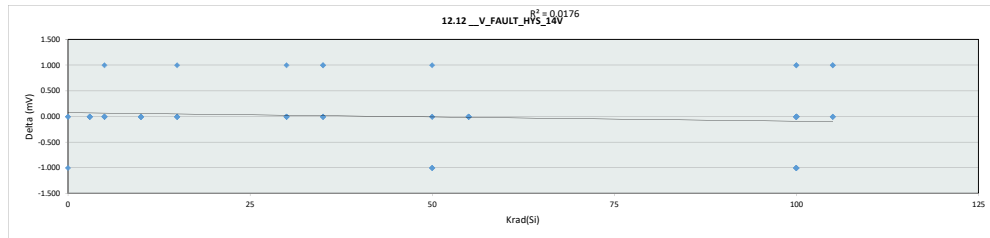


12.9 _V_FAULT_HYS_12V											
Test Site											
Tester											
Test Number											
Max Limit	110		mV								
Min Limit	90		mV								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min	101.000	101.000	101.000	102.000	102.000	102.000	102.000	102.000	101.000	102.000	101.000
Average	101.667	102.000	102.000	102.167	102.000	102.000	102.167	101.667	102.000	101.833	102.000
Max	102.000	103.000	103.000	103.000	102.000	102.000	103.000	103.000	103.000	103.000	102.000
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

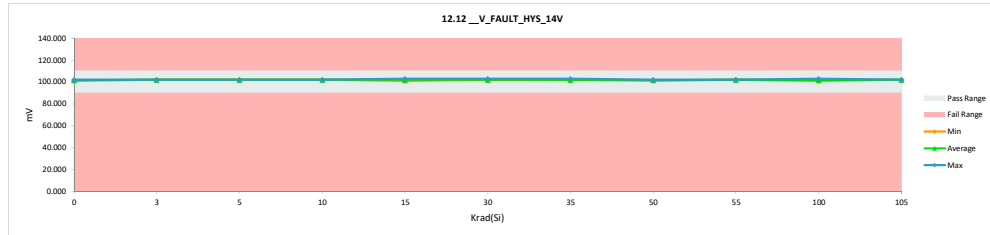


LDR TID Report
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12.12_V_FAULT_HYS_14V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	110	110		
Min Limit	90	90		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	102.000	102.000	0.000
0	118	102.000	101.000	-1.000
0	122	102.000	102.000	0.000
3	61	102.000	102.000	0.000
3	62	102.000	102.000	0.000
3	63	102.000	102.000	0.000
3	64	102.000	102.000	0.000
3	65	102.000	102.000	0.000
3	66	102.000	102.000	0.000
5	67	102.000	102.000	0.000
5	68	102.000	102.000	0.000
5	69	102.000	102.000	0.000
5	70	102.000	102.000	0.000
5	71	101.000	102.000	1.000
5	72	102.000	102.000	0.000
10	73	102.000	102.000	0.000
10	74	102.000	102.000	0.000
10	75	102.000	102.000	0.000
10	76	102.000	102.000	0.000
10	77	102.000	102.000	0.000
10	78	102.000	102.000	0.000
15	79	102.000	103.000	1.000
15	80	102.000	102.000	0.000
15	81	102.000	102.000	0.000
15	82	102.000	102.000	0.000
15	83	101.000	101.000	0.000
15	84	102.000	102.000	0.000
30	86	102.000	103.000	1.000
30	89	102.000	102.000	0.000
30	91	102.000	102.000	0.000
30	92	102.000	102.000	0.000
30	94	102.000	102.000	0.000
30	95	102.000	102.000	0.000
35	100	102.000	102.000	0.000
35	101	102.000	102.000	0.000
35	102	102.000	102.000	0.000
35	103	102.000	102.000	0.000
35	104	101.000	102.000	1.000
35	105	102.000	103.000	1.000
50	106	103.000	102.000	-1.000
50	107	101.000	102.000	1.000
50	108	102.000	101.000	-1.000
50	109	102.000	101.000	-1.000
50	110	102.000	102.000	0.000
50	111	102.000	102.000	0.000
55	113	102.000	102.000	0.000
55	114	102.000	102.000	0.000
55	115	102.000	102.000	0.000
55	116	102.000	102.000	0.000
55	126	102.000	102.000	0.000
55	128	102.000	102.000	0.000
100	129	102.000	102.000	0.000
100	131	102.000	102.000	0.000
100	132	102.000	103.000	1.000
100	133	103.000	102.000	-1.000
100	134	102.000	102.000	0.000
100	135	102.000	102.000	0.000
100	138	102.000	101.000	-1.000
100	139	102.000	102.000	0.000
100	140	102.000	102.000	0.000
100	141	102.000	102.000	0.000
100	142	102.000	102.000	0.000
100	143	102.000	102.000	0.000
100	144	103.000	102.000	-1.000
100	220	102.000	102.000	0.000
100	221	102.000	102.000	0.000
100	224	103.000	102.000	-1.000
100	225	102.000	101.000	-1.000
100	226	102.000	101.000	-1.000
100	228	102.000	101.000	-1.000
100	232	102.000	102.000	0.000
100	233	102.000	102.000	0.000
100	236	102.000	102.000	0.000
100	238	101.000	102.000	1.000
100	243	102.000	102.000	0.000
105	244	101.000	102.000	1.000
105	246	102.000	102.000	0.000
105	248	102.000	102.000	0.000
105	374	102.000	102.000	0.000
105	376	101.000	102.000	1.000
Max		103.000	103.000	1.000
Average		101.963	101.950	-0.013
Min		101.000	101.000	-1.000
Std Dev		0.371	0.386	0.515

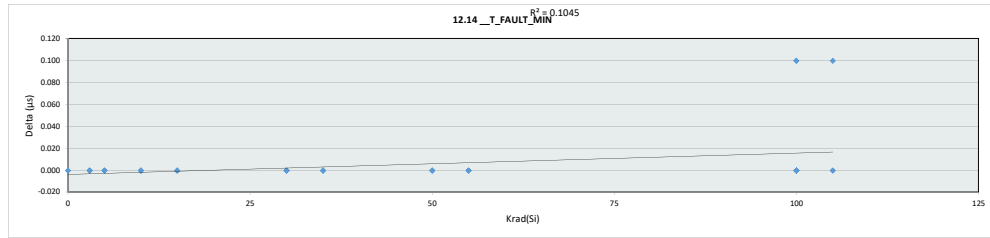


12.12_V_FAULT_HYS_14V											
Test Site											
Tester											
Test Number											
Max Limit	110	mV									
Min Limit	90	mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min	101.000	102.000	102.000	102.000	102.000	101.000	102.000	101.000	102.000	101.000	102.000
Average	101.667	102.000	102.000	102.000	102.000	102.167	102.167	101.667	102.000	101.875	102.000
Max	102.000	102.000	102.000	102.000	102.000	103.000	103.000	102.000	102.000	103.000	102.000
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

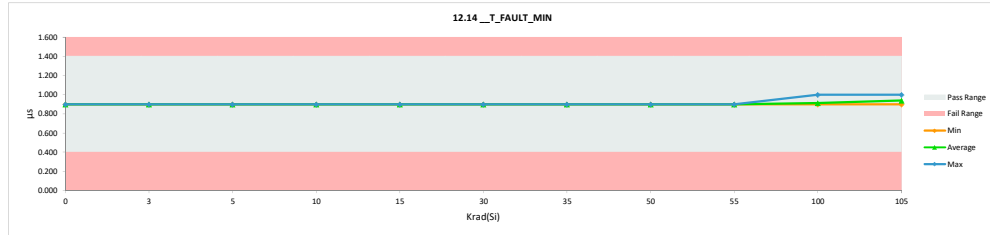


LDR TID Report
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12.14 _ T_FAULT_MIN					
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit
			µs	1.4	0.4
			µs	1.4	0.4
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta	
0	117	0.900	0.900	0.000	
0	118	0.900	0.900	0.000	
0	122	0.900	0.900	0.000	
3	61	0.900	0.900	0.000	
3	62	0.900	0.900	0.000	
3	63	0.900	0.900	0.000	
3	64	0.900	0.900	0.000	
3	65	0.900	0.900	0.000	
3	66	0.900	0.900	0.000	
5	67	0.900	0.900	0.000	
5	68	0.900	0.900	0.000	
5	69	0.900	0.900	0.000	
5	70	0.900	0.900	0.000	
5	71	0.900	0.900	0.000	
5	72	0.900	0.900	0.000	
10	73	0.900	0.900	0.000	
10	74	0.900	0.900	0.000	
10	75	0.900	0.900	0.000	
10	76	0.900	0.900	0.000	
10	77	0.900	0.900	0.000	
10	78	0.900	0.900	0.000	
15	79	0.900	0.900	0.000	
15	80	0.900	0.900	0.000	
15	81	0.900	0.900	0.000	
15	82	0.900	0.900	0.000	
15	83	0.900	0.900	0.000	
15	84	0.900	0.900	0.000	
30	86	0.900	0.900	0.000	
30	89	0.900	0.900	0.000	
30	91	0.900	0.900	0.000	
30	92	0.900	0.900	0.000	
30	94	0.900	0.900	0.000	
30	95	0.900	0.900	0.000	
35	100	0.900	0.900	0.000	
35	101	0.900	0.900	0.000	
35	102	0.900	0.900	0.000	
35	103	0.900	0.900	0.000	
35	104	0.900	0.900	0.000	
35	105	0.900	0.900	0.000	
50	106	0.900	0.900	0.000	
50	107	0.900	0.900	0.000	
50	108	0.900	0.900	0.000	
50	109	0.900	0.900	0.000	
50	110	0.900	0.900	0.000	
50	111	0.900	0.900	0.000	
55	113	0.900	0.900	0.000	
55	114	0.900	0.900	0.000	
55	115	0.900	0.900	0.000	
55	116	0.900	0.900	0.000	
55	125	0.900	0.900	0.000	
55	128	0.900	0.900	0.000	
100	129	0.900	0.900	0.000	
100	131	0.900	0.900	0.000	
100	132	0.900	0.900	0.000	
100	133	0.900	1.000	0.100	
100	134	0.900	0.900	0.000	
100	135	0.900	0.900	0.000	
100	138	0.900	1.000	0.100	
100	139	0.900	0.900	0.000	
100	140	0.900	0.900	0.000	
100	141	0.900	0.900	0.000	
100	142	0.900	0.900	0.000	
100	143	0.900	0.900	0.000	
100	144	0.900	0.900	0.000	
100	220	0.900	0.900	0.000	
100	221	0.900	0.900	0.000	
100	224	0.900	0.900	0.000	
100	225	0.900	1.000	0.100	
100	226	0.900	0.900	0.000	
100	228	0.900	0.900	0.000	
100	232	0.900	0.900	0.000	
100	233	0.900	0.900	0.000	
100	236	0.900	0.900	0.000	
100	238	0.900	0.900	0.000	
100	243	0.900	0.900	0.000	
105	244	0.900	0.900	0.000	
105	246	0.900	1.000	0.100	
105	248	0.900	1.000	0.100	
105	374	0.900	0.900	0.000	
105	376	0.900	0.900	0.000	
	Max	0.900	1.000	0.100	
	Average	0.900	0.906	0.006	
	Min	0.900	0.900	0.000	
	Std Dev	0.000	0.024	0.024	

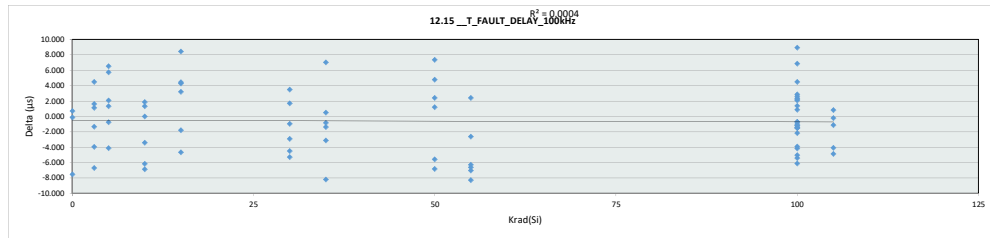


12.14 _ T_FAULT_MIN											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			1.4	0.4	µs	µs					
			0.4	0.4	µs	µs					
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.400	0.400	0.400	0.400	0.400	0.400	0.400	0.400	0.400	0.400	0.400
Min	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900
Average	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.913	0.940
Max	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	1.000	1.000
UL	1.400	1.400	1.400	1.400	1.400	1.400	1.400	1.400	1.400	1.400	1.400

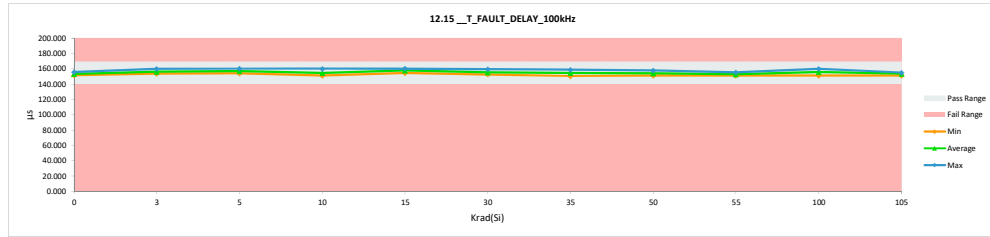


LDR TID Report
TPS7H5001-SP

12.15 _T_FAULT_DELAY_100kHz				
Test Site				
Tester				
Test Number				
Unit	µs	µs		
Max Limit	169	169		
Min Limit	140	140		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	156.143	156.056	-0.087
0	118	159.074	151.573	-7.501
0	122	151.484	152.229	0.745
3	61	156.932	155.615	-1.317
3	62	158.789	159.947	1.158
3	63	157.986	154.051	-3.935
3	64	151.448	155.983	4.535
3	65	156.182	157.851	1.669
3	66	160.282	153.618	-6.664
5	67	151.792	158.349	6.557
5	68	154.968	154.237	-0.731
5	69	158.714	160.092	1.378
5	70	154.510	160.288	5.778
5	71	159.183	155.113	-4.070
5	72	153.705	155.822	2.117
10	73	156.932	156.955	0.023
10	74	158.789	160.172	1.383
10	75	157.986	151.153	-6.833
10	76	151.448	153.366	1.918
10	77	156.182	152.786	-3.396
10	78	160.282	154.164	-6.118
15	79	151.792	160.258	8.466
15	80	154.968	159.295	4.327
15	81	158.714	156.952	-1.762
15	82	154.510	157.764	3.254
15	83	159.183	154.551	-4.632
15	84	153.705	158.177	4.472
30	86	156.932	154.070	-2.862
30	89	158.789	154.344	-4.445
30	91	157.986	152.738	-5.248
30	92	151.448	153.204	1.756
30	94	156.182	159.721	3.539
30	95	160.282	159.368	-0.914
35	100	151.792	158.873	7.081
35	101	154.968	155.493	0.525
35	102	158.714	157.898	-0.816
35	103	154.510	153.189	-1.321
35	104	159.183	151.038	-8.145
35	105	153.705	150.633	-3.072
50	106	158.671	151.863	-6.808
50	107	150.844	155.662	4.818
50	108	156.731	151.185	-5.546
50	109	151.456	153.882	2.426
50	110	150.688	158.091	7.403
50	111	152.919	154.157	1.238
55	113	157.832	151.572	-6.260
55	114	158.852	151.858	-6.994
55	115	157.847	155.276	-2.571
55	116	152.416	154.863	2.447
55	126	159.618	153.048	-6.570
55	128	159.685	151.431	-8.254
100	129	159.530	158.555	-0.975
100	131	158.720	154.857	-3.863
100	132	156.479	155.007	-1.472
100	133	152.354	151.192	-1.162
100	134	153.883	152.446	-1.437
100	135	153.011	157.528	4.517
100	138	151.345	158.222	6.877
100	139	157.626	156.962	-0.664
100	140	155.256	157.550	2.294
100	141	153.392	154.290	0.898
100	142	151.035	160.028	8.993
100	143	155.673	154.489	-1.184
100	144	153.909	152.443	-1.466
100	220	160.301	154.222	-6.079
100	221	158.395	153.020	-5.375
100	224	157.600	160.016	2.416
100	225	158.006	160.123	2.117
100	226	156.717	159.604	2.887
100	228	159.064	156.919	-2.145
100	232	150.810	153.462	2.652
100	233	158.394	159.793	1.399
100	236	156.087	151.104	-4.983
100	238	158.605	157.912	-0.693
100	243	159.534	155.424	-4.110
105	244	154.325	155.179	0.854
105	246	156.249	151.434	-4.815
105	248	154.956	154.761	-0.195
105	374	159.035	154.997	-4.038
105	376	154.127	153.017	-1.110
Max		160.301	160.288	8.993
Average		156.027	155.430	-0.597
Min		150.688	150.633	-8.254
Std Dev		2.934	2.885	4.207

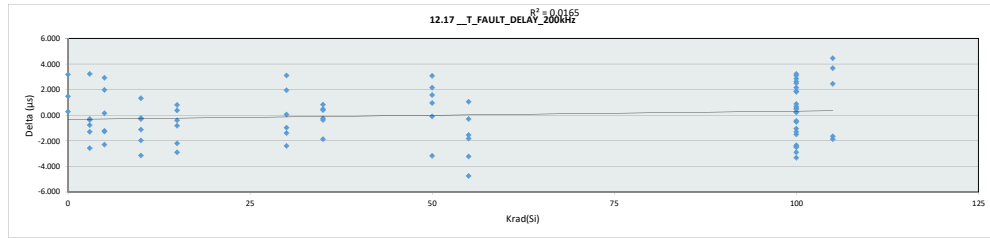


12.15 _T_FAULT_DELAY_100k											
Test Site											
Tester											
Test Number											
Max Limit	169	µs									
Min Limit	140	µs									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000
Min	151.573	153.618	154.237	151.153	154.551	152.738	150.633	151.185	151.431	151.104	151.434
Average	153.286	156.178	157.317	154.766	157.833	155.574	154.521	154.140	153.008	156.049	153.878
Max	156.056	159.947	160.288	160.172	160.258	159.721	158.873	158.091	155.276	160.123	155.179
UL	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000

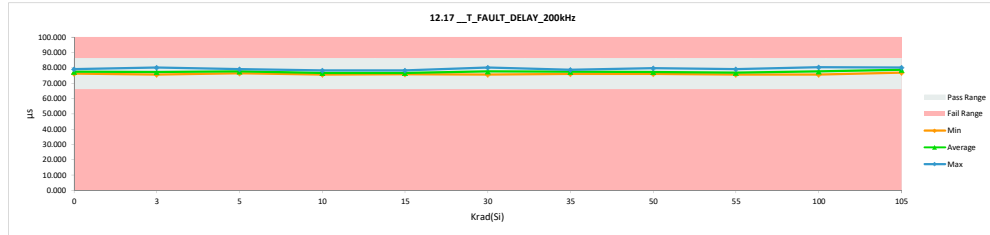


LDR TID Report
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12.17_T_FAULT_DELAY_200kHz				
Test Site				
Tester				
Test Number				
Unit	µs	µs		
Max Limit	86	86		
Min Limit	66	66		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	76.024	79.236	3.212
0	118	75.738	77.243	1.505
0	122	75.955	76.263	0.308
3	61	77.634	76.356	-1.278
3	62	78.253	75.702	-2.551
3	63	76.991	80.247	3.256
3	64	76.031	75.684	-0.347
3	65	76.345	75.597	-0.748
3	66	80.256	79.979	-0.277
5	67	76.314	79.272	2.958
5	68	77.920	78.091	0.171
5	69	76.563	78.563	2.000
5	70	79.071	76.803	-2.268
5	71	77.758	76.512	-1.246
5	72	77.981	76.787	-1.194
10	73	77.634	75.681	-1.953
10	74	78.253	77.150	-1.103
10	75	76.991	78.357	1.366
10	76	76.031	75.760	-0.271
10	77	76.345	76.143	-0.202
10	78	80.256	77.139	-3.117
15	79	76.314	77.136	0.822
15	80	77.920	78.333	0.413
15	81	76.563	76.186	-0.377
15	82	79.071	76.213	-2.858
15	83	77.758	76.956	-0.802
15	84	77.981	75.813	-2.168
30	86	77.634	76.696	-0.938
30	89	78.253	80.229	1.976
30	91	76.991	75.628	-1.363
30	92	76.031	79.157	3.126
30	94	76.345	76.422	0.077
30	95	80.256	77.898	-2.358
35	100	76.314	76.103	-0.211
35	101	77.920	76.067	-1.853
35	102	76.563	77.412	0.849
35	103	79.071	78.736	-0.335
35	104	77.758	78.275	0.517
35	105	77.981	78.407	0.426
50	106	76.126	78.320	2.194
50	107	76.178	76.112	-0.066
50	108	75.739	76.721	0.982
50	109	76.659	79.763	3.104
50	110	75.674	77.292	1.618
50	111	79.271	76.130	-3.141
55	113	78.894	75.700	-3.194
55	114	79.548	79.285	-0.263
55	115	78.432	76.903	-1.529
55	116	78.081	76.295	-1.786
55	126	80.422	75.700	-4.722
55	128	76.158	77.250	1.092
100	129	79.056	77.581	-1.475
100	131	76.518	79.780	3.262
100	132	77.120	75.847	-1.273
100	133	75.773	76.469	0.696
100	134	78.090	78.372	0.282
100	135	76.534	78.726	2.192
100	138	78.036	77.535	-0.501
100	139	76.750	76.330	-0.420
100	140	78.159	80.060	1.901
100	141	80.463	77.176	-3.287
100	142	76.838	75.807	-1.031
100	143	77.498	78.078	0.580
100	144	79.988	77.558	-2.430
100	220	78.921	79.423	0.502
100	221	77.287	79.146	1.859
100	224	77.185	79.720	2.535
100	225	79.691	77.212	-2.479
100	226	76.238	77.144	0.906
100	228	77.318	80.453	3.135
100	232	75.636	75.884	0.248
100	233	78.075	75.746	-2.329
100	236	78.451	75.569	-2.882
100	238	76.571	79.237	2.666
100	243	75.494	78.388	2.894
105	244	77.119	79.611	2.492
105	246	78.618	77.010	-1.608
105	248	76.175	79.889	3.714
105	374	75.807	80.286	4.479
105	376	78.693	76.840	-1.853
	Max	80.463	80.453	4.479
	Average	77.504	77.507	0.003
	Min	75.494	75.569	-4.722
	Std Dev	1.335	1.475	2.020

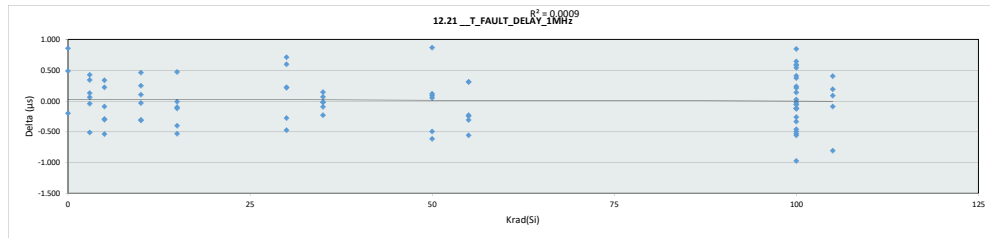


12.17_T_FAULT_DELAY_200k											
Test Site											
Tester											
Test Number											
Max Limit	86	µs									
Min Limit	66	µs									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000
Min	76.263	75.597	76.512	75.681	75.813	75.628	76.067	76.112	75.700	75.569	76.840
Average	77.581	77.261	77.671	76.705	76.773	77.672	77.500	77.390	76.856	77.802	78.727
Max	79.236	80.247	79.272	78.357	78.333	80.229	78.736	79.763	79.285	80.453	80.286
UL	86.000	86.000	86.000	86.000	86.000	86.000	86.000	86.000	86.000	86.000	86.000

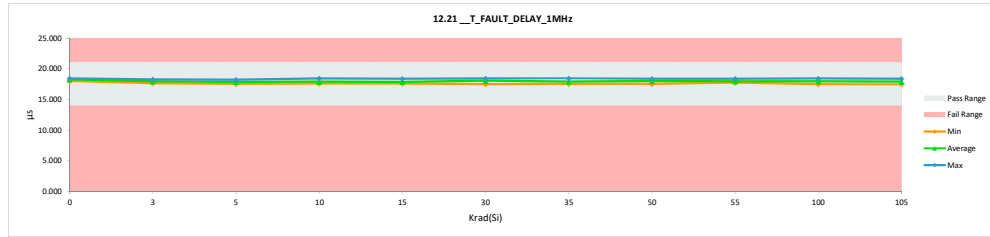


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12.21_T_FAULT_DELAY_1MHz				
Test Site	Tester	Unit	Max Limit	Min Limit
12.21	T_FAULT_DELAY_1MHz	µs	21	14
0	117	17.570	18.429	0.859
0	118	17.881	18.375	0.494
0	122	18.212	18.019	-0.193
3	61	17.751	17.713	-0.038
3	62	17.738	18.166	0.428
3	63	17.654	18.002	0.348
3	64	17.996	18.061	0.065
3	65	18.145	17.640	-0.505
3	66	18.164	18.297	0.133
5	67	17.579	17.921	0.342
5	68	17.825	17.739	-0.086
5	69	17.998	18.223	0.225
5	70	18.392	18.106	-0.286
5	71	18.123	17.593	-0.530
5	72	17.856	17.557	-0.299
10	73	17.751	17.857	0.106
10	74	17.738	18.205	0.467
10	75	17.654	17.628	-0.026
10	76	17.996	17.688	-0.308
10	77	18.145	17.843	-0.302
10	78	18.164	18.416	0.252
15	79	17.579	18.054	0.475
15	80	17.825	17.729	-0.096
15	81	17.998	17.599	-0.399
15	82	18.392	18.384	-0.008
15	83	18.123	17.595	-0.528
15	84	17.856	17.739	-0.117
30	86	17.751	17.481	-0.270
30	89	17.738	18.454	0.716
30	91	17.654	18.257	0.603
30	92	17.996	18.217	0.221
30	94	18.145	17.675	-0.470
30	95	18.164	18.393	0.229
35	100	17.579	17.565	-0.014
35	101	17.825	17.734	-0.091
35	102	17.998	18.146	0.148
35	103	18.392	18.463	0.071
35	104	18.123	18.105	-0.018
35	105	17.856	17.629	-0.227
50	106	18.470	17.859	-0.611
50	107	18.134	18.224	0.090
50	108	18.057	18.179	0.122
50	109	17.500	18.370	0.870
50	110	17.508	17.564	0.056
50	111	18.435	17.943	-0.492
55	113	18.317	17.762	-0.555
55	114	18.096	18.411	0.315
55	115	18.127	17.884	-0.243
55	116	18.319	18.092	-0.227
55	126	18.170	17.868	-0.302
55	128	17.556	17.873	0.317
100	129	18.391	18.423	0.032
100	131	17.500	18.351	0.851
100	132	17.567	18.152	0.585
100	133	18.306	17.780	-0.526
100	134	18.439	18.328	-0.111
100	135	18.111	17.988	-0.123
100	138	17.550	17.792	0.242
100	139	18.393	18.062	-0.331
100	140	18.322	17.767	-0.555
100	141	17.554	17.499	-0.055
100	142	18.088	17.831	-0.257
100	143	17.568	17.982	0.414
100	144	17.745	17.726	-0.019
100	220	18.369	18.323	-0.046
100	221	17.642	18.187	0.545
100	224	17.564	17.708	0.144
100	225	17.507	18.154	0.647
100	226	18.195	17.739	-0.456
100	228	17.810	18.408	0.598
100	232	18.209	17.719	-0.490
100	233	17.746	18.125	0.379
100	236	18.464	17.494	-0.970
100	238	17.821	17.703	-0.118
100	243	17.658	17.877	0.219
105	244	17.861	17.776	-0.085
105	246	17.844	18.038	0.194
105	248	18.276	17.475	-0.801
105	374	17.825	17.919	0.094
105	376	18.001	18.409	0.408
Max		18.470	18.463	0.870
Average		17.954	17.968	0.014
Min		17.500	17.475	-0.970
Std Dev		0.292	0.293	0.398

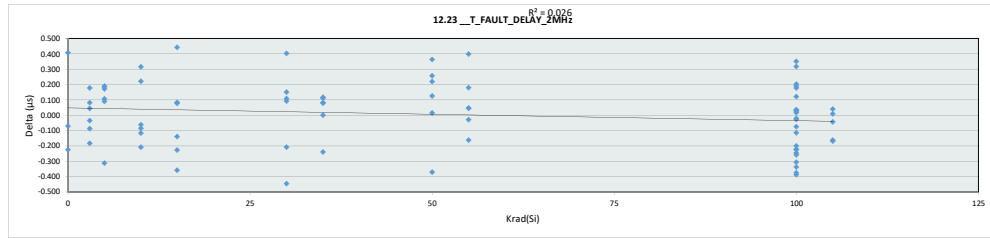


12.21_T_FAULT_DELAY_1MHz															
Test Site	Tester	Unit	Max Limit	Min Limit	0	3	5	10	15	30	35	50	55	100	105
LL			14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000
Min			18.019	17.640	17.557	17.628	17.595	17.481	17.565	17.564	17.762	17.494	17.475		
Average			18.274	17.980	17.857	17.940	17.850	18.080	17.940	18.023	17.982	17.963	17.923		
Max			18.429	18.297	18.223	18.416	18.384	18.454	18.463	18.370	18.411	18.423	18.409		
UL			21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000

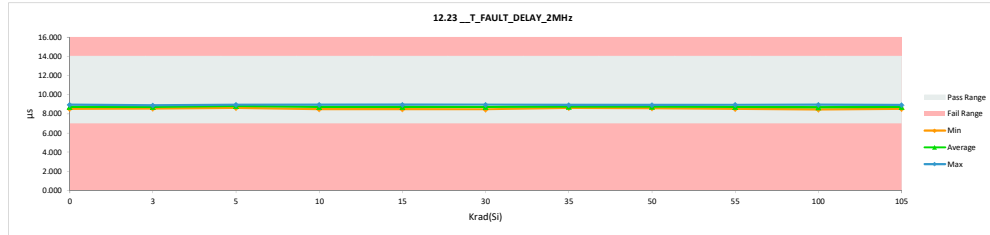


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12.23 _T_FAULT_DELAY_2MHz				
Test Site				
Tester				
Test Number				
Unit	µs	µs		
Max Limit	14	14		
Min Limit	7	7		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.722	8.654	-0.068
0	118	8.560	8.969	0.409
0	122	8.766	8.543	-0.223
3	61	8.587	8.634	0.047
3	62	8.569	8.652	0.083
3	63	8.923	8.891	-0.032
3	64	8.916	8.832	-0.084
3	65	8.565	8.745	0.180
3	66	8.746	8.566	-0.180
5	67	8.511	8.703	0.192
5	68	8.939	8.630	-0.309
5	69	8.740	8.913	0.173
5	70	8.677	8.863	0.186
5	71	8.853	8.945	0.092
5	72	8.758	8.866	0.108
10	73	8.587	8.528	-0.059
10	74	8.569	8.487	-0.082
10	75	8.923	8.808	-0.115
10	76	8.916	8.711	-0.205
10	77	8.565	8.882	0.317
10	78	8.746	8.969	0.223
15	79	8.511	8.955	0.444
15	80	8.939	8.714	-0.225
15	81	8.740	8.604	-0.136
15	82	8.677	8.762	0.085
15	83	8.853	8.497	-0.356
15	84	8.758	8.837	0.079
30	86	8.587	8.699	0.112
30	89	8.569	8.974	0.405
30	91	8.923	8.718	-0.205
30	92	8.916	8.473	-0.443
30	94	8.565	8.660	0.095
30	95	8.746	8.899	0.153
35	100	8.511	8.631	0.120
35	101	8.939	8.702	-0.237
35	102	8.740	8.854	0.114
35	103	8.677	8.680	0.003
35	104	8.853	8.936	0.083
35	105	8.758	8.840	0.082
50	106	8.529	8.894	0.365
50	107	8.602	8.369	-0.233
50	108	8.701	8.923	0.222
50	109	8.496	8.756	0.260
50	110	8.725	8.742	0.017
50	111	8.513	8.641	0.128
55	113	8.801	8.641	-0.160
55	114	8.561	8.962	0.401
55	115	8.485	8.534	0.049
55	116	8.548	8.596	0.048
55	126	8.779	8.753	-0.026
55	128	8.708	8.890	0.182
100	129	8.872	8.759	-0.113
100	131	8.891	8.693	-0.198
100	132	8.910	8.885	-0.025
100	133	8.619	8.652	0.033
100	134	8.833	8.498	-0.335
100	135	8.902	8.646	-0.256
100	138	8.934	8.631	-0.303
100	139	8.964	8.745	-0.219
100	140	8.737	8.775	0.038
100	141	8.599	8.918	0.319
100	142	8.865	8.621	-0.244
100	143	8.564	8.601	0.037
100	144	8.534	8.560	0.026
100	220	8.577	8.756	0.179
100	221	8.558	8.762	0.204
100	224	8.846	8.474	-0.372
100	225	8.492	8.844	0.352
100	226	8.706	8.634	-0.072
100	228	8.976	8.958	-0.018
100	232	8.480	8.603	0.123
100	233	8.830	8.607	-0.223
100	236	8.687	8.707	0.020
100	238	8.942	8.556	-0.386
100	243	8.674	8.867	0.193
105	244	8.953	8.786	-0.167
105	246	8.681	8.639	-0.042
105	248	8.882	8.722	-0.160
105	374	8.897	8.940	0.043
105	376	8.517	8.527	0.010
Max		8.976	8.974	0.444
Average		8.727	8.732	0.005
Min		8.480	8.473	-0.443
Std Dev		0.156	0.143	0.210

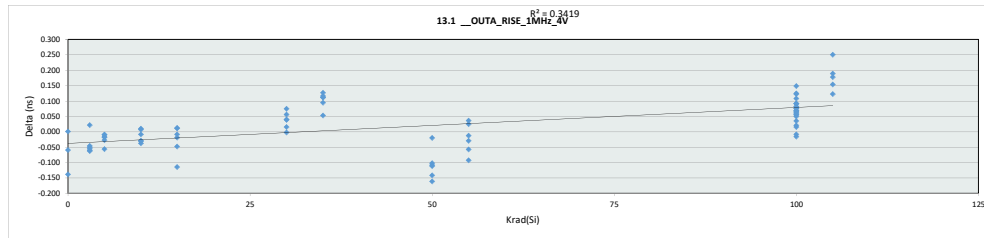


12.23 _T_FAULT_DELAY_2MHz											
Test Site											
Tester											
Test Number											
Max Limit	14	µs									
Min Limit	7	µs									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000
Min	8.543	8.566	8.630	8.487	8.497	8.473	8.631	8.602	8.534	8.474	8.527
Average	8.722	8.720	8.820	8.731	8.728	8.737	8.774	8.760	8.729	8.698	8.723
Max	8.969	8.891	8.945	8.969	8.955	8.974	8.936	8.923	8.962	8.958	8.940
UL	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000

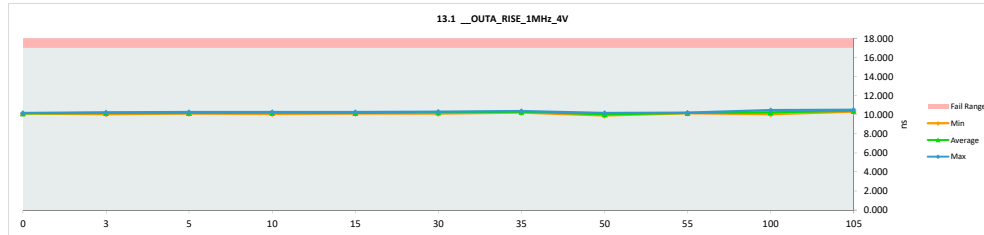


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13.1 _OUTA_RISE_1MHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	10.240	10.103	-0.137
0	118	10.173	10.175	0.002
0	122	10.176	10.118	-0.058
3	61	10.269	10.224	-0.045
3	62	10.279	10.230	-0.049
3	63	10.245	10.184	-0.061
3	64	10.098	10.039	-0.059
3	65	10.220	10.166	-0.054
3	66	10.139	10.162	0.023
5	67	10.162	10.145	-0.017
5	68	10.274	10.248	-0.026
5	69	10.210	10.194	-0.016
5	70	10.183	10.128	-0.055
5	71	10.280	10.271	-0.009
5	72	10.109	10.100	-0.009
10	73	10.269	10.280	0.011
10	74	10.279	10.287	0.008
10	75	10.245	10.216	-0.029
10	76	10.098	10.061	-0.037
10	77	10.220	10.192	-0.028
10	78	10.139	10.132	-0.007
15	79	10.162	10.155	-0.007
15	80	10.274	10.287	0.013
15	81	10.210	10.097	-0.113
15	82	10.183	10.136	-0.047
15	83	10.280	10.262	-0.018
15	84	10.109	10.121	0.012
30	86	10.269	10.309	0.040
30	89	10.279	10.278	-0.001
30	91	10.245	10.321	0.076
30	92	10.098	10.114	0.016
30	94	10.220	10.260	0.040
30	95	10.139	10.195	0.057
35	100	10.162	10.290	0.128
35	101	10.274	10.386	0.112
35	102	10.210	10.323	0.113
35	103	10.183	10.237	0.054
35	104	10.280	10.376	0.096
35	105	10.109	10.226	0.117
50	106	10.152	10.011	-0.141
50	107	10.014	9.904	-0.110
50	108	10.197	10.178	-0.019
50	109	10.195	10.035	-0.160
50	110	10.230	10.129	-0.101
50	111	10.142	10.035	-0.107
55	113	10.188	10.176	-0.012
55	114	10.275	10.219	-0.056
55	115	10.229	10.201	-0.028
55	116	10.233	10.141	-0.092
55	126	10.155	10.181	0.026
55	128	10.140	10.177	0.037
100	129	10.101	10.184	0.083
100	131	10.120	10.197	0.077
100	132	10.098	10.175	0.077
100	133	10.179	10.261	0.082
100	134	10.068	10.053	-0.015
100	135	10.200	10.236	0.036
100	138	10.108	10.175	0.067
100	139	10.087	10.179	0.092
100	140	10.218	10.269	0.051
100	141	10.223	10.216	-0.007
100	142	10.242	10.303	0.061
100	143	10.313	10.463	0.150
100	144	10.115	10.241	0.126
100	220	10.183	10.292	0.109
100	221	10.151	10.221	0.070
100	224	10.207	10.331	0.124
100	225	10.157	10.173	0.016
100	226	10.262	10.318	0.056
100	228	10.227	10.249	0.022
100	232	10.057	10.149	0.092
100	233	10.209	10.273	0.064
100	236	10.262	10.319	0.057
100	238	10.114	10.184	0.070
100	243	10.195	10.287	0.092
105	244	10.297	10.476	0.179
105	246	10.159	10.314	0.155
105	248	10.190	10.380	0.190
105	374	10.270	10.521	0.251
105	376	10.203	10.327	0.124
	Max	10.313	10.521	0.251
	Average	10.192	10.215	0.023
	Min	10.014	9.904	-0.160
	Std Dev	0.066	0.106	0.081

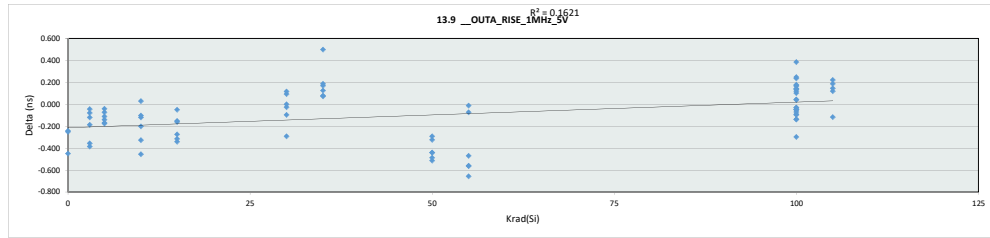


13.1 _OUTA_RISE_1MHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	10.103	10.039	10.100	10.061	10.097	10.114	10.226	9.904	10.141	10.053	10.314
Average	10.132	10.168	10.181	10.195	10.176	10.246	10.306	10.049	10.183	10.240	10.404
Max	10.175	10.230	10.271	10.287	10.287	10.321	10.386	10.178	10.219	10.463	10.521
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

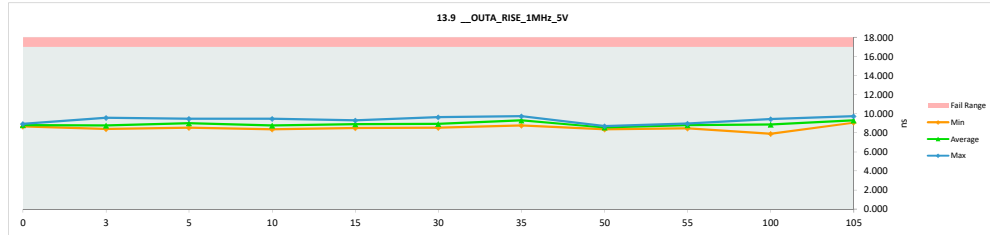


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13.9 _OUTA_RISE_1MHz_5V					
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit
		17	ns	17	17
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta	
0	117	9.283	8.837	-0.446	
0	118	8.909	8.660	-0.249	
0	122	9.163	8.923	-0.240	
3	61	9.934	9.580	-0.354	
3	62	8.427	8.385	-0.042	
3	63	8.843	8.660	-0.183	
3	64	8.801	8.683	-0.118	
3	65	8.463	8.386	-0.077	
3	66	9.369	8.986	-0.383	
5	67	8.772	8.664	-0.108	
5	68	9.638	9.465	-0.173	
5	69	9.347	9.276	-0.071	
5	70	8.585	8.546	-0.039	
5	71	9.246	9.109	-0.137	
5	72	9.069	8.902	-0.167	
10	73	9.934	9.482	-0.452	
10	74	8.427	8.459	0.032	
10	75	8.843	8.645	-0.198	
10	76	8.801	8.685	-0.116	
10	77	8.463	8.365	-0.098	
10	78	9.369	9.044	-0.325	
15	79	8.772	8.501	-0.271	
15	80	9.638	9.300	-0.338	
15	81	9.347	9.198	-0.149	
15	82	8.585	8.537	-0.048	
15	83	9.246	8.935	-0.311	
15	84	9.069	8.912	-0.157	
30	86	9.934	9.645	-0.289	
30	89	8.427	8.547	0.120	
30	91	8.843	8.821	-0.022	
30	92	8.801	8.805	0.004	
30	94	8.463	8.560	0.097	
30	95	9.369	9.277	-0.092	
35	100	8.772	8.853	0.081	
35	101	9.638	9.715	0.077	
35	102	9.347	9.474	0.127	
35	103	8.585	8.758	0.173	
35	104	9.246	9.748	0.502	
35	105	9.069	9.259	0.190	
50	106	9.213	8.703	-0.510	
50	107	8.664	8.376	-0.288	
50	108	9.095	8.612	-0.483	
50	109	8.945	8.624	-0.321	
50	110	8.829	8.390	-0.439	
50	111	9.037	8.600	-0.437	
55	113	9.250	8.784	-0.466	
55	114	9.565	8.912	-0.653	
55	115	9.386	8.830	-0.556	
55	116	9.505	8.945	-0.560	
55	126	8.482	8.473	-0.009	
55	128	9.036	8.965	-0.071	
100	129	8.367	8.617	0.250	
100	131	8.990	8.897	-0.093	
100	132	8.790	8.765	-0.025	
100	133	9.114	8.980	-0.134	
100	134	8.798	8.752	-0.046	
100	135	9.175	9.127	-0.048	
100	138	8.850	8.955	0.105	
100	139	8.010	8.249	0.239	
100	140	9.030	8.964	-0.066	
100	141	9.452	9.158	-0.294	
100	142	8.807	8.989	0.182	
100	143	9.060	9.228	0.168	
100	144	7.856	7.900	0.044	
100	220	8.953	9.075	0.122	
100	221	8.794	8.705	-0.089	
100	224	8.576	8.965	0.389	
100	225	9.199	9.156	-0.043	
100	226	9.282	9.424	0.142	
100	228	9.113	9.252	0.139	
100	232	8.589	8.636	0.047	
100	233	8.901	8.872	-0.029	
100	236	8.882	9.027	0.145	
100	238	9.132	8.997	-0.135	
100	243	8.216	8.392	0.176	
105	244	9.545	9.735	0.190	
105	246	9.038	9.187	0.149	
105	248	8.833	9.057	0.224	
105	374	9.229	9.115	-0.114	
105	376	9.249	9.372	0.123	
	Max	9.934	9.748	0.502	
	Average	8.996	8.904	-0.092	
	Min	7.856	7.900	-0.653	
	Std Dev	0.415	0.372	0.234	

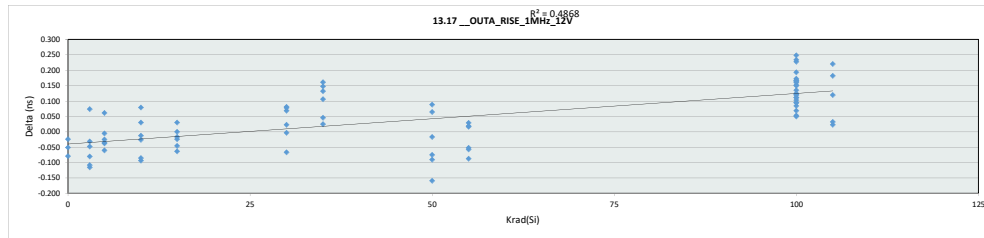


13.9 _OUTA_RISE_1MHz_5V										
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit					
		17	ns	17	17					
Krad(Si)	LL	Min	Average	Max	UL					
0	8.660	8.385	8.546	8.365	8.501					
3	8.807	8.780	8.994	8.780	8.897					
5	8.923	9.580	9.465	9.482	9.300					
10	17.000	17.000	17.000	17.000	17.000					
15	17.000	17.000	17.000	17.000	17.000					
30	17.000	17.000	17.000	17.000	17.000					
35	17.000	17.000	17.000	17.000	17.000					
50	17.000	17.000	17.000	17.000	17.000					
55	17.000	17.000	17.000	17.000	17.000					
100	17.000	17.000	17.000	17.000	17.000					
105	17.000	17.000	17.000	17.000	17.000					

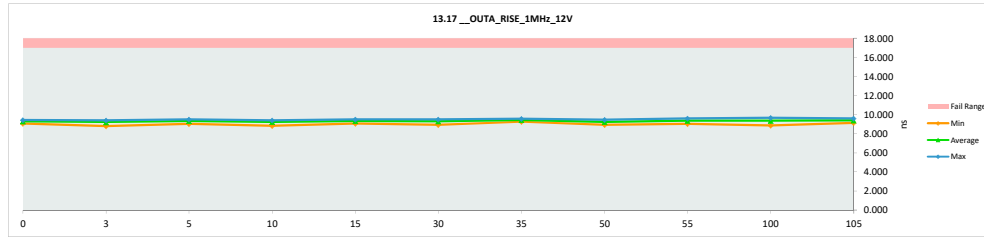


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13.17_OUTA_RISE_1MHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	9.494	9.444	-0.050
0	118	9.091	9.068	-0.023
0	122	9.464	9.386	-0.078
3	61	9.508	9.393	-0.115
3	62	9.106	9.181	0.075
3	63	9.372	9.342	-0.030
3	64	9.253	9.174	-0.079
3	65	8.858	8.811	-0.047
3	66	9.501	9.394	-0.107
5	67	9.183	9.245	0.062
5	68	9.489	9.454	-0.035
5	69	9.544	9.507	-0.037
5	70	9.109	9.050	-0.059
5	71	9.257	9.233	-0.024
5	72	9.471	9.467	-0.004
10	73	9.508	9.415	-0.093
10	74	9.106	9.186	0.080
10	75	9.372	9.360	-0.012
10	76	9.253	9.284	0.031
10	77	8.858	8.833	-0.025
10	78	9.501	9.417	-0.084
15	79	9.183	9.214	0.031
15	80	9.489	9.473	-0.016
15	81	9.544	9.521	-0.023
15	82	9.109	9.064	-0.045
15	83	9.257	9.194	-0.063
15	84	9.471	9.472	0.001
30	86	9.508	9.442	-0.066
30	89	9.106	9.185	0.079
30	91	9.372	9.396	0.024
30	92	9.253	9.323	0.070
30	94	8.858	8.940	0.082
30	95	9.501	9.499	-0.002
35	100	9.183	9.316	0.133
35	101	9.489	9.536	0.047
35	102	9.544	9.570	0.026
35	103	9.109	9.258	0.149
35	104	9.257	9.419	0.162
35	105	9.471	9.578	0.107
50	106	9.433	9.343	-0.090
50	107	9.175	9.101	-0.074
50	108	9.122	9.187	0.065
50	109	9.375	9.464	0.089
50	110	9.108	8.950	-0.158
50	111	9.300	9.284	-0.016
55	113	9.236	9.185	-0.051
55	114	9.539	9.483	-0.056
55	115	9.487	9.401	-0.086
55	116	9.417	9.435	0.018
55	126	9.032	9.052	0.020
55	128	9.569	9.599	0.030
100	129	8.840	9.089	0.249
100	131	9.486	9.622	0.136
100	132	9.399	9.495	0.096
100	133	9.159	9.263	0.104
100	134	9.336	9.448	0.112
100	135	9.466	9.660	0.194
100	138	9.433	9.487	0.054
100	139	8.827	9.000	0.173
100	140	9.341	9.392	0.051
100	141	9.466	9.630	0.164
100	142	9.197	9.365	0.168
100	143	9.304	9.424	0.120
100	144	8.764	8.859	0.095
100	220	9.322	9.551	0.229
100	221	9.314	9.399	0.085
100	224	9.111	9.279	0.168
100	225	9.165	9.234	0.069
100	226	9.192	9.318	0.126
100	228	9.221	9.374	0.153
100	232	9.148	9.309	0.161
100	233	9.136	9.287	0.151
100	236	9.133	9.368	0.235
100	238	9.556	9.679	0.123
100	243	8.945	9.057	0.112
105	244	9.438	9.462	0.024
105	246	9.436	9.619	0.183
105	248	9.127	9.247	0.120
105	374	9.103	9.136	0.033
105	376	9.396	9.617	0.221
	Max	9.569	9.679	0.249
	Average	9.282	9.327	0.046
	Min	8.764	8.811	-0.158
	Std Dev	0.204	0.199	0.096

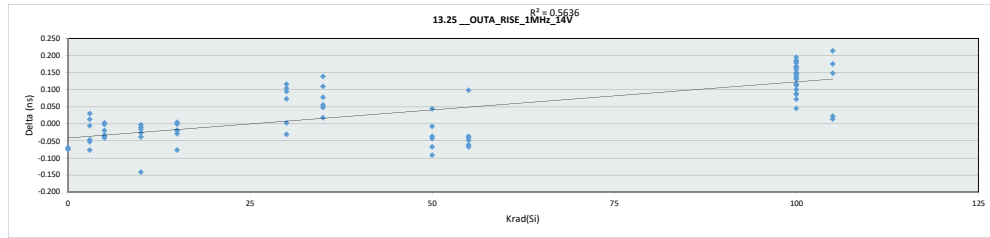


13.17_OUTA_RISE_1MHz_12V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.068	8.811	9.050	8.833	9.064	8.940	9.258	8.950	9.052	8.859	9.136
Average	9.299	9.216	9.326	9.249	9.323	9.298	9.446	9.222	9.359	9.358	9.416
Max	9.444	9.394	9.507	9.417	9.521	9.499	9.578	9.464	9.599	9.679	9.619
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

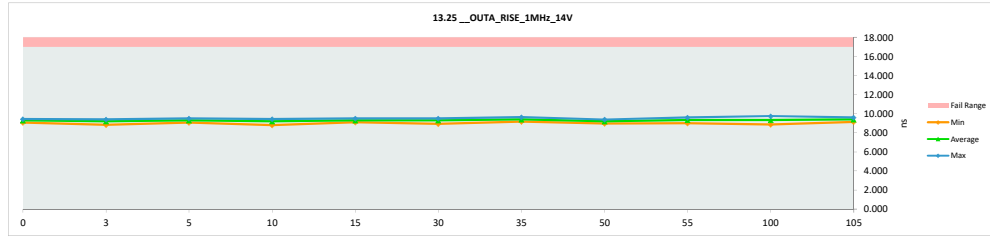


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13.25_OUTA_RISE_1MHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	9.513	9.440	-0.073
0	118	9.146	9.072	-0.074
0	122	9.462	9.393	-0.069
3	61	9.446	9.394	-0.052
3	62	9.160	9.191	0.031
3	63	9.397	9.321	-0.076
3	64	9.203	9.217	0.014
3	65	8.831	8.826	-0.005
3	66	9.417	9.371	-0.046
5	67	9.200	9.199	-0.001
5	68	9.495	9.455	-0.040
5	69	9.516	9.497	-0.019
5	70	9.117	9.085	-0.032
5	71	9.243	9.247	0.004
5	72	9.448	9.411	-0.037
10	73	9.446	9.444	-0.002
10	74	9.160	9.145	-0.015
10	75	9.397	9.256	-0.141
10	76	9.203	9.194	-0.009
10	77	8.831	8.806	-0.025
10	78	9.417	9.379	-0.038
15	79	9.200	9.200	0.000
15	80	9.495	9.419	-0.076
15	81	9.516	9.522	0.006
15	82	9.117	9.089	-0.028
15	83	9.243	9.224	-0.019
15	84	9.448	9.430	-0.018
30	86	9.446	9.416	-0.030
30	89	9.160	9.265	0.105
30	91	9.397	9.401	0.004
30	92	9.203	9.277	0.074
30	94	8.831	8.948	0.117
30	95	9.417	9.513	0.096
35	100	9.200	9.256	0.056
35	101	9.495	9.514	0.019
35	102	9.516	9.627	0.111
35	103	9.117	9.166	0.049
35	104	9.243	9.383	0.140
35	105	9.448	9.527	0.079
50	106	9.368	9.301	-0.067
50	107	9.179	9.088	-0.091
50	108	9.175	9.133	-0.042
50	109	9.345	9.390	0.045
50	110	9.018	8.982	-0.036
50	111	9.292	9.285	-0.007
55	113	9.206	9.139	-0.067
55	114	9.516	9.468	-0.048
55	115	9.446	9.410	-0.036
55	116	9.476	9.436	-0.040
55	126	9.074	9.013	-0.061
55	128	9.499	9.598	0.099
100	129	8.896	9.043	0.147
100	131	9.493	9.566	0.073
100	132	9.464	9.510	0.046
100	133	9.129	9.298	0.169
100	134	9.303	9.416	0.113
100	135	9.532	9.681	0.149
100	138	9.383	9.472	0.089
100	139	8.787	8.934	0.147
100	140	9.262	9.380	0.118
100	141	9.467	9.618	0.151
100	142	9.199	9.365	0.166
100	143	9.217	9.348	0.131
100	144	8.776	8.863	0.087
100	220	9.335	9.518	0.183
100	221	9.290	9.391	0.101
100	224	9.045	9.224	0.179
100	225	9.100	9.243	0.143
100	226	9.201	9.338	0.137
100	228	9.208	9.344	0.136
100	232	9.110	9.297	0.187
100	233	9.105	9.219	0.114
100	236	9.125	9.294	0.169
100	238	9.558	9.754	0.196
100	243	8.881	9.041	0.160
105	244	9.413	9.436	0.023
105	246	9.424	9.600	0.176
105	248	9.089	9.238	0.149
105	374	9.130	9.145	0.015
105	376	9.376	9.591	0.215
Max		9.558	9.754	0.215
Average		9.268	9.312	0.044
Min		8.776	8.806	-0.141
Std Dev		0.200	0.200	0.089

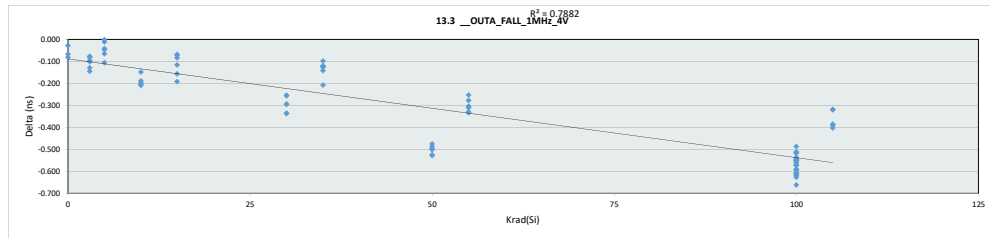


13.25_OUTA_RISE_1MHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.072	8.826	9.085	8.806	9.089	8.948	9.166	8.982	9.013	8.863	9.145
Average	9.302	9.220	9.316	9.204	9.314	9.303	9.412	9.197	9.344	9.340	9.402
Max	9.440	9.394	9.497	9.444	9.522	9.513	9.627	9.390	9.598	9.754	9.600
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

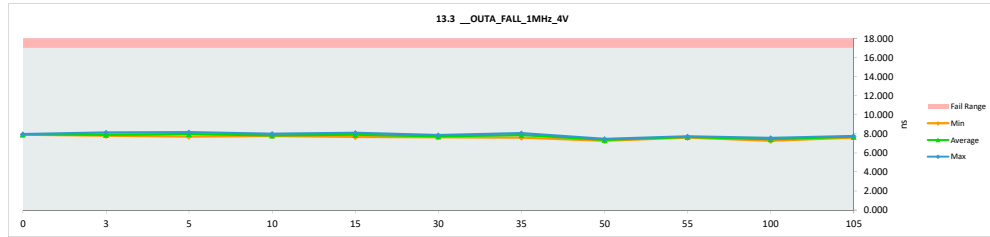


LDR TID Report
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13.3 _OUTA_FALL_1MHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.042	7.977	-0.065
0	118	7.930	7.905	-0.025
0	122	8.023	7.943	-0.080
3	61	8.004	7.925	-0.079
3	62	8.194	8.119	-0.075
3	63	7.950	7.850	-0.100
3	64	7.904	7.810	-0.094
3	65	7.934	7.806	-0.128
3	66	8.103	7.960	-0.143
5	67	7.728	7.684	-0.044
5	68	8.174	8.164	-0.010
5	69	7.995	7.932	-0.063
5	70	7.878	7.838	-0.040
5	71	7.968	7.968	0.000
5	72	8.256	8.152	-0.104
10	73	8.004	7.796	-0.208
10	74	8.194	7.993	-0.201
10	75	7.950	7.751	-0.199
10	76	7.904	7.758	-0.146
10	77	7.934	7.746	-0.188
10	78	8.103	7.905	-0.198
15	79	7.728	7.661	-0.067
15	80	8.174	8.105	-0.069
15	81	7.995	7.805	-0.190
15	82	7.878	7.763	-0.115
15	83	7.968	7.886	-0.082
15	84	8.256	8.101	-0.155
30	86	8.004	7.710	-0.294
30	89	8.194	7.860	-0.334
30	91	7.950	7.696	-0.254
30	92	7.904	7.651	-0.253
30	94	7.934	7.642	-0.292
30	95	8.103	7.767	-0.336
35	100	7.728	7.609	-0.119
35	101	8.174	8.049	-0.125
35	102	7.995	7.898	-0.097
35	103	7.878	7.737	-0.141
35	104	7.968	7.847	-0.121
35	105	8.256	8.050	-0.206
50	106	7.888	7.388	-0.500
50	107	7.783	7.287	-0.496
50	108	7.944	7.470	-0.474
50	109	7.723	7.237	-0.486
50	110	7.955	7.429	-0.526
50	111	7.889	7.365	-0.524
55	113	8.039	7.707	-0.332
55	114	7.933	7.624	-0.309
55	115	7.949	7.621	-0.328
55	116	8.032	7.729	-0.303
55	126	7.864	7.588	-0.276
55	128	7.934	7.683	-0.251
100	129	7.963	7.404	-0.559
100	131	8.051	7.515	-0.536
100	132	8.135	7.565	-0.570
100	133	8.110	7.562	-0.548
100	134	8.038	7.429	-0.609
100	135	8.099	7.507	-0.592
100	138	7.927	7.382	-0.545
100	139	7.845	7.295	-0.550
100	140	8.058	7.488	-0.570
100	141	7.934	7.397	-0.537
100	142	8.046	7.457	-0.589
100	143	7.886	7.375	-0.511
100	144	7.792	7.275	-0.517
100	220	8.039	7.414	-0.625
100	221	7.861	7.314	-0.547
100	224	7.985	7.499	-0.486
100	225	7.989	7.420	-0.569
100	226	7.931	7.380	-0.551
100	228	8.170	7.557	-0.613
100	232	7.890	7.378	-0.512
100	233	7.977	7.387	-0.590
100	236	7.920	7.317	-0.603
100	238	8.062	7.445	-0.617
100	243	7.997	7.336	-0.661
105	244	8.084	7.683	-0.401
105	246	8.038	7.721	-0.317
105	248	7.974	7.655	-0.319
105	374	7.983	7.599	-0.384
105	376	8.155	7.765	-0.390
Max		8.256	8.164	0.000
Average		7.989	7.688	-0.321
Min		7.723	7.237	-0.661
Std Dev		0.123	0.243	0.204

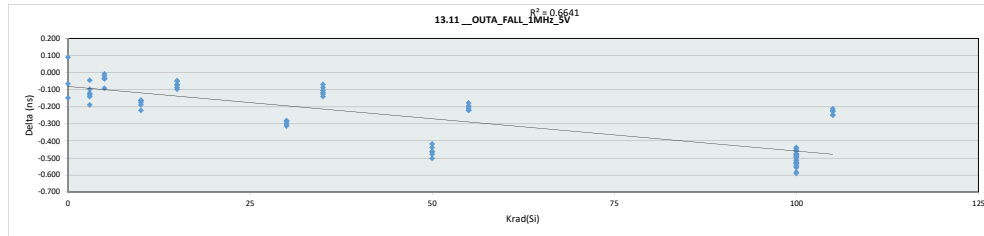


13.3 _OUTA_FALL_1MHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.905	7.806	7.684	7.746	7.661	7.642	7.609	7.237	7.588	7.275	7.599
Average	7.942	7.912	7.956	7.825	7.887	7.721	7.865	7.363	7.659	7.421	7.685
Max	7.977	8.119	8.164	7.993	8.105	7.860	8.050	7.470	7.729	7.565	7.765
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

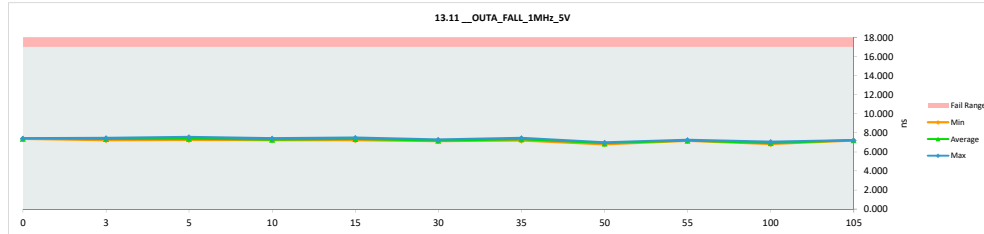


LDR TID Report
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13.11_OUTA_FALL_1MHz_5V				
Test Site	Tester	Unit	Max Limit	Min Limit
13.11_OUTA_FALL_1MHz_5V		ns	17	17
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	7.498	7.434	-0.064
0	118	7.334	7.425	0.091
0	122	7.508	7.361	-0.147
3	61	7.468	7.341	-0.127
3	62	7.591	7.453	-0.138
3	63	7.432	7.314	-0.118
3	64	7.417	7.229	-0.188
3	65	7.404	7.361	-0.043
3	66	7.514	7.419	-0.095
5	67	7.285	7.251	-0.034
5	68	7.562	7.471	-0.091
5	69	7.432	7.401	-0.031
5	70	7.343	7.327	-0.016
5	71	7.442	7.409	-0.033
5	72	7.558	7.553	-0.005
10	73	7.468	7.308	-0.160
10	74	7.591	7.428	-0.163
10	75	7.432	7.212	-0.220
10	76	7.417	7.229	-0.188
10	77	7.404	7.240	-0.164
10	78	7.514	7.339	-0.175
15	79	7.285	7.215	-0.070
15	80	7.562	7.466	-0.096
15	81	7.432	7.387	-0.045
15	82	7.343	7.258	-0.085
15	83	7.442	7.371	-0.071
15	84	7.558	7.510	-0.048
30	86	7.468	7.156	-0.312
30	89	7.591	7.295	-0.296
30	91	7.432	7.147	-0.285
30	92	7.417	7.111	-0.306
30	94	7.404	7.122	-0.282
30	95	7.514	7.235	-0.279
35	100	7.285	7.198	-0.087
35	101	7.562	7.445	-0.117
35	102	7.432	7.309	-0.123
35	103	7.343	7.204	-0.139
35	104	7.442	7.374	-0.068
35	105	7.558	7.455	-0.103
50	106	7.379	6.903	-0.476
50	107	7.362	6.862	-0.500
50	108	7.419	7.002	-0.417
50	109	7.255	6.794	-0.461
50	110	7.369	6.931	-0.438
50	111	7.374	6.913	-0.461
55	113	7.426	7.207	-0.219
55	114	7.376	7.199	-0.177
55	115	7.406	7.189	-0.217
55	116	7.438	7.239	-0.199
55	125	7.358	7.148	-0.210
55	128	7.459	7.266	-0.193
100	129	7.419	6.926	-0.493
100	131	7.453	6.999	-0.454
100	132	7.533	7.059	-0.474
100	133	7.539	6.983	-0.556
100	134	7.412	6.954	-0.458
100	135	7.480	6.951	-0.529
100	138	7.402	6.907	-0.495
100	139	7.316	6.874	-0.442
100	140	7.454	6.976	-0.478
100	141	7.477	6.950	-0.527
100	142	7.515	6.926	-0.589
100	143	7.430	6.904	-0.526
100	144	7.348	6.856	-0.492
100	220	7.426	6.910	-0.516
100	221	7.302	6.827	-0.475
100	224	7.420	6.983	-0.437
100	225	7.542	6.961	-0.581
100	226	7.387	6.855	-0.532
100	228	7.528	6.999	-0.529
100	232	7.344	6.861	-0.483
100	233	7.395	6.885	-0.510
100	236	7.387	6.844	-0.543
100	238	7.454	6.973	-0.481
100	243	7.485	6.935	-0.550
105	244	7.461	7.235	-0.226
105	246	7.445	7.223	-0.222
105	248	7.425	7.215	-0.210
105	374	7.424	7.178	-0.246
105	376	7.479	7.232	-0.247
Max		7.591	7.553	0.091
Average		7.437	7.160	-0.277
Min		7.255	6.794	-0.589
Std Dev		0.078	0.208	0.187

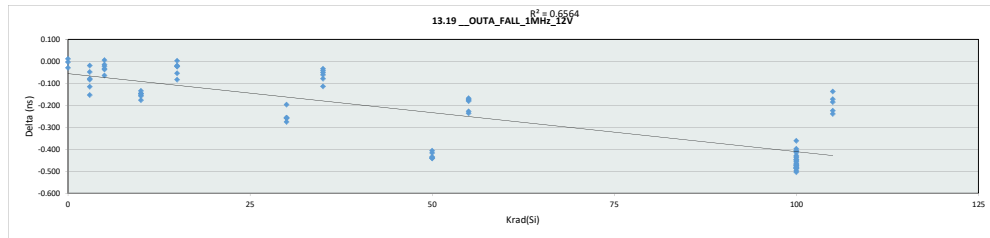


13.11_OUTA_FALL_1MHz_5V											
Test Site	Tester	Unit	Max Limit	Min Limit							
13.11_OUTA_FALL_1MHz_5V		ns	17	17							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.361	7.229	7.251	7.212	7.215	7.111	7.198	6.794	7.148	6.827	7.178
Average	7.407	7.353	7.402	7.293	7.368	7.178	7.331	6.901	7.208	6.929	7.217
Max	7.434	7.453	7.553	7.428	7.428	7.295	7.455	7.002	7.266	7.059	7.235
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

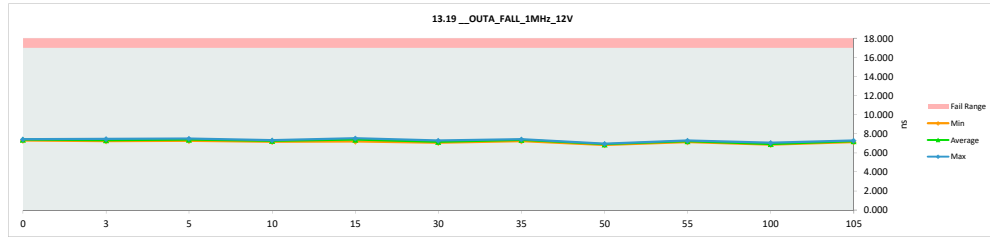


LDR TID Report
TPS7H5001-SP

13.19_OUTA_FALL_1MHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	7.428	7.427	-0.001
0	118	7.312	7.284	-0.028
0	122	7.420	7.433	0.013
3	61	7.372	7.295	-0.077
3	62	7.486	7.469	-0.017
3	63	7.446	7.365	-0.081
3	64	7.343	7.296	-0.047
3	65	7.325	7.211	-0.114
3	66	7.478	7.327	-0.151
5	67	7.289	7.254	-0.035
5	68	7.502	7.489	-0.013
5	69	7.385	7.365	-0.020
5	70	7.341	7.309	-0.032
5	71	7.393	7.400	0.007
5	72	7.543	7.480	-0.063
10	73	7.372	7.239	-0.133
10	74	7.486	7.334	-0.152
10	75	7.446	7.302	-0.144
10	76	7.343	7.187	-0.156
10	77	7.325	7.150	-0.175
10	78	7.478	7.334	-0.144
15	79	7.289	7.207	-0.082
15	80	7.502	7.450	-0.052
15	81	7.385	7.390	0.005
15	82	7.341	7.319	-0.022
15	83	7.393	7.372	-0.021
15	84	7.543	7.526	-0.017
30	86	7.372	7.116	-0.256
30	89	7.486	7.291	-0.195
30	91	7.446	7.173	-0.273
30	92	7.343	7.088	-0.255
30	94	7.325	7.069	-0.256
30	95	7.478	7.224	-0.254
35	100	7.289	7.240	-0.049
35	101	7.502	7.425	-0.077
35	102	7.385	7.353	-0.032
35	103	7.341	7.281	-0.060
35	104	7.393	7.352	-0.041
35	105	7.543	7.431	-0.112
50	106	7.322	6.918	-0.404
50	107	7.270	6.833	-0.437
50	108	7.390	6.958	-0.432
50	109	7.309	6.894	-0.415
50	110	7.332	6.894	-0.438
50	111	7.376	6.937	-0.439
55	113	7.411	7.185	-0.226
55	114	7.419	7.244	-0.175
55	115	7.415	7.245	-0.170
55	116	7.448	7.214	-0.234
55	126	7.301	7.135	-0.166
55	128	7.476	7.297	-0.179
100	129	7.377	6.912	-0.465
100	131	7.404	6.990	-0.414
100	132	7.453	7.048	-0.405
100	133	7.398	6.932	-0.466
100	134	7.432	6.949	-0.483
100	135	7.384	7.024	-0.360
100	138	7.415	6.930	-0.485
100	139	7.298	6.892	-0.406
100	140	7.383	6.937	-0.446
100	141	7.432	6.951	-0.481
100	142	7.430	6.934	-0.496
100	143	7.322	6.926	-0.396
100	144	7.297	6.860	-0.437
100	220	7.413	6.962	-0.451
100	221	7.344	6.889	-0.455
100	224	7.369	6.924	-0.445
100	225	7.378	6.906	-0.472
100	226	7.367	6.865	-0.502
100	228	7.440	6.966	-0.474
100	232	7.338	6.905	-0.433
100	233	7.341	6.914	-0.427
100	236	7.352	6.870	-0.482
100	238	7.453	6.967	-0.486
100	243	7.404	6.934	-0.470
105	244	7.456	7.219	-0.237
105	246	7.415	7.279	-0.136
105	248	7.396	7.212	-0.184
105	374	7.362	7.139	-0.223
105	376	7.444	7.273	-0.171
Max		7.543	7.526	0.013
Average		7.395	7.156	-0.239
Min		7.270	6.833	-0.502
Std Dev		0.066	0.200	0.177

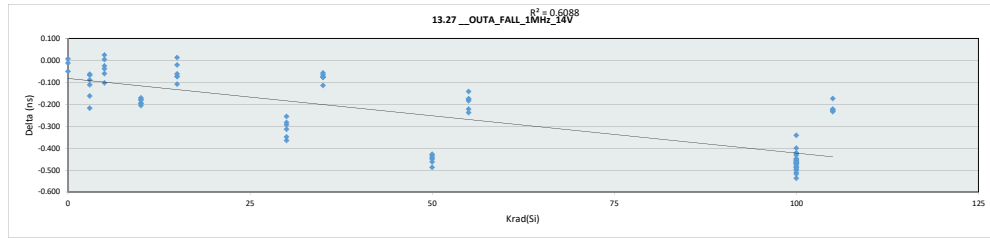


13.19_OUTA_FALL_1MHz_12V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.284	7.211	7.254	7.150	7.207	7.069	7.240	6.833	7.135	6.860	7.139
Average	7.381	7.327	7.383	7.258	7.377	7.160	7.347	6.906	7.220	6.933	7.224
Max	7.433	7.469	7.489	7.334	7.526	7.291	7.431	6.958	7.297	7.048	7.279
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

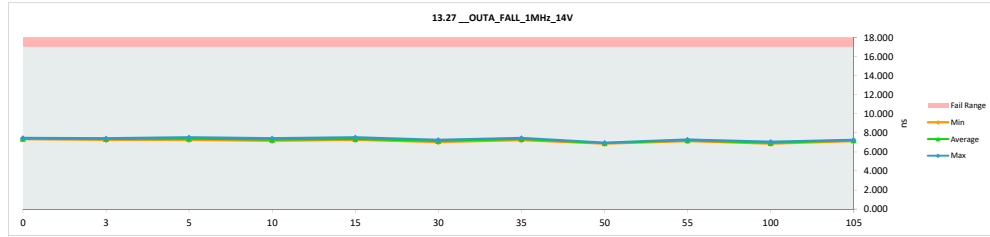


LDR TID Report
TPS7H5001-SP

13.27_OUTA_FALL_1MHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	7.454	7.463	0.009
0	118	7.340	7.329	-0.011
0	122	7.436	7.387	-0.049
3	61	7.450	7.388	-0.062
3	62	7.630	7.414	-0.216
3	63	7.447	7.360	-0.087
3	64	7.412	7.251	-0.161
3	65	7.346	7.280	-0.066
3	66	7.508	7.398	-0.110
5	67	7.271	7.276	0.005
5	68	7.533	7.497	-0.036
5	69	7.441	7.340	-0.101
5	70	7.350	7.326	-0.024
5	71	7.448	7.390	-0.058
5	72	7.509	7.535	0.026
10	73	7.450	7.246	-0.204
10	74	7.630	7.438	-0.192
10	75	7.447	7.278	-0.169
10	76	7.412	7.216	-0.196
10	77	7.346	7.171	-0.175
10	78	7.508	7.328	-0.180
15	79	7.271	7.252	-0.019
15	80	7.533	7.473	-0.060
15	81	7.441	7.370	-0.071
15	82	7.350	7.279	-0.071
15	83	7.448	7.341	-0.107
15	84	7.509	7.524	0.015
30	86	7.450	7.158	-0.292
30	89	7.630	7.266	-0.364
30	91	7.447	7.193	-0.254
30	92	7.412	7.131	-0.281
30	94	7.346	7.033	-0.313
30	95	7.508	7.161	-0.347
35	100	7.271	7.215	-0.056
35	101	7.533	7.468	-0.065
35	102	7.441	7.366	-0.075
35	103	7.350	7.275	-0.075
35	104	7.448	7.335	-0.113
35	105	7.509	7.435	-0.074
50	106	7.381	6.920	-0.461
50	107	7.447	6.847	-0.600
50	108	7.406	6.973	-0.433
50	109	7.315	6.867	-0.448
50	110	7.323	6.897	-0.426
50	111	7.390	6.948	-0.442
55	113	7.382	7.208	-0.174
55	114	7.437	7.255	-0.182
55	115	7.404	7.264	-0.140
55	116	7.447	7.273	-0.174
55	126	7.375	7.138	-0.237
55	128	7.498	7.278	-0.220
100	129	7.352	6.905	-0.447
100	131	7.466	7.036	-0.430
100	132	7.464	7.042	-0.422
100	133	7.437	6.962	-0.475
100	134	7.435	6.968	-0.467
100	135	7.411	7.071	-0.340
100	138	7.362	6.941	-0.421
100	139	7.360	6.875	-0.485
100	140	7.435	6.926	-0.509
100	141	7.433	6.978	-0.455
100	142	7.478	6.943	-0.535
100	143	7.414	6.946	-0.468
100	144	7.329	6.871	-0.458
100	220	7.456	6.940	-0.516
100	221	7.376	6.878	-0.498
100	224	7.388	6.922	-0.466
100	225	7.405	6.917	-0.488
100	226	7.390	6.896	-0.494
100	228	7.475	7.013	-0.462
100	232	7.323	6.925	-0.398
100	233	7.334	6.881	-0.453
100	236	7.394	6.929	-0.465
100	238	7.460	7.012	-0.448
100	243	7.400	6.917	-0.483
105	244	7.444	7.216	-0.228
105	246	7.486	7.262	-0.224
105	248	7.411	7.239	-0.172
105	374	7.372	7.140	-0.232
105	376	7.452	7.232	-0.220
Max		7.630	7.535	0.026
Average		7.424	7.165	-0.258
Min		7.271	6.847	-0.535
Std Dev		0.074	0.200	0.176

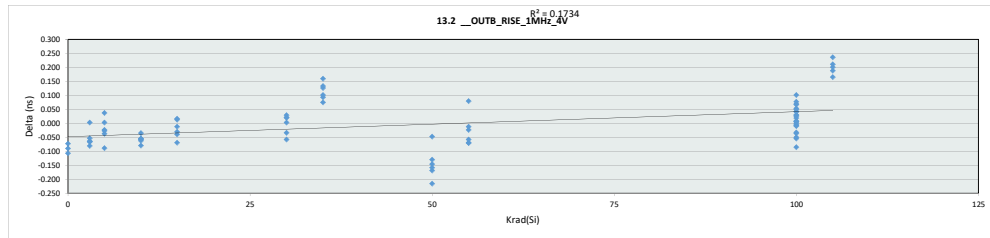


13.27_OUTA_FALL_1MHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.329	7.251	7.276	7.171	7.252	7.033	7.215	6.847	7.138	6.871	7.140
Average	7.393	7.349	7.394	7.280	7.373	7.157	7.349	6.909	7.236	6.946	7.218
Max	7.463	7.414	7.535	7.438	7.524	7.266	7.468	6.973	7.278	7.071	7.262
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

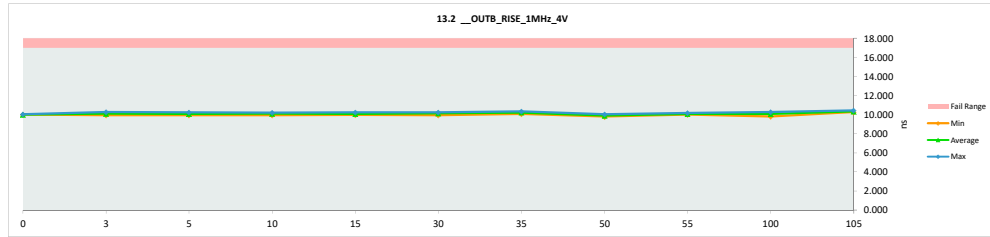


LDR TID Report
TPS7H5001-SP

13.2 _OUTB_RISE_1MHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	10.075	10.004	-0.071
0	118	10.100	10.011	-0.089
0	122	10.156	10.050	-0.106
3	61	10.261	10.265	0.004
3	62	10.197	10.132	-0.065
3	63	10.178	10.126	-0.052
3	64	10.014	9.935	-0.079
3	65	10.144	10.080	-0.064
3	66	10.138	10.076	-0.062
5	67	10.116	10.094	-0.022
5	68	10.188	10.227	0.039
5	69	10.095	10.059	-0.036
5	70	9.962	9.936	-0.026
5	71	10.224	10.228	0.004
5	72	10.080	9.993	-0.087
10	73	10.261	10.205	-0.056
10	74	10.197	10.136	-0.061
10	75	10.178	10.144	-0.034
10	76	10.014	9.936	-0.078
10	77	10.144	10.091	-0.053
10	78	10.138	10.084	-0.054
15	79	10.116	10.087	-0.029
15	80	10.188	10.177	-0.011
15	81	10.095	10.027	-0.068
15	82	9.962	9.976	0.014
15	83	10.224	10.242	0.018
15	84	10.080	10.042	-0.038
30	86	10.261	10.229	-0.032
30	89	10.197	10.201	0.004
30	91	10.178	10.209	0.031
30	92	10.014	9.958	-0.056
30	94	10.144	10.166	0.022
30	95	10.138	10.159	0.021
35	100	10.116	10.211	0.095
35	101	10.188	10.349	0.161
35	102	10.095	10.171	0.076
35	103	9.962	10.090	0.128
35	104	10.224	10.358	0.134
35	105	10.080	10.183	0.103
50	106	10.121	9.954	-0.167
50	107	9.831	9.785	-0.046
50	108	10.186	10.042	-0.144
50	109	10.000	9.872	-0.128
50	110	10.123	9.966	-0.157
50	111	10.059	9.845	-0.214
55	113	10.115	10.059	-0.056
55	114	10.156	10.087	-0.069
55	115	10.194	10.126	-0.068
55	116	10.188	10.177	-0.011
55	126	9.982	10.063	0.081
55	128	10.030	10.008	-0.022
100	129	9.958	10.012	0.054
100	131	10.086	10.113	0.027
100	132	10.049	10.152	0.103
100	133	10.105	10.137	0.032
100	134	9.944	9.896	-0.048
100	135	10.010	10.061	0.051
100	138	10.072	10.074	0.002
100	139	9.968	10.011	0.043
100	140	10.127	10.118	-0.009
100	141	10.078	10.072	-0.006
100	142	10.113	10.123	0.010
100	143	10.197	10.268	0.071
100	144	10.026	10.105	0.079
100	220	10.026	10.033	0.007
100	221	10.015	9.931	-0.084
100	224	10.076	10.144	0.068
100	225	10.001	9.970	-0.031
100	226	10.214	10.222	0.008
100	228	10.135	10.165	0.030
100	232	9.829	9.794	-0.035
100	233	10.131	10.174	0.043
100	236	10.242	10.238	-0.004
100	238	10.088	10.035	-0.053
100	243	10.059	10.079	0.020
105	244	10.241	10.443	0.202
105	246	10.045	10.257	0.212
105	248	10.123	10.312	0.189
105	374	10.259	10.426	0.167
105	376	10.076	10.314	0.238
Max		10.261	10.443	0.238
Average		10.105	10.104	-0.001
Min		9.829	9.785	-0.214
Std Dev		0.094	0.131	0.087

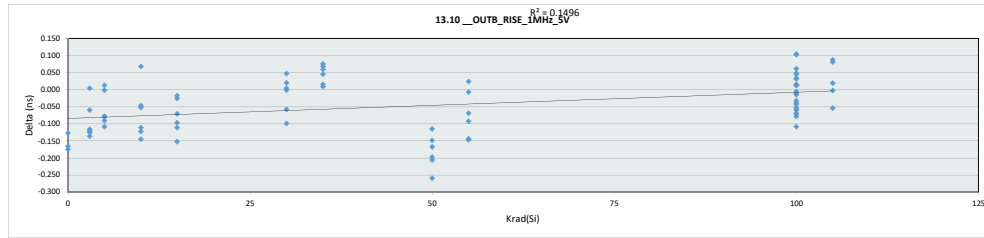


13.2 _OUTB_RISE_1MHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	10.004	9.935	9.936	9.936	9.976	9.958	10.090	9.785	10.008	9.794	10.257
Average	10.022	10.102	10.090	10.099	10.092	10.154	10.227	9.911	10.087	10.080	10.350
Max	10.050	10.265	10.228	10.205	10.242	10.229	10.358	10.042	10.177	10.268	10.443
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

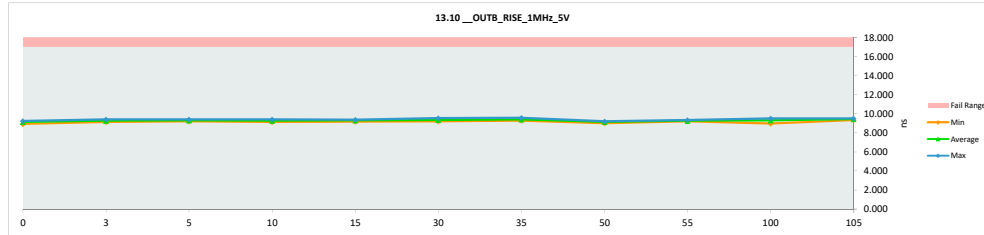


LDR TID Report
TPS7H5001-SP

13.10_OUTB_RISE_1MHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	9.404	9.239	-0.165
0	118	9.092	8.918	-0.174
0	122	9.353	9.227	-0.126
3	61	9.532	9.411	-0.121
3	62	9.245	9.186	-0.059
3	63	9.287	9.151	-0.136
3	64	9.312	9.188	-0.124
3	65	9.425	9.310	-0.115
3	66	9.320	9.325	0.005
5	67	9.381	9.394	0.013
5	68	9.387	9.279	-0.108
5	69	9.418	9.340	-0.078
5	70	9.192	9.191	-0.001
5	71	9.508	9.418	-0.090
5	72	9.361	9.283	-0.078
10	73	9.532	9.422	-0.110
10	74	9.245	9.194	-0.051
10	75	9.287	9.143	-0.144
10	76	9.312	9.190	-0.122
10	77	9.425	9.379	-0.046
10	78	9.320	9.389	0.069
15	79	9.381	9.311	-0.070
15	80	9.387	9.370	-0.017
15	81	9.418	9.322	-0.096
15	82	9.192	9.167	-0.025
15	83	9.508	9.356	-0.152
15	84	9.361	9.251	-0.110
30	86	9.532	9.531	-0.001
30	89	9.245	9.250	0.005
30	91	9.287	9.230	-0.057
30	92	9.312	9.214	-0.098
30	94	9.425	9.446	0.021
30	95	9.320	9.368	0.048
35	100	9.381	9.441	0.060
35	101	9.387	9.433	0.046
35	102	9.418	9.428	0.010
35	103	9.192	9.268	0.076
35	104	9.508	9.577	0.069
35	105	9.361	9.377	0.016
50	106	9.388	9.129	-0.259
50	107	9.146	8.998	-0.148
50	108	9.352	9.155	-0.197
50	109	9.280	9.166	-0.114
50	110	9.369	9.202	-0.167
50	111	9.378	9.173	-0.205
55	113	9.356	9.288	-0.068
55	114	9.286	9.194	-0.092
55	115	9.419	9.273	-0.146
55	116	9.498	9.355	-0.143
55	125	9.171	9.196	0.025
55	128	9.263	9.257	-0.006
100	129	9.220	9.323	0.103
100	131	9.266	9.298	0.032
100	132	9.233	9.282	0.049
100	133	9.359	9.302	-0.057
100	134	9.164	9.226	0.062
100	135	9.347	9.239	-0.108
100	138	9.353	9.301	-0.052
100	139	9.193	9.228	0.035
100	140	9.313	9.281	-0.032
100	141	9.408	9.349	-0.059
100	142	9.298	9.313	0.015
100	143	9.501	9.490	-0.011
100	144	9.169	9.215	0.046
100	220	9.315	9.237	-0.078
100	221	9.254	9.250	-0.004
100	224	9.293	9.306	0.013
100	225	9.288	9.274	-0.014
100	226	9.289	9.394	0.105
100	228	9.461	9.391	-0.070
100	232	9.015	8.973	-0.042
100	233	9.418	9.380	-0.038
100	236	9.423	9.355	-0.068
100	238	9.310	9.326	0.016
100	243	9.256	9.269	0.013
105	244	9.469	9.416	-0.053
105	246	9.328	9.326	-0.002
105	248	9.418	9.506	0.088
105	374	9.490	9.510	0.020
105	376	9.346	9.428	0.082
	Max	9.532	9.577	0.105
	Average	9.338	9.294	-0.045
	Min	9.015	8.918	-0.259
	Std Dev	0.106	0.119	0.080

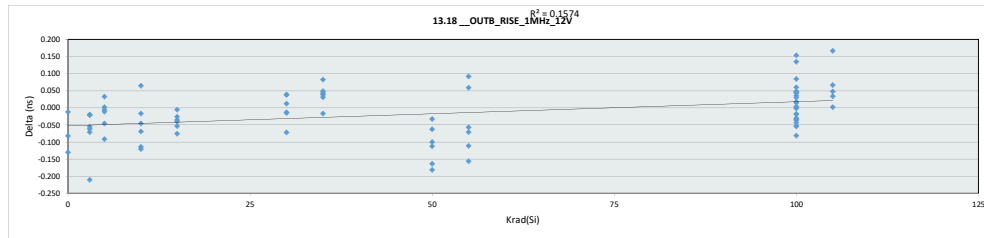


13.10_OUTB_RISE_1MHz_5V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.918	9.151	9.191	9.143	9.167	9.214	9.268	8.998	9.194	8.973	9.326
Average	9.128	9.262	9.318	9.286	9.296	9.340	9.421	9.137	9.261	9.292	9.437
Max	9.239	9.411	9.418	9.422	9.370	9.531	9.577	9.202	9.355	9.490	9.510
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

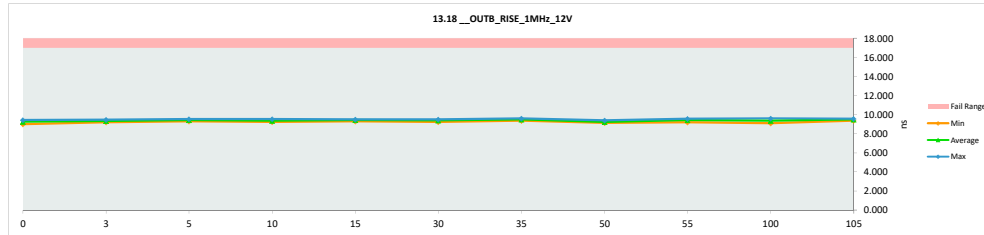


LDR TID Report
TPS7H5001-SP

13.18_OUTB_RISE_1MHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	9.520	9.391	-0.129
0	118	9.004	8.993	-0.011
0	122	9.507	9.426	-0.081
3	61	9.522	9.461	-0.061
3	62	9.416	9.206	-0.210
3	63	9.477	9.407	-0.070
3	64	9.346	9.292	-0.054
3	65	9.240	9.219	-0.021
3	66	9.459	9.440	-0.019
5	67	9.481	9.471	-0.010
5	68	9.472	9.506	0.034
5	69	9.536	9.531	-0.005
5	70	9.306	9.309	0.003
5	71	9.529	9.484	-0.045
5	72	9.503	9.412	-0.091
10	73	9.522	9.402	-0.120
10	74	9.416	9.348	-0.068
10	75	9.477	9.364	-0.113
10	76	9.346	9.301	-0.045
10	77	9.240	9.224	-0.016
10	78	9.459	9.525	0.066
15	79	9.481	9.442	-0.039
15	80	9.472	9.420	-0.052
15	81	9.536	9.461	-0.075
15	82	9.306	9.301	-0.005
15	83	9.529	9.494	-0.035
15	84	9.503	9.478	-0.025
30	86	9.522	9.451	-0.071
30	89	9.416	9.455	0.039
30	91	9.477	9.465	-0.012
30	92	9.346	9.359	0.013
30	94	9.240	9.226	-0.014
30	95	9.459	9.498	0.039
35	100	9.481	9.525	0.044
35	101	9.472	9.456	-0.016
35	102	9.536	9.619	0.083
35	103	9.306	9.356	0.050
35	104	9.529	9.568	0.039
35	105	9.503	9.535	0.032
50	106	9.301	9.269	-0.032
50	107	9.227	9.128	-0.099
50	108	9.315	9.152	-0.163
50	109	9.589	9.408	-0.181
50	110	9.260	9.149	-0.111
50	111	9.424	9.362	-0.062
55	113	9.349	9.194	-0.155
55	114	9.528	9.472	-0.056
55	115	9.628	9.558	-0.070
55	116	9.603	9.493	-0.110
55	126	9.281	9.374	0.093
55	128	9.528	9.588	0.060
100	129	9.237	9.274	0.037
100	131	9.435	9.440	0.005
100	132	9.312	9.448	0.136
100	133	9.131	9.133	0.002
100	134	9.312	9.397	0.085
100	135	9.466	9.620	0.154
100	138	9.433	9.478	0.045
100	139	9.265	9.230	-0.035
100	140	9.330	9.279	-0.051
100	141	9.571	9.518	-0.053
100	142	9.463	9.383	-0.080
100	143	9.479	9.495	0.016
100	144	9.318	9.289	-0.029
100	220	9.508	9.491	-0.017
100	221	9.456	9.475	0.019
100	224	9.348	9.379	0.031
100	225	9.211	9.256	0.045
100	226	9.303	9.352	0.049
100	228	9.455	9.503	0.048
100	232	9.151	9.109	-0.042
100	233	9.398	9.459	0.061
100	236	9.444	9.426	-0.018
100	238	9.561	9.560	-0.001
100	243	9.353	9.319	-0.034
105	244	9.437	9.472	0.035
105	246	9.517	9.566	0.049
105	248	9.383	9.551	0.168
105	374	9.357	9.360	0.003
105	376	9.469	9.536	0.067
	Max	9.628	9.620	0.168
	Average	9.413	9.397	-0.016
	Min	9.004	8.993	-0.210
	Std Dev	0.120	0.131	0.072

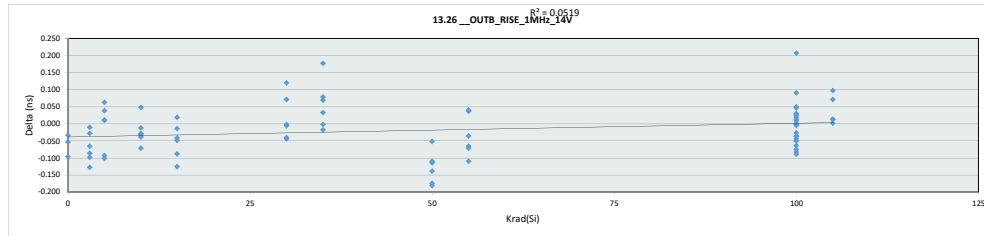


13.18_OUTB_RISE_1MHz_12V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.993	9.206	9.309	9.224	9.301	9.226	9.356	9.128	9.194	9.109	9.360
Average	9.270	9.338	9.452	9.361	9.433	9.409	9.510	9.245	9.447	9.388	9.497
Max	9.426	9.461	9.531	9.525	9.494	9.498	9.619	9.408	9.588	9.620	9.566
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

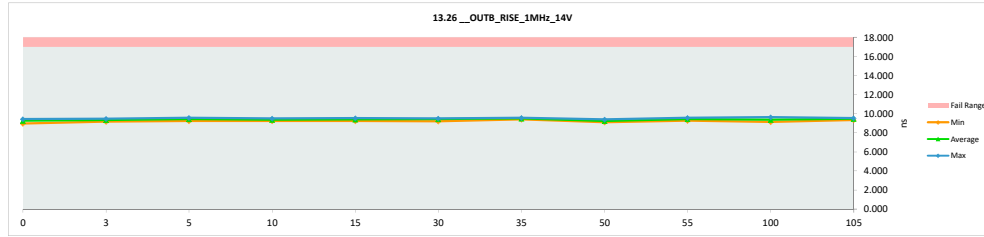


LDR TID Report
TPS7H5001-SP

13.26_OUTB_RISE_1MHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	9.462	9.366	-0.096
0	118	9.027	8.974	-0.053
0	122	9.459	9.426	-0.033
3	61	9.520	9.422	-0.098
3	62	9.378	9.251	-0.127
3	63	9.466	9.401	-0.065
3	64	9.320	9.311	-0.009
3	65	9.261	9.176	-0.085
3	66	9.505	9.478	-0.027
5	67	9.506	9.518	0.012
5	68	9.453	9.464	0.011
5	69	9.504	9.568	0.064
5	70	9.322	9.230	-0.092
5	71	9.582	9.482	-0.100
5	72	9.374	9.413	0.039
10	73	9.520	9.450	-0.070
10	74	9.378	9.427	0.049
10	75	9.466	9.428	-0.038
10	76	9.320	9.293	-0.027
10	77	9.261	9.230	-0.031
10	78	9.505	9.494	-0.011
15	79	9.506	9.493	-0.013
15	80	9.453	9.405	-0.048
15	81	9.504	9.524	0.020
15	82	9.322	9.235	-0.087
15	83	9.582	9.457	-0.125
15	84	9.374	9.334	-0.040
30	86	9.520	9.481	-0.039
30	89	9.378	9.499	0.121
30	91	9.466	9.461	-0.005
30	92	9.320	9.392	0.072
30	94	9.261	9.218	-0.043
30	95	9.505	9.504	-0.001
35	100	9.506	9.540	0.034
35	101	9.453	9.452	-0.001
35	102	9.504	9.574	0.070
35	103	9.322	9.402	0.080
35	104	9.582	9.565	-0.017
35	105	9.374	9.552	0.178
50	106	9.381	9.267	-0.114
50	107	9.235	9.097	-0.138
50	108	9.297	9.123	-0.174
50	109	9.511	9.402	-0.109
50	110	9.336	9.156	-0.180
50	111	9.422	9.371	-0.051
55	113	9.326	9.256	-0.070
55	114	9.561	9.496	-0.065
55	115	9.577	9.542	-0.035
55	116	9.592	9.483	-0.109
55	126	9.287	9.324	0.037
55	128	9.528	9.569	0.041
100	129	9.256	9.270	0.014
100	131	9.464	9.429	-0.035
100	132	9.456	9.420	-0.036
100	133	9.178	9.128	-0.050
100	134	9.322	9.318	-0.004
100	135	9.530	9.554	0.024
100	138	9.453	9.484	0.031
100	139	9.239	9.239	0.000
100	140	9.335	9.272	-0.063
100	141	9.538	9.567	0.029
100	142	9.460	9.386	-0.074
100	143	9.518	9.475	-0.043
100	144	9.349	9.324	-0.025
100	220	9.513	9.431	-0.082
100	221	9.480	9.531	0.051
100	224	9.372	9.370	-0.002
100	225	9.281	9.193	-0.088
100	226	9.223	9.431	0.208
100	228	9.443	9.453	0.010
100	232	9.164	9.128	-0.036
100	233	9.382	9.430	0.048
100	236	9.397	9.417	0.020
100	238	9.538	9.630	0.092
100	243	9.272	9.279	0.007
105	244	9.364	9.462	0.098
105	246	9.500	9.514	0.014
105	248	9.433	9.447	0.014
105	374	9.352	9.355	0.003
105	376	9.472	9.544	0.072
	Max	9.592	9.630	0.208
	Average	9.411	9.393	-0.018
	Min	9.027	8.974	-0.180
	Std Dev	0.114	0.134	0.071

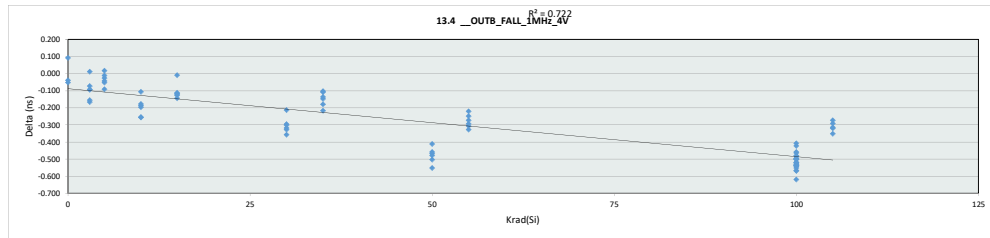


13.26_OUTB_RISE_1MHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.974	9.176	9.230	9.230	9.235	9.218	9.402	9.097	9.256	9.128	9.355
Average	9.255	9.340	9.446	9.387	9.408	9.426	9.514	9.236	9.445	9.382	9.464
Max	9.426	9.478	9.568	9.494	9.524	9.504	9.574	9.402	9.569	9.630	9.544
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

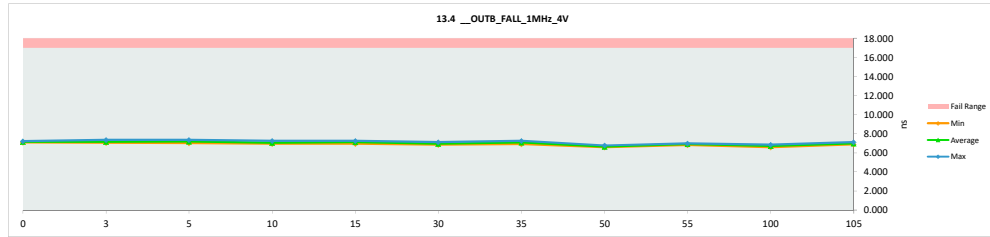


LDR TID Report
TPS7H5001-SP

13.4_OUTB_FALL_1MHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	7.123	7.217	0.094
0	118	7.135	7.098	-0.037
0	122	7.212	7.161	-0.051
3	61	7.246	7.154	-0.092
3	62	7.434	7.364	-0.070
3	63	7.173	7.081	-0.092
3	64	7.136	7.149	0.013
3	65	7.216	7.062	-0.154
3	66	7.324	7.159	-0.165
5	67	7.059	7.019	-0.040
5	68	7.345	7.364	0.019
5	69	7.178	7.169	-0.009
5	70	7.194	7.105	-0.089
5	71	7.236	7.185	-0.051
5	72	7.358	7.334	-0.024
10	73	7.246	6.992	-0.254
10	74	7.434	7.258	-0.176
10	75	7.173	6.978	-0.195
10	76	7.136	7.031	-0.105
10	77	7.216	6.964	-0.252
10	78	7.324	7.139	-0.185
15	79	7.059	6.948	-0.111
15	80	7.345	7.220	-0.125
15	81	7.178	7.170	-0.008
15	82	7.194	7.052	-0.142
15	83	7.236	7.119	-0.117
15	84	7.358	7.243	-0.115
30	86	7.246	6.952	-0.294
30	89	7.434	7.118	-0.316
30	91	7.173	6.847	-0.326
30	92	7.136	6.925	-0.211
30	94	7.216	6.916	-0.300
30	95	7.324	6.968	-0.356
35	100	7.059	6.913	-0.146
35	101	7.345	7.239	-0.106
35	102	7.178	7.044	-0.134
35	103	7.194	7.016	-0.178
35	104	7.236	7.021	-0.215
35	105	7.358	7.257	-0.101
50	106	7.245	6.695	-0.550
50	107	7.090	6.588	-0.502
50	108	7.133	6.723	-0.410
50	109	7.038	6.581	-0.457
50	110	7.241	6.776	-0.465
50	111	7.101	6.625	-0.476
55	113	7.231	6.984	-0.247
55	114	7.164	6.838	-0.326
55	115	7.179	6.875	-0.304
55	116	7.268	6.977	-0.291
55	126	7.167	6.949	-0.218
55	128	7.224	6.953	-0.271
100	129	7.198	6.662	-0.536
100	131	7.245	6.729	-0.516
100	132	7.290	6.828	-0.462
100	133	7.349	6.849	-0.500
100	134	7.251	6.701	-0.550
100	135	7.318	6.781	-0.537
100	138	7.163	6.643	-0.520
100	139	7.075	6.581	-0.494
100	140	7.297	6.769	-0.528
100	141	7.160	6.663	-0.497
100	142	7.168	6.711	-0.457
100	143	7.176	6.656	-0.520
100	144	7.053	6.648	-0.405
100	220	7.333	6.795	-0.538
100	221	7.153	6.653	-0.500
100	224	7.197	6.776	-0.421
100	225	7.205	6.727	-0.478
100	226	7.182	6.703	-0.479
100	228	7.435	6.869	-0.566
100	232	7.180	6.687	-0.493
100	233	7.261	6.697	-0.564
100	236	7.111	6.580	-0.531
100	238	7.279	6.743	-0.536
100	243	7.204	6.586	-0.618
105	244	7.293	6.944	-0.349
105	246	7.260	6.943	-0.317
105	248	7.290	6.977	-0.313
105	374	7.184	6.895	-0.289
105	376	7.393	7.122	-0.271
	Max	7.435	7.364	0.094
	Average	7.224	6.930	-0.294
	Min	7.038	6.580	-0.618
	Std Dev	0.095	0.214	0.189

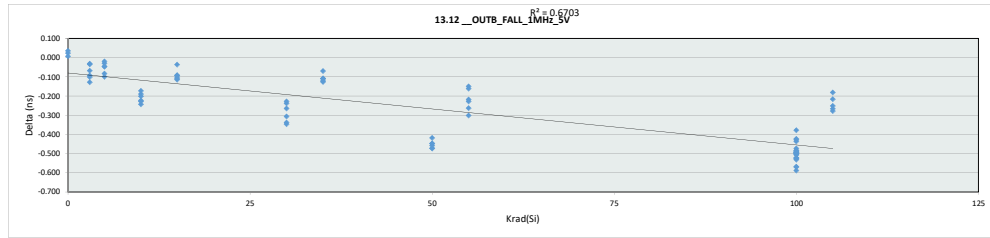


13.4_OUTB_FALL_1MHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.098	7.062	7.019	6.964	6.948	6.847	6.913	6.581	6.838	6.580	6.895
Average	7.159	7.162	7.196	7.060	7.125	6.954	7.082	6.665	6.929	6.710	6.976
Max	7.217	7.364	7.364	7.258	7.243	7.118	7.257	6.776	6.984	6.869	7.122
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

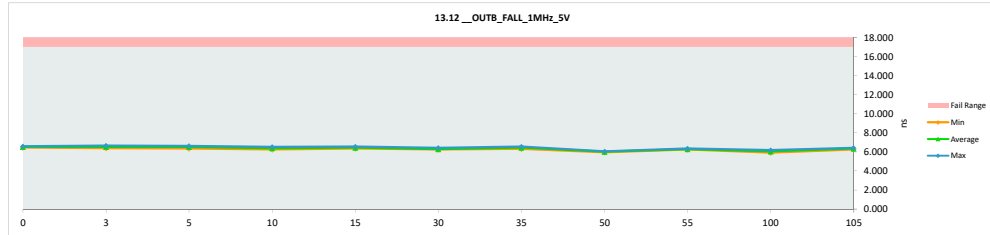


LDR TID Report
TPS7H5001-SP

13.12_OUTB_FALL_1MHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	6.537	6.575	0.038
0	118	6.422	6.447	0.025
0	122	6.532	6.541	0.009
3	61	6.582	6.455	-0.127
3	62	6.696	6.663	-0.033
3	63	6.484	6.393	-0.091
3	64	6.582	6.482	-0.100
3	65	6.488	6.459	-0.029
3	66	6.657	6.591	-0.066
5	67	6.446	6.348	-0.098
5	68	6.651	6.570	-0.081
5	69	6.543	6.526	-0.017
5	70	6.484	6.457	-0.027
5	71	6.560	6.515	-0.045
5	72	6.674	6.629	-0.045
10	73	6.582	6.382	-0.200
10	74	6.696	6.508	-0.188
10	75	6.484	6.312	-0.172
10	76	6.582	6.355	-0.227
10	77	6.488	6.267	-0.221
10	78	6.657	6.415	-0.242
15	79	6.446	6.356	-0.090
15	80	6.651	6.560	-0.091
15	81	6.543	6.509	-0.034
15	82	6.484	6.385	-0.099
15	83	6.560	6.452	-0.108
15	84	6.674	6.562	-0.112
30	86	6.582	6.246	-0.336
30	89	6.696	6.433	-0.263
30	91	6.484	6.248	-0.236
30	92	6.582	6.238	-0.344
30	94	6.488	6.262	-0.226
30	95	6.657	6.352	-0.305
35	100	6.446	6.322	-0.124
35	101	6.651	6.532	-0.119
35	102	6.543	6.435	-0.108
35	103	6.484	6.416	-0.068
35	104	6.560	6.454	-0.106
35	105	6.674	6.567	-0.107
50	106	6.507	6.063	-0.444
50	107	6.441	5.970	-0.471
50	108	6.504	6.035	-0.469
50	109	6.396	5.939	-0.457
50	110	6.482	6.066	-0.416
50	111	6.452	6.005	-0.447
55	113	6.497	6.337	-0.160
55	114	6.465	6.249	-0.216
55	115	6.537	6.311	-0.226
55	116	6.605	6.343	-0.262
55	126	6.448	6.300	-0.148
55	128	6.569	6.270	-0.299
100	129	6.532	6.013	-0.519
100	131	6.546	6.054	-0.492
100	132	6.589	6.167	-0.422
100	133	6.595	6.168	-0.427
100	134	6.527	6.033	-0.494
100	135	6.640	6.072	-0.568
100	138	6.502	6.013	-0.489
100	139	6.470	5.941	-0.529
100	140	6.586	6.098	-0.488
100	141	6.568	6.086	-0.482
100	142	6.590	6.119	-0.471
100	143	6.394	6.018	-0.376
100	144	6.458	5.975	-0.483
100	220	6.622	6.125	-0.497
100	221	6.554	6.063	-0.491
100	224	6.549	6.046	-0.503
100	225	6.644	6.058	-0.586
100	226	6.515	5.992	-0.523
100	228	6.696	6.200	-0.496
100	232	6.434	6.000	-0.434
100	233	6.621	6.116	-0.505
100	236	6.449	5.927	-0.522
100	238	6.612	6.045	-0.567
100	243	6.532	6.036	-0.496
105	244	6.587	6.373	-0.214
105	246	6.525	6.346	-0.179
105	248	6.555	6.306	-0.249
105	374	6.517	6.253	-0.264
105	376	6.694	6.418	-0.276
Max		6.696	6.663	0.038
Average		6.550	6.277	-0.273
Min		6.394	5.927	-0.586
Std Dev		0.079	0.204	0.185

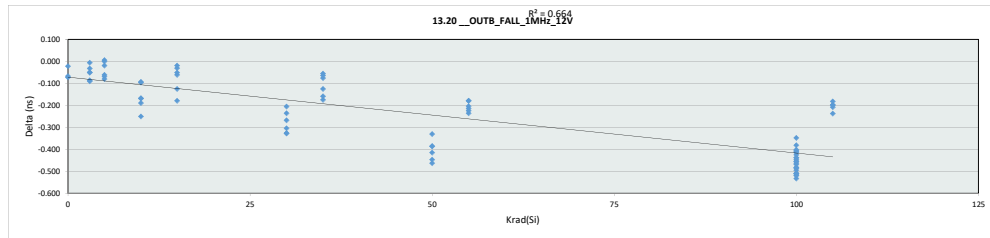


13.12_OUTB_FALL_1MHz_5V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.447	6.393	6.348	6.267	6.356	6.238	6.322	5.939	6.249	5.927	6.253
Average	6.521	6.507	6.508	6.373	6.471	6.297	6.454	6.013	6.302	6.057	6.339
Max	6.575	6.663	6.629	6.508	6.562	6.433	6.567	6.066	6.343	6.200	6.418
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

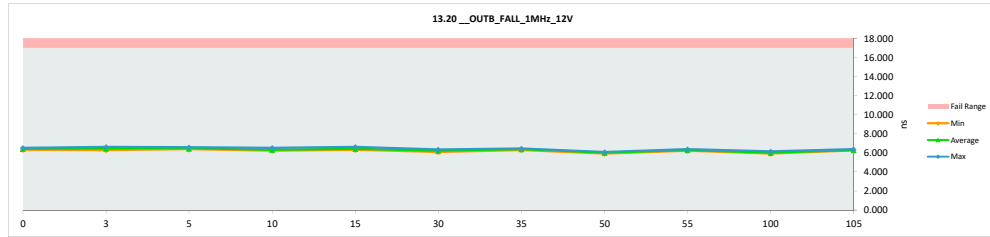


LDR TID Report
TPS7H5001-SP

13.20_OUTB_FALL_1MHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	6.548	6.528	-0.020
0	118	6.408	6.337	-0.071
0	122	6.494	6.428	-0.066
3	61	6.524	6.440	-0.084
3	62	6.521	6.616	-0.095
3	63	6.439	6.408	-0.031
3	64	6.486	6.397	-0.089
3	65	6.330	6.281	-0.049
3	66	6.553	6.505	-0.048
5	67	6.375	6.382	0.007
5	68	6.584	6.585	0.001
5	69	6.494	6.477	-0.017
5	70	6.429	6.369	-0.060
5	71	6.527	6.460	-0.067
5	72	6.524	6.545	-0.079
10	73	6.524	6.275	-0.249
10	74	6.621	6.529	-0.092
10	75	6.439	6.271	-0.168
10	76	6.486	6.320	-0.166
10	77	6.330	6.236	-0.094
10	78	6.553	6.365	-0.188
15	79	6.375	6.326	-0.049
15	80	6.584	6.460	-0.124
15	81	6.494	6.434	-0.060
15	82	6.429	6.400	-0.029
15	83	6.527	6.350	-0.177
15	84	6.624	6.606	-0.018
30	86	6.524	6.198	-0.326
30	89	6.621	6.355	-0.266
30	91	6.439	6.235	-0.204
30	92	6.486	6.162	-0.324
30	94	6.330	6.096	-0.234
30	95	6.553	6.250	-0.303
35	100	6.375	6.301	-0.074
35	101	6.584	6.461	-0.123
35	102	6.494	6.440	-0.054
35	103	6.429	6.367	-0.062
35	104	6.527	6.370	-0.157
35	105	6.624	6.451	-0.173
50	106	6.430	6.101	-0.329
50	107	6.379	5.933	-0.446
50	108	6.402	6.018	-0.384
50	109	6.408	6.023	-0.385
50	110	6.448	6.035	-0.413
50	111	6.439	5.977	-0.462
55	113	6.436	6.259	-0.177
55	114	6.497	6.274	-0.223
55	115	6.542	6.330	-0.212
55	116	6.571	6.336	-0.235
55	126	6.397	6.219	-0.178
55	128	6.603	6.401	-0.202
100	129	6.425	5.972	-0.453
100	131	6.541	6.033	-0.508
100	132	6.429	6.083	-0.346
100	133	6.482	6.036	-0.446
100	134	6.478	6.042	-0.436
100	135	6.525	6.145	-0.380
100	138	6.431	6.022	-0.409
100	139	6.423	5.905	-0.518
100	140	6.435	5.981	-0.454
100	141	6.549	6.033	-0.516
100	142	6.532	6.024	-0.508
100	143	6.497	6.030	-0.467
100	144	6.477	5.994	-0.483
100	220	6.562	6.068	-0.494
100	221	6.469	6.056	-0.413
100	224	6.436	6.035	-0.401
100	225	6.372	5.949	-0.423
100	226	6.445	6.009	-0.436
100	228	6.576	6.092	-0.484
100	232	6.420	6.016	-0.404
100	233	6.501	6.039	-0.462
100	236	6.432	5.951	-0.481
100	238	6.528	6.020	-0.508
100	243	6.508	5.976	-0.532
105	244	6.511	6.275	-0.236
105	246	6.559	6.362	-0.197
105	248	6.441	6.244	-0.197
105	374	6.449	6.242	-0.207
105	376	6.566	6.385	-0.181
Max		6.624	6.616	0.007
Average		6.487	6.237	-0.250
Min		6.330	5.905	-0.532
Std Dev		0.075	0.194	0.172

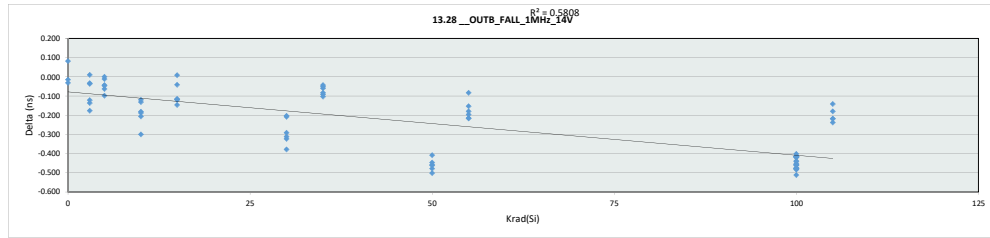


13.20_OUTB_FALL_1MHz_12V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.337	6.281	6.369	6.236	6.326	6.096	6.301	5.933	6.219	5.905	6.242
Average	6.431	6.441	6.470	6.333	6.429	6.216	6.398	6.015	6.303	6.021	6.302
Max	6.528	6.616	6.585	6.529	6.606	6.355	6.461	6.101	6.401	6.145	6.385
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

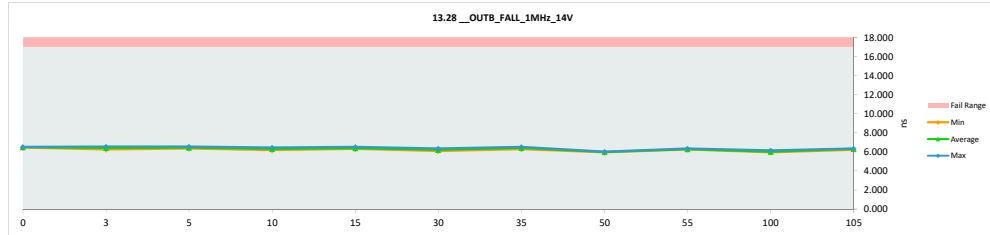


LDR TID Report
TPS7H5001-SP

13.28_OUTB_FALL_1MHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	6.550	6.520	-0.030
0	118	6.366	6.450	0.084
0	122	6.492	6.480	-0.012
3	61	6.490	6.459	-0.031
3	62	6.556	6.569	0.013
3	63	6.541	6.366	-0.175
3	64	6.431	6.397	-0.034
3	65	6.403	6.268	-0.135
3	66	6.627	6.508	-0.119
5	67	6.357	6.359	0.002
5	68	6.563	6.553	-0.010
5	69	6.478	6.416	-0.062
5	70	6.428	6.384	-0.044
5	71	6.488	6.446	-0.042
5	72	6.626	6.529	-0.097
10	73	6.490	6.310	-0.180
10	74	6.556	6.438	-0.118
10	75	6.541	6.354	-0.187
10	76	6.431	6.302	-0.129
10	77	6.403	6.198	-0.205
10	78	6.627	6.329	-0.298
15	79	6.357	6.368	0.011
15	80	6.563	6.523	-0.040
15	81	6.478	6.360	-0.118
15	82	6.428	6.312	-0.116
15	83	6.488	6.375	-0.113
15	84	6.626	6.481	-0.145
30	86	6.490	6.200	-0.290
30	89	6.556	6.350	-0.206
30	91	6.541	6.232	-0.309
30	92	6.431	6.230	-0.201
30	94	6.403	6.081	-0.322
30	95	6.627	6.250	-0.377
35	100	6.357	6.299	-0.058
35	101	6.563	6.514	-0.049
35	102	6.478	6.388	-0.090
35	103	6.428	6.346	-0.082
35	104	6.488	6.447	-0.041
35	105	6.626	6.525	-0.101
50	106	6.446	5.989	-0.457
50	107	6.414	5.955	-0.459
50	108	6.427	6.021	-0.406
50	109	6.433	5.933	-0.500
50	110	6.428	5.983	-0.445
50	111	6.442	5.966	-0.476
55	113	6.420	6.268	-0.152
55	114	6.466	6.253	-0.213
55	115	6.522	6.328	-0.194
55	116	6.536	6.322	-0.214
55	126	6.328	6.247	-0.081
55	128	6.537	6.359	-0.178
100	129	6.396	5.940	-0.456
100	131	6.506	5.997	-0.509
100	132	6.435	6.023	-0.412
100	133	6.500	6.024	-0.476
100	134	6.486	6.004	-0.482
100	135	6.531	6.117	-0.414
100	138	6.427	6.027	-0.400
100	139	6.390	5.972	-0.418
100	140	6.445	6.009	-0.436
100	141	6.512	6.040	-0.472
100	142	6.535	6.058	-0.477
100	143	6.473	6.015	-0.458
100	144	6.465	5.987	-0.478
100	220	6.557	6.143	-0.414
100	221	6.465	6.050	-0.415
100	224	6.454	5.996	-0.458
100	225	6.405	5.966	-0.439
100	226	6.427	5.950	-0.477
100	228	6.586	6.110	-0.476
100	232	6.420	6.003	-0.417
100	233	6.459	6.004	-0.455
100	236	6.422	5.971	-0.451
100	238	6.484	6.071	-0.413
100	243	6.409	5.996	-0.413
105	244	6.544	6.327	-0.217
105	246	6.536	6.358	-0.178
105	248	6.439	6.299	-0.140
105	374	6.449	6.213	-0.236
105	376	6.565	6.348	-0.217
Max	6.627	6.569	0.084	
Average	6.481	6.232	-0.249	
Min	6.328	5.933	-0.509	
Std Dev	0.072	0.195	0.175	

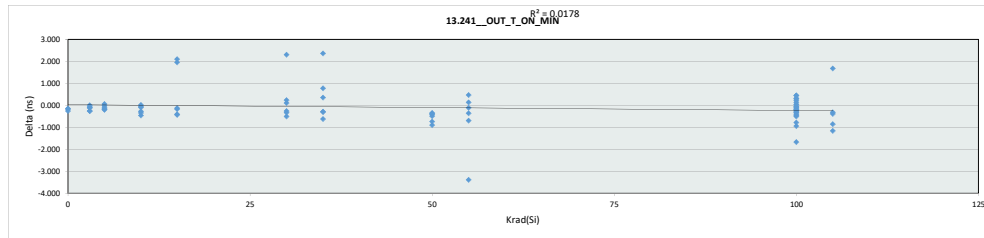


13.28_OUTB_FALL_1MHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.450	6.268	6.359	6.198	6.312	6.081	6.299	5.933	6.247	5.940	6.213
Average	6.483	6.428	6.448	6.322	6.403	6.224	6.420	5.975	6.296	6.020	6.309
Max	6.520	6.569	6.553	6.438	6.523	6.350	6.525	6.021	6.359	6.143	6.358
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

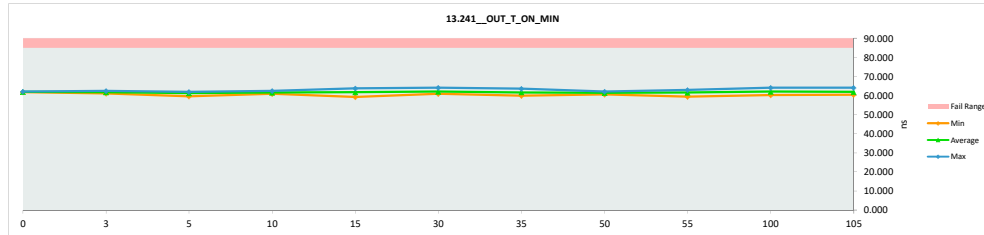


LDR TID Report
TPS7H5001-SP

13.241_OUT_T_ON_MIN				
Test Site	Tester	Unit	Max Limit	Min Limit
ns	ns	ns	85	85
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	62.194	62.046	-0.148
0	118	61.936	61.809	-0.127
0	122	62.069	61.827	-0.242
3	61	62.508	62.529	0.021
3	62	61.780	61.681	-0.099
3	63	61.428	61.182	-0.246
3	64	61.685	61.628	-0.057
3	65	61.842	61.745	-0.097
3	66	61.761	61.518	-0.243
5	67	61.840	61.664	-0.176
5	68	61.378	61.311	-0.067
5	69	62.081	61.906	-0.175
5	70	61.278	61.357	0.079
5	71	61.598	61.470	-0.128
5	72	59.635	59.636	0.001
10	73	62.508	62.471	-0.037
10	74	61.780	61.808	0.028
10	75	61.428	60.976	-0.452
10	76	61.685	61.609	-0.076
10	77	61.842	61.582	-0.260
10	78	61.761	61.435	-0.326
15	79	61.840	63.812	1.972
15	80	61.378	61.261	-0.117
15	81	62.081	61.928	-0.153
15	82	61.278	63.394	2.116
15	83	61.598	61.210	-0.388
15	84	59.635	59.219	-0.416
30	86	62.508	62.266	-0.242
30	89	61.780	64.094	2.314
30	91	61.428	60.934	-0.494
30	92	61.685	61.803	0.118
30	94	61.842	62.100	0.258
30	95	61.761	61.448	-0.313
35	100	61.840	61.554	-0.286
35	101	61.378	60.774	-0.604
35	102	62.081	61.814	-0.267
35	103	61.278	63.660	2.382
35	104	61.598	62.384	0.786
35	105	59.635	60.000	0.365
50	106	61.004	60.652	-0.352
50	107	62.561	62.156	-0.405
50	108	62.694	61.975	-0.719
50	109	62.422	61.540	-0.882
50	110	61.625	61.146	-0.479
50	111	61.945	61.610	-0.335
55	113	60.862	61.016	0.154
55	114	63.003	62.902	-0.101
55	115	60.109	59.429	-0.680
55	116	65.227	61.862	-3.365
55	126	62.586	62.236	-0.350
55	128	61.675	62.166	0.491
100	129	60.544	60.286	-0.258
100	131	62.657	63.113	0.456
100	132	60.800	60.752	-0.048
100	133	61.969	61.653	-0.316
100	134	62.743	62.261	-0.482
100	135	63.668	63.525	-0.143
100	138	61.265	60.930	-0.335
100	139	63.211	63.268	0.057
100	140	61.700	60.938	-0.762
100	141	63.559	63.870	0.311
100	142	61.470	61.061	-0.409
100	143	62.422	62.462	0.040
100	144	62.144	61.917	-0.227
100	220	60.954	60.887	-0.067
100	221	63.467	63.614	0.147
100	224	62.128	62.069	-0.059
100	225	63.476	63.709	0.233
100	226	63.103	61.443	-1.660
100	228	61.497	60.573	-0.924
100	232	62.399	61.964	-0.435
100	233	63.745	64.080	0.335
100	236	63.672	63.499	-0.173
100	238	61.054	61.532	0.478
100	243	62.175	61.998	-0.177
105	244	62.614	62.244	-0.370
105	246	62.715	61.565	-1.150
105	248	60.732	60.429	-0.303
105	374	61.925	61.092	-0.833
105	376	62.411	64.104	1.693
	Max	65.227	64.104	2.382
	Average	61.932	61.830	-0.102
	Min	59.635	59.219	-3.365
	Std Dev	0.941	1.060	0.767

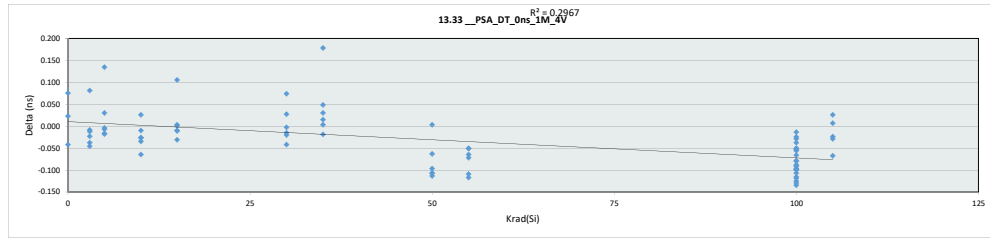


13.241_OUT_T_ON_MIN											
Test Site	Tester	Unit	Max Limit	Min Limit							
85	ns	ns	85	85							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	61.809	61.182	59.636	60.976	59.219	60.934	60.000	60.652	59.429	60.286	60.429
Average	61.894	61.714	61.224	61.647	61.804	62.108	61.698	61.513	61.602	62.142	61.887
Max	62.046	62.529	61.906	62.471	63.812	64.094	63.660	62.156	62.902	64.080	64.104
UL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000

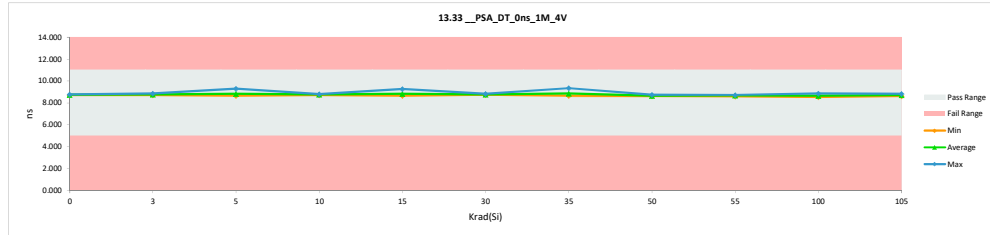


LDR TID Report
TPS7H5001-SP

13.33_PSA_DT_0ns_1M_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.727	8.751	0.024
0	118	8.745	8.704	-0.041
0	122	8.691	8.767	0.076
3	61	8.768	8.850	0.082
3	62	8.807	8.785	-0.022
3	63	8.805	8.760	-0.045
3	64	8.757	8.720	-0.037
3	65	8.806	8.798	-0.008
3	66	8.766	8.755	-0.011
5	67	8.737	8.734	-0.003
5	68	8.693	8.724	0.031
5	69	8.746	8.740	-0.006
5	70	9.171	9.306	0.135
5	71	8.876	8.860	-0.016
5	72	8.665	8.648	-0.017
10	73	8.768	8.795	0.027
10	74	8.807	8.773	-0.034
10	75	8.805	8.779	-0.026
10	76	8.757	8.732	-0.025
10	77	8.806	8.742	-0.064
10	78	8.766	8.757	-0.009
15	79	8.737	8.727	-0.010
15	80	8.693	8.695	0.002
15	81	8.745	8.716	-0.030
15	82	9.171	9.277	0.106
15	83	8.876	8.867	-0.009
15	84	8.665	8.669	0.004
30	86	8.768	8.843	0.075
30	89	8.807	8.766	-0.041
30	91	8.805	8.791	-0.014
30	92	8.757	8.755	-0.002
30	94	8.806	8.834	0.028
30	95	8.766	8.747	-0.019
35	100	8.737	8.768	0.031
35	101	8.693	8.742	0.049
35	102	8.746	8.728	-0.018
35	103	9.171	9.350	0.179
35	104	8.876	8.892	0.016
35	105	8.665	8.669	0.004
50	106	8.704	8.642	-0.062
50	107	8.850	8.737	-0.113
50	108	8.761	8.655	-0.106
50	109	8.723	8.627	-0.096
50	110	8.637	8.641	0.004
50	111	8.825	8.719	-0.106
55	113	8.784	8.720	-0.064
55	114	8.822	8.714	-0.108
55	115	8.843	8.727	-0.116
55	116	8.775	8.704	-0.071
55	126	8.655	8.605	-0.050
55	128	8.728	8.678	-0.050
100	129	8.742	8.677	-0.065
100	131	8.712	8.659	-0.053
100	132	8.674	8.596	-0.078
100	133	8.727	8.621	-0.106
100	134	8.677	8.581	-0.096
100	135	8.718	8.619	-0.099
100	138	8.785	8.661	-0.124
100	139	8.749	8.670	-0.079
100	140	8.672	8.538	-0.134
100	141	8.785	8.694	-0.091
100	142	8.694	8.607	-0.087
100	143	8.826	8.727	-0.099
100	144	8.706	8.682	-0.024
100	220	8.790	8.661	-0.129
100	221	8.669	8.580	-0.089
100	224	8.621	8.566	-0.055
100	225	8.774	8.677	-0.097
100	226	8.867	8.749	-0.118
100	228	8.726	8.675	-0.051
100	232	8.708	8.695	-0.013
100	233	8.721	8.672	-0.049
100	236	8.894	8.866	-0.028
100	238	8.783	8.668	-0.115
100	243	8.744	8.707	-0.037
105	244	8.719	8.652	-0.067
105	246	8.661	8.638	-0.023
105	248	8.719	8.746	0.027
105	374	8.828	8.836	0.008
105	376	8.691	8.663	-0.028
Max		9.171	9.350	0.179
Average		8.768	8.736	-0.032
Min		8.621	8.538	-0.134
Std Dev		0.100	0.136	0.061

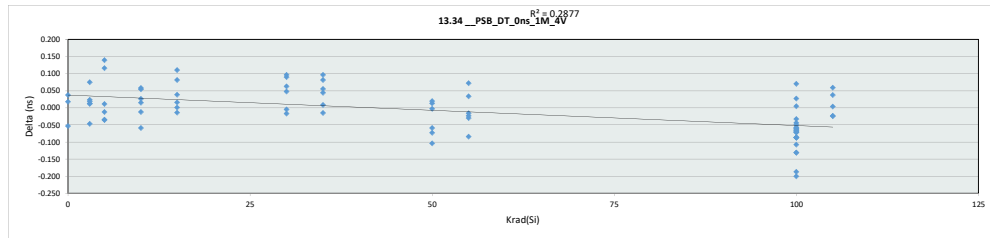


13.33_PSA_DT_0ns_1M_4V											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	8.704	8.720	8.648	8.732	8.669	8.747	8.669	8.627	8.605	8.538	8.638
Average	8.741	8.778	8.835	8.763	8.825	8.789	8.858	8.670	8.691	8.660	8.707
Max	8.767	8.850	9.306	8.795	9.277	8.843	9.350	8.737	8.727	8.866	8.836
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

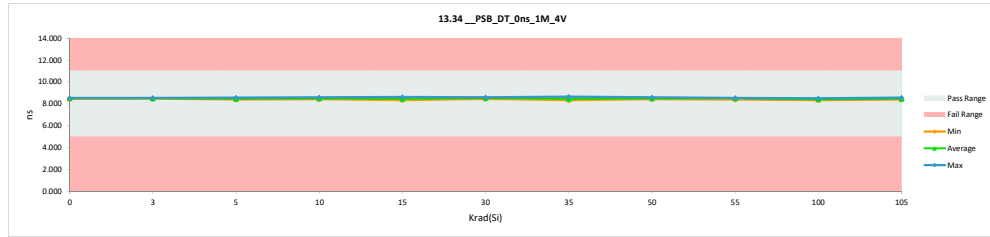


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13.34_PSB_DT_Ons_1M_4V				
Test Site	Tester	Unit	Max Limit	Min Limit
ns	ns	11	11	5
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	8.498	8.517	0.019
0	118	8.509	8.547	0.038
0	122	8.502	8.450	-0.052
3	61	8.443	8.467	0.024
3	62	8.500	8.520	0.020
3	63	8.478	8.554	0.076
3	64	8.459	8.472	0.013
3	65	8.534	8.547	0.013
3	66	8.539	8.493	-0.046
5	67	8.364	8.481	0.117
5	68	8.445	8.410	-0.035
5	69	8.512	8.501	-0.011
5	70	8.250	8.390	0.140
5	71	8.620	8.586	-0.034
5	72	8.463	8.475	0.012
10	73	8.443	8.432	-0.011
10	74	8.500	8.555	0.055
10	75	8.478	8.494	0.016
10	76	8.459	8.486	0.027
10	77	8.534	8.593	0.059
10	78	8.539	8.481	-0.058
15	79	8.364	8.475	0.111
15	80	8.445	8.432	-0.013
15	81	8.512	8.551	0.039
15	82	8.250	8.332	0.082
15	83	8.620	8.622	0.002
15	84	8.463	8.480	0.017
30	86	8.443	8.540	0.097
30	89	8.500	8.591	0.091
30	91	8.478	8.542	0.064
30	92	8.459	8.455	-0.004
30	94	8.534	8.583	0.049
30	95	8.539	8.523	-0.016
35	100	8.364	8.461	0.097
35	101	8.445	8.431	-0.014
35	102	8.512	8.521	0.009
35	103	8.250	8.332	0.082
35	104	8.620	8.665	0.045
35	105	8.463	8.519	0.056
50	106	8.519	8.461	-0.058
50	107	8.587	8.573	-0.014
50	108	8.474	8.472	-0.002
50	109	8.488	8.416	-0.072
50	110	8.611	8.508	-0.103
50	111	8.532	8.553	0.021
55	113	8.545	8.523	-0.022
55	114	8.637	8.554	-0.083
55	115	8.514	8.549	0.035
55	116	8.467	8.452	-0.015
55	126	8.429	8.400	-0.029
55	128	8.475	8.548	0.073
100	129	8.532	8.446	-0.086
100	131	8.452	8.387	-0.065
100	132	8.475	8.404	-0.071
100	133	8.479	8.393	-0.086
100	134	8.449	8.342	-0.107
100	135	8.497	8.412	-0.085
100	138	8.551	8.421	-0.130
100	139	8.371	8.442	0.071
100	140	8.400	8.330	-0.070
100	141	8.475	8.432	-0.043
100	142	8.460	8.411	-0.049
100	143	8.678	8.479	-0.199
100	144	8.536	8.475	-0.061
100	220	8.510	8.380	-0.130
100	221	8.499	8.433	-0.066
100	224	8.442	8.373	-0.069
100	225	8.540	8.479	-0.061
100	226	8.634	8.448	-0.186
100	228	8.498	8.412	-0.086
100	232	8.454	8.396	-0.058
100	233	8.403	8.431	0.028
100	236	8.543	8.511	-0.032
100	238	8.499	8.433	-0.066
100	243	8.485	8.491	0.006
105	244	8.512	8.489	-0.023
105	246	8.402	8.407	0.005
105	248	8.461	8.499	0.038
105	374	8.506	8.566	0.060
105	376	8.429	8.407	-0.022
Max		8.678	8.665	0.140
Average		8.484	8.475	-0.009
Min		8.250	8.330	-0.199
Std Dev		0.078	0.071	0.067

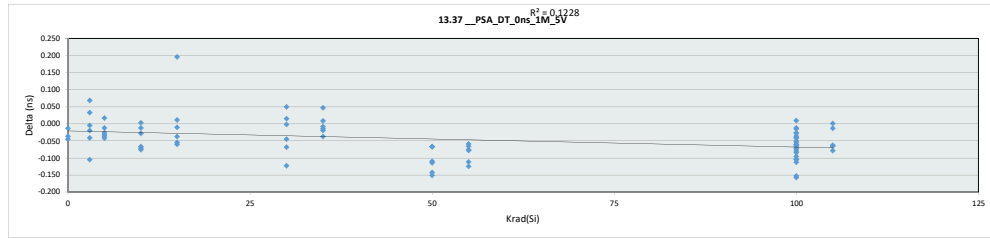


13.34_PSB_DT_Ons_1M_4V											
Test Site	Tester	Unit	Max Limit	Min Limit							
ns	ns	11	11	5							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	8.450	8.467	8.390	8.432	8.332	8.455	8.332	8.416	8.400	8.330	8.407
Average	8.505	8.509	8.474	8.507	8.482	8.539	8.488	8.500	8.504	8.422	8.474
Max	8.547	8.554	8.586	8.593	8.622	8.591	8.665	8.587	8.554	8.511	8.566
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

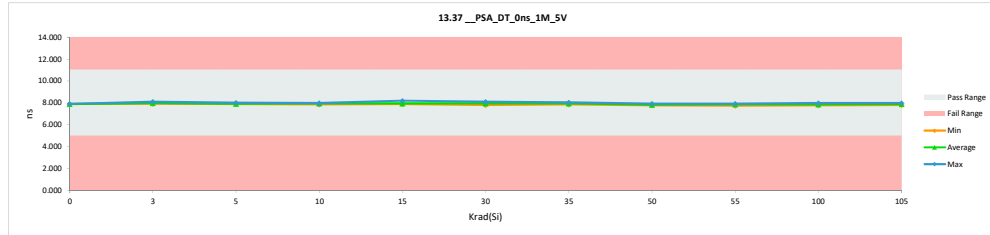


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13.37_PSA_DT_0ns_1M_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	7.941	7.897	-0.044
0	118	7.950	7.914	-0.036
0	122	7.927	7.915	-0.012
3	61	7.986	8.020	0.034
3	62	8.052	7.948	-0.104
3	63	7.991	7.951	-0.040
3	64	7.934	7.915	-0.019
3	65	8.047	8.116	0.069
3	66	7.983	7.979	-0.004
5	67	7.911	7.900	-0.011
5	68	7.959	7.934	-0.025
5	69	7.932	7.901	-0.031
5	70	8.005	7.964	-0.041
5	71	8.044	8.008	-0.036
5	72	7.890	7.908	0.018
10	73	7.986	7.990	0.004
10	74	8.052	7.986	-0.066
10	75	7.991	7.964	-0.027
10	76	7.934	7.864	-0.070
10	77	8.047	7.972	-0.075
10	78	7.983	7.972	-0.011
15	79	7.911	7.902	-0.009
15	80	7.959	7.905	-0.054
15	81	7.932	7.895	-0.037
15	82	8.005	8.202	0.197
15	83	8.044	7.985	-0.059
15	84	7.890	7.902	0.012
30	86	7.986	7.985	-0.001
30	89	8.052	7.984	-0.068
30	91	7.991	8.007	0.016
30	92	7.934	7.812	-0.122
30	94	8.047	8.098	0.051
30	95	7.983	7.939	-0.044
35	100	7.911	7.897	-0.014
35	101	7.959	7.940	-0.019
35	102	7.932	7.941	0.009
35	103	8.005	8.053	0.048
35	104	8.044	8.007	-0.037
35	105	7.890	7.882	-0.008
50	106	7.955	7.805	-0.150
50	107	8.036	7.894	-0.142
50	108	7.947	7.881	-0.066
50	109	7.886	7.777	-0.109
50	110	7.916	7.802	-0.114
50	111	7.996	7.929	-0.067
55	113	7.973	7.862	-0.111
55	114	7.961	7.896	-0.065
55	115	8.006	7.929	-0.077
55	116	7.958	7.900	-0.058
55	126	7.885	7.761	-0.124
55	128	7.966	7.891	-0.075
100	129	7.927	7.913	-0.014
100	131	7.989	7.885	-0.104
100	132	7.872	7.812	-0.060
100	133	7.954	7.859	-0.095
100	134	7.881	7.822	-0.059
100	135	7.935	7.851	-0.084
100	138	7.975	7.914	-0.061
100	139	7.944	7.832	-0.112
100	140	7.859	7.780	-0.079
100	141	8.034	7.877	-0.157
100	142	7.903	7.878	-0.025
100	143	7.984	7.931	-0.053
100	144	7.960	7.890	-0.070
100	220	7.934	7.869	-0.065
100	221	7.804	7.814	0.010
100	224	7.872	7.800	-0.072
100	225	7.893	7.790	-0.103
100	226	8.001	7.965	-0.036
100	228	7.936	7.925	-0.011
100	232	7.937	7.910	-0.027
100	233	7.895	7.854	-0.041
100	236	8.049	7.999	-0.050
100	238	7.958	7.806	-0.152
100	243	7.952	7.913	-0.039
105	244	7.932	7.870	-0.062
105	246	7.884	7.886	0.002
105	248	7.931	7.919	-0.012
105	374	8.046	7.981	-0.065
105	376	7.900	7.822	-0.078
Max		8.052	8.202	0.197
Average		7.958	7.913	-0.045
Min		7.804	7.761	-0.157
Std Dev		0.055	0.078	0.055

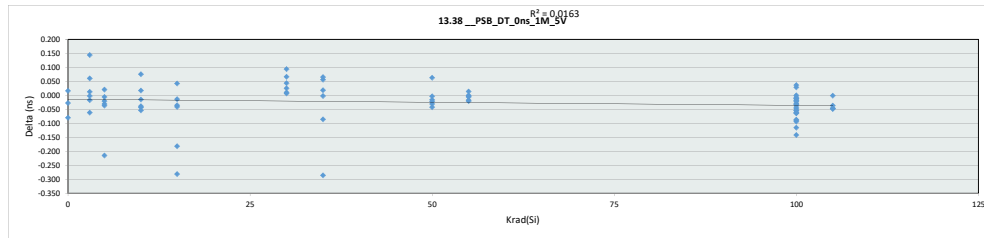


13.37_PSA_DT_0ns_1M_5V											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.897	7.915	7.900	7.864	7.895	7.812	7.882	7.777	7.761	7.780	7.822
Average	7.909	7.988	7.936	7.958	7.965	7.971	7.953	7.848	7.873	7.870	7.896
Max	7.915	8.116	8.008	7.990	8.202	8.098	8.053	7.929	7.929	7.999	7.981
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

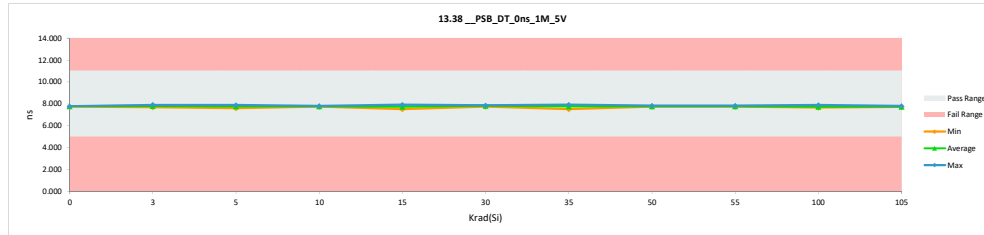


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13.38_PSB_DT_Ons_1M_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	7.839	7.760	-0.079
0	118	7.820	7.794	-0.026
0	122	7.756	7.773	0.017
3	61	7.805	7.745	-0.060
3	62	7.847	7.831	-0.016
3	63	7.816	7.830	0.014
3	64	7.708	7.707	-0.001
3	65	7.769	7.914	0.145
3	66	7.812	7.874	0.062
5	67	7.761	7.757	-0.004
5	68	7.896	7.861	-0.035
5	69	7.850	7.831	-0.019
5	70	7.810	7.596	-0.214
5	71	7.886	7.908	0.022
5	72	7.782	7.752	-0.030
10	73	7.805	7.767	-0.038
10	74	7.847	7.805	-0.042
10	75	7.816	7.802	-0.014
10	76	7.708	7.785	0.077
10	77	7.769	7.787	0.018
10	78	7.812	7.760	-0.052
15	79	7.761	7.721	-0.040
15	80	7.896	7.715	-0.181
15	81	7.850	7.836	-0.012
15	82	7.810	7.530	-0.280
15	83	7.886	7.930	0.044
15	84	7.782	7.748	-0.034
30	86	7.805	7.873	0.068
30	89	7.847	7.855	0.008
30	91	7.816	7.843	0.027
30	92	7.708	7.753	0.045
30	94	7.769	7.864	0.095
30	95	7.812	7.824	0.012
35	100	7.761	7.760	-0.001
35	101	7.896	7.812	-0.084
35	102	7.850	7.870	0.020
35	103	7.810	7.525	-0.285
35	104	7.886	7.943	0.057
35	105	7.782	7.848	0.066
50	106	7.697	7.761	0.064
50	107	7.851	7.831	-0.020
50	108	7.823	7.782	-0.041
50	109	7.792	7.777	-0.015
50	110	7.796	7.794	-0.002
50	111	7.843	7.813	-0.030
55	113	7.814	7.829	0.015
55	114	7.837	7.835	-0.002
55	115	7.811	7.812	0.001
55	116	7.753	7.749	-0.004
55	126	7.755	7.739	-0.016
55	128	7.792	7.773	-0.019
100	129	7.814	7.767	-0.047
100	131	7.794	7.824	0.030
100	132	7.768	7.716	-0.052
100	133	7.833	7.745	-0.088
100	134	7.762	7.702	-0.060
100	135	7.810	7.757	-0.053
100	138	7.825	7.807	-0.018
100	139	7.807	7.693	-0.114
100	140	7.708	7.678	-0.030
100	141	7.782	7.772	-0.010
100	142	7.776	7.766	-0.010
100	143	7.877	7.872	-0.005
100	144	7.820	7.804	-0.016
100	220	7.815	7.729	-0.086
100	221	7.745	7.682	-0.063
100	224	7.753	7.661	-0.092
100	225	7.847	7.885	0.038
100	226	7.854	7.813	-0.041
100	228	7.775	7.753	-0.022
100	232	7.764	7.702	-0.062
100	233	7.736	7.706	-0.030
100	236	7.918	7.777	-0.141
100	238	7.828	7.829	0.001
100	243	7.822	7.789	-0.033
105	244	7.797	7.750	-0.047
105	246	7.765	7.730	-0.035
105	248	7.853	7.806	-0.047
105	374	7.866	7.822	-0.044
105	376	7.722	7.722	0.000
Max		7.918	7.943	0.145
Average		7.806	7.781	-0.025
Min		7.697	7.525	-0.285
Std Dev		0.049	0.075	0.069

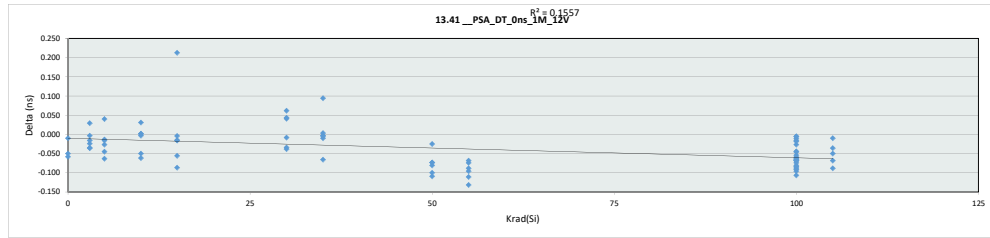


13.38_PSB_DT_Ons_1M_5V											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.760	7.707	7.596	7.760	7.530	7.753	7.525	7.761	7.739	7.661	7.722
Average	7.776	7.817	7.784	7.784	7.747	7.835	7.793	7.793	7.790	7.760	7.766
Max	7.794	7.914	7.908	7.805	7.930	7.873	7.943	7.831	7.835	7.885	7.822
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

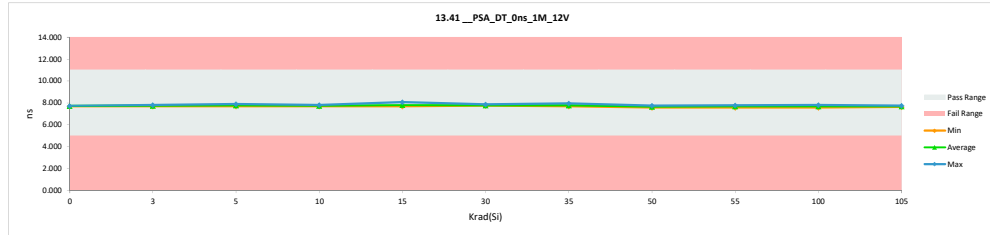


LDR TID Report
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13.41_PSA_DT_Ons_1M_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	7.784	7.735	-0.049
0	118	7.711	7.702	-0.009
0	122	7.774	7.717	-0.057
3	61	7.748	7.778	0.030
3	62	7.810	7.776	-0.034
3	63	7.818	7.803	-0.015
3	64	7.699	7.697	-0.002
3	65	7.770	7.735	-0.035
3	66	7.797	7.774	-0.023
5	67	7.746	7.684	-0.062
5	68	7.777	7.762	-0.015
5	69	7.776	7.764	-0.012
5	70	7.853	7.894	0.041
5	71	7.892	7.848	-0.044
5	72	7.710	7.684	-0.026
10	73	7.748	7.780	0.032
10	74	7.810	7.813	0.003
10	75	7.818	7.821	0.003
10	76	7.699	7.697	-0.002
10	77	7.770	7.709	-0.061
10	78	7.797	7.748	-0.049
15	79	7.746	7.732	-0.014
15	80	7.777	7.774	-0.003
15	81	7.776	7.721	-0.055
15	82	7.853	8.067	0.214
15	83	7.892	7.806	-0.086
15	84	7.710	7.695	-0.015
30	86	7.748	7.811	0.063
30	89	7.810	7.777	-0.033
30	91	7.818	7.860	0.042
30	92	7.699	7.743	0.044
30	94	7.770	7.763	-0.007
30	95	7.797	7.760	-0.037
35	100	7.746	7.737	-0.009
35	101	7.777	7.781	0.004
35	102	7.776	7.773	-0.003
35	103	7.853	7.948	0.095
35	104	7.892	7.827	-0.065
35	105	7.710	7.708	-0.002
50	106	7.677	7.653	-0.024
50	107	7.783	7.703	-0.080
50	108	7.736	7.664	-0.072
50	109	7.757	7.658	-0.099
50	110	7.700	7.592	-0.108
50	111	7.823	7.751	-0.072
55	113	7.760	7.665	-0.095
55	114	7.782	7.651	-0.131
55	115	7.852	7.785	-0.067
55	116	7.777	7.704	-0.073
55	126	7.691	7.581	-0.110
55	128	7.822	7.735	-0.087
100	129	7.726	7.642	-0.084
100	131	7.783	7.730	-0.053
100	132	7.716	7.649	-0.067
100	133	7.679	7.584	-0.095
100	134	7.692	7.634	-0.058
100	135	7.721	7.716	-0.005
100	138	7.789	7.728	-0.061
100	139	7.732	7.643	-0.089
100	140	7.670	7.655	-0.015
100	141	7.780	7.700	-0.080
100	142	7.712	7.702	-0.010
100	143	7.809	7.742	-0.067
100	144	7.766	7.676	-0.090
100	220	7.779	7.696	-0.083
100	221	7.741	7.635	-0.106
100	224	7.638	7.593	-0.045
100	225	7.668	7.642	-0.026
100	226	7.762	7.689	-0.073
100	228	7.717	7.713	-0.004
100	232	7.770	7.703	-0.067
100	233	7.709	7.692	-0.017
100	236	7.847	7.804	-0.043
100	238	7.780	7.716	-0.064
100	243	7.791	7.728	-0.063
105	244	7.721	7.686	-0.035
105	246	7.728	7.641	-0.087
105	248	7.750	7.701	-0.049
105	374	7.809	7.742	-0.067
105	376	7.683	7.674	-0.009
	Max	7.892	8.067	0.214
	Average	7.764	7.728	-0.036
	Min	7.638	7.581	-0.131
	Std Dev	0.055	0.079	0.052

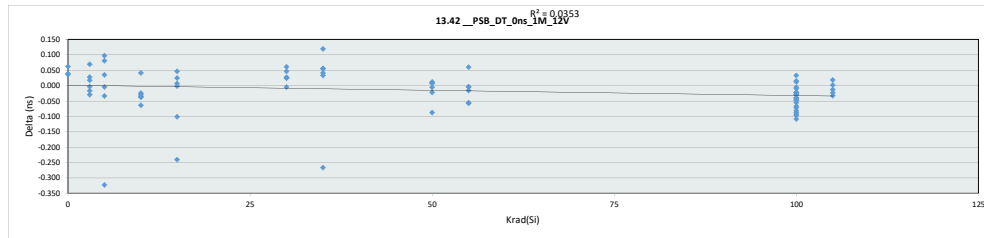


13.41_PSA_DT_Ons_1M_12V											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.702	7.697	7.684	7.697	7.695	7.743	7.708	7.592	7.581	7.584	7.641
Average	7.718	7.761	7.773	7.761	7.799	7.786	7.796	7.670	7.687	7.684	7.689
Max	7.735	7.803	7.894	7.821	8.067	7.860	7.948	7.751	7.785	7.804	7.742
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

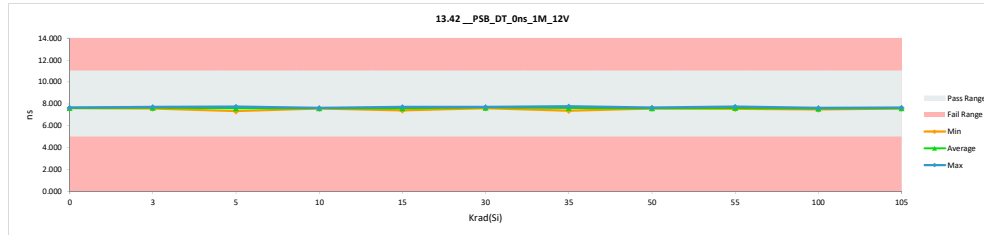


LDR TID Report
TPS7H5001-SP

13.42_PSB_DT_Ons_1M_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	7.605	7.642	0.037
0	118	7.548	7.611	0.063
0	122	7.636	7.676	0.040
3	61	7.570	7.640	0.070
3	62	7.699	7.717	0.018
3	63	7.679	7.676	-0.003
3	64	7.608	7.580	-0.028
3	65	7.656	7.685	0.029
3	66	7.659	7.643	-0.016
5	67	7.661	7.628	-0.033
5	68	7.555	7.653	0.098
5	69	7.697	7.693	-0.004
5	70	7.646	7.324	-0.322
5	71	7.668	7.750	0.082
5	72	7.588	7.624	0.036
10	73	7.570	7.612	0.042
10	74	7.699	7.636	-0.063
10	75	7.679	7.645	-0.034
10	76	7.608	7.585	-0.023
10	77	7.656	7.619	-0.037
10	78	7.659	7.631	-0.028
15	79	7.661	7.561	-0.100
15	80	7.555	7.563	0.008
15	81	7.697	7.697	0.000
15	82	7.646	7.406	-0.240
15	83	7.668	7.715	0.047
15	84	7.588	7.614	0.026
30	86	7.570	7.632	0.062
30	89	7.699	7.726	0.027
30	91	7.679	7.705	0.026
30	92	7.608	7.604	-0.004
30	94	7.656	7.703	0.047
30	95	7.659	7.687	0.028
35	100	7.661	7.718	0.057
35	101	7.555	7.611	0.056
35	102	7.697	7.739	0.042
35	103	7.646	7.380	-0.266
35	104	7.668	7.788	0.120
35	105	7.588	7.622	0.034
50	106	7.553	7.565	0.012
50	107	7.618	7.630	0.012
50	108	7.574	7.582	0.008
50	109	7.666	7.579	-0.087
50	110	7.621	7.600	-0.021
50	111	7.665	7.660	-0.005
55	113	7.605	7.551	-0.054
55	114	7.692	7.753	0.061
55	115	7.719	7.704	-0.015
55	116	7.602	7.600	-0.002
55	126	7.602	7.545	-0.057
55	128	7.648	7.644	-0.004
100	129	7.642	7.546	-0.096
100	131	7.615	7.576	-0.039
100	132	7.588	7.549	-0.039
100	133	7.547	7.538	-0.009
100	134	7.582	7.554	-0.028
100	135	7.654	7.588	-0.066
100	138	7.626	7.539	-0.087
100	139	7.526	7.539	0.013
100	140	7.582	7.553	-0.029
100	141	7.642	7.596	-0.046
100	142	7.707	7.626	-0.081
100	143	7.732	7.640	-0.092
100	144	7.656	7.602	-0.054
100	220	7.601	7.580	-0.021
100	221	7.623	7.583	-0.040
100	224	7.563	7.492	-0.071
100	225	7.576	7.555	-0.021
100	226	7.671	7.563	-0.108
100	228	7.589	7.584	-0.005
100	232	7.531	7.565	0.034
100	233	7.575	7.591	0.016
100	236	7.658	7.636	-0.022
100	238	7.650	7.624	-0.026
100	243	7.625	7.579	-0.046
105	244	7.598	7.601	0.003
105	246	7.595	7.573	-0.022
105	248	7.667	7.655	-0.012
105	374	7.627	7.646	0.019
105	376	7.600	7.568	-0.032
Max		7.732	7.788	0.120
Average		7.628	7.612	-0.016
Min		7.526	7.324	-0.322
Std Dev		0.049	0.077	0.070

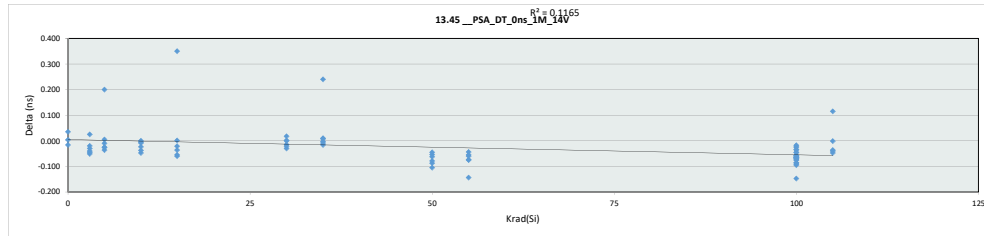


13.42_PSB_DT_Ons_1M_12V											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.611	7.580	7.324	7.585	7.406	7.604	7.380	7.565	7.545	7.492	7.568
Average	7.643	7.657	7.612	7.621	7.593	7.676	7.643	7.603	7.633	7.575	7.609
Max	7.676	7.717	7.750	7.645	7.715	7.726	7.788	7.660	7.753	7.640	7.655
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

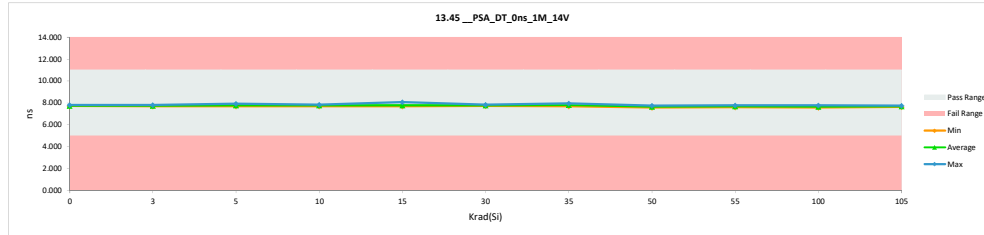


LDR TID Report
TPS7H5001-SP

13.45_PSA_DT_Ons_1M_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	7.758	7.743	-0.015
0	118	7.714	7.719	0.005
0	122	7.762	7.798	0.036
3	61	7.782	7.763	-0.019
3	62	7.861	7.822	-0.039
3	63	7.823	7.794	-0.029
3	64	7.752	7.702	-0.050
3	65	7.762	7.718	-0.044
3	66	7.772	7.799	0.027
5	67	7.743	7.735	-0.008
5	68	7.759	7.765	0.006
5	69	7.767	7.732	-0.035
5	70	7.729	7.931	0.202
5	71	7.857	7.833	-0.024
5	72	7.706	7.681	-0.025
10	73	7.782	7.746	-0.036
10	74	7.861	7.839	-0.022
10	75	7.823	7.824	0.001
10	76	7.752	7.706	-0.046
10	77	7.762	7.755	-0.007
10	78	7.772	7.768	-0.004
15	79	7.743	7.685	-0.058
15	80	7.759	7.739	-0.020
15	81	7.767	7.732	-0.035
15	82	7.729	7.801	0.352
15	83	7.857	7.804	-0.053
15	84	7.706	7.709	0.003
30	86	7.782	7.785	0.003
30	89	7.861	7.847	-0.014
30	91	7.823	7.824	0.001
30	92	7.752	7.723	-0.029
30	94	7.762	7.742	-0.020
30	95	7.772	7.791	0.019
35	100	7.743	7.736	-0.007
35	101	7.759	7.768	0.009
35	102	7.767	7.766	-0.001
35	103	7.729	7.971	0.242
35	104	7.857	7.869	0.012
35	105	7.706	7.691	-0.015
50	106	7.686	7.634	-0.052
50	107	7.758	7.681	-0.077
50	108	7.758	7.654	-0.104
50	109	7.734	7.691	-0.043
50	110	7.670	7.584	-0.086
50	111	7.804	7.743	-0.061
55	113	7.743	7.669	-0.074
55	114	7.832	7.690	-0.142
55	115	7.844	7.785	-0.059
55	116	7.777	7.705	-0.072
55	126	7.665	7.612	-0.053
55	128	7.785	7.743	-0.042
100	129	7.754	7.669	-0.085
100	131	7.784	7.699	-0.085
100	132	7.721	7.659	-0.062
100	133	7.662	7.589	-0.073
100	134	7.711	7.638	-0.073
100	135	7.738	7.693	-0.045
100	138	7.801	7.708	-0.093
100	139	7.704	7.671	-0.033
100	140	7.650	7.583	-0.067
100	141	7.786	7.696	-0.090
100	142	7.677	7.657	-0.020
100	143	7.802	7.745	-0.057
100	144	7.702	7.669	-0.033
100	220	7.772	7.707	-0.065
100	221	7.715	7.659	-0.057
100	224	7.685	7.621	-0.064
100	225	7.665	7.600	-0.065
100	226	7.819	7.673	-0.146
100	228	7.746	7.721	-0.025
100	232	7.788	7.719	-0.069
100	233	7.690	7.630	-0.060
100	236	7.852	7.796	-0.056
100	238	7.743	7.727	-0.016
100	243	7.789	7.746	-0.043
105	244	7.728	7.690	-0.038
105	246	7.596	7.713	0.117
105	248	7.711	7.711	0.000
105	374	7.791	7.745	-0.046
105	376	7.685	7.650	-0.035
Max		7.861	8.081	0.352
Average		7.756	7.730	-0.026
Min		7.596	7.583	-0.146
Std Dev		0.055	0.083	0.071

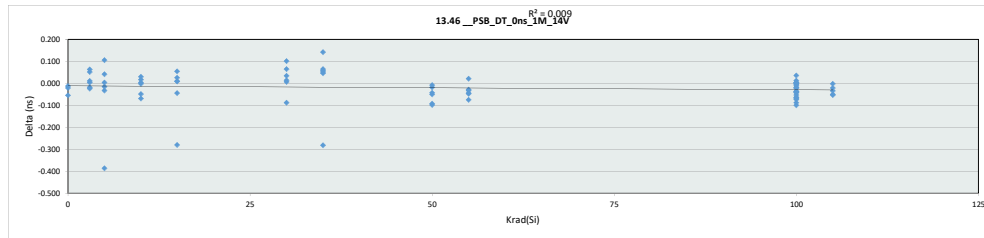


13.45_PSA_DT_Ons_1M_14V											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.719	7.702	7.681	7.706	7.685	7.723	7.691	7.584	7.612	7.583	7.650
Average	7.753	7.766	7.780	7.773	7.792	7.785	7.800	7.665	7.701	7.678	7.702
Max	7.798	7.822	7.931	7.839	8.081	7.847	7.971	7.743	7.785	7.796	7.745
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

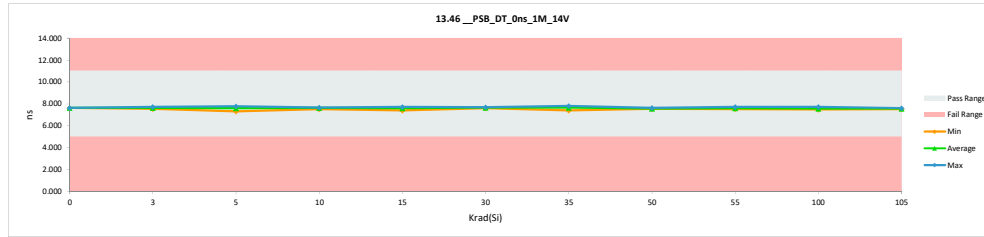


LDR TID Report
TPS7H5001-SP

13.46_PSB_DT_Ons_1M_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	7.644	7.624	-0.020
0	118	7.638	7.627	-0.011
0	122	7.694	7.641	-0.053
3	61	7.582	7.588	0.006
3	62	7.679	7.732	0.053
3	63	7.660	7.725	0.065
3	64	7.572	7.550	-0.022
3	65	7.622	7.635	0.013
3	66	7.684	7.667	-0.017
5	67	7.535	7.642	0.107
5	68	7.601	7.570	-0.031
5	69	7.665	7.652	-0.013
5	70	7.693	7.308	-0.385
5	71	7.765	7.771	0.006
5	72	7.613	7.657	0.044
10	73	7.582	7.600	0.018
10	74	7.679	7.679	0.000
10	75	7.660	7.665	0.005
10	76	7.572	7.525	-0.047
10	77	7.622	7.653	0.031
10	78	7.684	7.617	-0.067
15	79	7.535	7.591	0.056
15	80	7.601	7.612	0.011
15	81	7.665	7.692	0.027
15	82	7.693	7.414	-0.279
15	83	7.765	7.722	-0.043
15	84	7.613	7.624	0.011
30	86	7.582	7.685	0.103
30	89	7.679	7.687	0.008
30	91	7.660	7.696	0.036
30	92	7.572	7.639	0.067
30	94	7.622	7.637	0.015
30	95	7.684	7.597	-0.087
35	100	7.535	7.679	0.144
35	101	7.601	7.654	0.053
35	102	7.665	7.732	0.067
35	103	7.693	7.413	-0.280
35	104	7.765	7.813	0.048
35	105	7.613	7.672	0.059
50	106	7.577	7.536	-0.041
50	107	7.633	7.627	-0.006
50	108	7.632	7.542	-0.090
50	109	7.581	7.565	-0.016
50	110	7.639	7.591	-0.048
50	111	7.705	7.609	-0.096
55	113	7.626	7.553	-0.073
55	114	7.676	7.650	-0.026
55	115	7.751	7.720	-0.031
55	116	7.629	7.583	-0.046
55	126	7.511	7.534	0.023
55	128	7.660	7.618	-0.042
100	129	7.605	7.533	-0.072
100	131	7.603	7.609	0.006
100	132	7.556	7.557	0.001
100	133	7.576	7.478	-0.098
100	134	7.555	7.521	-0.034
100	135	7.648	7.586	-0.062
100	138	7.626	7.589	-0.037
100	139	7.582	7.545	-0.037
100	140	7.578	7.561	-0.017
100	141	7.658	7.591	-0.067
100	142	7.617	7.596	-0.021
100	143	7.731	7.714	-0.017
100	144	7.632	7.636	0.004
100	220	7.657	7.606	-0.051
100	221	7.577	7.572	-0.005
100	224	7.520	7.514	-0.006
100	225	7.624	7.638	0.014
100	226	7.675	7.589	-0.086
100	228	7.650	7.587	-0.063
100	232	7.575	7.613	0.038
100	233	7.529	7.534	0.005
100	236	7.653	7.615	-0.038
100	238	7.686	7.688	0.002
100	243	7.641	7.600	-0.041
105	244	7.563	7.563	0.000
105	246	7.615	7.567	-0.048
105	248	7.639	7.619	-0.020
105	374	7.620	7.569	-0.051
105	376	7.540	7.507	-0.033
	Max	7.765	7.813	0.144
	Average	7.629	7.609	-0.020
	Min	7.511	7.308	-0.385
	Std Dev	0.058	0.079	0.076

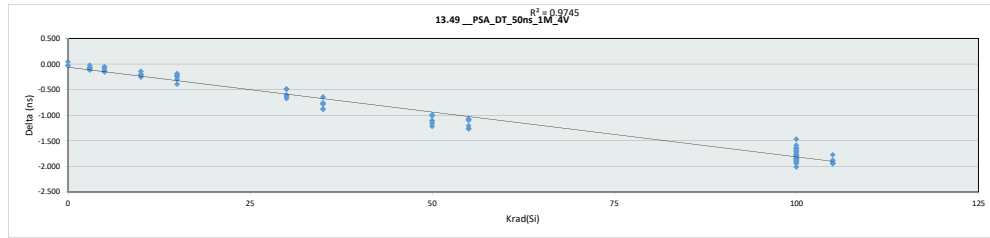


13.46_PSB_DT_Ons_1M_14V										
Test Site										
Tester										
Test Number										
Max Limit	11	ns								
Min Limit	5	ns								
Krad(Si)	0	3	5	10	15	30	35	50	55	100
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.624	7.550	7.308	7.525	7.414	7.597	7.413	7.536	7.534	7.478
Average	7.631	7.650	7.600	7.623	7.609	7.657	7.661	7.578	7.610	7.586
Max	7.641	7.732	7.771	7.679	7.722	7.696	7.813	7.627	7.720	7.714
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

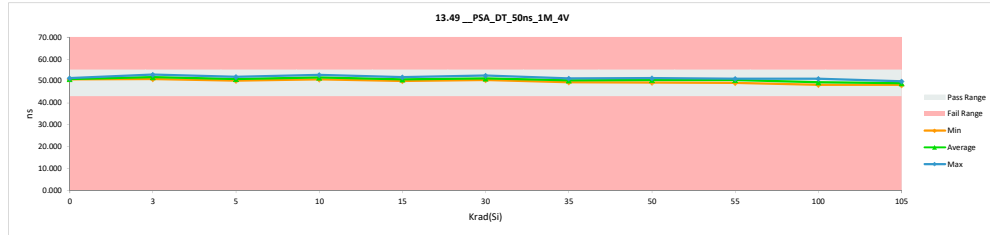


LDR TID Report
TPS7H5001-SP

13.49_PSA_DT_50ns_1M_4V				
Test Site	Tester	Unit	Max Limit	Min Limit
ns	ns	ns	55	43
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	50.682	50.737	0.055
0	118	51.338	51.312	-0.026
0	122	50.868	50.843	-0.025
3	61	51.313	51.254	-0.059
3	62	52.980	52.963	-0.017
3	63	51.004	50.939	-0.065
3	64	51.966	51.858	-0.108
3	65	51.799	51.723	-0.076
3	66	51.748	51.664	-0.084
5	67	51.189	51.138	-0.051
5	68	50.470	50.421	-0.049
5	69	50.363	50.236	-0.127
5	70	50.224	50.153	-0.071
5	71	52.012	51.898	-0.114
5	72	51.963	51.818	-0.145
10	73	51.313	51.114	-0.199
10	74	52.980	52.842	-0.138
10	75	51.004	50.753	-0.251
10	76	51.966	51.773	-0.193
10	77	51.799	51.601	-0.198
10	78	51.748	51.541	-0.207
15	79	51.189	50.978	-0.211
15	80	50.470	50.290	-0.180
15	81	50.363	49.980	-0.383
15	82	50.224	49.983	-0.241
15	83	52.012	51.780	-0.232
15	84	51.963	51.674	-0.289
30	86	51.313	50.835	-0.478
30	89	52.980	52.495	-0.485
30	91	51.004	50.394	-0.610
30	92	51.966	51.337	-0.629
30	94	51.799	51.201	-0.598
30	95	51.748	51.083	-0.665
35	100	51.189	50.437	-0.752
35	101	50.470	49.835	-0.635
35	102	50.363	49.489	-0.874
35	103	50.224	49.465	-0.759
35	104	52.012	51.231	-0.781
35	105	51.963	51.088	-0.875
50	106	52.437	51.332	-1.105
50	107	51.664	50.657	-1.007
50	108	50.287	49.300	-0.987
50	109	51.196	50.095	-1.101
50	110	51.858	50.702	-1.156
50	111	52.120	50.911	-1.209
55	113	52.097	50.898	-1.199
55	114	50.146	49.076	-1.070
55	115	52.306	51.054	-1.252
55	116	52.143	51.069	-1.074
55	126	51.106	50.014	-1.092
55	128	51.616	50.361	-1.255
100	129	51.738	49.985	-1.753
100	131	50.532	48.528	-2.004
100	132	51.183	49.288	-1.895
100	133	50.449	48.584	-1.865
100	134	52.003	50.067	-1.936
100	135	51.405	49.610	-1.795
100	138	51.545	49.839	-1.706
100	139	50.075	48.380	-1.695
100	140	50.244	48.463	-1.781
100	141	51.904	50.027	-1.877
100	142	50.236	48.469	-1.767
100	143	50.123	48.302	-1.821
100	144	51.652	49.979	-1.673
100	220	51.164	49.321	-1.843
100	221	51.700	50.119	-1.581
100	224	50.379	48.754	-1.625
100	225	51.880	50.033	-1.847
100	226	52.909	50.999	-1.910
100	228	51.889	50.038	-1.851
100	232	49.875	48.242	-1.633
100	233	51.575	50.115	-1.460
100	236	50.242	48.599	-1.643
100	238	51.481	49.716	-1.765
100	243	51.984	50.274	-1.710
105	244	50.456	48.542	-1.914
105	246	50.126	48.192	-1.934
105	248	51.269	49.393	-1.876
105	374	50.526	48.761	-1.765
105	376	51.850	49.915	-1.935
Max		52.980	52.963	0.055
Average		51.317	50.352	-0.965
Min		49.875	48.192	-2.004
Std Dev		0.788	1.128	0.716

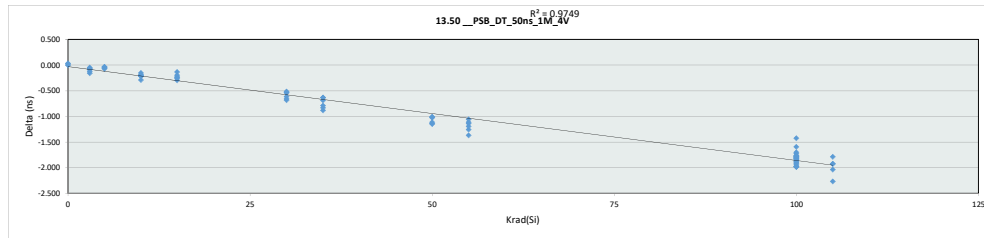


13.49_PSA_DT_50ns_1M_4V											
Test Site	Tester	Unit	Max Limit	Min Limit							
ns	ns	ns	55	43							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	50.737	50.939	50.153	50.753	49.980	50.394	49.465	49.300	49.076	48.242	48.192
Average	50.964	51.734	50.944	51.604	50.781	51.224	50.258	50.500	50.412	49.405	48.961
Max	51.312	52.963	51.898	52.842	51.780	52.495	51.231	51.332	51.069	50.999	49.915
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

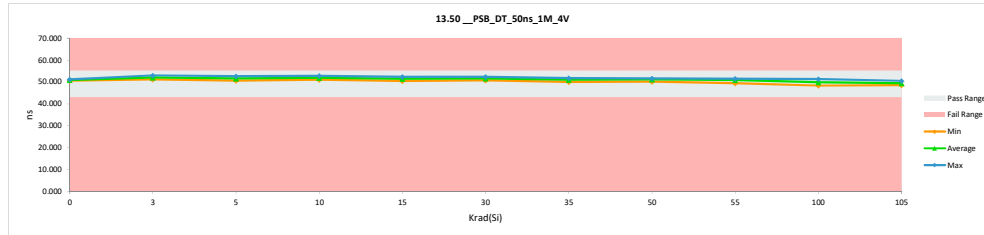


LDR TID Report
TPS7H5001-SP

13.50_PSB_DT_50ns_1M_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	55	55		
Min Limit	43	43		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	50.628	50.656	0.028
0	118	51.256	51.256	0.000
0	122	50.728	50.768	0.040
3	61	51.647	51.589	-0.058
3	62	53.075	53.003	-0.072
3	63	51.284	51.199	-0.085
3	64	52.284	52.131	-0.153
3	65	52.463	52.421	-0.042
3	66	52.288	52.164	-0.124
5	67	51.880	51.831	-0.049
5	68	50.633	50.604	-0.029
5	69	51.192	51.125	-0.067
5	70	52.221	52.170	-0.051
5	71	52.637	52.590	-0.047
5	72	51.764	51.734	-0.030
10	73	51.647	51.450	-0.197
10	74	53.075	52.866	-0.209
10	75	51.284	51.073	-0.211
10	76	52.284	51.997	-0.287
10	77	52.463	52.315	-0.148
10	78	52.288	52.108	-0.180
15	79	51.880	51.687	-0.193
15	80	50.633	50.507	-0.126
15	81	51.192	50.902	-0.290
15	82	52.221	51.994	-0.227
15	83	52.637	52.386	-0.251
15	84	51.764	51.537	-0.227
30	86	51.647	51.140	-0.507
30	89	53.075	52.396	-0.679
30	91	51.284	50.753	-0.531
30	92	52.284	51.633	-0.651
30	94	52.463	51.934	-0.529
30	95	52.288	51.680	-0.608
35	100	51.880	51.252	-0.628
35	101	50.633	50.008	-0.625
35	102	51.192	50.410	-0.782
35	103	52.221	51.392	-0.829
35	104	52.637	51.763	-0.874
35	105	51.764	51.079	-0.685
50	106	52.022	51.012	-1.010
50	107	52.647	51.646	-1.001
50	108	51.318	50.181	-1.137
50	109	52.175	51.034	-1.141
50	110	52.443	51.424	-1.019
50	111	52.590	51.480	-1.110
55	113	52.544	51.356	-1.188
55	114	50.728	49.476	-1.252
55	115	52.532	51.399	-1.133
55	116	52.621	51.515	-1.106
55	126	51.910	50.859	-1.051
55	128	52.279	50.914	-1.365
100	129	53.151	51.279	-1.872
100	131	50.422	48.654	-1.768
100	132	51.016	49.223	-1.793
100	133	50.277	48.342	-1.935
100	134	52.010	50.196	-1.814
100	135	51.862	50.170	-1.692
100	138	52.042	50.258	-1.784
100	139	51.173	49.207	-1.966
100	140	50.668	48.842	-1.826
100	141	52.072	50.089	-1.983
100	142	51.367	49.650	-1.717
100	143	51.140	49.234	-1.906
100	144	52.937	51.124	-1.813
100	220	51.855	50.056	-1.799
100	221	51.531	50.113	-1.418
100	224	51.111	49.271	-1.840
100	225	52.374	50.398	-1.976
100	226	52.441	50.681	-1.760
100	228	52.793	51.013	-1.780
100	232	51.641	49.778	-1.863
100	233	52.035	50.445	-1.590
100	236	51.219	49.252	-1.967
100	238	51.970	50.211	-1.759
100	243	52.438	50.537	-1.901
105	244	50.537	48.621	-1.916
105	246	50.665	48.746	-1.919
105	248	52.549	50.520	-2.029
105	374	50.976	49.194	-1.782
105	376	52.589	50.326	-2.263
	Max	53.151	53.003	0.040
	Average	51.842	50.865	-0.977
	Min	50.277	48.342	-2.263
	Std Dev	0.735	1.078	0.747

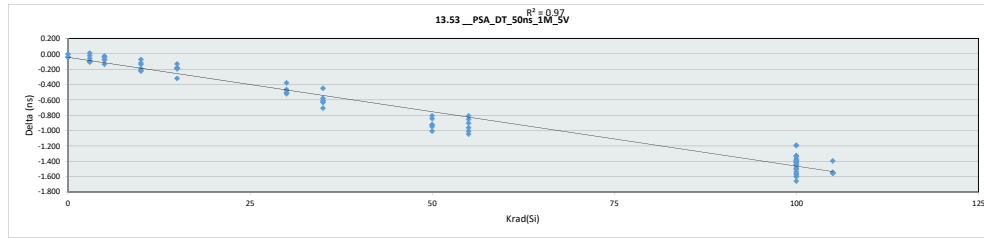


13.50_PSB_DT_50ns_1M_4V											
Test Site											
Tester											
Test Number											
Max Limit	55	ns									
Min Limit	43	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	50.656	51.199	50.604	51.073	50.507	50.753	50.008	50.181	49.476	48.342	48.621
Average	50.893	52.085	51.676	51.968	51.502	51.589	50.984	51.130	50.920	49.918	49.481
Max	51.256	53.003	52.590	52.866	52.386	52.396	51.763	51.646	51.515	51.279	50.520
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

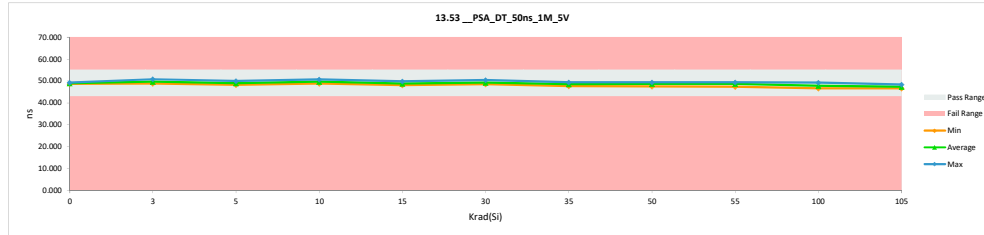


LDR TID Report
TPS7H5001-SP

13.53_PSA_DT_50ns_1M_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	55	55		
Min Limit	43	43		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	48.763	48.724	-0.039
0	118	49.284	49.284	0.000
0	122	48.872	48.836	-0.036
3	61	49.443	49.424	-0.019
3	62	50.945	50.842	-0.103
3	63	48.950	48.891	-0.059
3	64	50.024	49.929	-0.095
3	65	49.871	49.778	-0.093
3	66	49.743	49.757	0.014
5	67	49.259	49.186	-0.073
5	68	48.543	48.500	-0.043
5	69	48.434	48.300	-0.134
5	70	48.232	48.204	-0.028
5	71	50.067	49.994	-0.073
5	72	49.989	49.961	-0.028
10	73	49.443	49.374	-0.069
10	74	50.945	50.726	-0.219
10	75	48.950	48.817	-0.133
10	76	50.024	49.810	-0.214
10	77	49.871	49.681	-0.190
10	78	49.743	49.627	-0.116
15	79	49.259	49.078	-0.181
15	80	48.543	48.358	-0.185
15	81	48.434	48.120	-0.314
15	82	48.232	48.103	-0.129
15	83	50.067	49.888	-0.179
15	84	49.989	49.799	-0.190
30	86	49.443	49.070	-0.373
30	89	50.945	50.431	-0.514
30	91	48.950	48.489	-0.461
30	92	50.024	49.553	-0.471
30	94	49.871	49.366	-0.505
30	95	49.743	49.241	-0.502
35	100	49.259	48.634	-0.625
35	101	48.543	48.097	-0.446
35	102	48.434	47.731	-0.703
35	103	48.232	47.659	-0.573
35	104	50.067	49.437	-0.630
35	105	49.989	49.384	-0.605
50	106	50.389	49.473	-0.916
50	107	49.696	48.891	-0.805
50	108	48.355	47.515	-0.840
50	109	49.291	48.347	-0.944
50	110	49.935	49.011	-0.924
50	111	50.165	49.161	-1.004
55	113	50.175	49.218	-0.957
55	114	48.263	47.412	-0.851
55	115	50.220	49.218	-1.002
55	116	50.225	49.421	-0.804
55	125	49.167	48.268	-0.899
55	128	49.720	48.679	-1.041
100	129	49.798	48.433	-1.365
100	131	48.555	46.986	-1.569
100	132	49.133	47.644	-1.489
100	133	48.490	46.988	-1.502
100	134	50.073	48.483	-1.590
100	135	49.518	48.070	-1.448
100	138	49.690	48.286	-1.404
100	139	48.114	46.710	-1.404
100	140	48.309	46.899	-1.410
100	141	50.030	48.378	-1.652
100	142	48.311	46.927	-1.384
100	143	48.209	46.726	-1.483
100	144	49.693	48.361	-1.332
100	220	49.301	47.769	-1.532
100	221	49.776	48.588	-1.188
100	224	48.483	47.153	-1.330
100	225	49.930	48.522	-1.408
100	226	50.892	49.334	-1.558
100	228	50.023	48.427	-1.596
100	232	47.972	46.651	-1.321
100	233	49.639	48.447	-1.192
100	236	48.348	46.961	-1.387
100	238	49.646	48.207	-1.439
100	243	50.112	48.681	-1.431
105	244	48.519	46.978	-1.541
105	246	48.229	46.680	-1.549
105	248	49.386	47.844	-1.542
105	374	48.517	47.127	-1.390
105	376	49.925	48.377	-1.548
Max	50.945	50.842	0.014	
Average	49.371	48.592	-0.779	
Min	47.972	46.651	-1.652	
Std Dev	0.777	1.011	0.582	

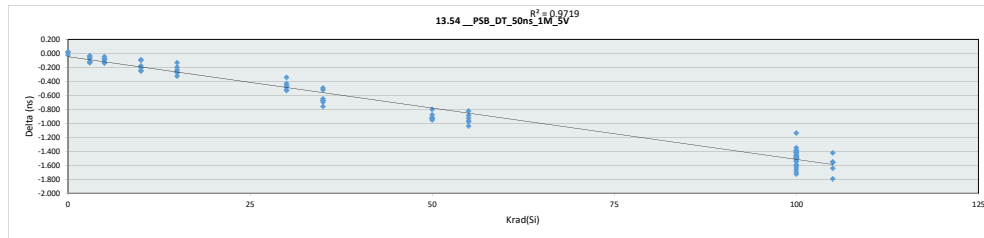


13.53_PSA_DT_50ns_1M_5V											
Test Site											
Tester											
Test Number											
Max Limit	55	ns									
Min Limit	43	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	48.724	48.891	48.204	48.817	48.103	48.489	47.659	47.515	47.412	46.651	46.680
Average	48.948	49.770	49.024	49.673	48.891	49.358	48.490	48.733	48.703	47.818	47.401
Max	49.284	50.842	49.994	50.726	49.888	50.431	49.437	49.421	49.334	48.377	
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

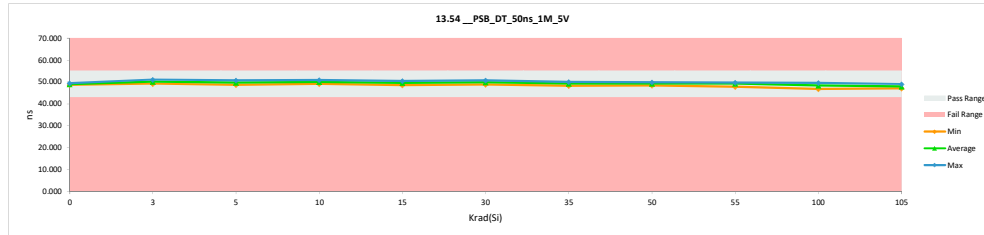


LDR TID Report
TPS7H5001-SP

13.54_PSB_DT_50ns_1M_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	55	55		
Min Limit	43	43		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	48.771	48.793	0.022
0	118	49.398	49.379	-0.019
0	122	48.749	48.775	0.026
3	61	49.761	49.717	-0.044
3	62	51.217	51.099	-0.118
3	63	49.341	49.213	-0.128
3	64	50.294	50.178	-0.116
3	65	50.620	50.555	-0.065
3	66	50.340	50.314	-0.026
5	67	49.969	49.881	-0.088
5	68	48.788	48.746	-0.042
5	69	49.429	49.303	-0.126
5	70	50.177	50.102	-0.075
5	71	50.771	50.696	-0.075
5	72	49.991	49.860	-0.131
10	73	49.761	49.675	-0.086
10	74	51.217	50.971	-0.246
10	75	49.341	49.138	-0.203
10	76	50.294	50.054	-0.240
10	77	50.620	50.529	-0.091
10	78	50.340	50.166	-0.174
15	79	49.969	49.842	-0.127
15	80	48.788	48.596	-0.192
15	81	49.429	49.111	-0.318
15	82	50.177	49.946	-0.231
15	83	50.771	50.501	-0.270
15	84	49.991	49.726	-0.265
30	86	49.761	49.423	-0.338
30	89	51.217	50.689	-0.528
30	91	49.341	48.917	-0.424
30	92	50.294	49.782	-0.512
30	94	50.620	50.169	-0.451
30	95	50.340	49.873	-0.467
35	100	49.969	49.478	-0.491
35	101	48.788	48.271	-0.517
35	102	49.429	48.740	-0.689
35	103	50.177	49.510	-0.667
35	104	50.771	50.016	-0.755
35	105	49.991	49.348	-0.643
50	106	50.139	49.265	-0.874
50	107	50.648	49.854	-0.794
50	108	49.378	48.430	-0.948
50	109	50.190	49.270	-0.920
50	110	50.550	49.634	-0.916
50	111	50.634	49.705	-0.929
55	113	50.686	49.760	-0.926
55	114	48.845	47.879	-0.966
55	115	50.586	49.705	-0.881
55	116	50.753	49.782	-0.971
55	126	49.956	49.135	-0.821
55	128	50.436	49.400	-1.036
100	129	51.242	49.592	-1.650
100	131	48.496	47.116	-1.380
100	132	49.100	47.634	-1.466
100	133	48.553	46.832	-1.721
100	134	50.175	48.638	-1.537
100	135	49.939	48.596	-1.343
100	138	50.252	48.761	-1.491
100	139	49.215	47.628	-1.587
100	140	48.624	47.173	-1.451
100	141	50.193	48.504	-1.689
100	142	49.288	47.871	-1.417
100	143	49.168	47.631	-1.537
100	144	51.030	49.575	-1.455
100	220	50.043	48.577	-1.466
100	221	49.659	48.523	-1.136
100	224	49.228	47.748	-1.480
100	225	50.391	48.773	-1.618
100	226	50.538	49.118	-1.420
100	228	50.902	49.494	-1.408
100	232	49.672	48.178	-1.494
100	233	50.217	48.829	-1.388
100	236	49.192	47.700	-1.492
100	238	50.231	48.736	-1.495
100	243	50.610	49.017	-1.593
105	244	48.678	47.124	-1.554
105	246	48.891	47.345	-1.546
105	248	50.702	49.062	-1.640
105	374	49.113	47.693	-1.420
105	376	50.664	48.876	-1.788
Max	51.242	51.099	0.026	
Average	49.948	49.141	-0.807	
Min	48.496	46.832	-1.788	
Std Dev	0.730	0.970	0.601	

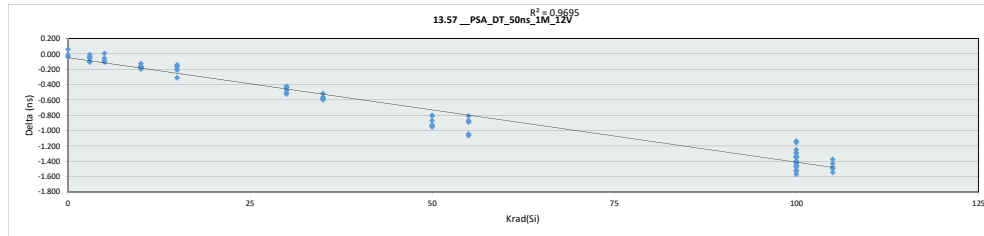


13.54_PSB_DT_50ns_1M_5V											
Test Site											
Tester											
Test Number											
Max Limit	55	ns									
Min Limit	43	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	48.775	49.213	48.746	49.138	48.596	48.917	48.271	48.430	47.879	46.832	47.124
Average	48.982	50.179	49.765	50.089	49.620	49.809	49.227	49.360	49.277	48.344	48.020
Max	49.379	51.099	50.696	50.971	50.501	50.689	50.016	49.854	49.782	49.592	49.062
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

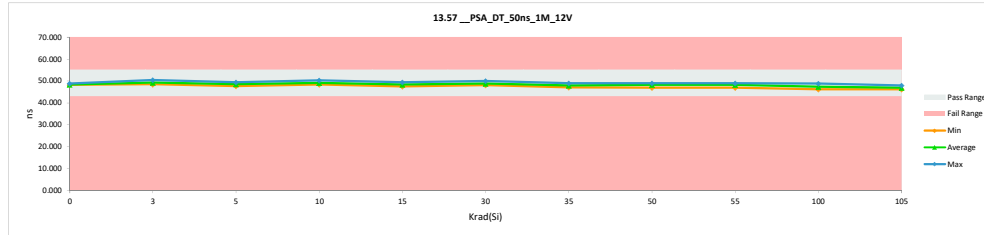


LDR TID Report
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13.57_PSA_DT_50ns_1M_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	55	55		
Min Limit	43	43		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	48.273	48.261	-0.012
0	118	48.721	48.784	0.063
0	122	48.415	48.377	-0.038
3	61	48.966	48.935	-0.031
3	62	50.477	50.425	-0.052
3	63	48.527	48.480	-0.047
3	64	49.510	49.412	-0.098
3	65	49.314	49.212	-0.102
3	66	49.286	49.279	-0.007
5	67	48.809	48.726	-0.083
5	68	48.146	48.093	-0.053
5	69	47.914	47.828	-0.086
5	70	47.744	47.755	0.011
5	71	49.572	49.479	-0.093
5	72	49.506	49.405	-0.101
10	73	48.966	48.809	-0.157
10	74	50.477	50.314	-0.163
10	75	48.527	48.350	-0.177
10	76	49.510	49.314	-0.196
10	77	49.314	49.139	-0.175
10	78	49.286	49.161	-0.125
15	79	48.809	48.605	-0.204
15	80	48.146	47.945	-0.201
15	81	47.914	47.609	-0.305
15	82	47.744	47.593	-0.151
15	83	49.572	49.431	-0.141
15	84	49.506	49.346	-0.160
30	86	48.966	48.547	-0.419
30	89	50.477	50.041	-0.436
30	91	48.527	48.098	-0.429
30	92	49.510	48.992	-0.518
30	94	49.314	48.806	-0.508
30	95	49.286	48.811	-0.475
35	100	48.809	48.249	-0.560
35	101	48.146	47.635	-0.511
35	102	47.914	47.343	-0.571
35	103	47.744	47.157	-0.587
35	104	49.572	48.981	-0.591
35	105	49.506	48.913	-0.593
50	106	49.884	49.018	-0.866
50	107	49.194	48.391	-0.803
50	108	47.820	47.022	-0.798
50	109	48.911	47.960	-0.951
50	110	49.352	48.423	-0.929
50	111	49.667	48.745	-0.922
55	113	49.577	48.691	-0.886
55	114	47.864	46.997	-0.867
55	115	49.865	48.803	-1.062
55	116	49.793	48.904	-0.889
55	126	48.695	47.815	-0.880
55	128	49.323	48.281	-1.042
100	129	49.261	47.924	-1.337
100	131	48.160	46.595	-1.565
100	132	48.702	47.188	-1.514
100	133	47.939	46.488	-1.451
100	134	49.528	47.998	-1.530
100	135	49.087	47.666	-1.421
100	138	49.230	47.889	-1.341
100	139	47.602	46.261	-1.341
100	140	47.844	46.447	-1.397
100	141	49.451	47.987	-1.464
100	142	47.823	46.494	-1.329
100	143	47.753	46.305	-1.448
100	144	49.214	47.923	-1.291
100	220	48.804	47.389	-1.415
100	221	49.342	48.188	-1.154
100	224	48.013	46.683	-1.330
100	225	49.348	48.008	-1.340
100	226	50.336	48.824	-1.512
100	228	49.438	47.963	-1.475
100	232	47.494	46.213	-1.281
100	233	49.072	47.938	-1.134
100	236	47.775	46.532	-1.243
100	238	49.134	47.741	-1.393
100	243	49.549	48.204	-1.345
105	244	47.960	46.535	-1.425
105	246	47.796	46.255	-1.541
105	248	48.905	47.433	-1.472
105	374	48.016	46.645	-1.371
105	376	49.418	47.924	-1.494
	Max	50.477	50.425	0.063
	Average	48.883	48.130	-0.753
	Min	47.494	46.213	-1.565
	Std Dev	0.767	0.998	0.559

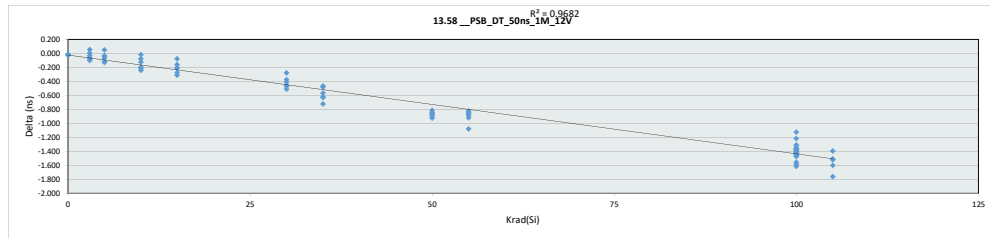


13.57_PSA_DT_50ns_1M_12V											
Test Site											
Tester											
Test Number											
Max Limit	55	ns									
Min Limit	43	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	48.261	48.480	47.755	48.350	47.593	48.098	47.157	47.022	46.997	46.213	46.255
Average	48.474	49.291	48.548	49.181	48.422	48.883	48.046	48.260	48.262	47.369	46.958
Max	48.784	50.425	49.479	50.314	49.431	50.041	48.981	49.018	48.984	48.824	47.924
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

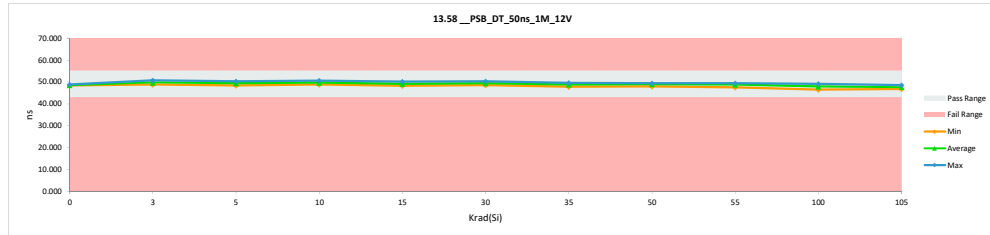


LDR TID Report
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13.58_PSB_DT_50ns_1M_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	55	55		
Min Limit	43	43		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	48.388	48.367	-0.021
0	118	48.897	48.889	-0.008
0	122	48.397	48.387	-0.010
3	61	49.279	49.340	0.061
3	62	50.823	50.727	-0.096
3	63	48.934	48.976	-0.058
3	64	49.820	49.792	-0.028
3	65	50.147	50.072	-0.075
3	66	49.899	49.910	0.011
5	67	49.558	49.529	-0.029
5	68	48.323	48.377	0.054
5	69	48.979	48.855	-0.124
5	70	49.746	49.684	-0.062
5	71	50.361	50.325	-0.036
5	72	49.547	49.435	-0.112
10	73	49.279	49.266	-0.013
10	74	50.823	50.585	-0.238
10	75	48.934	48.815	-0.119
10	76	49.820	49.629	-0.191
10	77	50.147	49.943	-0.204
10	78	49.899	49.824	-0.075
15	79	49.558	49.401	-0.157
15	80	48.323	48.250	-0.073
15	81	48.979	48.673	-0.306
15	82	49.746	49.478	-0.268
15	83	50.361	50.161	-0.200
15	84	49.547	49.342	-0.205
30	86	49.279	49.005	-0.274
30	89	50.823	50.314	-0.509
30	91	48.934	48.564	-0.370
30	92	49.820	49.363	-0.457
30	94	50.147	49.680	-0.467
30	95	49.899	49.492	-0.407
35	100	49.558	49.082	-0.476
35	101	48.323	47.863	-0.460
35	102	48.979	48.361	-0.618
35	103	49.746	49.121	-0.625
35	104	50.361	49.641	-0.720
35	105	49.547	48.983	-0.564
50	106	49.651	48.842	-0.809
50	107	50.241	49.399	-0.842
50	108	48.871	47.984	-0.887
50	109	49.817	48.943	-0.874
50	110	50.085	49.228	-0.857
50	111	50.238	49.317	-0.921
55	113	50.303	49.390	-0.913
55	114	48.409	47.533	-0.876
55	115	50.193	49.355	-0.838
55	116	50.324	49.463	-0.861
55	126	49.592	48.772	-0.820
55	128	50.103	49.028	-1.075
100	129	50.653	49.192	-1.461
100	131	48.139	46.825	-1.314
100	132	48.754	47.371	-1.383
100	133	48.093	46.481	-1.612
100	134	49.714	48.287	-1.427
100	135	49.538	48.328	-1.210
100	138	49.797	48.444	-1.353
100	139	48.783	47.204	-1.579
100	140	48.207	46.848	-1.359
100	141	49.773	48.224	-1.549
100	142	48.973	47.505	-1.468
100	143	48.746	47.315	-1.431
100	144	50.556	49.208	-1.348
100	220	49.614	48.201	-1.413
100	221	49.330	48.208	-1.122
100	224	48.859	47.441	-1.418
100	225	49.881	48.442	-1.439
100	226	50.067	48.693	-1.374
100	228	50.447	49.090	-1.357
100	232	49.195	47.840	-1.355
100	233	49.718	48.416	-1.302
100	236	48.731	47.341	-1.390
100	238	49.803	48.403	-1.400
100	243	50.128	48.722	-1.406
105	244	48.271	46.768	-1.503
105	246	48.519	47.004	-1.515
105	248	50.174	48.578	-1.596
105	374	48.671	47.281	-1.390
105	376	50.241	48.486	-1.755
	Max	50.823	50.727	0.061
	Average	49.514	48.760	-0.754
	Min	48.093	46.481	-1.755
	Std Dev	0.722	0.948	0.579

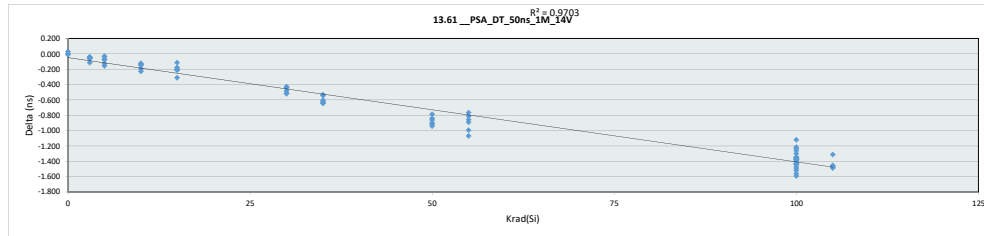


13.58_PSB_DT_50ns_1M_12V											
Test Site											
Tester											
Test Number											
Max Limit	55	ns									
Min Limit	43	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	48.367	48.876	48.377	48.815	48.250	48.564	47.863	47.984	47.533	46.481	46.768
Average	48.548	49.786	49.368	49.677	49.218	49.403	48.842	48.952	48.924	48.001	47.623
Max	48.889	50.727	50.325	50.585	50.161	50.314	49.641	49.399	49.463	49.208	48.578
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

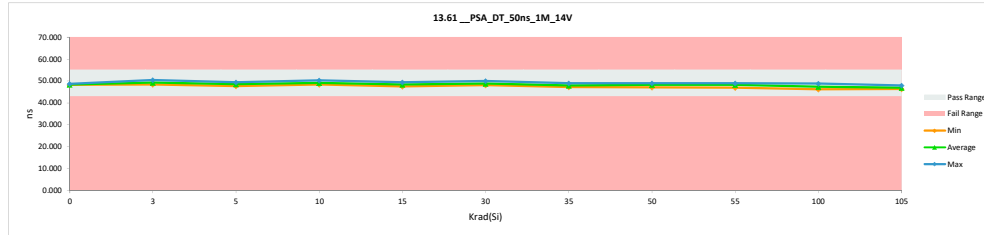


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13.61_PSA_DT_50ns_1M_14V				
Test Site	Tester	Unit	Max Limit	Min Limit
ns	ns	ns	55	43
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	48.245	48.240	-0.005
0	118	48.741	48.747	0.006
0	122	48.361	48.395	0.034
3	61	48.972	48.933	-0.039
3	62	50.482	50.446	-0.036
3	63	48.514	48.454	-0.060
3	64	49.532	49.421	-0.111
3	65	49.271	49.217	-0.054
3	66	49.307	49.268	-0.039
5	67	48.792	48.725	-0.067
5	68	48.115	48.081	-0.034
5	69	47.934	47.810	-0.124
5	70	47.753	47.724	-0.029
5	71	49.596	49.445	-0.151
5	72	49.517	49.442	-0.075
10	73	48.972	48.852	-0.120
10	74	50.482	50.290	-0.192
10	75	48.514	48.388	-0.126
10	76	49.532	49.307	-0.225
10	77	49.271	49.134	-0.137
10	78	49.307	49.164	-0.143
15	79	48.792	48.620	-0.172
15	80	48.115	47.922	-0.193
15	81	47.934	47.625	-0.309
15	82	47.753	47.551	-0.202
15	83	49.596	49.390	-0.206
15	84	49.517	49.407	-0.110
30	86	48.972	48.545	-0.427
30	89	50.482	50.043	-0.439
30	91	48.514	48.092	-0.422
30	92	49.532	49.054	-0.478
30	94	49.271	48.763	-0.508
30	95	49.307	48.791	-0.516
35	100	48.792	48.192	-0.600
35	101	48.115	47.587	-0.528
35	102	47.934	47.295	-0.639
35	103	47.753	47.212	-0.541
35	104	49.596	48.977	-0.619
35	105	49.517	48.880	-0.637
50	106	49.865	49.028	-0.837
50	107	49.211	48.354	-0.857
50	108	47.838	47.056	-0.782
50	109	48.894	47.957	-0.937
50	110	49.370	48.460	-0.910
50	111	49.640	48.744	-0.896
55	113	49.584	48.698	-0.886
55	114	47.784	46.978	-0.806
55	115	49.868	48.804	-1.064
55	116	49.744	48.983	-0.761
55	126	48.680	47.826	-0.854
55	128	49.329	48.338	-0.991
100	129	49.245	47.904	-1.341
100	131	48.179	46.591	-1.588
100	132	48.677	47.243	-1.434
100	133	47.942	46.506	-1.436
100	134	49.587	48.028	-1.559
100	135	49.076	47.644	-1.432
100	138	49.270	47.907	-1.363
100	139	47.626	46.250	-1.376
100	140	47.810	46.439	-1.371
100	141	49.477	47.985	-1.492
100	142	47.851	46.496	-1.355
100	143	47.692	46.301	-1.391
100	144	49.192	47.850	-1.342
100	220	48.790	47.421	-1.369
100	221	49.308	48.194	-1.114
100	224	47.959	46.727	-1.232
100	225	49.329	47.979	-1.350
100	226	50.329	48.814	-1.515
100	228	49.455	47.984	-1.471
100	232	47.476	46.219	-1.257
100	233	49.150	47.940	-1.210
100	236	47.812	46.519	-1.293
100	238	49.198	47.770	-1.428
100	243	49.559	48.157	-1.402
105	244	48.002	46.551	-1.451
105	246	47.802	46.345	-1.457
105	248	48.894	47.410	-1.484
105	374	47.975	46.666	-1.309
105	376	49.467	47.993	-1.474
Max		50.482	50.446	0.034
Average		48.883	48.131	-0.752
Min		47.476	46.219	-1.258
Std Dev		0.773	0.995	0.556



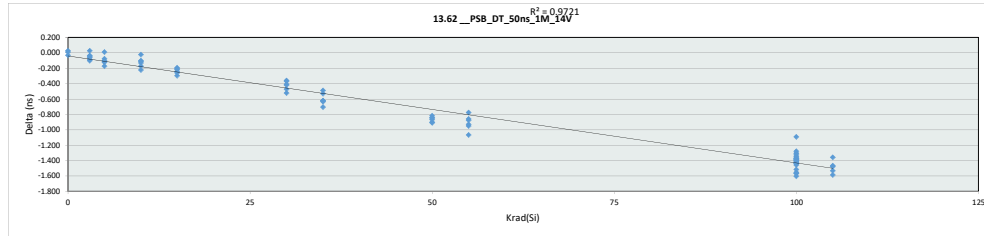
13.61_PSA_DT_50ns_1M_14V											
Test Site	Tester	Unit	Max Limit	Min Limit							
ns	ns	ns	55	43							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	48.240	48.454	47.724	48.388	47.551	48.092	47.212	47.056	46.978	46.219	46.345
Average	48.461	49.290	48.538	49.189	48.419	48.881	48.024	48.267	48.271	47.370	46.993
Max	48.747	50.446	49.445	50.290	49.407	50.043	48.977	49.028	48.983	48.814	47.993
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000



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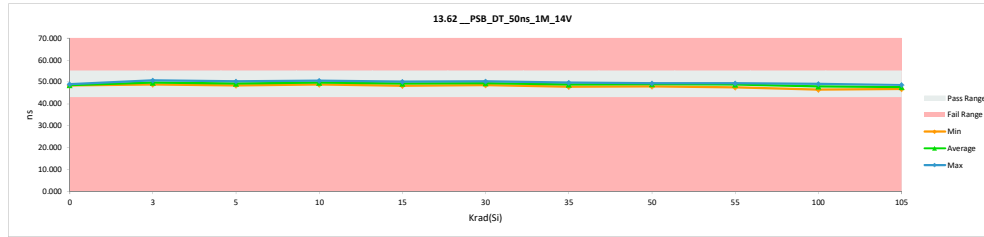
13.62_PSB_DT_50ns_1M_14V		
Test Site		
Tester		
Test Number		
Unit	ns	ns
Max Limit	55	55
Min Limit	43	43

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	48.384	48.357	-0.027
0	118	48.931	48.960	0.029
0	122	48.416	48.429	0.013
3	61	49.358	49.320	-0.038
3	62	50.817	50.760	-0.057
3	63	48.915	48.885	-0.030
3	64	49.862	49.781	-0.081
3	65	50.082	49.983	-0.099
3	66	49.862	49.891	0.029
5	67	49.618	49.544	-0.074
5	68	48.402	48.417	0.015
5	69	48.992	48.882	-0.110
5	70	49.732	49.613	-0.119
5	71	50.376	50.279	-0.097
5	72	49.589	49.422	-0.167
10	73	49.358	49.252	-0.106
10	74	50.817	50.600	-0.217
10	75	48.915	48.816	-0.099
10	76	49.862	49.692	-0.170
10	77	50.082	49.956	-0.126
10	78	49.862	49.845	-0.017
15	79	49.618	49.428	-0.190
15	80	48.402	48.201	-0.201
15	81	48.992	48.699	-0.293
15	82	49.732	49.517	-0.215
15	83	50.376	50.119	-0.257
15	84	49.589	49.351	-0.238
30	86	49.358	48.992	-0.366
30	89	50.817	50.297	-0.520
30	91	48.915	48.559	-0.356
30	92	49.862	49.392	-0.470
30	94	50.082	49.667	-0.415
30	95	49.862	49.457	-0.405
35	100	49.618	49.133	-0.485
35	101	48.402	47.875	-0.527
35	102	48.992	48.360	-0.632
35	103	49.732	49.115	-0.617
35	104	50.376	49.676	-0.700
35	105	49.589	48.972	-0.617
50	106	49.697	48.860	-0.837
50	107	50.205	49.393	-0.812
50	108	48.894	48.030	-0.864
50	109	49.821	48.914	-0.907
50	110	50.076	49.232	-0.844
50	111	50.214	49.316	-0.898
55	113	50.273	49.419	-0.854
55	114	48.421	47.475	-0.946
55	115	50.203	49.332	-0.871
55	116	50.393	49.467	-0.926
55	126	49.565	48.795	-0.770
55	128	50.131	49.068	-1.063
100	129	50.705	49.141	-1.564
100	131	48.111	46.812	-1.299
100	132	48.734	47.332	-1.402
100	133	48.035	46.471	-1.564
100	134	49.746	48.291	-1.455
100	135	49.614	48.276	-1.338
100	138	49.826	48.445	-1.381
100	139	48.760	47.249	-1.511
100	140	48.209	46.812	-1.397
100	141	49.772	48.221	-1.551
100	142	48.915	47.540	-1.375
100	143	48.680	47.292	-1.388
100	144	50.613	49.193	-1.420
100	220	49.647	48.208	-1.439
100	221	49.257	48.170	-1.087
100	224	48.850	47.422	-1.428
100	225	49.915	48.316	-1.599
100	226	50.079	48.711	-1.368
100	228	50.435	49.085	-1.350
100	232	49.132	47.858	-1.274
100	233	49.732	48.420	-1.312
100	236	48.766	47.336	-1.430
100	239	49.751	48.386	-1.365
100	243	50.130	48.716	-1.414
105	244	48.213	46.751	-1.462
105	246	48.567	47.091	-1.476
105	248	50.169	48.642	-1.527
105	374	48.643	47.287	-1.356
105	376	50.210	48.626	-1.584
	Max	50.817	50.760	0.029
	Average	49.521	48.761	-0.760
	Min	48.035	46.471	-1.599
	Std Dev	0.722	0.948	0.570



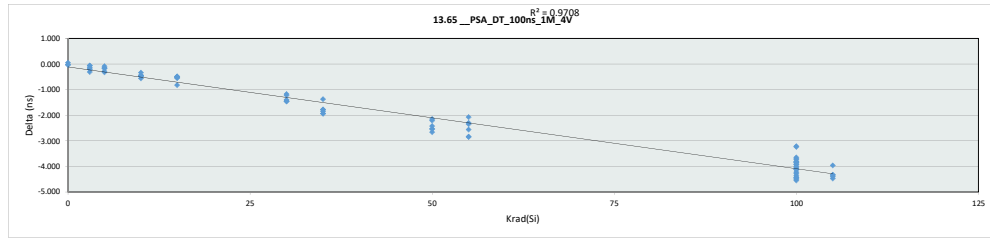
13.62_PSB_DT_50ns_1M_14V		
Test Site		
Tester		
Test Number		
Max Limit	55	ns
Min Limit	43	ns

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	48.357	48.885	48.417	48.816	48.201	48.559	47.875	48.030	47.475	46.471	46.751
Average	48.582	49.770	49.360	49.694	49.219	49.394	48.855	48.958	48.926	47.988	47.679
Max	48.960	50.760	50.279	50.604	50.119	50.297	49.676	49.393	49.467	49.193	48.642
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

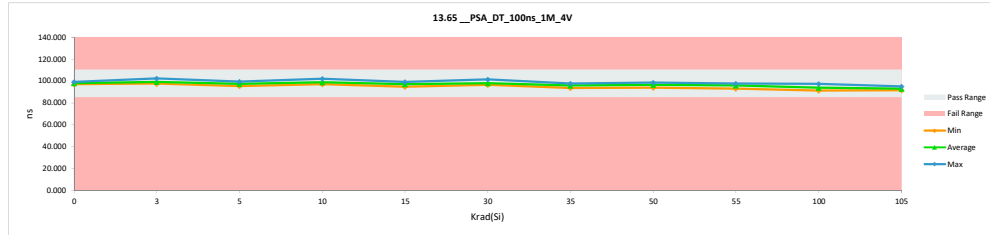


LDR TID Report
TPS7H5001-SP

13.65_PSA_DT_100ns_1M_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	110	110		
Min Limit	85	85		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	97.176	97.173	-0.003
0	118	99.068	99.052	-0.016
0	122	97.299	97.363	0.064
3	61	97.588	97.544	-0.044
3	62	102.572	102.448	-0.124
3	63	98.006	97.828	-0.178
3	64	99.489	99.193	-0.296
3	65	99.039	98.860	-0.179
3	66	99.116	99.075	-0.041
5	67	97.897	97.763	-0.134
5	68	96.572	96.428	-0.144
5	69	96.300	96.027	-0.273
5	70	95.363	95.293	-0.070
5	71	99.674	99.368	-0.306
5	72	99.730	99.573	-0.157
10	73	97.588	97.168	-0.420
10	74	102.572	102.111	-0.461
10	75	98.006	97.462	-0.544
10	76	99.489	99.026	-0.463
10	77	99.039	98.610	-0.429
10	78	99.116	98.795	-0.321
15	79	97.897	97.404	-0.493
15	80	96.572	96.056	-0.516
15	81	96.300	95.497	-0.803
15	82	95.363	94.895	-0.468
15	83	99.674	99.144	-0.530
15	84	99.730	99.260	-0.470
30	86	97.588	96.424	-1.164
30	89	102.572	101.366	-1.206
30	91	98.006	96.566	-1.440
30	92	99.489	98.111	-1.378
30	94	99.039	97.606	-1.433
30	95	99.116	97.664	-1.452
35	100	97.897	96.123	-1.774
35	101	96.572	95.213	-1.359
35	102	96.300	94.377	-1.923
35	103	95.363	93.598	-1.765
35	104	99.674	97.836	-1.838
35	105	99.730	97.810	-1.920
50	106	101.121	98.716	-2.405
50	107	98.107	95.905	-2.202
50	108	96.127	93.990	-2.137
50	109	97.795	95.271	-2.524
50	110	99.783	97.258	-2.525
50	111	99.910	97.266	-2.644
55	113	99.650	97.101	-2.549
55	114	95.259	92.971	-2.288
55	115	100.037	97.208	-2.829
55	116	99.758	97.703	-2.055
55	126	97.775	95.438	-2.337
55	128	98.372	95.547	-2.825
100	129	98.933	95.253	-3.680
100	131	96.543	92.061	-4.482
100	132	98.066	93.656	-4.410
100	133	96.724	92.516	-4.208
100	134	99.925	95.387	-4.538
100	135	98.307	94.235	-4.072
100	138	98.252	94.449	-3.803
100	139	95.769	91.856	-3.913
100	140	95.731	91.826	-3.905
100	141	99.467	95.129	-4.338
100	142	96.066	92.217	-3.849
100	143	95.652	91.397	-4.255
100	144	99.095	95.278	-3.817
100	220	97.370	93.265	-4.105
100	221	99.108	94.018	-5.090
100	224	96.177	92.373	-3.804
100	225	99.300	95.185	-4.115
100	226	101.765	97.316	-4.449
100	228	99.504	95.091	-4.413
100	232	94.965	91.328	-3.637
100	233	98.879	95.655	-3.224
100	236	95.647	91.928	-3.719
100	238	98.006	94.018	-3.988
100	243	99.830	95.808	-4.022
105	244	96.337	91.955	-4.382
105	246	95.908	91.445	-4.463
105	248	97.840	93.520	-4.320
105	374	96.655	92.705	-3.950
105	376	99.357	95.049	-4.308
	Max	102.572	102.448	0.064
	Average	98.206	96.041	-2.165
	Min	94.965	91.328	-4.538
	Std Dev	1.781	2.570	1.628

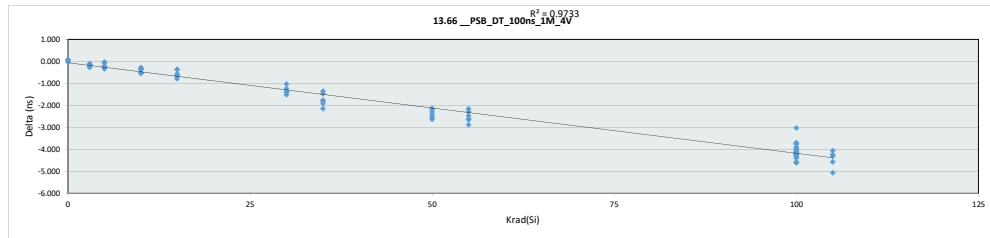


13.65_PSA_DT_100ns_1M_4V										
Test Site										
Tester										
Test Number										
Max Limit	110	ns								
Min Limit	85	ns								
Krad(Si)	0	3	5	10	15	30	35	50	55	100
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	97.173	97.544	95.293	97.168	94.895	96.424	93.598	93.990	92.971	91.328
Average	97.863	99.158	97.409	98.862	97.043	97.956	95.826	96.401	95.995	93.881
Max	99.052	102.448	99.573	102.111	99.260	101.366	97.836	98.716	97.703	97.316
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

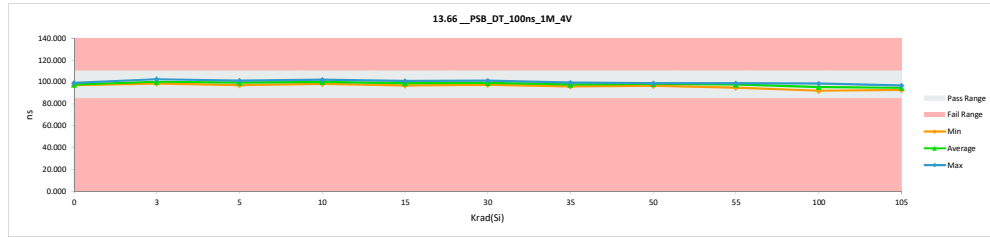


LDR TID Report
TPS7H5001-SP

13.66_PSB_DT_100ns_1M_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	110	110		
Min Limit	85	85		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	97.123	97.106	-0.017
0	118	98.964	99.036	0.072
0	122	97.206	97.310	0.104
3	61	98.582	98.491	-0.091
3	62	102.728	102.541	-0.187
3	63	98.642	98.503	-0.139
3	64	100.382	100.117	-0.265
3	65	101.045	100.896	-0.149
3	66	100.813	100.685	-0.128
5	67	99.846	99.662	-0.184
5	68	97.194	97.184	-0.010
5	69	98.722	98.404	-0.318
5	70	100.576	100.515	-0.061
5	71	101.584	101.333	-0.251
5	72	99.376	99.097	-0.279
10	73	98.582	98.326	-0.256
10	74	102.728	102.232	-0.496
10	75	98.642	98.314	-0.328
10	76	100.382	99.837	-0.545
10	77	101.045	100.689	-0.356
10	78	100.813	100.475	-0.338
15	79	99.846	99.493	-0.353
15	80	97.194	96.844	-0.350
15	81	98.722	97.946	-0.776
15	82	100.576	100.020	-0.556
15	83	101.584	100.936	-0.648
15	84	99.376	98.756	-0.620
30	86	98.582	97.557	-1.025
30	89	102.728	101.340	-1.388
30	91	98.642	97.419	-1.223
30	92	100.382	98.882	-1.500
30	94	101.045	99.672	-1.373
30	95	100.813	99.441	-1.372
35	100	99.846	98.391	-1.455
35	101	97.194	95.848	-1.346
35	102	98.722	96.809	-1.913
35	103	100.576	98.768	-1.808
35	104	101.584	99.447	-2.137
35	105	99.376	97.624	-1.752
50	106	100.110	97.989	-2.121
50	107	101.167	99.006	-2.161
50	108	99.133	96.513	-2.620
50	109	100.339	97.819	-2.520
50	110	100.956	98.670	-2.286
50	111	101.354	98.934	-2.420
55	113	101.162	98.553	-2.609
55	114	97.315	94.676	-2.639
55	115	100.964	98.696	-2.268
55	116	101.247	98.792	-2.455
55	126	99.551	97.409	-2.142
55	128	100.393	97.528	-2.865
100	129	102.839	98.574	-4.265
100	131	96.672	92.989	-3.683
100	132	98.142	93.960	-4.182
100	133	96.595	91.990	-4.605
100	134	100.281	96.136	-4.145
100	135	99.751	96.004	-3.747
100	138	99.671	95.670	-4.001
100	139	98.968	94.387	-4.581
100	140	97.156	92.991	-4.165
100	141	100.252	95.696	-4.556
100	142	99.580	95.697	-3.883
100	143	98.461	94.214	-4.247
100	144	102.533	98.459	-4.074
100	220	99.620	95.633	-3.987
100	221	98.600	95.584	-3.016
100	224	98.529	94.301	-4.228
100	225	100.770	96.427	-4.343
100	226	100.933	97.024	-3.909
100	228	101.928	97.927	-4.001
100	232	99.451	95.325	-4.126
100	233	100.222	96.514	-3.708
100	236	98.459	94.238	-4.221
100	238	99.874	95.630	-4.244
100	243	101.152	96.769	-4.383
105	244	96.966	92.743	-4.223
105	246	97.416	93.121	-4.295
105	248	101.376	96.825	-4.551
105	374	97.983	93.940	-4.043
105	376	101.719	96.673	-5.046
	Max	102.839	102.541	0.104
	Average	99.767	97.575	-2.192
	Min	96.595	91.990	-5.046
	Std Dev	1.583	2.337	1.681

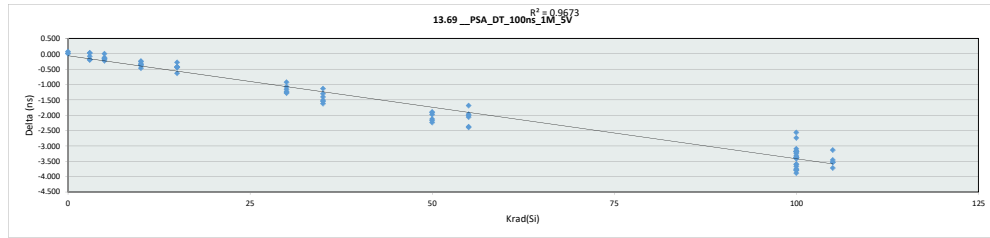


13.66_PSB_DT_100ns_1M_4V											
Test Site											
Tester											
Test Number											
Max Limit	110	ns									
Min Limit	85	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	97.106	98.491	97.184	98.314	96.844	97.419	95.848	96.513	94.676	91.990	92.743
Average	97.817	100.206	99.366	99.979	98.999	99.052	97.815	98.155	97.609	95.506	94.660
Max	99.036	102.541	101.333	102.232	100.936	101.340	99.447	99.006	98.792	98.574	96.825
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

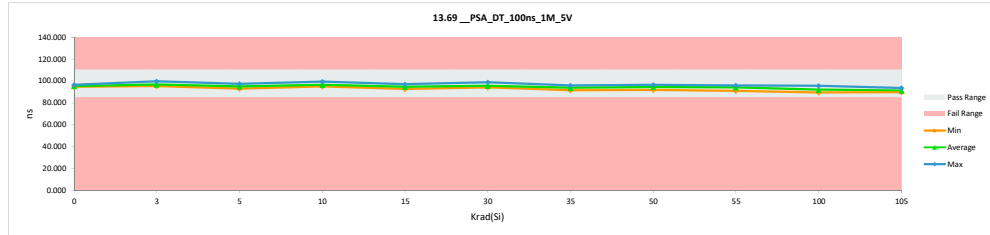


LDR TID Report
TPS7H5001-SP

13.69_PSA_DT_100ns_1M_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	110	110		
Min Limit	85	85		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	94.675	94.691	0.016
0	118	96.440	96.478	0.038
0	122	94.897	94.981	0.084
3	61	95.431	95.475	0.044
3	62	99.889	99.824	-0.065
3	63	95.486	95.312	-0.174
3	64	97.108	96.930	-0.178
3	65	96.827	96.638	-0.189
3	66	96.803	96.838	0.035
5	67	95.613	95.497	-0.116
5	68	94.254	94.103	-0.151
5	69	93.862	93.707	-0.155
5	70	92.964	92.980	0.016
5	71	97.333	97.115	-0.218
5	72	97.490	97.332	-0.158
10	73	95.431	95.181	-0.250
10	74	99.889	99.516	-0.373
10	75	95.486	95.034	-0.452
10	76	97.108	96.787	-0.321
10	77	96.827	96.501	-0.326
10	78	96.803	96.581	-0.222
15	79	95.613	95.197	-0.416
15	80	94.254	93.820	-0.434
15	81	92.862	93.235	0.373
15	82	92.964	92.697	-0.267
15	83	97.333	96.908	-0.425
15	84	97.490	97.075	-0.415
30	86	95.431	94.522	-0.909
30	89	99.889	98.833	-1.056
30	91	95.486	94.221	-1.265
30	92	97.108	95.961	-1.147
30	94	96.827	95.579	-1.248
30	95	96.803	95.592	-1.211
35	100	95.613	94.215	-1.398
35	101	94.254	93.136	-1.118
35	102	93.862	92.323	-1.539
35	103	92.964	91.672	-1.292
35	104	97.333	95.838	-1.495
35	105	97.490	95.875	-1.615
50	106	98.563	96.599	-1.964
50	107	95.935	94.033	-1.902
50	108	93.747	91.867	-1.880
50	109	95.555	93.441	-2.114
50	110	97.445	95.281	-2.164
50	111	97.593	95.364	-2.229
55	113	97.404	95.355	-2.049
55	114	93.056	91.098	-1.958
55	115	97.653	95.294	-2.359
55	116	97.544	95.870	-1.674
55	125	95.591	93.592	-1.999
55	128	96.250	93.866	-2.384
100	129	96.687	93.537	-3.150
100	131	94.159	90.379	-3.780
100	132	95.526	91.763	-3.763
100	133	94.345	90.750	-3.595
100	134	97.561	93.687	-3.874
100	135	96.111	92.744	-3.367
100	138	96.131	92.969	-3.162
100	139	93.285	90.051	-3.234
100	140	93.428	90.078	-3.350
100	141	97.040	93.383	-3.657
100	142	93.719	90.412	-3.307
100	143	93.255	89.670	-3.585
100	144	96.747	93.573	-3.174
100	220	95.186	91.820	-3.366
100	221	96.989	94.261	-2.728
100	224	93.877	90.691	-3.186
100	225	97.042	93.643	-3.399
100	226	99.320	95.582	-3.738
100	228	97.229	93.447	-3.782
100	232	92.657	89.580	-3.077
100	233	96.405	93.854	-2.551
100	236	93.372	90.207	-3.165
100	238	95.907	92.533	-3.374
100	243	97.518	94.142	-3.376
105	244	93.927	90.413	-3.514
105	246	93.466	89.758	-3.708
105	248	95.644	92.122	-3.522
105	374	94.169	91.047	-3.122
105	376	97.084	93.642	-3.442
	Max	99.889	99.824	0.084
	Average	95.866	94.070	-1.796
	Min	92.657	89.580	-3.074
	Std Dev	1.762	2.360	1.375



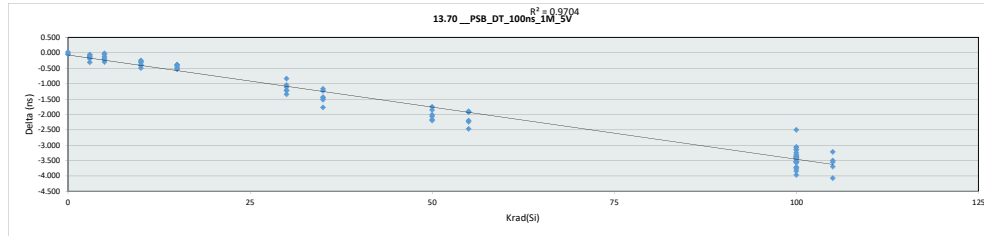
13.69_PSA_DT_100ns_1M_5V											
Test Site											
Tester											
Test Number											
Max Limit	110	ns									
Min Limit	85	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.691	95.312	92.980	95.034	92.697	94.221	91.672	91.867	91.098	89.580	89.758
Average	95.383	96.836	95.122	96.600	94.822	95.785	93.843	94.431	94.179	92.198	91.396
Max	96.478	99.824	97.332	99.516	97.075	98.833	95.875	96.599	95.870	95.582	93.642
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000



LDR TID Report
TPS7H5001-SP

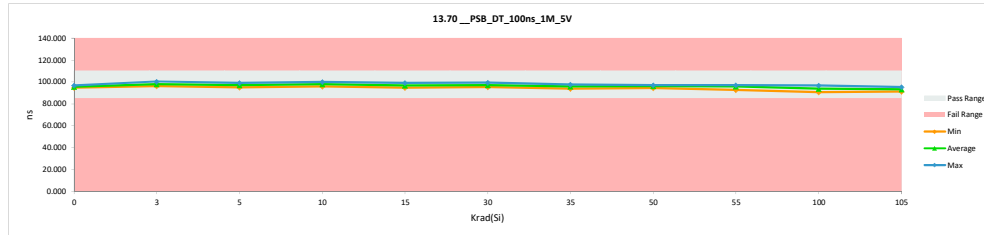
13.70_PSB_DT_100ns_1M_5V		
Test Site		
Tester		
Test Number		
Unit	ns	ns
Max Limit	110	110
Min Limit	85	85

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	94.964	94.924	-0.040
0	118	96.825	96.796	-0.029
0	122	94.973	95.010	0.037
3	61	96.571	96.527	-0.044
3	62	100.537	100.475	-0.062
3	63	96.312	96.184	-0.128
3	64	98.265	97.964	-0.301
3	65	98.839	98.730	-0.109
3	66	98.704	98.521	-0.183
5	67	97.662	97.498	-0.164
5	68	95.053	95.014	-0.039
5	69	96.453	96.344	-0.109
5	70	98.065	98.064	-0.001
5	71	99.513	99.298	-0.215
5	72	97.392	97.105	-0.287
10	73	96.571	96.336	-0.235
10	74	100.537	100.147	-0.390
10	75	96.312	96.032	-0.280
10	76	98.265	97.778	-0.487
10	77	98.839	98.531	-0.308
10	78	98.704	98.317	-0.387
15	79	97.662	97.285	-0.377
15	80	95.053	94.683	-0.370
15	81	96.453	95.933	-0.520
15	82	98.065	97.669	-0.396
15	83	99.513	99.063	-0.450
15	84	97.392	96.880	-0.512
30	86	96.571	95.742	-0.829
30	89	100.537	99.323	-1.214
30	91	96.312	95.278	-1.034
30	92	98.265	96.924	-1.341
30	94	98.839	97.728	-1.111
30	95	98.704	97.487	-1.217
35	100	97.662	96.439	-1.223
35	101	95.053	93.896	-1.157
35	102	96.453	94.938	-1.515
35	103	98.065	96.635	-1.430
35	104	99.513	97.751	-1.762
35	105	97.392	95.944	-1.448
50	106	97.946	96.205	-1.741
50	107	98.965	97.114	-1.851
50	108	96.754	94.598	-2.156
50	109	98.147	95.956	-2.191
50	110	98.805	96.800	-2.005
50	111	99.123	97.066	-2.057
55	113	99.213	97.026	-2.187
55	114	95.036	92.804	-2.232
55	115	98.793	96.880	-1.913
55	116	99.117	97.201	-1.916
55	126	97.572	95.689	-1.883
55	128	98.457	96.000	-2.457
100	129	100.423	96.894	-3.529
100	131	94.569	91.338	-3.231
100	132	95.861	92.330	-3.531
100	133	94.652	90.691	-3.961
100	134	97.987	94.666	-3.321
100	135	97.550	94.493	-3.057
100	138	97.589	94.456	-3.133
100	139	96.522	92.760	-3.762
100	140	94.886	91.454	-3.432
100	141	98.156	94.323	-3.833
100	142	96.951	93.663	-3.288
100	143	95.971	92.443	-3.528
100	144	100.301	96.935	-3.366
100	220	97.511	94.150	-3.361
100	221	96.677	94.190	-2.487
100	224	96.439	92.921	-3.518
100	225	98.513	94.806	-3.707
100	226	98.752	95.613	-3.139
100	228	99.821	96.437	-3.384
100	232	97.045	93.660	-3.385
100	233	98.101	95.052	-3.049
100	236	96.048	92.486	-3.562
100	238	97.847	94.399	-3.448
100	243	99.067	95.357	-3.710
105	244	94.747	91.261	-3.486
105	246	95.455	91.910	-3.545
105	248	99.099	95.417	-3.682
105	374	95.764	92.555	-3.209
105	376	99.469	95.412	-4.057
	Max	100.537	100.475	0.037
	Average	97.582	95.758	-1.824
	Min	94.569	90.691	-4.057
	Std Dev	1.584	2.149	1.389



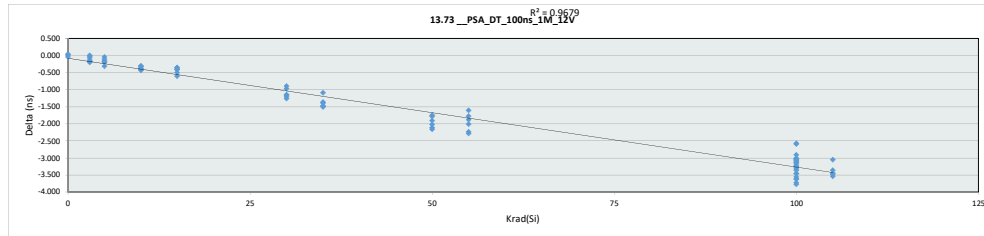
13.70_PSB_DT_100ns_1M_5V		
Test Site		
Tester		
Test Number		
Max Limit	110	ns
Min Limit	85	ns

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.924	96.184	95.014	96.032	94.683	95.278	93.896	94.598	92.804	90.691	91.261
Average	95.577	98.067	97.221	97.857	96.919	97.080	95.934	96.290	95.933	93.980	93.311
Max	96.796	100.475	99.298	100.147	99.063	99.323	97.751	97.114	97.201	96.935	95.417
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

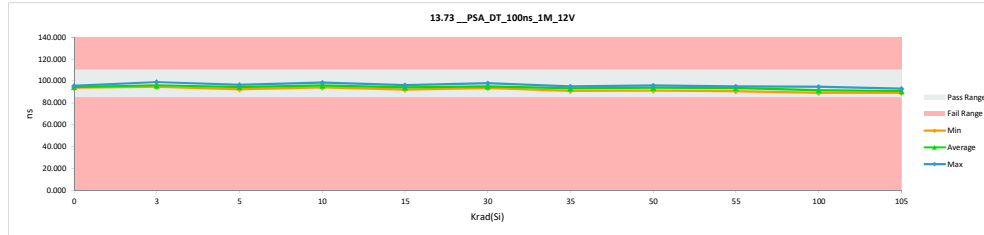


LDR TID Report
TPS7H5001-SP

13.73_PSA_DT_100ns_1M_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	110	110		
Min Limit	85	85		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	93.956	93.978	0.022
0	118	95.586	95.551	-0.035
0	122	94.171	94.221	0.050
3	61	94.782	94.797	0.015
3	62	99.041	99.030	-0.011
3	63	94.714	94.586	-0.128
3	64	96.399	96.211	-0.188
3	65	95.940	95.773	-0.167
3	66	96.152	96.093	-0.059
5	67	94.878	94.785	-0.093
5	68	93.547	93.383	-0.164
5	69	93.207	93.019	-0.188
5	70	92.442	92.413	-0.029
5	71	96.595	96.296	-0.299
5	72	96.708	96.555	-0.153
10	73	94.782	94.492	-0.290
10	74	99.041	98.640	-0.401
10	75	94.714	94.289	-0.425
10	76	96.399	96.029	-0.370
10	77	95.940	95.633	-0.307
10	78	96.152	95.831	-0.321
15	79	94.878	94.469	-0.409
15	80	93.547	93.156	-0.391
15	81	93.207	92.610	-0.597
15	82	92.442	92.102	-0.340
15	83	96.595	96.104	-0.491
15	84	96.708	96.360	-0.348
30	86	94.782	93.897	-0.885
30	89	99.041	98.077	-0.964
30	91	94.714	93.574	-1.140
30	92	96.399	95.253	-1.146
30	94	95.940	94.744	-1.196
30	95	96.152	94.897	-1.255
35	100	94.878	93.526	-1.352
35	101	93.547	92.463	-1.084
35	102	93.207	91.742	-1.465
35	103	92.442	91.061	-1.381
35	104	96.595	95.121	-1.474
35	105	96.708	95.211	-1.497
50	106	97.735	95.839	-1.896
50	107	95.230	93.455	-1.775
50	108	93.007	91.268	-1.739
50	109	94.888	92.882	-2.006
50	110	96.603	94.451	-2.152
50	111	96.784	94.683	-2.101
55	113	96.579	94.581	-1.998
55	114	92.434	90.663	-1.771
55	115	96.899	94.673	-2.226
55	116	96.792	95.192	-1.600
55	126	94.816	92.946	-1.870
55	128	95.684	93.407	-2.277
100	129	95.887	92.831	-3.056
100	131	93.569	89.856	-3.713
100	132	94.802	91.198	-3.604
100	133	93.432	90.080	-3.352
100	134	96.817	93.050	-3.767
100	135	95.488	92.219	-3.269
100	138	95.435	92.434	-3.001
100	139	92.544	89.466	-3.078
100	140	92.707	89.587	-3.120
100	141	96.267	92.811	-3.456
100	142	92.950	89.784	-3.166
100	143	92.585	89.139	-3.446
100	144	95.893	92.874	-3.019
100	220	94.587	91.274	-3.313
100	221	96.211	93.632	-2.579
100	224	93.134	90.131	-3.003
100	225	96.147	93.026	-3.121
100	226	98.453	94.840	-3.613
100	228	96.444	92.895	-3.549
100	232	92.069	89.166	-2.903
100	233	95.653	93.089	-2.564
100	236	92.667	89.635	-3.032
100	238	95.256	92.040	-3.216
100	243	96.684	93.440	-3.244
105	244	93.241	89.781	-3.460
105	246	92.890	89.363	-3.527
105	248	94.875	91.439	-3.436
105	374	93.401	90.363	-3.038
105	376	96.362	93.008	-3.354
Max		99.041	99.030	0.050
Average		95.135	93.406	-1.730
Min		92.069	89.139	-3.767
Std Dev		1.709	2.277	1.307



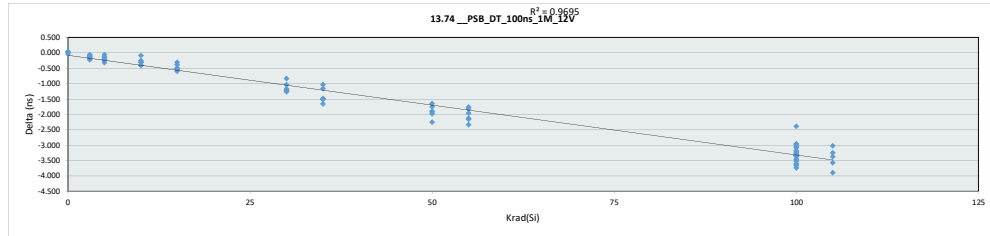
13.73_PSA_DT_100ns_1M_12V											
Test Site											
Tester											
Test Number											
Max Limit	110	ns									
Min Limit	85	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	93.978	94.586	92.413	94.289	92.102	93.574	91.061	91.268	90.663	89.139	89.363
Average	94.583	96.082	94.409	95.819	94.134	95.074	93.187	93.763	93.577	91.604	90.791
Max	95.551	99.030	96.555	98.640	96.360	98.077	95.211	95.839	95.192	94.840	93.008
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000



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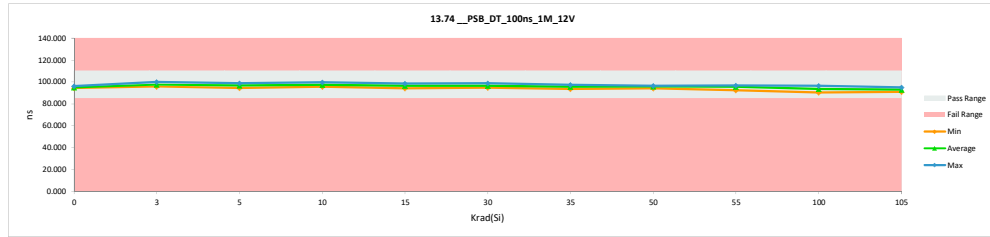
13.74_PSB_DT_100ns_1M_12V		
Test Site		
Tester		
Test Number		
Unit	ns	ns
Max Limit	110	110
Min Limit	85	85

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	94.542	94.595	0.053
0	118	96.236	96.266	0.030
0	122	94.599	94.569	-0.030
3	61	96.118	96.070	-0.048
3	62	100.096	99.951	-0.145
3	63	95.892	95.791	-0.101
3	64	97.722	97.506	-0.216
3	65	98.339	98.148	-0.191
3	66	98.181	98.091	-0.090
5	67	97.217	97.023	-0.194
5	68	94.633	94.590	-0.043
5	69	96.058	95.827	-0.231
5	70	97.627	97.512	-0.115
5	71	98.984	98.833	-0.151
5	72	97.025	96.719	-0.306
10	73	96.118	96.042	-0.076
10	74	100.096	99.690	-0.406
10	75	95.892	95.647	-0.245
10	76	97.722	97.333	-0.389
10	77	98.339	98.050	-0.289
10	78	98.181	97.884	-0.297
15	79	97.217	96.840	-0.377
15	80	94.633	94.340	-0.293
15	81	96.058	95.550	-0.508
15	82	97.627	97.107	-0.520
15	83	98.984	98.499	-0.485
15	84	97.025	96.437	-0.588
30	86	96.118	95.289	-0.829
30	89	100.096	98.879	-1.217
30	91	95.892	94.866	-1.026
30	92	97.722	96.470	-1.252
30	94	98.339	97.135	-1.204
30	95	98.181	97.019	-1.162
35	100	97.217	96.064	-1.153
35	101	94.633	93.605	-1.028
35	102	96.058	94.551	-1.507
35	103	97.627	96.153	-1.474
35	104	98.984	97.334	-1.650
35	105	97.025	95.527	-1.498
50	106	97.442	95.799	-1.643
50	107	98.395	96.654	-1.741
50	108	96.132	94.220	-1.912
50	109	97.768	95.530	-2.238
50	110	98.234	96.350	-1.884
50	111	98.579	96.608	-1.971
55	113	98.697	96.581	-2.116
55	114	94.696	92.553	-2.143
55	115	98.273	96.458	-1.815
55	116	98.679	96.733	-1.946
55	126	97.099	95.343	-1.756
55	128	98.028	95.705	-2.323
100	129	99.843	96.371	-3.472
100	131	94.099	91.149	-2.950
100	132	95.400	92.099	-3.301
100	133	94.129	90.398	-3.731
100	134	97.607	94.277	-3.330
100	135	97.178	94.152	-3.026
100	138	97.210	94.168	-3.042
100	139	95.990	92.378	-3.612
100	140	94.391	91.129	-3.262
100	141	97.694	94.038	-3.656
100	142	96.383	93.212	-3.171
100	143	95.531	92.089	-3.442
100	144	99.906	96.591	-3.315
100	220	97.047	93.820	-3.227
100	221	96.254	93.875	-2.379
100	224	95.937	92.614	-3.323
100	225	97.943	94.349	-3.594
100	226	98.226	95.152	-3.074
100	228	99.215	96.022	-3.193
100	232	96.591	93.256	-3.335
100	233	97.607	94.649	-2.958
100	236	95.531	92.181	-3.350
100	238	97.509	94.173	-3.336
100	243	98.553	95.047	-3.506
105	244	94.256	91.018	-3.238
105	246	95.085	91.725	-3.360
105	248	98.562	94.999	-3.563
105	374	95.197	92.182	-3.015
105	376	98.987	95.105	-3.882
	Max	100.096	99.951	0.053
	Average	97.112	95.357	-1.755
	Min	94.099	90.398	-3.882
	Std Dev	1.564	2.083	1.329



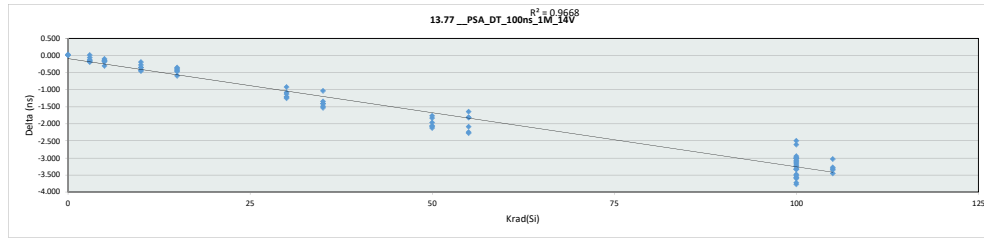
13.74_PSB_DT_100ns_1M_12V		
Test Site		
Tester		
Test Number		
Max Limit	110	ns
Min Limit	85	ns

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.569	95.791	94.590	95.647	94.340	94.866	93.605	94.220	92.553	90.398	91.018
Average	95.143	97.593	96.751	97.441	96.462	96.610	95.539	95.860	95.562	93.633	93.006
Max	96.266	99.951	98.833	99.690	98.499	98.879	97.334	96.654	96.733	96.591	95.105
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

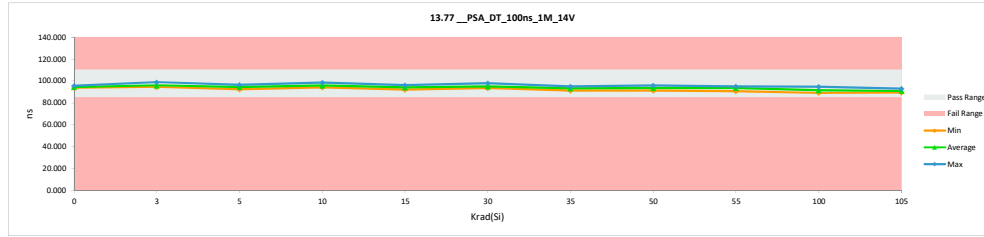


LDR TID Report
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13.77_PSA_DT_100ns_1M_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	110	110		
Min Limit	85	85		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	93.955	93.979	0.024
0	118	95.575	95.590	0.015
0	122	94.179	94.225	0.046
3	61	94.745	94.766	0.021
3	62	99.120	99.006	-0.114
3	63	94.728	94.594	-0.134
3	64	96.375	96.217	-0.158
3	65	95.940	95.760	-0.180
3	66	96.129	96.082	-0.047
5	67	94.902	94.774	-0.128
5	68	93.536	93.360	-0.176
5	69	93.195	93.043	-0.152
5	70	92.466	92.382	-0.084
5	71	96.536	96.245	-0.291
5	72	96.722	96.577	-0.145
10	73	94.745	94.564	-0.181
10	74	99.120	98.673	-0.447
10	75	94.728	94.310	-0.418
10	76	96.375	96.034	-0.341
10	77	95.940	95.596	-0.344
10	78	96.129	95.861	-0.268
15	79	94.902	94.442	-0.460
15	80	93.536	93.134	-0.402
15	81	93.195	92.611	-0.584
15	82	92.466	92.128	-0.338
15	83	96.536	96.099	-0.437
15	84	96.722	96.353	-0.369
30	86	94.745	93.830	-0.915
30	89	99.120	98.048	-1.072
30	91	94.728	93.528	-1.200
30	92	96.375	95.252	-1.123
30	94	95.940	94.735	-1.205
30	95	96.129	94.888	-1.241
35	100	94.902	93.499	-1.403
35	101	93.536	92.514	-1.022
35	102	93.195	91.715	-1.480
35	103	92.466	91.135	-1.331
35	104	96.536	95.138	-1.398
35	105	96.722	95.202	-1.520
50	106	97.764	95.814	-1.950
50	107	95.234	93.410	-1.824
50	108	93.002	91.242	-1.760
50	109	94.909	92.839	-2.070
50	110	96.512	94.464	-2.048
50	111	96.757	94.640	-2.117
55	113	96.626	94.551	-2.075
55	114	92.446	90.646	-1.800
55	115	96.882	94.643	-2.239
55	116	96.775	95.144	-1.631
55	126	94.796	92.992	-1.804
55	128	95.643	93.381	-2.262
100	129	95.882	92.814	-3.068
100	131	93.571	89.804	-3.767
100	132	94.767	91.211	-3.556
100	133	93.436	90.114	-3.322
100	134	96.747	93.029	-3.718
100	135	95.479	92.169	-3.310
100	138	95.413	92.440	-2.973
100	139	92.521	89.433	-3.088
100	140	92.700	89.575	-3.125
100	141	96.247	92.776	-3.471
100	142	93.007	89.842	-3.165
100	143	92.587	89.084	-3.503
100	144	95.889	92.879	-3.010
100	220	94.576	91.256	-3.320
100	221	96.228	93.628	-2.600
100	224	93.160	90.161	-2.999
100	225	96.225	92.986	-3.239
100	226	98.438	94.841	-3.597
100	228	96.456	92.889	-3.567
100	232	92.028	89.087	-2.941
100	233	95.596	93.107	-2.489
100	236	92.633	89.619	-3.014
100	238	95.267	92.025	-3.242
100	243	96.682	93.479	-3.203
105	244	93.115	89.804	-3.311
105	246	92.842	89.394	-3.448
105	248	94.923	91.582	-3.341
105	374	93.382	90.357	-3.025
105	376	96.341	93.077	-3.264
Max		99.120	99.006	0.046
Average		95.130	93.401	-1.728
Min		92.028	89.084	-3.767
Std Dev		1.715	2.275	1.302

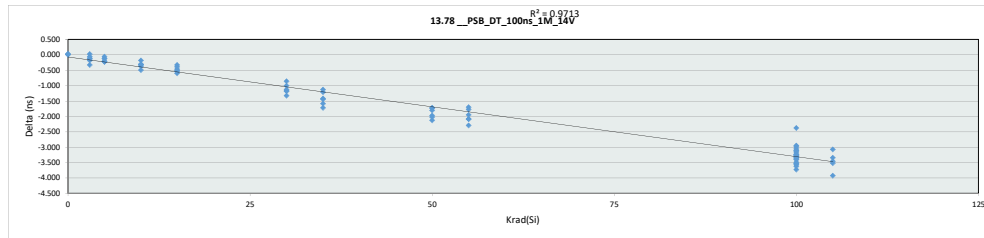


13.77_PSA_DT_100ns_1M_14											
Test Site											
Tester											
Test Number											
Max Limit	110	ns									
Min Limit	85	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	93.979	94.594	92.382	94.310	92.128	93.528	91.135	91.242	90.646	89.084	89.394
Average	94.598	96.071	94.397	95.840	94.128	95.047	93.201	93.735	93.560	91.594	90.843
Max	95.590	99.006	96.577	98.673	96.353	98.048	95.202	95.814	95.144	94.841	93.077
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

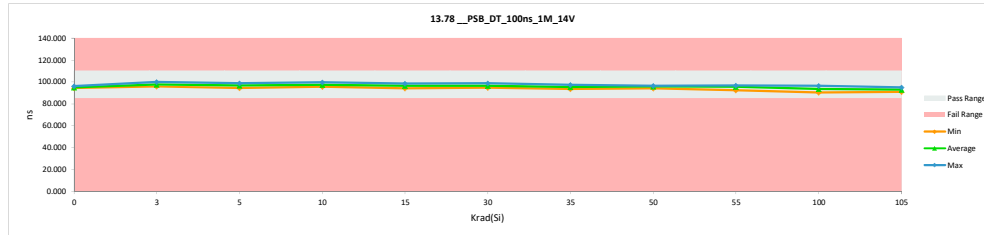


LDR TID Report
TPS7H5001-SP

13.78_PSB_DT_100ns_1M_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	110	110		
Min Limit	85	85		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	94.553	94.572	0.019
0	118	96.192	96.235	0.043
0	122	94.542	94.572	0.030
3	61	96.142	96.173	0.031
3	62	100.090	99.972	-0.118
3	63	95.890	95.841	-0.049
3	64	97.770	97.450	-0.320
3	65	98.305	98.129	-0.176
3	66	98.186	98.082	-0.104
5	67	97.200	97.087	-0.113
5	68	94.623	94.572	-0.051
5	69	96.089	95.873	-0.216
5	70	97.584	97.479	-0.105
5	71	99.040	98.822	-0.218
5	72	96.927	96.731	-0.196
10	73	96.142	95.973	-0.169
10	74	100.090	99.726	-0.364
10	75	95.890	95.574	-0.316
10	76	97.770	97.284	-0.486
10	77	98.305	98.006	-0.299
10	78	98.186	97.891	-0.295
15	79	97.200	96.817	-0.383
15	80	94.623	94.306	-0.317
15	81	96.089	95.497	-0.592
15	82	97.584	97.129	-0.455
15	83	99.040	98.547	-0.493
15	84	96.927	96.406	-0.521
30	86	96.142	95.293	-0.849
30	89	100.090	98.920	-1.170
30	91	95.890	94.896	-0.994
30	92	97.770	96.450	-1.320
30	94	98.305	97.182	-1.123
30	95	98.186	97.028	-1.158
35	100	97.200	96.002	-1.198
35	101	94.623	93.507	-1.116
35	102	96.089	94.516	-1.573
35	103	97.584	96.153	-1.431
35	104	99.040	97.329	-1.711
35	105	96.927	95.520	-1.407
50	106	97.459	95.743	-1.716
50	107	98.392	96.673	-1.719
50	108	96.164	94.149	-2.015
50	109	97.723	95.606	-2.117
50	110	98.180	96.381	-1.799
50	111	98.598	96.633	-1.965
55	113	98.701	96.626	-2.075
55	114	94.620	92.547	-2.073
55	115	98.270	96.505	-1.765
55	116	98.676	96.741	-1.935
55	126	96.987	95.301	-1.686
55	128	97.982	95.703	-2.279
100	129	99.865	96.384	-3.481
100	131	94.155	91.129	-3.026
100	132	95.436	92.063	-3.373
100	133	94.148	90.425	-3.723
100	134	97.531	94.291	-3.240
100	135	97.125	94.167	-2.958
100	138	97.205	94.084	-3.121
100	139	95.985	92.423	-3.562
100	140	94.378	91.140	-3.238
100	141	97.641	94.023	-3.618
100	142	96.418	93.327	-3.091
100	143	95.536	92.144	-3.392
100	144	99.721	96.547	-3.174
100	220	97.065	93.836	-3.229
100	221	96.232	93.869	-2.363
100	224	95.917	92.612	-3.305
100	225	97.877	94.351	-3.526
100	226	98.183	95.100	-3.083
100	228	99.277	95.998	-3.279
100	232	96.568	93.242	-3.326
100	233	97.594	94.653	-2.941
100	236	95.475	92.131	-3.344
100	238	97.477	94.214	-3.263
100	243	98.559	95.036	-3.523
105	244	94.310	90.979	-3.331
105	246	95.116	91.673	-3.443
105	248	98.543	95.033	-3.510
105	374	95.273	92.215	-3.058
105	376	98.963	95.048	-3.915
Max		100.090	99.972	0.043
Average		97.102	95.354	-1.748
Min		94.148	90.425	-3.915
Std Dev		1.559	2.087	1.325

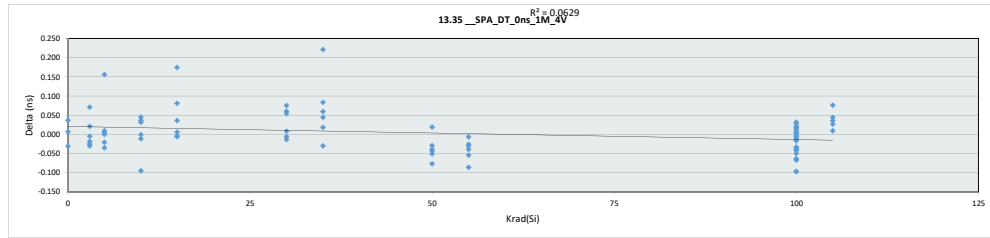


13.78_PSB_DT_100ns_1M_14V											
Test Site											
Tester											
Test Number											
Max Limit	110	ns									
Min Limit	85	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.572	95.841	94.572	95.574	94.306	94.896	93.507	94.149	92.547	90.425	90.979
Average	95.126	97.608	96.761	97.409	96.450	96.628	95.505	95.864	95.571	93.633	92.990
Max	96.235	99.972	98.822	99.726	98.547	98.920	97.329	96.673	96.741	96.547	95.048
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

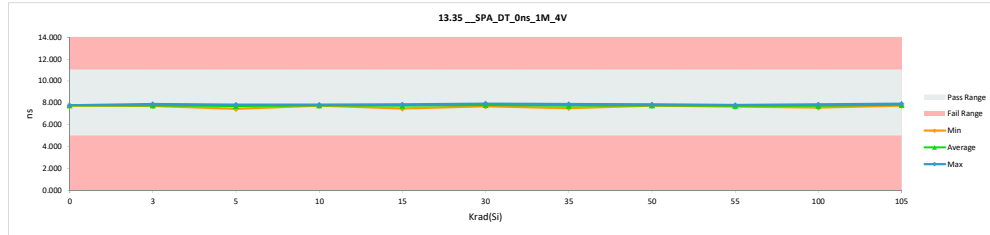


LDR TID Report
TPS7H5001-SP

13.35_SPA_DT_0ns_1M_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	7.789	7.797	0.008
0	118	7.753	7.791	0.038
0	122	7.786	7.756	-0.030
3	61	7.792	7.788	-0.004
3	62	7.719	7.741	0.022
3	63	7.786	7.769	-0.017
3	64	7.693	7.765	0.072
3	65	7.938	7.909	-0.029
3	66	7.747	7.723	-0.024
5	67	7.836	7.816	-0.020
5	68	7.714	7.720	0.006
5	69	7.712	7.722	0.010
5	70	7.312	7.469	0.157
5	71	7.876	7.842	-0.034
5	72	7.745	7.746	0.001
10	73	7.792	7.781	-0.011
10	74	7.719	7.752	0.033
10	75	7.786	7.824	0.038
10	76	7.693	7.739	0.046
10	77	7.938	7.844	-0.094
10	78	7.747	7.747	0.000
15	79	7.836	7.843	0.007
15	80	7.714	7.709	-0.005
15	81	7.712	7.794	0.082
15	82	7.312	7.487	0.175
15	83	7.876	7.873	-0.003
15	84	7.745	7.782	0.037
30	86	7.792	7.847	0.055
30	89	7.719	7.707	-0.012
30	91	7.786	7.847	0.061
30	92	7.693	7.769	0.076
30	94	7.938	7.947	0.009
30	95	7.747	7.742	-0.005
35	100	7.836	7.807	-0.029
35	101	7.714	7.774	0.060
35	102	7.712	7.796	0.084
35	103	7.312	7.534	0.222
35	104	7.876	7.895	0.019
35	105	7.745	7.790	0.045
50	106	7.832	7.782	-0.050
50	107	7.797	7.799	0.002
50	108	7.851	7.775	-0.076
50	109	7.792	7.764	-0.028
50	110	7.810	7.768	-0.042
50	111	7.859	7.879	0.020
55	113	7.822	7.769	-0.053
55	114	7.825	7.787	-0.038
55	115	7.845	7.820	-0.025
55	116	7.772	7.766	-0.006
55	126	7.691	7.661	-0.030
55	128	7.864	7.779	-0.085
100	129	7.851	7.755	-0.096
100	131	7.752	7.711	-0.041
100	132	7.678	7.644	-0.034
100	133	7.753	7.719	-0.034
100	134	7.793	7.809	0.016
100	135	7.751	7.757	0.006
100	138	7.812	7.823	0.011
100	139	7.745	7.773	0.028
100	140	7.785	7.777	-0.008
100	141	7.892	7.827	-0.065
100	142	7.745	7.729	-0.016
100	143	7.778	7.798	0.020
100	144	7.811	7.798	-0.013
100	220	7.772	7.740	-0.032
100	221	7.798	7.749	-0.049
100	224	7.765	7.769	0.004
100	225	7.814	7.810	-0.004
100	226	7.895	7.799	-0.096
100	228	7.777	7.738	-0.039
100	232	7.632	7.570	-0.062
100	233	7.750	7.749	-0.001
100	236	7.885	7.819	-0.066
100	238	7.747	7.767	0.020
100	243	7.835	7.868	0.033
105	244	7.798	7.835	0.037
105	246	7.748	7.793	0.045
105	248	7.757	7.834	0.077
105	374	7.909	7.919	0.010
105	376	7.722	7.750	0.028
	Max	7.938	7.947	0.222
	Average	7.768	7.772	0.003
	Min	7.312	7.469	-0.096
	Std Dev	0.111	0.080	0.055

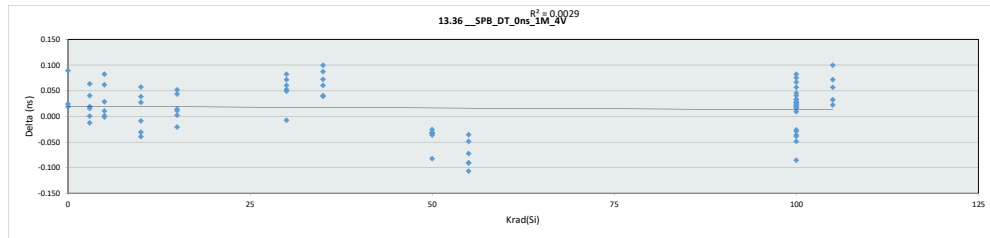


13.35_SPA_DT_0ns_1M_4V											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.756	7.723	7.469	7.739	7.487	7.707	7.534	7.759	7.661	7.570	7.750
Average	7.781	7.783	7.719	7.781	7.748	7.810	7.766	7.788	7.764	7.762	7.826
Max	7.797	7.909	7.842	7.844	7.873	7.947	7.895	7.879	7.820	7.868	7.919
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

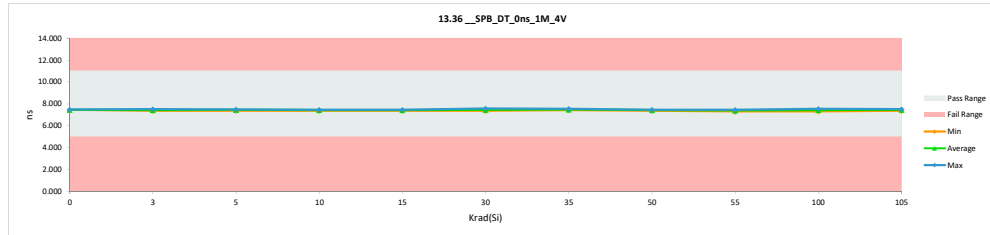


LDR TID Report
TPS7H5001-SP

13.36_SPB_DT_Ons_1M_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	7.410	7.500	0.090
0	118	7.418	7.443	0.025
0	122	7.425	7.444	0.019
3	61	7.372	7.436	0.064
3	62	7.367	7.387	0.020
3	63	7.422	7.463	0.041
3	64	7.412	7.428	0.016
3	65	7.513	7.514	0.001
3	66	7.392	7.380	-0.012
5	67	7.431	7.493	0.062
5	68	7.364	7.375	0.011
5	69	7.457	7.459	0.002
5	70	7.413	7.412	-0.001
5	71	7.397	7.480	0.083
5	72	7.455	7.484	0.029
10	73	7.372	7.411	0.039
10	74	7.367	7.395	0.028
10	75	7.422	7.392	-0.030
10	76	7.412	7.470	0.058
10	77	7.513	7.474	-0.039
10	78	7.392	7.384	-0.008
15	79	7.431	7.475	0.044
15	80	7.364	7.367	0.003
15	81	7.457	7.469	0.012
15	82	7.413	7.465	0.052
15	83	7.397	7.412	0.015
15	84	7.455	7.435	-0.020
30	86	7.372	7.425	0.053
30	89	7.367	7.417	0.050
30	91	7.422	7.505	0.083
30	92	7.412	7.484	0.072
30	94	7.513	7.574	0.061
30	95	7.392	7.385	-0.007
35	100	7.431	7.472	0.041
35	101	7.364	7.437	0.073
35	102	7.457	7.557	0.100
35	103	7.413	7.474	0.061
35	104	7.397	7.485	0.088
35	105	7.455	7.495	0.040
50	106	7.474	7.449	-0.025
50	107	7.422	7.390	-0.032
50	108	7.448	7.416	-0.032
50	109	7.459	7.377	-0.082
50	110	7.492	7.456	-0.036
50	111	7.492	7.461	-0.031
55	113	7.491	7.401	-0.090
55	114	7.514	7.424	-0.090
55	115	7.511	7.463	-0.048
55	116	7.360	7.325	-0.035
55	126	7.368	7.296	-0.072
55	128	7.543	7.437	-0.106
100	129	7.469	7.431	-0.038
100	131	7.454	7.473	0.019
100	132	7.314	7.390	0.076
100	133	7.352	7.372	0.020
100	134	7.400	7.434	0.034
100	135	7.460	7.487	0.027
100	138	7.478	7.503	0.025
100	139	7.466	7.438	-0.028
100	140	7.345	7.319	-0.026
100	141	7.499	7.414	-0.085
100	142	7.421	7.442	0.021
100	143	7.441	7.498	0.057
100	144	7.401	7.429	0.028
100	220	7.403	7.449	0.046
100	221	7.381	7.414	0.033
100	224	7.432	7.442	0.010
100	225	7.494	7.458	-0.036
100	226	7.503	7.520	0.017
100	228	7.377	7.400	0.023
100	232	7.307	7.374	0.067
100	233	7.450	7.402	-0.048
100	236	7.496	7.537	0.041
100	238	7.407	7.490	0.083
100	243	7.464	7.492	0.028
105	244	7.343	7.376	0.033
105	246	7.418	7.518	0.100
105	248	7.415	7.438	0.023
105	374	7.459	7.516	0.057
105	376	7.341	7.413	0.072
Max		7.543	7.574	0.100
Average		7.425	7.442	0.016
Min		7.307	7.296	-0.106
Std Dev		0.052	0.053	0.048

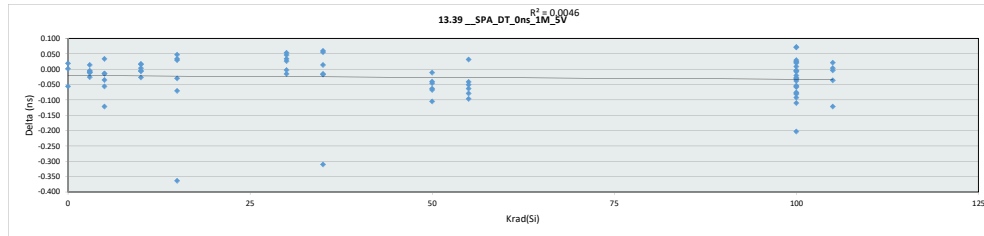


13.36_SPB_DT_Ons_1M_4V											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.443	7.380	7.375	7.384	7.367	7.385	7.437	7.377	7.296	7.319	7.376
Average	7.462	7.435	7.451	7.421	7.437	7.465	7.487	7.425	7.391	7.442	7.452
Max	7.500	7.514	7.493	7.474	7.475	7.574	7.557	7.461	7.463	7.537	7.518
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

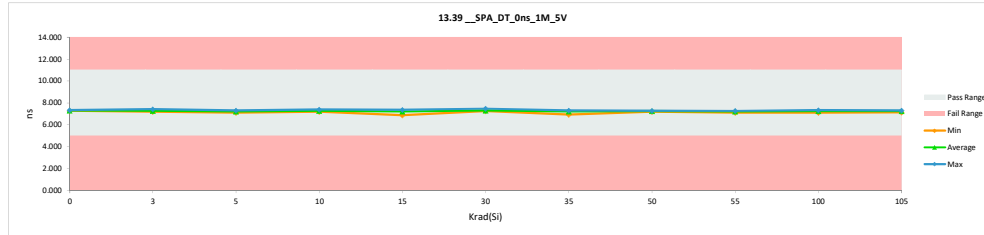


LDR TID Report
TPS7H5001-SP

13.39_SPA_DT_0ns_1M_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	7.326	7.328	0.002
0	118	7.390	7.335	-0.055
0	122	7.256	7.276	0.020
3	61	7.256	7.252	-0.004
3	62	7.281	7.256	-0.025
3	63	7.253	7.268	0.015
3	64	7.214	7.204	-0.010
3	65	7.430	7.423	-0.007
3	66	7.253	7.243	-0.010
5	67	7.243	7.278	0.035
5	68	7.221	7.208	-0.013
5	69	7.286	7.252	-0.034
5	70	7.240	7.119	-0.121
5	71	7.328	7.315	-0.013
5	72	7.306	7.251	-0.055
10	73	7.256	7.273	0.017
10	74	7.281	7.276	-0.005
10	75	7.253	7.271	0.018
10	76	7.214	7.209	-0.005
10	77	7.430	7.404	-0.026
10	78	7.253	7.258	0.005
15	79	7.243	7.291	0.048
15	80	7.221	7.256	0.035
15	81	7.286	7.216	-0.070
15	82	7.240	6.878	-0.362
15	83	7.328	7.359	0.031
15	84	7.306	7.277	-0.029
30	86	7.256	7.283	0.027
30	89	7.281	7.267	-0.014
30	91	7.253	7.251	-0.002
30	92	7.214	7.269	0.055
30	94	7.430	7.477	0.047
30	95	7.253	7.288	0.035
35	100	7.243	7.300	0.057
35	101	7.221	7.282	0.061
35	102	7.286	7.272	-0.014
35	103	7.240	6.931	-0.309
35	104	7.328	7.312	-0.016
35	105	7.306	7.321	0.015
50	106	7.316	7.250	-0.066
50	107	7.288	7.184	-0.104
50	108	7.308	7.263	-0.045
50	109	7.235	7.225	-0.010
50	110	7.317	7.278	-0.039
50	111	7.340	7.278	-0.062
55	113	7.308	7.258	-0.050
55	114	7.296	7.256	-0.040
55	115	7.241	7.274	0.033
55	116	7.238	7.160	-0.078
55	126	7.183	7.121	-0.062
55	128	7.360	7.264	-0.096
100	129	7.357	7.282	-0.075
100	131	7.248	7.321	0.073
100	132	7.249	7.172	-0.077
100	133	7.199	7.271	0.072
100	134	7.310	7.306	-0.004
100	135	7.283	7.253	-0.030
100	138	7.323	7.266	-0.057
100	139	7.298	7.279	-0.019
100	140	7.250	7.275	0.025
100	141	7.393	7.302	-0.091
100	142	7.291	7.264	-0.027
100	143	7.272	7.235	-0.037
100	144	7.240	7.233	-0.007
100	220	7.363	7.254	-0.109
100	221	7.377	7.175	-0.202
100	224	7.265	7.234	-0.031
100	225	7.292	7.212	-0.080
100	226	7.319	7.350	0.031
100	228	7.273	7.270	-0.003
100	232	7.157	7.104	-0.053
100	233	7.208	7.232	0.024
100	236	7.370	7.315	-0.055
100	238	7.259	7.269	0.010
100	243	7.312	7.333	0.021
105	244	7.245	7.267	0.022
105	246	7.273	7.270	-0.003
105	248	7.245	7.250	0.005
105	374	7.353	7.318	-0.035
105	376	7.257	7.136	-0.121
Max		7.430	7.477	0.073
Average		7.283	7.256	-0.027
Min		7.157	6.878	-0.362
Std Dev		0.056	0.083	0.070

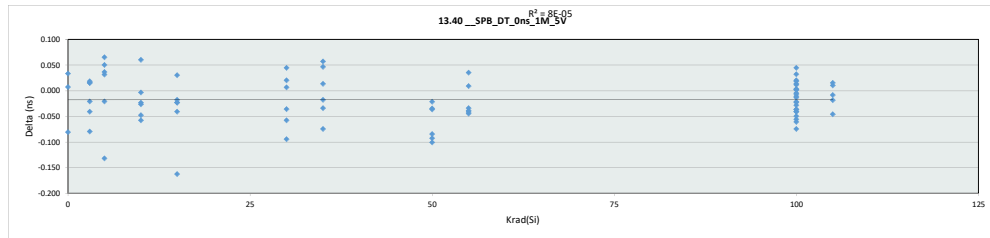


13.39_SPA_DT_0ns_1M_5V											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.276	7.204	7.119	7.209	6.878	7.251	6.931	7.184	7.121	7.104	7.136
Average	7.313	7.274	7.237	7.282	7.213	7.306	7.236	7.246	7.222	7.259	7.248
Max	7.335	7.423	7.315	7.404	7.359	7.477	7.321	7.278	7.274	7.350	7.318
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

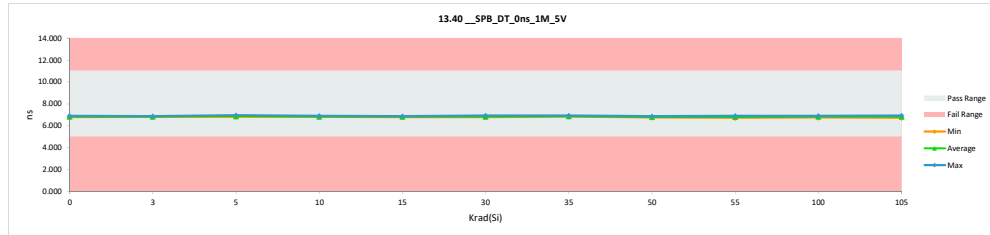


LDR TID Report
TPS7H5001-SP

13.40_SPB_DT_Ons_1M_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	6.898	6.906	0.008
0	118	6.882	6.802	-0.080
0	122	6.831	6.865	0.034
3	61	6.848	6.864	0.016
3	62	6.869	6.829	-0.040
3	63	6.905	6.885	-0.020
3	64	6.839	6.858	0.019
3	65	6.961	6.882	-0.079
3	66	6.794	6.810	0.016
5	67	6.819	6.870	0.051
5	68	6.949	6.818	-0.131
5	69	6.908	6.974	0.066
5	70	6.836	6.868	0.032
5	71	6.851	6.888	0.037
5	72	6.914	6.894	-0.020
10	73	6.848	6.825	-0.023
10	74	6.869	6.812	-0.057
10	75	6.905	6.879	-0.026
10	76	6.839	6.836	-0.003
10	77	6.961	6.914	-0.047
10	78	6.794	6.855	0.061
15	79	6.819	6.850	0.031
15	80	6.949	6.787	-0.162
15	81	6.908	6.885	-0.023
15	82	6.836	6.814	-0.022
15	83	6.851	6.834	-0.017
15	84	6.914	6.874	-0.040
30	86	6.848	6.813	-0.035
30	89	6.869	6.876	0.007
30	91	6.905	6.926	0.021
30	92	6.839	6.782	-0.057
30	94	6.961	6.867	-0.094
30	95	6.794	6.839	0.045
35	100	6.819	6.866	0.047
35	101	6.949	6.875	-0.074
35	102	6.908	6.922	0.014
35	103	6.836	6.894	0.058
35	104	6.851	6.834	-0.017
35	105	6.914	6.881	-0.033
50	106	6.812	6.791	-0.021
50	107	6.881	6.789	-0.092
50	108	6.934	6.850	-0.084
50	109	6.804	6.770	-0.034
50	110	6.909	6.873	-0.036
50	111	6.942	6.842	-0.100
55	113	6.860	6.816	-0.044
55	114	6.833	6.800	-0.033
55	115	6.858	6.894	0.036
55	116	6.760	6.719	-0.041
55	126	6.818	6.780	-0.038
55	128	6.864	6.874	0.010
100	129	6.891	6.894	0.003
100	131	6.898	6.888	-0.010
100	132	6.805	6.769	-0.036
100	133	6.859	6.785	-0.074
100	134	6.873	6.837	-0.036
100	135	6.834	6.821	-0.013
100	138	6.911	6.890	-0.021
100	139	6.820	6.816	-0.004
100	140	6.832	6.810	-0.022
100	141	6.881	6.875	-0.006
100	142	6.873	6.845	-0.028
100	143	6.886	6.837	-0.049
100	144	6.874	6.814	-0.060
100	220	6.834	6.879	0.045
100	221	6.778	6.799	0.021
100	224	6.820	6.832	0.012
100	225	6.908	6.868	-0.040
100	226	6.901	6.915	0.014
100	228	6.798	6.802	0.004
100	232	6.823	6.768	-0.055
100	233	6.835	6.837	0.002
100	236	6.883	6.843	-0.040
100	238	6.843	6.862	0.019
100	243	6.876	6.909	0.033
105	244	6.779	6.734	-0.045
105	246	6.925	6.936	0.011
105	248	6.872	6.864	-0.008
105	374	6.887	6.903	0.016
105	376	6.791	6.773	-0.018
	Max	6.961	6.974	0.066
	Average	6.864	6.847	-0.017
	Min	6.760	6.719	-0.041
	Std Dev	0.048	0.048	0.044

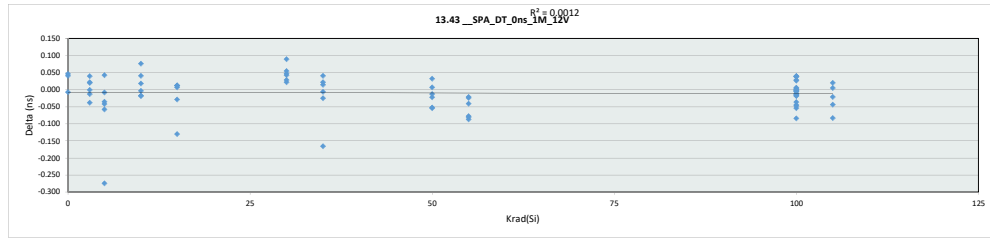


13.40_SPB_DT_Ons_1M_5V											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	6.802	6.810	6.818	6.812	6.787	6.782	6.834	6.770	6.719	6.768	6.734
Average	6.858	6.855	6.885	6.854	6.841	6.851	6.879	6.819	6.814	6.841	6.842
Max	6.906	6.885	6.974	6.914	6.885	6.926	6.922	6.873	6.894	6.915	6.936
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

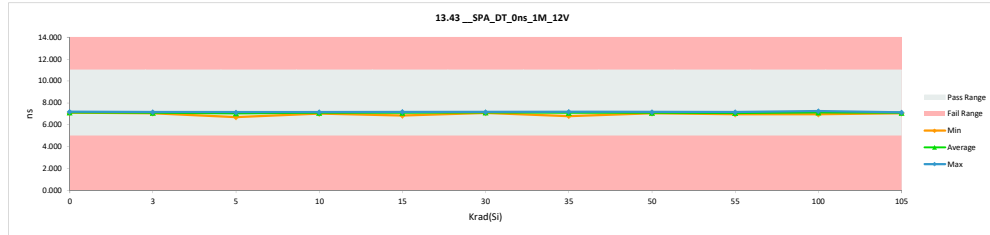


LDR TID Report
TPS7H5001-SP

13.43_SPA_DT_Ons_1M_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	7.151	7.198	0.047
0	118	7.100	7.094	-0.006
0	122	7.069	7.111	0.042
3	61	7.063	7.051	-0.012
3	62	7.093	7.114	0.021
3	63	7.124	7.147	0.023
3	64	7.038	7.038	0.000
3	65	7.135	7.176	0.041
3	66	7.134	7.097	-0.037
5	67	7.145	7.088	-0.057
5	68	7.179	7.145	-0.034
5	69	7.103	7.146	0.043
5	70	6.968	6.694	-0.274
5	71	7.182	7.141	-0.041
5	72	7.136	7.129	-0.007
10	73	7.063	7.105	0.042
10	74	7.093	7.170	0.077
10	75	7.124	7.143	0.019
10	76	7.038	7.021	-0.017
10	77	7.135	7.132	-0.003
10	78	7.134	7.116	-0.018
15	79	7.145	7.117	-0.028
15	80	7.179	7.192	0.013
15	81	7.103	7.116	0.013
15	82	6.968	6.839	-0.129
15	83	7.182	7.194	0.012
15	84	7.136	7.144	0.008
30	86	7.063	7.092	0.029
30	89	7.093	7.183	0.090
30	91	7.124	7.180	0.056
30	92	7.038	7.081	0.043
30	94	7.135	7.183	0.048
30	95	7.134	7.157	0.023
35	100	7.145	7.140	-0.005
35	101	7.179	7.202	0.023
35	102	7.103	7.118	0.015
35	103	6.968	6.803	-0.165
35	104	7.182	7.158	-0.024
35	105	7.136	7.178	0.042
50	106	7.156	7.105	-0.051
50	107	7.051	7.059	0.008
50	108	7.096	7.129	0.033
50	109	7.111	7.099	-0.012
50	110	7.134	7.081	-0.053
50	111	7.194	7.173	-0.021
55	113	7.095	7.075	-0.020
55	114	7.107	7.067	-0.040
55	115	7.181	7.158	-0.023
55	116	7.124	7.047	-0.077
55	126	7.044	6.958	-0.086
55	128	7.255	7.176	-0.079
100	129	7.156	7.112	-0.044
100	131	7.161	7.157	-0.004
100	132	7.123	7.076	-0.047
100	133	7.105	7.094	-0.011
100	134	7.188	7.171	-0.017
100	135	7.186	7.188	0.002
100	138	7.199	7.181	-0.018
100	139	7.132	7.132	0.000
100	140	7.131	7.136	0.005
100	141	7.205	7.246	0.041
100	142	7.088	7.087	-0.001
100	143	7.081	7.108	0.027
100	144	7.075	7.104	0.029
100	220	7.127	7.123	-0.004
100	221	7.103	7.110	0.007
100	224	7.181	7.098	-0.083
100	225	7.134	7.120	-0.014
100	226	7.135	7.100	-0.035
100	228	7.140	7.128	-0.012
100	232	7.010	6.957	-0.053
100	233	7.064	7.063	-0.001
100	236	7.146	7.187	0.041
100	238	7.140	7.179	0.039
100	243	7.172	7.212	0.040
105	244	7.130	7.048	-0.082
105	246	7.142	7.148	0.006
105	248	7.119	7.076	-0.043
105	374	7.146	7.126	-0.020
105	376	7.095	7.116	0.021
Max		7.255	7.246	0.090
Average		7.120	7.111	-0.009
Min		6.968	6.694	-0.274
Std Dev		0.054	0.085	0.053

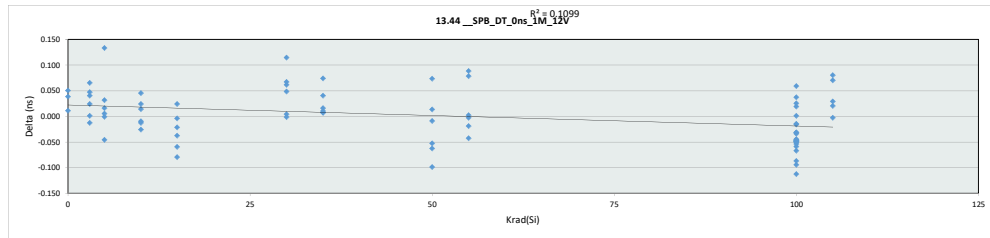


13.43_SPA_DT_Ons_1M_12V											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.094	7.038	6.694	7.021	6.839	7.081	6.803	7.059	6.958	6.957	7.048
Average	7.134	7.104	7.057	7.115	7.100	7.146	7.100	7.108	7.080	7.128	7.103
Max	7.198	7.176	7.146	7.170	7.194	7.183	7.202	7.173	7.176	7.246	7.148
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

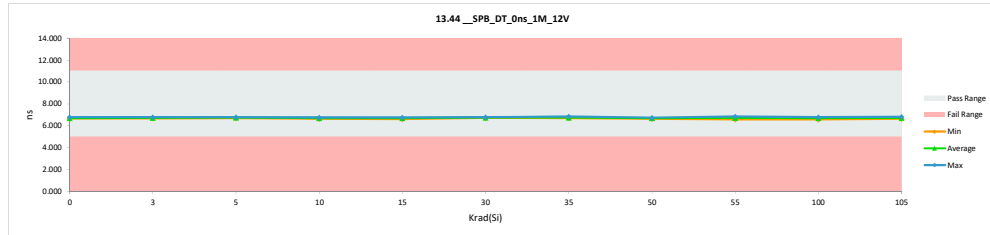


LDR TID Report
TPS7H5001-SP

13.44_SPB_DT_Ons_1M_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	6.739	6.778	0.039
0	118	6.664	6.676	0.012
0	122	6.621	6.672	0.051
3	61	6.709	6.697	-0.012
3	62	6.726	6.767	0.041
3	63	6.714	6.780	0.066
3	64	6.661	6.686	0.025
3	65	6.737	6.739	0.002
3	66	6.673	6.721	0.048
5	67	6.714	6.731	0.017
5	68	6.732	6.764	0.032
5	69	6.740	6.746	0.006
5	70	6.657	6.791	0.134
5	71	6.696	6.696	0.000
5	72	6.837	6.792	-0.045
10	73	6.709	6.684	-0.025
10	74	6.726	6.714	-0.012
10	75	6.714	6.739	0.025
10	76	6.661	6.652	-0.009
10	77	6.737	6.752	0.015
10	78	6.673	6.719	0.046
15	79	6.714	6.655	-0.059
15	80	6.732	6.729	-0.003
15	81	6.740	6.765	0.025
15	82	6.657	6.620	-0.037
15	83	6.696	6.675	-0.021
15	84	6.837	6.758	-0.079
30	86	6.709	6.714	0.005
30	89	6.726	6.725	-0.001
30	91	6.714	6.776	0.062
30	92	6.661	6.776	0.115
30	94	6.737	6.786	0.049
30	95	6.673	6.741	0.068
35	100	6.714	6.722	0.008
35	101	6.732	6.740	0.008
35	102	6.740	6.815	0.075
35	103	6.657	6.698	0.041
35	104	6.696	6.713	0.017
35	105	6.837	6.848	0.011
50	106	6.671	6.685	0.014
50	107	6.653	6.727	0.074
50	108	6.725	6.663	-0.062
50	109	6.687	6.635	-0.052
50	110	6.735	6.727	-0.008
50	111	6.837	6.739	-0.098
55	113	6.695	6.698	0.003
55	114	6.717	6.675	-0.042
55	115	6.733	6.812	0.079
55	116	6.712	6.694	-0.018
55	126	6.586	6.584	-0.002
55	128	6.752	6.841	0.089
100	129	6.705	6.639	-0.066
100	131	6.752	6.708	-0.044
100	132	6.685	6.639	-0.046
100	133	6.677	6.631	-0.046
100	134	6.753	6.641	-0.112
100	135	6.763	6.710	-0.053
100	138	6.772	6.724	-0.048
100	139	6.721	6.672	-0.049
100	140	6.670	6.576	-0.094
100	141	6.758	6.712	-0.046
100	142	6.734	6.686	-0.048
100	143	6.709	6.694	-0.015
100	144	6.743	6.710	-0.033
100	220	6.691	6.751	0.060
100	221	6.765	6.716	-0.050
100	224	6.770	6.684	-0.086
100	225	6.726	6.695	-0.031
100	226	6.745	6.714	-0.031
100	228	6.715	6.701	-0.014
100	232	6.693	6.635	-0.058
100	233	6.658	6.684	0.026
100	236	6.719	6.739	0.020
100	238	6.761	6.799	0.038
100	243	6.750	6.752	0.002
105	244	6.609	6.690	0.081
105	246	6.822	6.820	-0.002
105	248	6.569	6.640	0.071
105	374	6.714	6.744	0.030
105	376	6.667	6.688	0.021
Max		6.837	6.848	0.134
Average		6.714	6.715	0.001
Min		6.569	6.576	-0.112
Std Dev		0.051	0.055	0.050

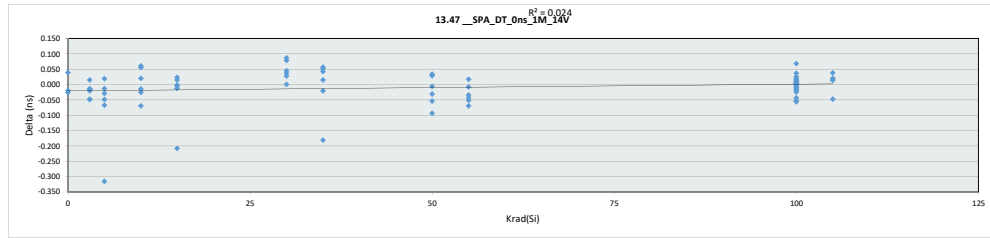


13.44_SPB_DT_Ons_1M_12V											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	6.672	6.686	6.696	6.652	6.620	6.714	6.698	6.635	6.584	6.576	6.640
Average	6.709	6.732	6.753	6.710	6.700	6.753	6.756	6.696	6.717	6.692	6.716
Max	6.778	6.780	6.792	6.752	6.765	6.786	6.848	6.739	6.841	6.799	6.820
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

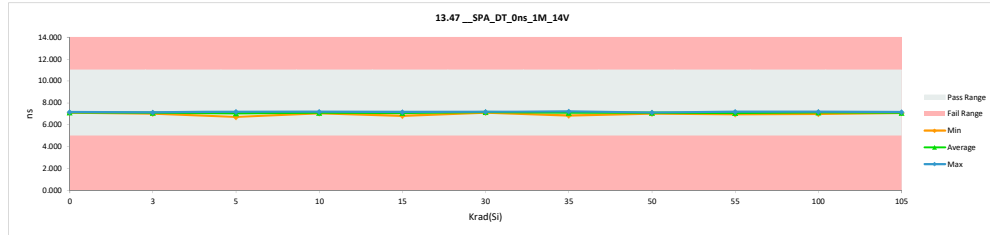


LDR TID Report
TPS7H5001-SP

13.47_SPA_DT_Ons_1M_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	7.125	7.165	0.040
0	118	7.145	7.127	-0.018
0	122	7.128	7.103	-0.025
3	61	7.083	7.099	0.016
3	62	7.124	7.112	-0.012
3	63	7.156	7.141	-0.015
3	64	7.070	7.023	-0.047
3	65	7.186	7.138	-0.048
3	66	7.084	7.065	-0.019
5	67	7.096	7.048	-0.048
5	68	7.131	7.119	-0.012
5	69	7.140	7.074	-0.066
5	70	7.028	6.714	-0.314
5	71	7.170	7.190	0.020
5	72	7.139	7.111	-0.028
10	73	7.083	7.104	0.021
10	74	7.124	7.186	0.062
10	75	7.156	7.143	-0.013
10	76	7.070	7.045	-0.025
10	77	7.186	7.118	-0.068
10	78	7.084	7.141	0.057
15	79	7.096	7.093	-0.003
15	80	7.131	7.147	0.016
15	81	7.140	7.129	-0.011
15	82	7.028	6.821	-0.207
15	83	7.170	7.169	-0.001
15	84	7.139	7.163	0.024
30	86	7.083	7.121	0.038
30	89	7.124	7.204	0.080
30	91	7.156	7.202	0.046
30	92	7.070	7.098	0.028
30	94	7.186	7.187	0.001
30	95	7.084	7.172	0.088
35	100	7.096	7.154	0.058
35	101	7.131	7.174	0.043
35	102	7.140	7.121	-0.019
35	103	7.028	6.848	-0.180
35	104	7.170	7.222	0.052
35	105	7.139	7.155	0.016
50	106	7.160	7.107	-0.053
50	107	7.100	7.008	-0.092
50	108	7.113	7.108	-0.005
50	109	7.083	7.118	0.035
50	110	7.091	7.120	0.029
50	111	7.158	7.128	-0.030
55	113	7.043	7.061	0.018
55	114	7.129	7.061	-0.068
55	115	7.183	7.150	-0.033
55	116	7.124	7.083	-0.041
55	126	7.004	6.953	-0.051
55	128	7.210	7.203	-0.007
100	129	7.113	7.114	0.001
100	131	7.169	7.156	-0.013
100	132	7.077	7.085	0.008
100	133	7.138	7.086	-0.052
100	134	7.180	7.166	-0.014
100	135	7.144	7.151	0.007
100	138	7.170	7.146	-0.024
100	139	7.172	7.130	-0.042
100	140	7.116	7.185	0.069
100	141	7.243	7.188	-0.055
100	142	7.096	7.096	0.000
100	143	7.125	7.119	-0.006
100	144	7.098	7.089	-0.009
100	220	7.116	7.127	0.011
100	221	7.118	7.138	0.020
100	224	7.117	7.120	0.003
100	225	7.116	7.124	0.008
100	226	7.083	7.086	0.003
100	228	7.134	7.116	-0.018
100	232	7.001	7.005	0.004
100	233	7.080	7.096	0.016
100	236	7.147	7.173	0.026
100	238	7.140	7.178	0.038
100	243	7.167	7.183	0.016
105	244	7.131	7.084	-0.047
105	246	7.151	7.172	0.021
105	248	7.086	7.101	0.015
105	374	7.105	7.144	0.039
105	376	7.076	7.096	0.020
Max		7.243	7.222	0.088
Average		7.120	7.111	-0.009
Min		7.001	6.714	-0.314
Std Dev		0.045	0.080	0.059

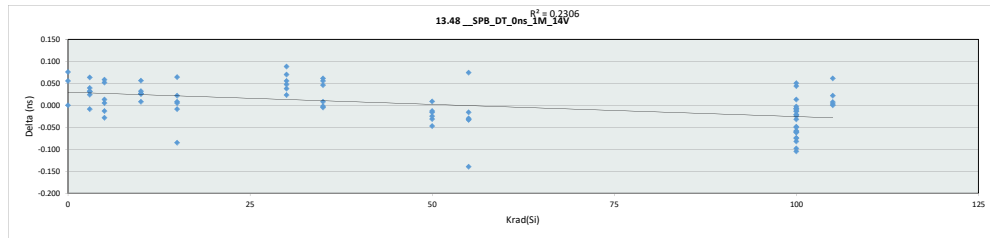


13.47_SPA_DT_Ons_1M_14V											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.103	7.023	6.714	7.045	6.821	7.098	6.848	7.008	6.953	7.005	7.084
Average	7.132	7.096	7.043	7.123	7.087	7.164	7.112	7.098	7.085	7.127	7.119
Max	7.165	7.141	7.190	7.186	7.169	7.204	7.222	7.128	7.203	7.188	7.172
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

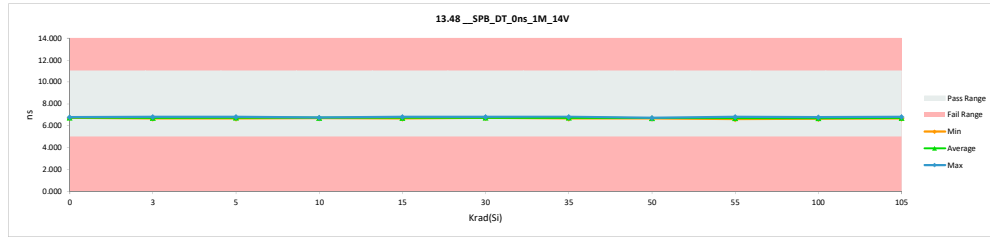


LDR TID Report
TPS7H5001-SP

13.48_SPB_DT_Ons_1M_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	6.743	6.799	0.056
0	118	6.684	6.761	0.077
0	122	6.727	6.728	0.001
3	61	6.679	6.671	-0.008
3	62	6.745	6.778	0.033
3	63	6.749	6.813	0.064
3	64	6.688	6.713	0.025
3	65	6.737	6.777	0.040
3	66	6.674	6.706	0.032
5	67	6.746	6.719	-0.027
5	68	6.729	6.782	0.053
5	69	6.775	6.763	-0.012
5	70	6.656	6.670	0.014
5	71	6.700	6.759	0.059
5	72	6.798	6.804	0.006
10	73	6.679	6.712	0.033
10	74	6.745	6.772	0.027
10	75	6.749	6.758	0.009
10	76	6.688	6.715	0.027
10	77	6.737	6.765	0.028
10	78	6.674	6.731	0.057
15	79	6.746	6.662	-0.084
15	80	6.729	6.738	0.009
15	81	6.775	6.767	-0.008
15	82	6.656	6.679	0.023
15	83	6.700	6.765	0.065
15	84	6.798	6.804	0.006
30	86	6.679	6.718	0.039
30	89	6.745	6.769	0.024
30	91	6.749	6.805	0.056
30	92	6.688	6.777	0.089
30	94	6.737	6.785	0.048
30	95	6.674	6.745	0.071
35	100	6.746	6.742	-0.004
35	101	6.729	6.776	0.047
35	102	6.775	6.831	0.056
35	103	6.656	6.665	0.009
35	104	6.700	6.762	0.062
35	105	6.798	6.797	-0.001
50	106	6.707	6.692	-0.015
50	107	6.709	6.663	-0.046
50	108	6.684	6.694	0.010
50	109	6.693	6.681	-0.012
50	110	6.705	6.675	-0.030
50	111	6.759	6.735	-0.024
55	113	6.687	6.672	-0.015
55	114	6.702	6.670	-0.032
55	115	6.745	6.820	0.075
55	116	6.681	6.652	-0.029
55	126	6.636	6.604	-0.032
55	128	6.885	6.746	-0.139
100	129	6.718	6.657	-0.061
100	131	6.803	6.797	-0.006
100	132	6.742	6.645	-0.097
100	133	6.677	6.628	-0.049
100	134	6.726	6.667	-0.059
100	135	6.781	6.700	-0.081
100	138	6.779	6.767	-0.012
100	139	6.698	6.674	-0.024
100	140	6.727	6.670	-0.057
100	141	6.740	6.719	-0.021
100	142	6.780	6.676	-0.104
100	143	6.704	6.698	-0.006
100	144	6.712	6.652	-0.060
100	220	6.725	6.651	-0.074
100	221	6.705	6.692	-0.013
100	224	6.755	6.682	-0.073
100	225	6.693	6.744	0.051
100	226	6.725	6.717	-0.008
100	228	6.662	6.631	-0.031
100	232	6.718	6.670	-0.048
100	233	6.663	6.677	0.014
100	236	6.765	6.763	-0.002
100	238	6.687	6.732	0.045
100	243	6.743	6.723	-0.020
105	244	6.660	6.664	0.004
105	246	6.762	6.824	0.062
105	248	6.712	6.735	0.023
105	374	6.720	6.721	0.001
105	376	6.675	6.683	0.008
Max		6.885	6.831	0.089
Average		6.722	6.723	0.001
Min		6.636	6.604	-0.139
Std Dev		0.043	0.054	0.047

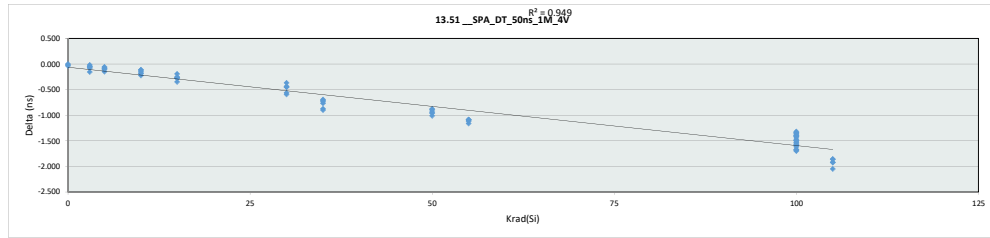


13.48_SPB_DT_Ons_1M_14V											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	6.728	6.671	6.670	6.712	6.662	6.718	6.665	6.663	6.604	6.628	6.664
Average	6.763	6.743	6.750	6.742	6.736	6.767	6.762	6.690	6.694	6.693	6.725
Max	6.799	6.813	6.804	6.772	6.804	6.805	6.831	6.735	6.820	6.797	6.824
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

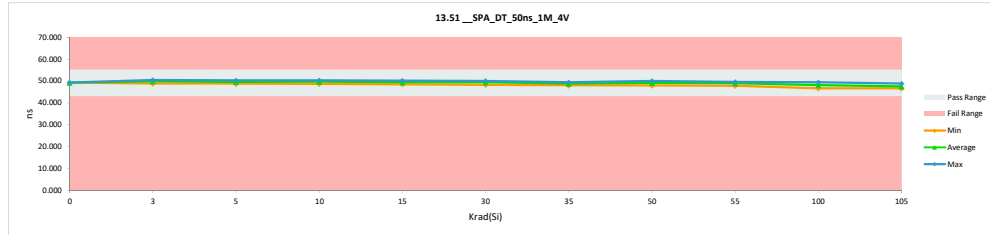


LDR TID Report
TPS7H5001-SP

13.51_SPA_DT_50ns_1M_4V				
Test Site	Tester	Unit	Max Limit	Min Limit
ns	ns	ns	55	43
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	49.332	49.322	-0.010
0	118	49.195	49.204	0.009
0	122	49.323	49.296	-0.027
3	61	50.106	50.098	-0.008
3	62	49.876	49.821	-0.055
3	63	48.893	48.822	-0.071
3	64	50.456	50.420	-0.036
3	65	49.692	49.545	-0.147
3	66	50.320	50.280	-0.040
5	67	49.609	49.543	-0.066
5	68	48.741	48.694	-0.047
5	69	49.560	49.421	-0.139
5	70	49.678	49.592	-0.086
5	71	50.162	50.088	-0.074
5	72	50.373	50.278	-0.095
10	73	50.106	49.999	-0.107
10	74	49.876	49.742	-0.134
10	75	48.893	48.677	-0.216
10	76	50.456	50.279	-0.177
10	77	49.692	49.525	-0.167
10	78	50.320	50.208	-0.112
15	79	49.609	49.425	-0.184
15	80	48.741	48.494	-0.247
15	81	49.560	49.220	-0.340
15	82	49.678	49.413	-0.265
15	83	50.162	49.913	-0.249
15	84	50.373	50.098	-0.275
30	86	50.106	49.664	-0.442
30	89	49.876	49.444	-0.432
30	91	48.893	48.308	-0.585
30	92	50.456	50.011	-0.445
30	94	49.692	49.135	-0.557
30	95	50.320	49.961	-0.359
35	100	49.609	48.902	-0.707
35	101	48.741	48.012	-0.729
35	102	49.560	48.666	-0.894
35	103	49.678	48.916	-0.762
35	104	50.162	49.478	-0.684
35	105	50.373	49.507	-0.866
50	106	50.583	49.709	-0.874
50	107	51.024	50.082	-0.942
50	108	48.894	47.984	-0.910
50	109	49.962	49.088	-0.874
50	110	49.684	48.681	-1.003
50	111	50.531	49.579	-0.952
55	113	50.660	49.565	-1.095
55	114	48.933	47.778	-1.155
55	115	50.282	49.200	-1.082
55	116	50.613	49.531	-1.082
55	126	49.292	48.180	-1.112
55	128	50.445	49.363	-1.082
100	129	50.340	48.731	-1.609
100	131	49.080	47.518	-1.562
100	132	49.570	47.996	-1.574
100	133	48.890	47.407	-1.483
100	134	50.598	49.137	-1.461
100	135	50.074	48.710	-1.364
100	138	50.404	48.744	-1.660
100	139	48.192	46.666	-1.526
100	140	48.908	47.228	-1.680
100	141	50.403	48.737	-1.666
100	142	48.779	47.444	-1.335
100	143	48.784	47.093	-1.691
100	144	49.343	47.853	-1.490
100	220	49.550	48.214	-1.336
100	221	49.413	48.070	-1.343
100	224	48.623	47.205	-1.418
100	225	50.371	48.785	-1.586
100	226	50.205	48.791	-1.414
100	228	50.507	49.118	-1.389
100	232	49.287	47.974	-1.313
100	233	49.869	48.483	-1.386
100	236	48.739	47.341	-1.398
100	238	50.867	49.372	-1.495
100	243	49.513	47.978	-1.535
105	244	48.786	46.874	-1.912
105	246	48.906	47.055	-1.851
105	248	49.968	48.110	-1.858
105	374	48.677	46.636	-2.041
105	376	50.730	48.812	-1.918
	Max	51.024	50.420	0.009
	Average	49.732	48.878	-0.854
	Min	48.192	46.636	-2.041
	Std Dev	0.665	0.967	0.633

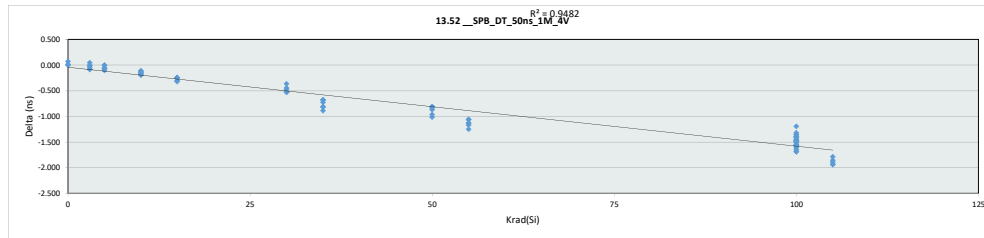


13.51_SPA_DT_50ns_1M_4V											
Test Site	Tester	Unit	Max Limit	Min Limit							
ns	ns	ns	55	43							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	49.204	48.822	48.694	48.677	48.494	48.308	48.012	47.984	47.778	46.666	46.636
Average	49.274	49.831	49.603	49.738	49.427	49.421	48.914	49.187	48.936	48.108	47.497
Max	49.322	50.420	50.278	50.279	50.098	50.011	49.507	50.082	49.565	49.372	48.812
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

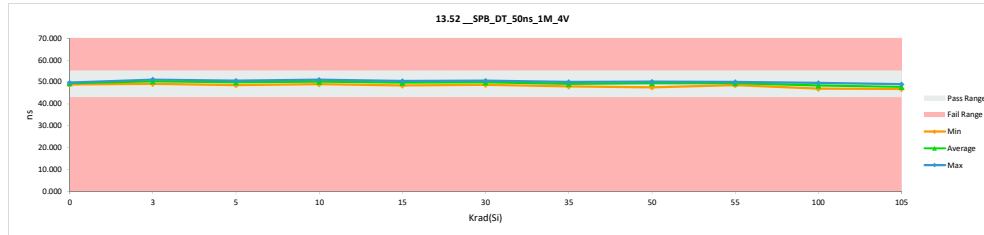


LDR TID Report
TPS7H5001-SP

13.52_SPB_DT_50ns_1M_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	55	55		
Min Limit	43	43		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	49.591	49.611	0.020
0	118	48.875	48.876	0.001
0	122	49.625	49.701	0.076
3	61	50.618	50.614	-0.004
3	62	50.398	50.452	0.054
3	63	49.177	49.108	-0.069
3	64	51.133	51.133	0.000
3	65	50.389	50.310	-0.079
3	66	50.377	50.343	-0.034
5	67	50.198	50.141	-0.057
5	68	48.615	48.616	0.001
5	69	49.791	49.742	-0.049
5	70	49.936	49.837	-0.099
5	71	50.678	50.613	-0.065
5	72	50.440	50.440	0.000
10	73	50.618	50.428	-0.190
10	74	50.398	50.277	-0.121
10	75	49.177	49.010	-0.167
10	76	51.133	50.994	-0.139
10	77	50.389	50.286	-0.103
10	78	50.377	50.214	-0.163
15	79	50.198	49.955	-0.243
15	80	48.615	48.382	-0.233
15	81	49.791	49.548	-0.243
15	82	49.936	49.623	-0.313
15	83	50.678	50.432	-0.246
15	84	50.440	50.143	-0.297
30	86	50.618	50.178	-0.440
30	89	50.398	50.041	-0.357
30	91	49.177	48.658	-0.519
30	92	51.133	50.671	-0.462
30	94	50.389	49.868	-0.521
30	95	50.377	49.868	-0.509
35	100	50.198	49.518	-0.680
35	101	48.615	47.942	-0.673
35	102	49.791	48.992	-0.799
35	103	49.936	49.056	-0.880
35	104	50.678	49.959	-0.719
35	105	50.440	49.624	-0.816
50	106	50.710	49.755	-0.955
50	107	51.230	50.220	-1.010
50	108	48.386	47.583	-0.803
50	109	50.619	49.810	-0.809
50	110	50.142	49.314	-0.828
50	111	50.474	49.607	-0.867
55	113	50.912	49.860	-1.052
55	114	49.645	48.525	-1.120
55	115	50.580	49.448	-1.132
55	116	50.837	49.594	-1.243
55	126	50.131	48.966	-1.165
55	128	51.090	50.031	-1.059
100	129	50.556	49.005	-1.551
100	131	48.959	47.350	-1.609
100	132	49.646	48.106	-1.540
100	133	49.224	47.545	-1.679
100	134	50.639	49.163	-1.476
100	135	50.642	49.135	-1.507
100	138	50.665	49.095	-1.570
100	139	48.322	46.931	-1.391
100	140	48.833	47.187	-1.646
100	141	50.930	49.376	-1.554
100	142	48.949	47.546	-1.403
100	143	49.260	47.580	-1.680
100	144	49.594	48.151	-1.443
100	220	50.422	49.109	-1.313
100	221	50.430	48.974	-1.456
100	224	49.120	47.775	-1.345
100	225	50.749	49.182	-1.567
100	226	50.768	49.582	-1.186
100	228	50.833	49.376	-1.457
100	232	50.012	48.630	-1.382
100	233	50.607	49.125	-1.482
100	236	48.802	47.448	-1.354
100	238	50.787	49.376	-1.411
100	243	50.086	48.607	-1.479
105	244	48.834	46.901	-1.933
105	246	48.629	46.847	-1.782
105	248	50.665	48.776	-1.889
105	374	49.198	47.269	-1.929
105	376	50.867	49.020	-1.847
Max		51.230	51.133	0.076
Average		50.064	49.226	-0.838
Min		48.322	46.847	-1.933
Std Dev		0.770	1.041	0.636

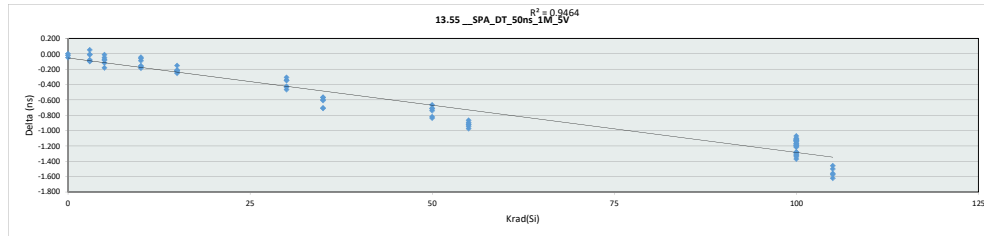


13.52_SPB_DT_50ns_1M_4V											
Test Site											
Tester											
Test Number											
Max Limit	55	ns									
Min Limit	43	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	48.876	49.108	48.616	49.010	48.382	48.658	47.942	47.583	48.525	46.931	46.847
Average	49.396	50.327	49.898	50.202	49.681	49.881	49.182	49.382	49.404	48.473	47.763
Max	49.701	51.133	50.613	50.994	50.432	50.671	49.959	50.220	50.031	49.582	49.020
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

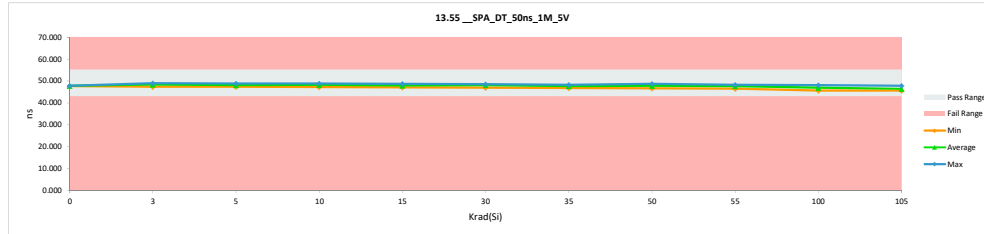


LDR TID Report
TPS7H5001-SP

13.55_SPA_DT_50ns_1M_5V					
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit
		ns	ns	55	55
				43	43
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta	
0	117	47.893	47.899	0.006	
0	118	47.754	47.710	-0.044	
0	122	47.819	47.805	-0.014	
3	61	48.655	48.646	-0.009	
3	62	48.469	48.467	-0.002	
3	63	47.420	47.348	-0.072	
3	64	49.042	48.950	-0.092	
3	65	48.257	48.164	-0.093	
3	66	48.862	48.919	0.057	
5	67	48.211	48.141	-0.070	
5	68	47.341	47.334	-0.007	
5	69	48.016	47.838	-0.178	
5	70	48.195	48.117	-0.078	
5	71	48.726	48.610	-0.116	
5	72	48.926	48.883	-0.043	
10	73	48.655	48.613	-0.042	
10	74	48.469	48.412	-0.057	
10	75	47.420	47.239	-0.181	
10	76	49.042	48.890	-0.152	
10	77	48.257	48.100	-0.157	
10	78	48.862	48.774	-0.088	
15	79	48.211	47.998	-0.213	
15	80	47.341	47.126	-0.215	
15	81	48.016	47.768	-0.248	
15	82	48.195	48.046	-0.149	
15	83	48.726	48.496	-0.230	
15	84	48.926	48.718	-0.208	
30	86	48.655	48.353	-0.302	
30	89	48.469	48.129	-0.340	
30	91	47.420	46.957	-0.463	
30	92	49.042	48.615	-0.427	
30	94	48.257	47.836	-0.421	
30	95	48.862	48.523	-0.339	
35	100	48.211	47.649	-0.562	
35	101	47.341	46.777	-0.564	
35	102	48.016	47.315	-0.701	
35	103	48.195	47.601	-0.594	
35	104	48.726	48.124	-0.602	
35	105	48.926	48.224	-0.702	
50	106	49.088	48.386	-0.702	
50	107	49.551	48.734	-0.817	
50	108	47.382	46.671	-0.711	
50	109	48.480	47.819	-0.661	
50	110	48.283	47.450	-0.833	
50	111	49.009	48.271	-0.738	
55	113	49.241	48.299	-0.942	
55	114	47.412	46.493	-0.919	
55	115	48.808	47.919	-0.889	
55	116	49.146	48.283	-0.863	
55	126	47.934	47.021	-0.913	
55	128	49.053	48.085	-0.968	
100	129	48.927	47.617	-1.310	
100	131	47.592	46.412	-1.180	
100	132	48.045	46.762	-1.283	
100	133	47.457	46.245	-1.212	
100	134	49.182	47.992	-1.190	
100	135	48.625	47.465	-1.160	
100	138	48.989	47.625	-1.364	
100	139	46.830	45.631	-1.199	
100	140	47.406	46.107	-1.299	
100	141	48.906	47.587	-1.319	
100	142	47.363	46.267	-1.096	
100	143	47.241	45.904	-1.337	
100	144	47.928	46.764	-1.164	
100	220	48.252	47.134	-1.118	
100	221	48.067	46.936	-1.131	
100	224	47.191	46.068	-1.123	
100	225	48.845	47.567	-1.278	
100	226	48.761	47.651	-1.110	
100	228	49.082	47.963	-1.119	
100	232	47.867	46.725	-1.142	
100	233	48.410	47.344	-1.066	
100	236	47.250	46.146	-1.104	
100	238	49.379	48.189	-1.190	
100	243	48.155	46.882	-1.273	
105	244	47.318	45.746	-1.572	
105	246	47.407	45.955	-1.452	
105	248	48.510	47.013	-1.497	
105	374	47.211	45.596	-1.615	
105	376	49.334	47.779	-1.555	
	Max	49.551	48.950	0.057	
	Average	48.284	47.595	-0.689	
	Min	46.830	45.596	-1.615	
	Std Dev	0.668	0.878	0.510	

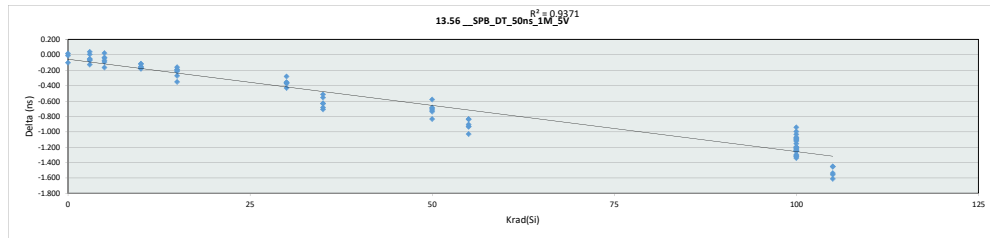


13.55_SPA_DT_50ns_1M_5V											
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit						
		55	ns	55	43						
				43	ns						
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	47.710	47.348	47.334	47.239	47.126	46.957	46.777	46.671	46.493	45.631	45.596
Average	47.805	48.416	48.154	48.338	48.025	48.069	47.615	47.889	47.683	46.958	46.418
Max	47.899	48.950	48.883	48.890	48.718	48.615	48.224	48.734	48.299	48.189	47.779
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

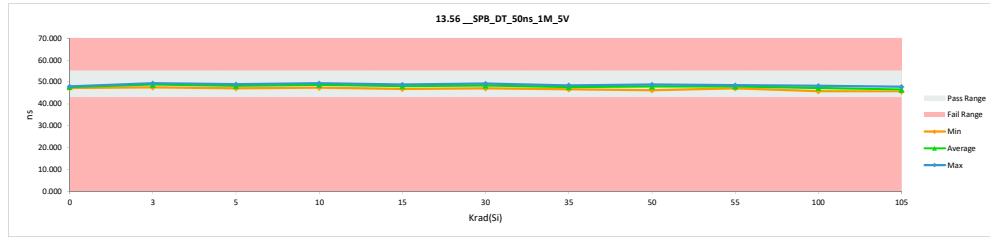


LDR TID Report
TPS7H5001-SP

13.56_SPB_DT_50ns_1M_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	55	55		
Min Limit	43	43		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	47.960	47.982	0.022
0	118	47.375	47.279	-0.096
0	122	48.009	48.002	-0.007
3	61	49.050	48.997	-0.053
3	62	48.900	48.911	0.011
3	63	47.598	47.548	-0.050
3	64	49.600	49.479	-0.121
3	65	48.814	48.745	-0.069
3	66	48.785	48.828	0.043
5	67	48.609	48.635	0.026
5	68	47.179	47.090	-0.089
5	69	48.176	48.145	-0.031
5	70	48.266	48.195	-0.071
5	71	49.116	48.956	-0.160
5	72	48.867	48.832	-0.035
10	73	49.050	48.893	-0.157
10	74	48.900	48.789	-0.111
10	75	47.598	47.451	-0.147
10	76	49.600	49.421	-0.179
10	77	48.814	48.700	-0.114
10	78	48.785	48.615	-0.170
15	79	48.609	48.454	-0.155
15	80	47.179	46.833	-0.346
15	81	48.176	47.967	-0.209
15	82	48.266	48.074	-0.192
15	83	49.116	48.849	-0.267
15	84	48.867	48.680	-0.187
30	86	49.050	48.691	-0.359
30	89	48.900	48.625	-0.275
30	91	47.598	47.171	-0.427
30	92	49.600	49.230	-0.370
30	94	48.814	48.458	-0.356
30	95	48.785	48.433	-0.352
35	100	48.609	48.099	-0.510
35	101	47.179	46.629	-0.550
35	102	48.176	47.471	-0.705
35	103	48.266	47.585	-0.681
35	104	49.116	48.488	-0.628
35	105	48.867	48.236	-0.631
50	106	49.081	48.347	-0.734
50	107	49.620	48.789	-0.831
50	108	46.935	46.221	-0.714
50	109	48.995	48.419	-0.576
50	110	48.605	47.924	-0.681
50	111	48.915	48.218	-0.697
55	113	49.340	48.412	-0.928
55	114	47.868	47.036	-0.832
55	115	49.115	48.089	-1.026
55	116	49.256	48.330	-0.926
55	126	48.528	47.693	-0.835
55	128	49.473	48.574	-0.899
100	129	49.091	47.778	-1.313
100	131	47.479	46.234	-1.245
100	132	48.103	46.767	-1.336
100	133	47.621	46.328	-1.293
100	134	49.061	47.868	-1.193
100	135	49.045	47.817	-1.228
100	138	49.053	47.841	-1.212
100	139	46.782	45.790	-0.992
100	140	47.271	46.026	-1.245
100	141	49.264	48.060	-1.204
100	142	47.416	46.342	-1.074
100	143	47.632	46.386	-1.246
100	144	48.186	46.882	-1.304
100	220	48.970	47.774	-1.196
100	221	48.783	47.701	-1.082
100	224	47.567	46.463	-1.104
100	225	49.184	47.861	-1.323
100	226	49.250	48.310	-0.940
100	228	49.230	48.114	-1.116
100	232	48.401	47.335	-1.066
100	233	49.042	47.892	-1.150
100	236	47.221	46.193	-1.028
100	238	49.134	48.036	-1.098
100	243	48.587	47.344	-1.243
105	244	47.281	45.747	-1.534
105	246	47.160	45.709	-1.451
105	248	49.104	47.495	-1.609
105	374	47.656	46.101	-1.555
105	376	49.274	47.828	-1.446
	Max	49.620	49.479	0.043
	Average	48.498	47.819	-0.678
	Min	46.782	45.709	-1.069
	Std Dev	0.754	0.938	0.501

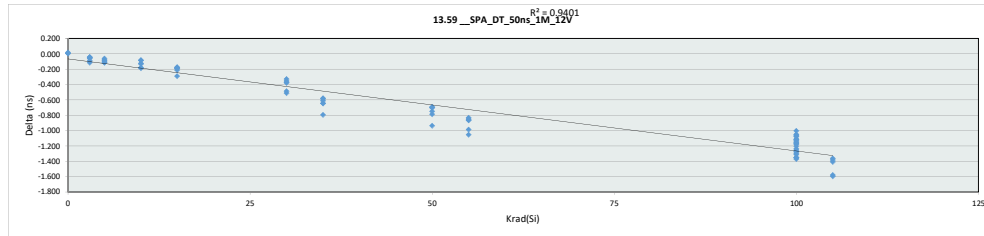


13.56_SPB_DT_50ns_1M_5V											
Test Site											
Tester											
Test Number											
Max Limit	55	ns									
Min Limit	43	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	47.279	47.548	47.090	47.451	46.833	47.171	46.629	46.221	47.036	45.790	45.709
Average	47.754	48.751	48.309	48.645	48.143	48.435	47.751	47.986	48.022	47.214	46.576
Max	48.002	49.479	48.956	49.421	48.849	49.230	48.488	48.789	48.574	48.310	47.828
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

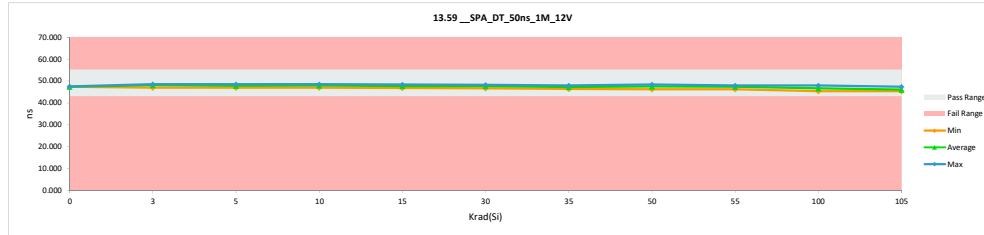


LDR TID Report
TPS7H5001-SP

13.59_SPA_DT_50ns_1M_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	55	55		
Min Limit	43	43		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	47.521	47.545	0.024
0	118	47.312	47.320	0.008
0	122	47.465	47.474	0.009
3	61	48.331	48.286	-0.045
3	62	48.201	48.163	-0.038
3	63	47.102	47.014	-0.088
3	64	48.661	48.603	-0.058
3	65	47.886	47.773	-0.113
3	66	48.586	48.541	-0.045
5	67	47.888	47.776	-0.112
5	68	46.973	46.915	-0.058
5	69	47.668	47.562	-0.106
5	70	47.847	47.761	-0.086
5	71	48.389	48.298	-0.091
5	72	48.641	48.567	-0.074
10	73	48.331	48.248	-0.083
10	74	48.201	48.083	-0.118
10	75	47.102	46.919	-0.183
10	76	48.661	48.583	-0.078
10	77	47.886	47.711	-0.175
10	78	48.586	48.459	-0.127
15	79	47.888	47.713	-0.175
15	80	46.973	46.766	-0.207
15	81	47.668	47.381	-0.287
15	82	47.847	47.677	-0.170
15	83	48.389	48.203	-0.186
15	84	48.641	48.455	-0.186
30	86	48.331	47.964	-0.367
30	89	48.201	47.826	-0.375
30	91	47.102	46.593	-0.509
30	92	48.661	48.311	-0.350
30	94	47.886	47.405	-0.481
30	95	48.586	48.262	-0.324
35	100	47.888	47.280	-0.608
35	101	46.973	46.333	-0.640
35	102	47.668	46.877	-0.791
35	103	47.847	47.264	-0.583
35	104	48.389	47.816	-0.573
35	105	48.641	48.002	-0.639
50	106	48.764	48.020	-0.744
50	107	49.168	48.385	-0.782
50	108	46.944	46.246	-0.698
50	109	48.194	47.500	-0.694
50	110	47.927	46.994	-0.933
50	111	48.657	47.970	-0.687
55	113	48.806	47.947	-0.859
55	114	47.019	46.176	-0.843
55	115	48.536	47.676	-0.860
55	116	48.806	47.976	-0.830
55	126	47.651	46.604	-1.047
55	128	48.795	47.812	-0.983
100	129	48.557	47.251	-1.306
100	131	47.280	46.029	-1.251
100	132	47.746	46.379	-1.367
100	133	47.015	45.844	-1.171
100	134	48.870	47.670	-1.200
100	135	48.329	47.221	-1.108
100	138	48.642	47.298	-1.344
100	139	46.408	45.336	-1.072
100	140	47.075	45.778	-1.297
100	141	48.610	47.261	-1.349
100	142	46.924	45.924	-1.000
100	143	46.864	45.631	-1.233
100	144	47.623	46.347	-1.276
100	220	47.938	46.789	-1.149
100	221	47.720	46.614	-1.106
100	224	46.795	45.740	-1.055
100	225	48.452	47.185	-1.267
100	226	48.402	47.284	-1.118
100	228	48.756	47.597	-1.159
100	232	47.453	46.387	-1.066
100	233	48.181	47.012	-1.169
100	236	46.843	45.800	-1.043
100	238	49.082	47.948	-1.134
100	243	47.801	46.451	-1.350
105	244	46.877	45.498	-1.379
105	246	47.059	45.701	-1.358
105	248	48.177	46.601	-1.576
105	374	46.757	45.352	-1.405
105	376	49.003	47.412	-1.591
	Max	49.168	48.603	0.024
	Average	47.942	47.255	-0.687
	Min	46.408	45.336	-1.072
	Std Dev	0.691	0.883	0.499

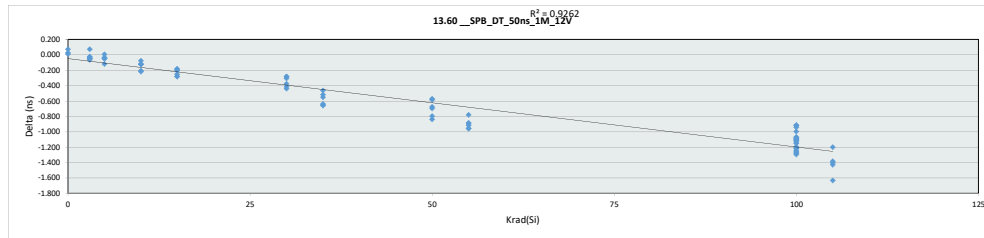


13.59_SPA_DT_50ns_1M_12V											
Test Site											
Tester											
Test Number											
Max Limit	55	ns									
Min Limit	43	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	47.320	47.014	46.915	46.919	46.766	46.593	46.333	46.246	46.176	45.336	45.352
Average	47.446	48.063	47.813	48.001	47.699	47.727	47.262	47.519	47.365	46.616	46.113
Max	47.545	48.603	48.567	48.583	48.455	48.311	48.002	48.386	47.976	47.948	47.412
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

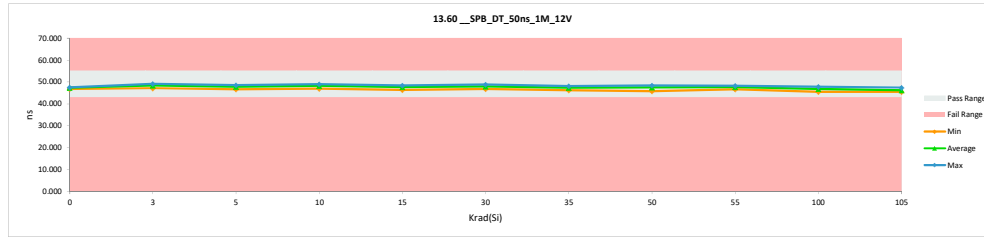


LDR TID Report
TPS7H5001-SP

13.60_SPB_DT_50ns_1M_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	55	55		
Min Limit	43	43		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	47.562	47.594	0.032
0	118	46.734	46.809	0.075
-1	122	47.566	47.579	0.013
3	61	48.584	48.538	-0.046
3	62	48.514	48.590	0.076
3	63	47.202	47.197	-0.005
3	64	49.141	49.115	-0.026
3	65	48.399	48.340	-0.059
3	66	48.446	48.427	-0.019
5	67	48.256	48.211	-0.045
5	68	46.707	46.677	-0.030
5	69	47.701	47.667	-0.034
5	70	47.868	47.753	-0.115
5	71	48.643	48.600	-0.043
5	72	48.466	48.476	0.010
10	73	48.584	48.464	-0.120
10	74	48.514	48.440	-0.074
10	75	47.202	46.988	-0.214
10	76	49.141	49.028	-0.113
10	77	48.399	48.280	-0.119
10	78	48.446	48.242	-0.204
15	79	48.256	47.978	-0.278
15	80	46.707	46.431	-0.276
15	81	47.701	47.525	-0.176
15	82	47.868	47.621	-0.247
15	83	48.643	48.441	-0.202
15	84	48.466	48.280	-0.186
30	86	48.584	48.301	-0.283
30	89	48.514	48.236	-0.278
30	91	47.202	46.828	-0.374
30	92	49.141	48.841	-0.300
30	94	48.399	47.963	-0.436
30	95	48.446	48.035	-0.411
35	100	48.256	47.709	-0.547
35	101	46.707	46.249	-0.458
35	102	47.701	47.067	-0.634
35	103	47.868	47.219	-0.649
35	104	48.643	48.130	-0.513
35	105	48.466	47.809	-0.657
50	106	48.782	47.949	-0.833
50	107	49.183	48.390	-0.793
50	108	46.422	45.854	-0.568
50	109	48.617	48.036	-0.581
50	110	48.168	47.495	-0.673
50	111	48.511	47.819	-0.692
55	113	48.905	48.013	-0.892
55	114	47.537	46.658	-0.879
55	115	48.714	47.759	-0.955
55	116	48.886	47.971	-0.915
55	126	48.211	47.260	-0.951
55	128	49.074	48.298	-0.776
100	129	48.651	47.395	-1.256
100	131	47.047	45.904	-1.143
100	132	47.682	46.443	-1.239
100	133	47.189	45.975	-1.214
100	134	48.651	47.461	-1.190
100	135	48.667	47.465	-1.202
100	138	48.684	47.418	-1.266
100	139	46.375	45.465	-0.910
100	140	46.829	45.740	-1.089
100	141	49.008	47.717	-1.291
100	142	46.949	45.958	-0.991
100	143	47.142	46.024	-1.118
100	144	47.716	46.613	-1.103
100	220	48.523	47.417	-1.106
100	221	48.529	47.446	-1.083
100	224	47.076	46.137	-0.939
100	225	48.644	47.434	-1.210
100	226	48.787	47.869	-0.918
100	228	48.833	47.761	-1.072
100	232	48.051	46.950	-1.101
100	233	48.703	47.556	-1.147
100	236	46.784	45.872	-0.912
100	238	48.815	47.751	-1.064
100	243	48.208	46.934	-1.274
105	244	46.851	45.470	-1.381
105	246	46.645	45.449	-1.196
105	248	48.679	47.050	-1.629
105	374	47.154	45.756	-1.398
105	376	48.874	47.447	-1.427
	Max	49.183	49.115	0.076
	Average	48.080	47.438	-0.642
	Min	46.375	45.449	-1.629
	Std Dev	0.776	0.918	0.482

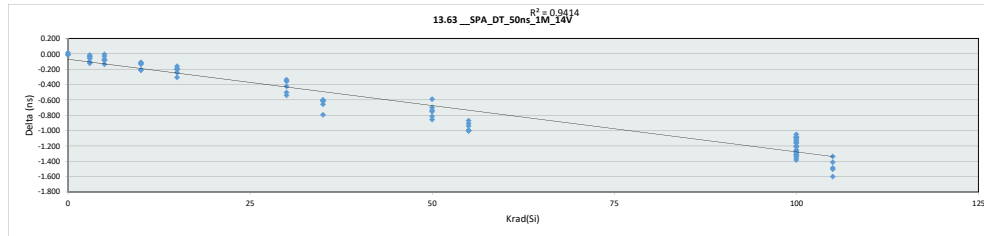


13.60_SPB_DT_50ns_1M_12V											
Test Site											
Tester											
Test Number											
Max Limit	55	ns									
Min Limit	43	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	46.809	47.157	46.677	46.988	46.431	46.828	46.249	45.854	46.658	45.465	45.449
Average	47.327	48.361	47.897	48.240	47.713	48.034	47.364	47.591	47.660	46.863	46.234
Max	47.594	49.115	48.600	49.028	48.441	48.841	48.130	48.390	48.298	47.869	47.447
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

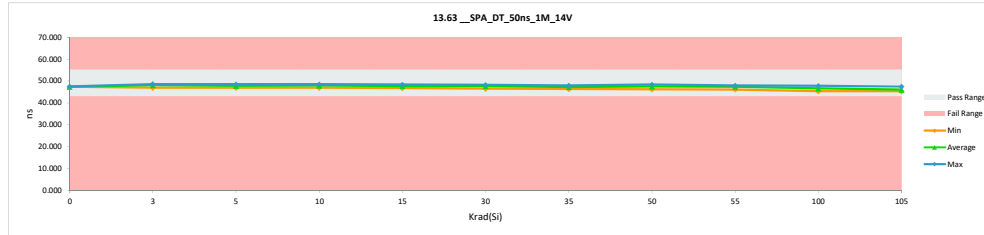


LDR TID Report
TPS7H5001-SP

13.63_SPA_DT_50ns_1M_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	55	55		
Min Limit	43	43		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	47.549	47.539	-0.010
0	118	47.316	47.335	0.019
0	122	47.483	47.481	-0.002
3	61	48.331	48.303	-0.028
3	62	48.227	48.107	-0.120
3	63	47.117	47.017	-0.100
3	64	48.661	48.651	-0.010
3	65	47.891	47.834	-0.057
3	66	48.570	48.534	-0.036
5	67	47.889	47.821	-0.068
5	68	46.979	46.975	-0.004
5	69	47.698	47.565	-0.133
5	70	47.819	47.748	-0.071
5	71	48.419	48.335	-0.084
5	72	48.628	48.602	-0.026
10	73	48.331	48.211	-0.120
10	74	48.227	48.095	-0.132
10	75	47.117	46.911	-0.206
10	76	48.661	48.551	-0.110
10	77	47.891	47.683	-0.208
10	78	48.570	48.465	-0.105
15	79	47.889	47.701	-0.188
15	80	46.979	46.786	-0.193
15	81	47.698	47.397	-0.301
15	82	47.819	47.619	-0.200
15	83	48.419	48.177	-0.242
15	84	48.628	48.471	-0.157
30	86	48.331	47.983	-0.348
30	89	48.227	47.809	-0.418
30	91	47.117	46.580	-0.537
30	92	48.661	48.306	-0.355
30	94	47.891	47.391	-0.500
30	95	48.570	48.237	-0.333
35	100	47.889	47.282	-0.607
35	101	46.979	46.383	-0.596
35	102	47.698	46.907	-0.791
35	103	47.819	47.217	-0.602
35	104	48.419	47.809	-0.610
35	105	48.628	47.973	-0.655
50	106	48.742	48.047	-0.695
50	107	49.181	48.369	-0.812
50	108	46.952	46.203	-0.749
50	109	48.150	47.563	-0.587
50	110	47.906	47.053	-0.853
50	111	48.713	47.979	-0.734
55	113	48.787	47.923	-0.864
55	114	47.051	46.149	-0.902
55	115	48.598	47.609	-0.989
55	116	48.839	47.904	-0.935
55	126	47.620	46.626	-0.994
55	128	48.793	47.796	-0.997
100	129	48.574	47.290	-1.284
100	131	47.248	46.041	-1.207
100	132	47.708	46.396	-1.312
100	133	47.014	45.860	-1.154
100	134	48.828	47.635	-1.193
100	135	48.333	47.230	-1.103
100	138	48.685	47.332	-1.353
100	139	46.382	45.301	-1.081
100	140	47.048	45.736	-1.312
100	141	48.631	47.252	-1.379
100	142	46.944	45.901	-1.043
100	143	46.900	45.592	-1.308
100	144	47.597	46.339	-1.258
100	220	47.948	46.789	-1.159
100	221	47.776	46.621	-1.155
100	224	46.812	45.734	-1.078
100	225	48.453	47.203	-1.250
100	226	48.412	47.286	-1.126
100	228	48.732	47.605	-1.127
100	232	47.449	46.370	-1.079
100	233	48.197	46.995	-1.202
100	236	46.857	45.769	-1.088
100	238	49.160	47.886	-1.274
100	243	47.750	46.417	-1.333
105	244	46.833	45.502	-1.331
105	246	47.069	45.660	-1.409
105	248	48.193	46.598	-1.595
105	374	46.787	45.303	-1.484
105	376	48.973	47.473	-1.500
Max		49.181	48.651	0.019
Average		47.946	47.252	-0.694
Min		46.382	45.301	-1.595
Std Dev		0.691	0.888	0.502

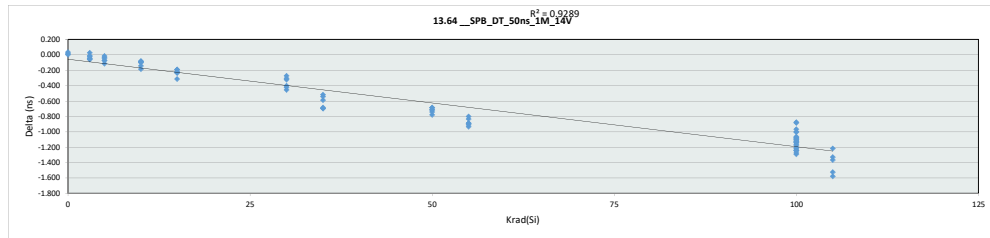


13.63_SPA_DT_50ns_1M_14V											
Test Site											
Tester											
Test Number											
Max Limit	55	ns									
Min Limit	43	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	47.335	47.017	46.975	46.911	46.786	46.580	46.383	46.203	46.149	45.301	45.303
Average	47.452	48.074	47.841	47.986	47.692	47.718	47.262	47.536	47.335	46.608	46.107
Max	47.539	48.651	48.602	48.551	48.471	48.306	47.973	48.369	47.923	47.886	47.473
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

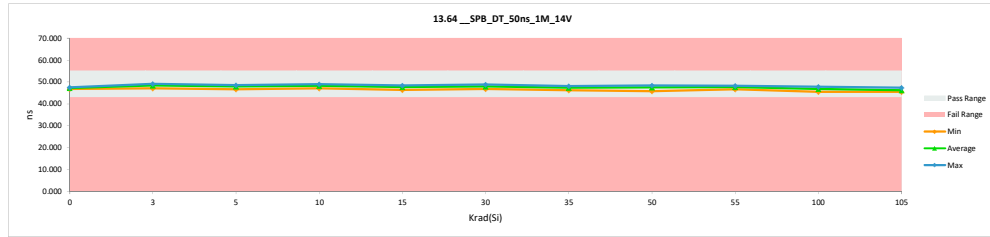


LDR TID Report
TPS7H5001-SP

13.64_SPB_DT_50ns_1M_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	55	55		
Min Limit	43	43		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	47.545	47.553	0.008
0	118	46.753	46.793	0.040
0	122	47.572	47.589	0.017
3	61	48.590	48.585	-0.005
3	62	48.523	48.504	-0.019
3	63	47.191	47.136	-0.055
3	64	49.120	49.149	0.029
3	65	48.362	48.322	-0.040
3	66	48.435	48.388	-0.047
5	67	48.243	48.175	-0.068
5	68	46.752	46.641	-0.111
5	69	47.731	47.691	-0.040
5	70	47.865	47.836	-0.029
5	71	48.680	48.613	-0.067
5	72	48.508	48.497	-0.011
10	73	48.590	48.492	-0.098
10	74	48.523	48.440	-0.083
10	75	47.191	47.050	-0.141
10	76	49.120	49.029	-0.091
10	77	48.362	48.286	-0.076
10	78	48.435	48.254	-0.181
15	79	48.243	48.030	-0.213
15	80	46.752	46.440	-0.312
15	81	47.731	47.544	-0.187
15	82	47.865	47.635	-0.230
15	83	48.680	48.452	-0.228
15	84	48.508	48.318	-0.190
30	86	48.590	48.320	-0.270
30	89	48.523	48.223	-0.300
30	91	47.191	46.773	-0.418
30	92	49.120	48.800	-0.320
30	94	48.362	47.966	-0.396
30	95	48.435	47.983	-0.452
35	100	48.243	47.659	-0.584
35	101	46.752	46.236	-0.516
35	102	47.731	47.039	-0.692
35	103	47.865	47.174	-0.691
35	104	48.680	48.142	-0.538
35	105	48.508	47.826	-0.682
50	106	48.691	47.951	-0.740
50	107	49.164	48.459	-0.705
50	108	46.507	45.826	-0.681
50	109	48.719	48.005	-0.714
50	110	48.224	47.450	-0.774
50	111	48.510	47.824	-0.686
55	113	48.872	47.987	-0.885
55	114	47.524	46.617	-0.907
55	115	48.695	47.810	-0.885
55	116	48.893	47.961	-0.932
55	126	48.125	47.295	-0.830
55	128	49.119	48.322	-0.797
100	129	48.614	47.373	-1.241
100	131	46.992	45.892	-1.100
100	132	47.684	46.450	-1.234
100	133	47.133	45.933	-1.200
100	134	48.645	47.487	-1.158
100	135	48.698	47.616	-1.082
100	138	48.646	47.406	-1.240
100	139	46.328	45.456	-0.872
100	140	46.798	45.682	-1.116
100	141	48.982	47.696	-1.286
100	142	46.926	45.961	-0.965
100	143	47.158	46.026	-1.132
100	144	47.761	46.591	-1.170
100	220	48.548	47.477	-1.071
100	221	48.511	47.409	-1.102
100	224	47.151	46.154	-0.997
100	225	48.646	47.434	-1.212
100	226	48.773	47.891	-0.882
100	228	48.819	47.698	-1.121
100	232	48.048	46.972	-1.076
100	233	48.641	47.502	-1.139
100	236	46.813	45.813	-1.000
100	238	48.835	47.778	-1.057
100	243	48.147	46.874	-1.273
105	244	46.823	45.498	-1.325
105	246	46.668	45.456	-1.212
105	248	48.681	47.107	-1.574
105	374	47.130	45.769	-1.361
105	376	48.932	47.412	-1.520
	Max	49.164	49.149	0.040
	Average	48.081	47.436	-0.644
	Min	46.328	45.456	-1.574
	Std Dev	0.772	0.925	0.477

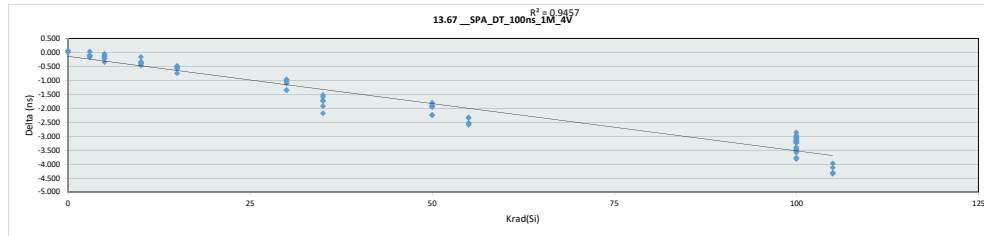


13.64_SPB_DT_50ns_1M_14V											
Test Site											
Tester											
Test Number											
Max Limit	55	ns									
Min Limit	43	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	46.793	47.136	46.641	47.050	46.440	46.773	46.236	45.826	46.617	45.456	45.456
Average	47.312	48.347	47.909	48.259	47.737	48.011	47.346	47.586	47.665	46.857	46.248
Max	47.589	49.149	48.613	49.029	48.452	48.800	48.142	48.459	48.322	47.891	47.412
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

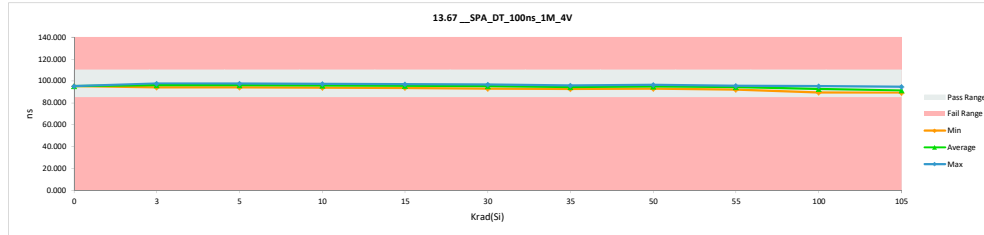


LDR TID Report
TPS7H5001-SP

13.67_SPA_DT_100ns_1M_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	110	110		
Min Limit	85	85		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	95.401	95.417	0.016
0	118	95.322	95.392	0.070
0	122	95.297	95.362	0.065
3	61	96.394	96.439	0.045
3	62	96.849	96.750	-0.099
3	63	94.367	94.206	-0.161
3	64	97.439	97.348	-0.091
3	65	95.343	95.228	-0.115
3	66	97.924	97.821	-0.103
5	67	95.703	95.520	-0.183
5	68	94.234	94.199	-0.035
5	69	96.742	96.404	-0.338
5	70	96.333	96.123	-0.210
5	71	96.864	96.726	-0.138
5	72	97.752	97.656	-0.096
10	73	96.394	96.241	-0.153
10	74	96.849	96.492	-0.357
10	75	94.367	93.910	-0.457
10	76	97.439	97.121	-0.318
10	77	95.343	94.936	-0.407
10	78	97.924	97.550	-0.374
15	79	95.703	95.196	-0.507
15	80	94.234	93.760	-0.474
15	81	96.742	96.006	-0.736
15	82	96.333	95.828	-0.505
15	83	96.864	96.312	-0.552
15	84	97.752	97.181	-0.571
30	86	96.394	95.446	-0.948
30	89	96.849	95.818	-1.031
30	91	94.367	93.026	-1.341
30	92	97.439	96.335	-1.104
30	94	95.343	94.013	-1.330
30	95	97.924	96.942	-0.982
35	100	95.703	93.993	-1.710
35	101	94.234	92.720	-1.514
35	102	96.742	94.575	-2.167
35	103	96.333	94.599	-1.734
35	104	96.864	95.281	-1.583
35	105	97.752	95.832	-1.920
50	106	98.594	96.644	-1.950
50	107	98.853	96.624	-2.229
50	108	95.034	93.116	-1.918
50	109	96.627	94.841	-1.786
50	110	95.985	93.745	-2.240
50	111	97.402	95.521	-1.881
55	113	98.265	95.755	-2.510
55	114	94.597	92.015	-2.582
55	115	97.100	94.783	-2.317
55	116	98.032	95.691	-2.341
55	126	95.403	92.879	-2.524
55	128	97.845	95.310	-2.535
100	129	97.415	93.833	-3.582
100	131	95.051	91.672	-3.379
100	132	96.619	92.826	-3.793
100	133	94.383	91.150	-3.233
100	134	98.149	94.997	-3.152
100	135	97.546	94.365	-3.181
100	138	97.735	93.933	-3.802
100	139	92.807	89.599	-3.208
100	140	94.129	90.645	-3.484
100	141	97.277	93.739	-3.538
100	142	94.598	91.745	-2.853
100	143	94.091	90.327	-3.764
100	144	94.791	91.580	-3.211
100	220	95.983	92.845	-3.138
100	221	95.122	92.104	-3.018
100	224	94.052	90.871	-3.181
100	225	97.459	94.006	-3.453
100	226	96.834	93.882	-2.952
100	228	97.710	94.593	-3.117
100	232	96.157	93.139	-3.018
100	233	95.995	92.936	-3.059
100	236	93.845	90.823	-3.022
100	238	98.898	95.408	-3.490
100	243	95.034	91.623	-3.411
105	244	94.153	90.034	-4.119
105	246	94.765	90.798	-3.967
105	248	96.533	92.219	-4.314
105	374	93.664	89.361	-4.303
105	376	99.159	94.826	-4.333
	Max	99.159	97.821	0.070
	Average	96.219	94.331	-1.888
	Min	92.807	89.361	-4.333
	Std Dev	1.456	2.094	1.406

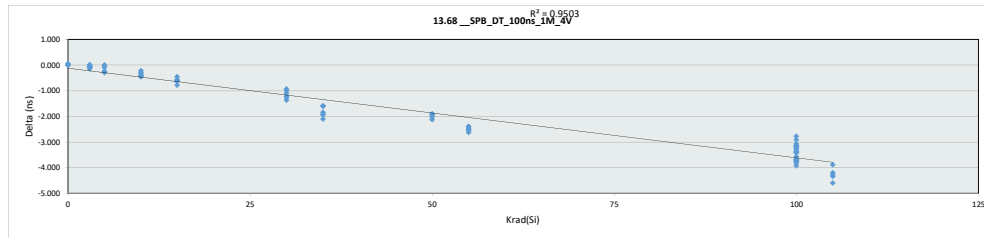


13.67_SPA_DT_100ns_1M_4V											
Test Site											
Tester											
Test Number											
Max Limit	110	ns									
Min Limit	85	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	95.362	94.206	94.199	93.910	93.760	93.026	92.720	93.116	92.015	89.599	89.361
Average	95.390	96.299	96.105	96.042	95.714	95.263	94.500	95.082	94.406	92.610	91.448
Max	95.417	97.821	97.656	97.550	97.181	96.942	95.832	96.644	95.755	95.408	94.826
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

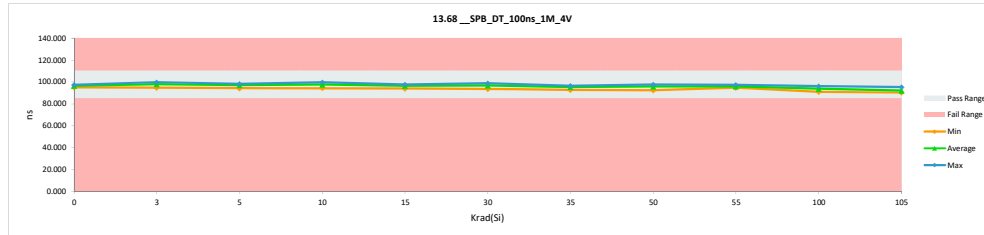


LDR TID Report
TPS7H5001-SP

13.68_SPB_DT_100ns_1M_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	110	110		
Min Limit	85	85		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	96.887	96.925	0.038
0	118	95.038	95.033	-0.005
0	122	97.231	97.302	0.071
3	61	98.440	98.420	-0.020
3	62	98.325	98.218	-0.107
3	63	94.959	94.824	-0.135
3	64	99.961	99.890	-0.071
3	65	97.813	97.740	-0.073
3	66	98.441	98.466	0.025
5	67	97.660	97.578	-0.082
5	68	94.327	94.322	-0.005
5	69	97.204	96.962	-0.242
5	70	97.176	96.945	-0.231
5	71	98.325	98.043	-0.282
5	72	98.164	98.188	0.024
10	73	98.440	98.237	-0.203
10	74	98.265	97.996	-0.269
10	75	94.959	94.546	-0.413
10	76	99.961	99.627	-0.334
10	77	97.813	97.422	-0.391
10	78	98.441	97.992	-0.449
15	79	97.660	97.222	-0.438
15	80	94.327	93.770	-0.557
15	81	97.204	96.598	-0.605
15	82	97.176	96.554	-0.622
15	83	98.325	97.563	-0.762
15	84	98.164	97.567	-0.597
30	86	98.440	97.520	-0.920
30	89	98.265	97.274	-0.991
30	91	94.959	93.756	-1.203
30	92	99.961	98.874	-1.087
30	94	97.813	96.458	-1.355
30	95	98.441	97.187	-1.254
35	100	97.660	96.080	-1.580
35	101	94.327	92.730	-1.597
35	102	97.204	95.113	-2.091
35	103	97.176	95.244	-1.932
35	104	98.325	96.496	-1.829
35	105	98.164	96.267	-1.897
50	106	99.163	97.243	-1.920
50	107	99.944	97.824	-2.120
50	108	94.352	92.428	-1.924
50	109	98.481	96.589	-1.892
50	110	97.600	95.581	-2.019
50	111	98.323	96.426	-1.897
55	113	99.109	96.640	-2.469
55	114	97.035	94.657	-2.378
55	115	98.717	96.104	-2.613
55	116	98.929	96.418	-2.511
55	126	97.652	95.256	-2.396
55	128	99.815	97.280	-2.535
100	129	98.335	94.677	-3.658
100	131	95.296	91.593	-3.703
100	132	97.378	93.570	-3.808
100	133	95.430	91.518	-3.912
100	134	98.668	95.269	-3.399
100	135	98.974	95.574	-3.400
100	138	98.792	95.095	-3.697
100	139	93.815	90.912	-2.903
100	140	95.119	91.449	-3.670
100	141	99.247	95.508	-3.739
100	142	95.434	92.310	-3.124
100	143	96.089	92.529	-3.560
100	144	95.994	92.627	-3.367
100	220	98.322	95.018	-3.304
100	221	98.344	95.207	-3.137
100	224	95.798	92.658	-3.140
100	225	99.328	95.566	-3.762
100	226	98.897	96.128	-2.769
100	228	99.168	95.930	-3.238
100	232	97.779	94.579	-3.200
100	233	98.611	95.238	-3.373
100	236	94.464	91.403	-3.061
100	238	99.068	95.874	-3.194
100	243	97.611	93.943	-3.668
105	244	94.455	90.273	-4.182
105	246	94.543	90.666	-3.877
105	248	98.758	94.176	-4.582
105	374	94.894	90.632	-4.262
105	376	99.665	95.335	-4.330
Max		99.961	99.890	0.071
Average		97.484	95.558	-1.926
Min		93.815	90.273	-4.582
Std Dev		1.669	2.262	1.449

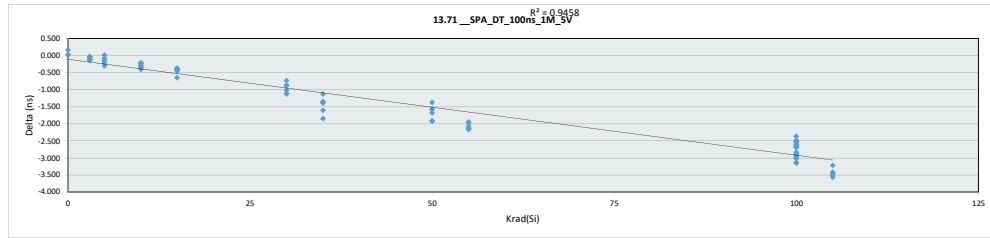


13.68_SPB_DT_100ns_1M_4V											
Test Site											
Tester											
Test Number											
Max Limit	110	ns									
Min Limit	85	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	95.033	94.824	94.322	94.546	93.770	93.756	92.730	92.428	94.657	90.912	90.273
Average	96.420	97.926	97.006	97.637	96.546	96.845	95.322	96.015	96.059	93.924	92.216
Max	97.302	99.890	98.188	99.627	97.567	98.874	96.496	97.824	97.280	96.128	95.335
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

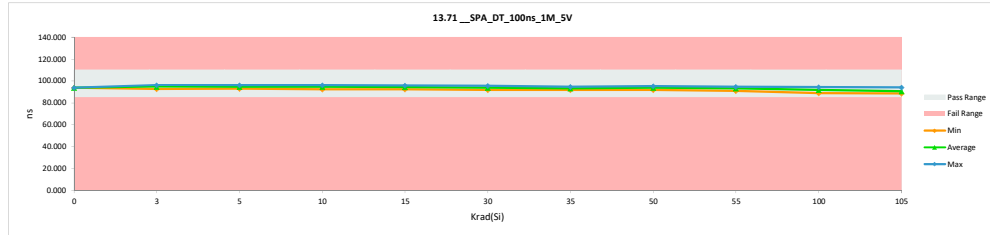


LDR TID Report
TPS7H5001-SP

13.71_SPA_DT_100ns_1M_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	110	110		
Min Limit	85	85		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	93.821	93.849	0.028
0	118	93.899	93.936	0.037
0	122	93.624	93.795	0.171
3	61	95.004	94.981	-0.023
3	62	95.541	95.427	-0.114
3	63	92.877	92.822	-0.055
3	64	96.085	95.970	-0.115
3	65	94.002	93.887	-0.115
3	66	96.410	96.384	-0.026
5	67	94.306	94.180	-0.126
5	68	92.868	92.892	0.024
5	69	95.000	94.709	-0.291
5	70	94.775	94.557	-0.218
5	71	95.523	95.363	-0.160
5	72	96.353	96.285	-0.068
10	73	95.004	94.800	-0.204
10	74	95.541	95.271	-0.270
10	75	92.877	92.483	-0.394
10	76	96.085	95.786	-0.299
10	77	94.002	93.654	-0.348
10	78	96.410	96.188	-0.222
15	79	94.306	93.931	-0.375
15	80	92.868	92.504	-0.364
15	81	95.000	94.359	-0.641
15	82	94.775	94.386	-0.389
15	83	95.523	95.075	-0.448
15	84	96.353	95.924	-0.429
30	86	95.004	94.125	-0.879
30	89	95.541	94.671	-0.870
30	91	92.877	91.757	-1.120
30	92	96.085	95.080	-1.005
30	94	94.002	92.898	-1.104
30	95	96.410	95.685	-0.725
35	100	94.306	92.955	-1.351
35	101	92.868	91.747	-1.121
35	102	95.000	93.158	-1.842
35	103	94.775	93.392	-1.383
35	104	95.523	94.188	-1.335
35	105	96.353	94.758	-1.595
50	106	97.046	95.376	-1.670
50	107	97.369	95.465	-1.904
50	108	93.413	91.854	-1.559
50	109	95.160	93.795	-1.365
50	110	94.735	92.818	-1.917
50	111	95.968	94.399	-1.569
55	113	96.849	94.719	-2.130
55	114	93.032	90.917	-2.115
55	115	95.730	93.797	-1.933
55	116	96.662	94.702	-1.960
55	125	94.234	92.074	-2.160
55	128	96.384	94.332	-2.052
100	129	96.181	93.170	-3.011
100	131	93.552	90.724	-2.828
100	132	94.957	91.821	-3.136
100	133	92.815	90.162	-2.653
100	134	96.750	94.065	-2.685
100	135	95.942	93.339	-2.603
100	138	96.255	93.117	-3.138
100	139	91.461	88.811	-2.650
100	140	92.725	89.830	-2.895
100	141	95.824	92.843	-2.981
100	142	93.103	90.743	-2.360
100	143	92.497	89.362	-3.135
100	144	93.440	90.762	-2.678
100	220	94.698	92.177	-2.521
100	221	93.904	91.415	-2.489
100	224	92.612	90.001	-2.611
100	225	96.002	93.121	-2.881
100	226	95.599	93.051	-2.548
100	228	96.402	93.787	-2.615
100	232	94.564	92.068	-2.496
100	233	94.742	92.211	-2.531
100	236	92.293	89.811	-2.482
100	238	97.335	94.394	-2.941
100	243	93.757	90.861	-2.896
105	244	92.629	89.219	-3.410
105	246	93.256	90.041	-3.215
105	248	95.228	91.668	-3.560
105	374	92.163	88.716	-3.447
105	376	97.643	94.143	-3.500
	Max	97.643	96.384	0.171
	Average	94.781	93.219	-1.562
	Min	91.461	88.716	-3.560
	Std Dev	1.461	1.923	1.167



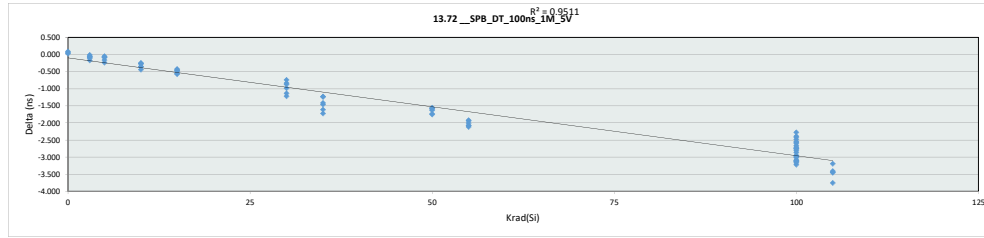
13.71_SPA_DT_100ns_1M_5V											
Test Site											
Tester											
Test Number											
Max Limit	110	ns									
Min Limit	85	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	93.795	92.822	92.892	92.483	92.504	91.757	91.747	91.854	90.917	88.811	88.716
Average	93.860	94.912	94.664	94.697	94.363	94.036	93.366	93.951	93.424	91.735	90.757
Max	93.936	96.384	96.285	96.188	95.924	95.685	94.758	95.465	94.719	94.394	94.143
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000



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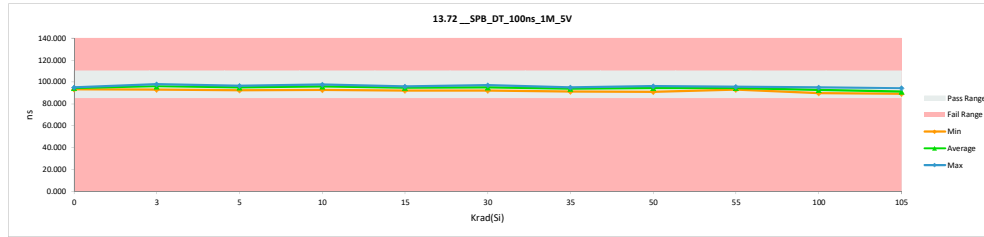
13.72_SPB_DT_100ns_1M_5V		
Test Site		
Tester		
Test Number		
Unit	ns	ns
Max Limit	110	110
Min Limit	85	85

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	94.861	94.924	0.063
0	118	93.201	93.233	0.032
0	122	95.096	95.195	0.099
3	61	96.537	96.492	-0.045
3	62	96.433	96.560	-0.127
3	63	93.077	93.000	-0.077
3	64	98.073	98.031	-0.042
3	65	96.068	95.910	-0.158
3	66	96.659	96.565	-0.094
5	67	95.862	95.795	-0.067
5	68	92.604	92.544	-0.060
5	69	95.040	94.890	-0.150
5	70	95.147	95.066	-0.081
5	71	96.433	96.207	-0.226
5	72	96.459	96.419	-0.040
10	73	96.537	96.304	-0.233
10	74	96.565	96.314	-0.251
10	75	93.077	92.739	-0.338
10	76	98.073	97.808	-0.265
10	77	96.068	95.723	-0.345
10	78	96.659	96.227	-0.432
15	79	95.862	95.408	-0.454
15	80	92.604	92.159	-0.445
15	81	95.040	94.560	-0.480
15	82	95.147	94.734	-0.413
15	83	96.433	95.874	-0.559
15	84	96.459	95.918	-0.541
30	86	96.537	95.806	-0.731
30	89	96.565	95.738	-0.827
30	91	93.077	92.098	-0.979
30	92	98.073	97.209	-0.864
30	94	96.068	94.855	-1.213
30	95	96.659	95.522	-1.127
35	100	95.862	94.645	-1.217
35	101	92.604	91.372	-1.232
35	102	95.040	93.328	-1.712
35	103	95.147	93.692	-1.455
35	104	96.433	95.023	-1.410
35	105	96.459	94.854	-1.605
50	106	97.286	95.568	-1.618
50	107	97.939	96.219	-1.720
50	108	92.522	90.978	-1.544
50	109	96.596	95.016	-1.580
50	110	95.989	94.245	-1.744
50	111	96.505	94.936	-1.569
55	113	97.196	95.132	-2.064
55	114	94.808	92.900	-1.908
55	115	96.846	94.739	-2.107
55	116	97.223	95.151	-2.072
55	126	95.925	93.991	-1.934
55	128	97.779	95.784	-1.995
100	129	96.708	93.603	-3.105
100	131	93.534	90.457	-3.077
100	132	95.395	92.235	-3.160
100	133	93.527	90.319	-3.208
100	134	96.725	93.989	-2.736
100	135	97.050	94.191	-2.859
100	138	96.924	93.839	-3.085
100	139	92.063	89.684	-2.379
100	140	93.228	90.285	-2.943
100	141	97.395	94.272	-3.123
100	142	93.510	90.975	-2.535
100	143	93.980	91.026	-2.954
100	144	94.336	91.608	-2.728
100	220	96.462	93.711	-2.751
100	221	96.568	94.011	-2.557
100	224	93.739	91.333	-2.406
100	225	97.255	94.278	-2.977
100	226	97.262	94.996	-2.266
100	228	97.392	94.738	-2.654
100	232	95.712	93.124	-2.588
100	233	96.929	94.133	-2.796
100	236	92.703	90.232	-2.471
100	238	97.241	94.547	-2.694
100	243	95.830	92.821	-3.009
105	244	92.696	89.265	-3.431
105	246	92.827	89.639	-3.188
105	248	96.771	93.021	-3.750
105	374	93.117	89.667	-3.450
105	376	97.761	94.361	-3.400
	Max	98.073	98.031	0.099
	Average	95.625	94.048	-1.576
	Min	92.063	89.265	-3.750
	Std Dev	1.663	2.077	1.183



13.72_SPB_DT_100ns_1M_5V		
Test Site		
Tester		
Test Number		
Max Limit	110	ns
Min Limit	85	ns

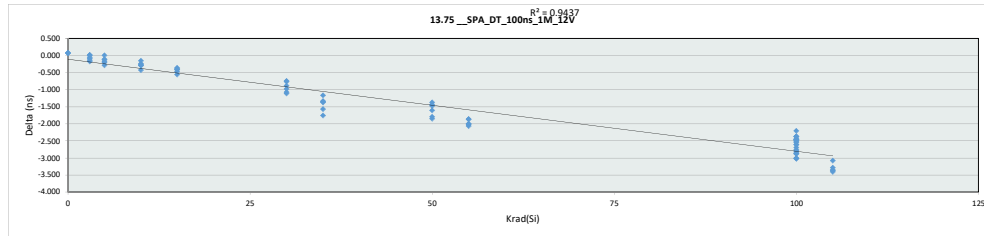
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	93.233	93.000	92.544	92.739	92.159	92.098	91.372	90.978	92.900	89.684	89.265
Average	94.451	96.093	95.154	95.853	94.776	95.206	93.819	94.510	94.616	92.684	91.191
Max	95.195	98.031	96.419	97.808	95.918	97.209	95.023	96.219	95.784	94.996	94.361
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000



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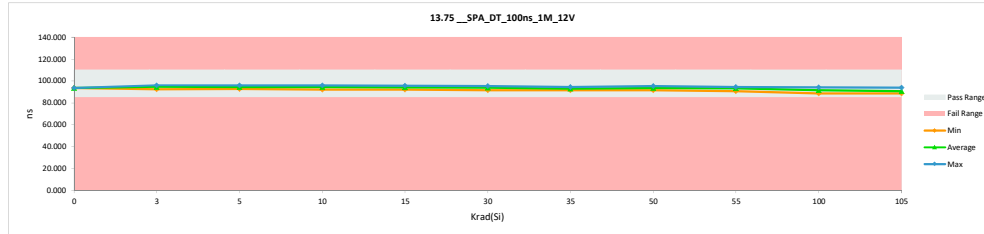
13.75_SPA_DT_100ns_1M_12V		
Test Site		
Tester		
Test Number		
Unit	ns	ns
Max Limit	110	110
Min Limit	85	85

Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	93.612	93.689	0.077
0	118	93.515	93.589	0.074
0	122	93.496	93.571	0.075
3	61	94.767	94.785	0.018
3	62	95.166	95.180	0.014
3	63	92.679	92.507	-0.172
3	64	95.786	95.653	-0.133
3	65	93.676	93.591	-0.085
3	66	96.147	96.095	-0.052
5	67	94.074	93.890	-0.184
5	68	92.640	92.648	0.008
5	69	94.672	94.383	-0.289
5	70	94.475	94.272	-0.203
5	71	95.251	95.130	-0.121
5	72	96.089	95.987	-0.102
10	73	94.767	94.532	-0.235
10	74	95.166	95.014	-0.152
10	75	92.679	92.258	-0.421
10	76	95.786	95.489	-0.297
10	77	93.676	93.406	-0.270
10	78	96.147	95.865	-0.282
15	79	94.074	93.684	-0.390
15	80	92.640	92.284	-0.356
15	81	94.672	94.118	-0.554
15	82	94.475	94.063	-0.412
15	83	95.251	94.777	-0.474
15	84	96.089	95.687	-0.402
30	86	94.767	93.880	-0.887
30	89	95.166	94.405	-0.761
30	91	92.679	91.572	-1.107
30	92	95.786	94.804	-0.982
30	94	93.676	92.604	-1.072
30	95	96.147	95.405	-0.742
35	100	94.074	92.704	-1.370
35	101	92.640	91.471	-1.169
35	102	94.672	92.917	-1.755
35	103	94.475	93.121	-1.354
35	104	95.251	93.913	-1.338
35	105	96.089	94.521	-1.568
50	106	96.711	95.100	-1.611
50	107	97.019	95.222	-1.797
50	108	93.093	91.617	-1.476
50	109	94.922	93.546	-1.376
50	110	94.417	92.566	-1.851
50	111	95.622	94.186	-1.436
55	113	96.600	94.572	-2.028
55	114	92.775	90.777	-1.998
55	115	95.460	93.595	-1.865
55	116	96.391	94.528	-1.863
55	126	93.921	91.857	-2.064
55	128	96.170	94.165	-2.005
100	129	95.811	92.949	-2.862
100	131	93.322	90.631	-2.691
100	132	94.666	91.654	-3.012
100	133	92.528	89.986	-2.542
100	134	96.511	93.903	-2.608
100	135	95.652	93.129	-2.523
100	138	95.950	92.943	-3.007
100	139	91.262	88.650	-2.612
100	140	92.506	89.719	-2.787
100	141	95.561	92.692	-2.869
100	142	92.794	90.590	-2.204
100	143	92.216	89.195	-3.021
100	144	93.150	90.670	-2.480
100	220	94.492	92.013	-2.479
100	221	93.704	91.259	-2.445
100	224	92.269	89.806	-2.463
100	225	95.668	92.906	-2.762
100	226	95.236	92.856	-2.380
100	228	96.049	93.578	-2.471
100	232	94.277	91.909	-2.368
100	233	94.488	92.032	-2.456
100	236	92.036	89.657	-2.379
100	239	97.100	94.216	-2.884
100	243	93.501	90.673	-2.828
105	244	92.308	89.026	-3.282
105	246	93.029	89.946	-3.083
105	248	94.836	91.428	-3.408
105	374	91.947	88.592	-3.355
105	376	97.345	93.977	-3.368
	Max	97.345	96.095	0.077
	Average	94.503	92.997	-1.505
	Min	91.262	88.592	-3.408
	Std Dev	1.445	1.884	1.118



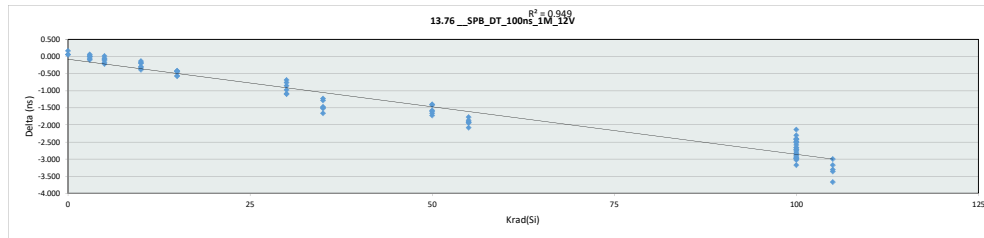
13.75_SPA_DT_100ns_1M_12V		
Test Site		
Tester		
Test Number		
Max Limit	110	ns
Min Limit	85	ns

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	93.571	92.507	92.648	92.258	92.284	91.572	91.471	91.617	90.777	88.650	88.592
Average	93.616	94.635	94.385	94.427	94.102	93.778	93.108	93.706	93.249	91.567	90.594
Max	93.689	96.095	95.987	95.865	95.687	95.405	94.521	95.222	94.572	94.216	93.977
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

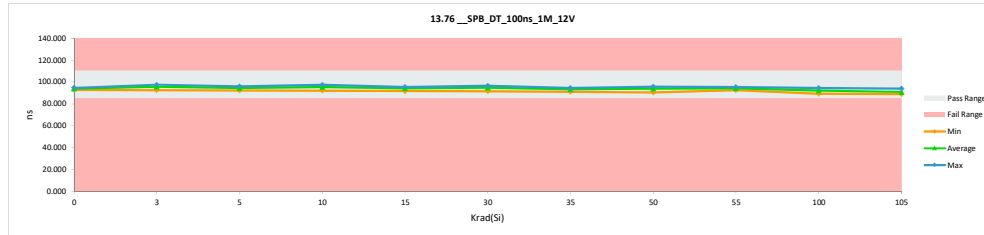


LDR TID Report
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13.76_SPB_DT_100ns_1M_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	110	110		
Min Limit	85	85		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	94.345	94.408	0.063
0	118	92.591	92.647	0.056
0	122	94.474	94.637	0.163
3	61	95.941	96.004	0.063
3	62	95.936	95.956	0.020
3	63	92.553	92.544	-0.009
3	64	97.530	97.536	0.006
3	65	95.419	95.347	-0.072
3	66	96.112	96.016	-0.096
5	67	95.337	95.292	-0.045
5	68	92.138	92.060	-0.078
5	69	94.519	94.352	-0.167
5	70	94.635	94.528	-0.107
5	71	95.889	95.671	-0.218
5	72	95.859	95.879	0.020
10	73	95.941	95.806	-0.135
10	74	95.936	95.758	-0.178
10	75	92.553	92.260	-0.293
10	76	97.530	97.330	-0.200
10	77	95.419	95.089	-0.330
10	78	96.112	95.726	-0.386
15	79	95.337	94.915	-0.422
15	80	92.138	91.576	-0.562
15	81	94.519	94.107	-0.412
15	82	94.635	94.196	-0.439
15	83	95.889	95.313	-0.576
15	84	95.859	95.416	-0.443
30	86	95.941	95.255	-0.686
30	89	95.936	95.179	-0.757
30	91	92.553	91.582	-0.971
30	92	97.530	96.673	-0.857
30	94	95.419	94.320	-1.099
30	95	96.112	95.027	-1.085
35	100	95.337	94.107	-1.230
35	101	92.138	90.852	-1.286
35	102	94.519	92.861	-1.658
35	103	94.635	93.113	-1.522
35	104	95.889	94.418	-1.471
35	105	95.859	94.372	-1.487
50	106	96.680	95.081	-1.599
50	107	97.439	95.714	-1.725
50	108	91.938	90.526	-1.412
50	109	96.026	94.633	-1.393
50	110	95.315	93.658	-1.657
50	111	95.944	94.373	-1.571
55	113	96.570	94.708	-1.862
55	114	94.373	92.445	-1.928
55	115	96.322	94.242	-2.080
55	116	96.637	94.724	-1.913
55	126	95.347	93.574	-1.773
55	128	97.364	95.422	-1.942
100	129	96.181	93.154	-3.027
100	131	92.996	90.040	-2.956
100	132	94.853	91.864	-2.989
100	133	92.985	89.807	-3.178
100	134	96.197	93.534	-2.663
100	135	96.528	93.857	-2.671
100	138	96.425	93.434	-2.991
100	139	91.516	89.208	-2.308
100	140	92.674	89.847	-2.827
100	141	96.831	93.861	-2.970
100	142	92.917	90.496	-2.421
100	143	93.499	90.674	-2.825
100	144	93.835	91.076	-2.759
100	220	96.085	93.355	-2.730
100	221	96.013	93.509	-2.504
100	224	93.361	90.941	-2.420
100	225	96.697	93.815	-2.882
100	226	96.630	94.490	-2.140
100	228	96.854	94.272	-2.582
100	232	95.195	92.713	-2.482
100	233	96.324	93.598	-2.726
100	236	92.159	89.755	-2.404
100	238	96.747	94.227	-2.520
100	243	95.275	92.361	-2.914
105	244	92.127	88.951	-3.176
105	246	92.258	89.262	-2.996
105	248	96.314	92.640	-3.674
105	374	92.535	89.233	-3.302
105	376	97.237	93.871	-3.366
Max		97.530	97.536	0.163
Average		95.078	93.563	-1.514
Min		91.516	88.951	-3.674
Std Dev		1.654	2.048	1.152

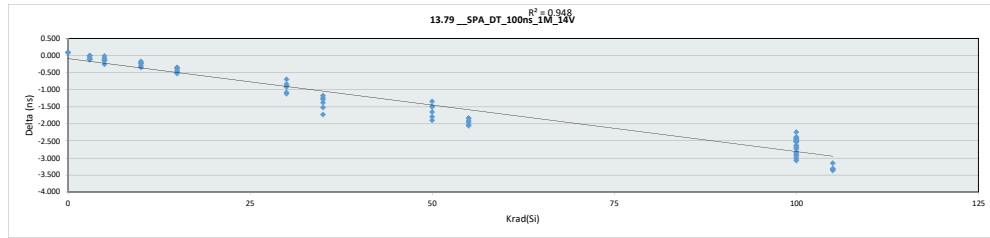


13.76_SPB_DT_100ns_1M_12V											
Test Site											
Tester											
Test Number											
Max Limit	110	ns									
Min Limit	85	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	92.647	92.544	92.060	92.260	91.576	91.582	90.852	90.526	92.445	89.208	88.951
Average	93.897	95.567	94.630	95.328	94.254	94.673	93.287	93.998	94.186	92.245	90.791
Max	94.637	97.536	95.879	97.330	95.416	96.673	94.418	95.714	95.422	94.490	93.871
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

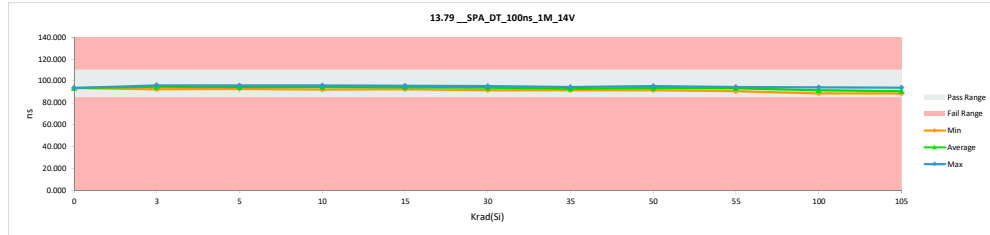


LDR TID Report
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13.79_SPA_DT_100ns_1M_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	110	110		
Min Limit	85	85		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	93.565	93.659	0.094
0	118	93.535	93.616	0.081
0	122	93.462	93.566	0.104
3	61	94.745	94.742	-0.003
3	62	95.193	95.189	-0.004
3	63	92.639	92.536	-0.103
3	64	95.754	95.685	-0.069
3	65	93.700	93.584	-0.116
3	66	96.130	96.129	-0.001
5	67	94.002	93.881	-0.121
5	68	92.659	92.648	-0.011
5	69	94.659	94.405	-0.254
5	70	94.474	94.321	-0.153
5	71	95.192	95.049	-0.143
5	72	96.098	96.023	-0.075
10	73	94.745	94.572	-0.173
10	74	95.193	95.004	-0.189
10	75	92.639	92.291	-0.348
10	76	95.754	95.522	-0.232
10	77	93.700	93.398	-0.302
10	78	96.130	95.889	-0.241
15	79	94.002	93.653	-0.349
15	80	92.659	92.313	-0.346
15	81	94.659	94.130	-0.529
15	82	94.474	94.093	-0.381
15	83	95.192	94.755	-0.437
15	84	96.098	95.624	-0.474
30	86	94.745	93.907	-0.838
30	89	95.193	94.316	-0.877
30	91	92.639	91.560	-1.079
30	92	95.754	94.831	-0.923
30	94	93.700	92.584	-1.116
30	95	96.130	95.434	-0.696
35	100	94.002	92.712	-1.290
35	101	92.659	91.480	-1.179
35	102	94.659	92.927	-1.732
35	103	94.474	93.091	-1.383
35	104	95.192	93.949	-1.243
35	105	96.098	94.576	-1.522
50	106	96.748	95.090	-1.658
50	107	97.032	95.237	-1.795
50	108	93.058	91.573	-1.485
50	109	94.905	93.557	-1.348
50	110	94.406	92.513	-1.893
50	111	95.637	94.136	-1.501
55	113	96.532	94.476	-2.056
55	114	92.761	90.810	-1.951
55	115	95.446	93.616	-1.830
55	116	96.360	94.523	-1.837
55	126	93.924	91.894	-2.030
55	128	96.087	94.169	-1.918
100	129	95.886	92.969	-2.917
100	131	93.330	90.648	-2.682
100	132	94.685	91.609	-3.076
100	133	92.529	90.016	-2.513
100	134	96.405	93.868	-2.537
100	135	95.669	93.179	-2.490
100	138	95.944	92.961	-2.983
100	139	91.222	88.586	-2.636
100	140	92.521	89.688	-2.833
100	141	95.525	92.656	-2.869
100	142	92.839	90.595	-2.244
100	143	92.224	89.193	-3.031
100	144	93.258	90.622	-2.636
100	220	94.430	92.008	-2.422
100	221	93.661	91.256	-2.405
100	224	92.319	89.849	-2.470
100	225	95.636	92.932	-2.704
100	226	95.220	92.839	-2.381
100	228	96.092	93.562	-2.530
100	232	94.321	91.892	-2.429
100	233	94.512	92.021	-2.491
100	236	92.129	89.648	-2.481
100	238	97.106	94.187	-2.919
100	243	93.421	90.677	-2.744
105	244	92.320	89.010	-3.310
105	246	93.024	89.872	-3.152
105	248	94.854	91.522	-3.332
105	374	91.901	88.577	-3.324
105	376	97.334	93.962	-3.372
	Max	97.334	96.129	0.104
	Average	94.494	92.996	-1.498
	Min	91.222	88.577	-3.372
	Std Dev	1.439	1.888	1.129

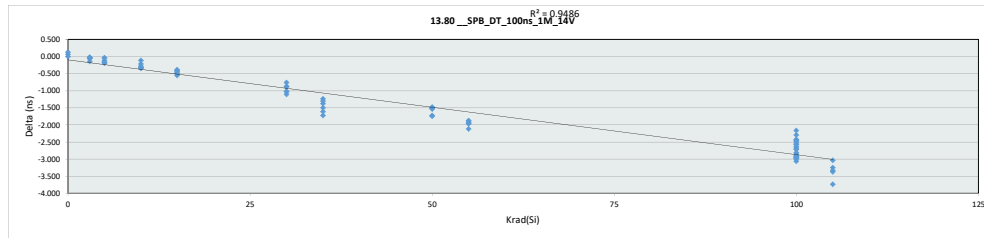


13.79_SPA_DT_100ns_1M_14V											
Test Site											
Tester											
Test Number											
Max Limit	110	ns									
Min Limit	85	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	93.566	92.536	92.648	92.291	92.313	91.560	91.480	91.573	90.810	88.586	88.577
Average	93.614	94.644	94.388	94.446	94.095	93.772	93.123	93.684	93.248	91.561	90.589
Max	93.659	96.129	96.023	95.889	95.624	95.434	94.576	95.237	94.523	94.187	93.962
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

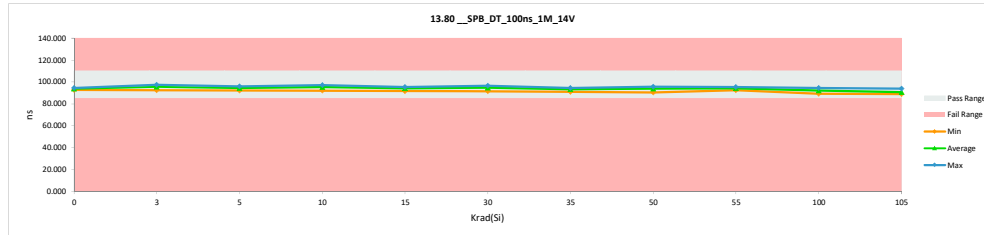


LDR TID Report
TPS7H5001-SP

13.80_SPB_DT_100ns_1M_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	110	110		
Min Limit	85	85		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	94.338	94.406	0.068
0	118	92.635	92.632	-0.003
0	122	94.473	94.599	0.126
3	61	96.002	95.952	-0.050
3	62	95.968	95.940	-0.028
3	63	92.611	92.556	-0.055
3	64	97.533	97.497	-0.036
3	65	95.410	95.272	-0.138
3	66	96.046	96.022	-0.024
5	67	95.350	95.233	-0.117
5	68	92.155	92.117	-0.038
5	69	94.535	94.378	-0.157
5	70	94.649	94.460	-0.189
5	71	95.814	95.631	-0.183
5	72	95.941	95.899	-0.042
10	73	96.002	95.784	-0.218
10	74	95.968	95.849	-0.119
10	75	92.611	92.276	-0.335
10	76	97.533	97.253	-0.280
10	77	95.410	95.088	-0.322
10	78	96.046	95.713	-0.333
15	79	95.350	94.946	-0.404
15	80	92.155	91.652	-0.503
15	81	94.535	94.090	-0.445
15	82	94.649	94.265	-0.384
15	83	95.814	95.337	-0.477
15	84	95.941	95.383	-0.558
30	86	96.002	95.239	-0.763
30	89	95.968	95.082	-0.886
30	91	92.611	91.602	-1.009
30	92	97.533	96.661	-0.872
30	94	95.410	94.299	-1.111
30	95	96.046	95.011	-1.035
35	100	95.350	94.109	-1.241
35	101	92.155	90.854	-1.301
35	102	94.535	92.812	-1.723
35	103	94.649	93.146	-1.503
35	104	95.814	94.437	-1.377
35	105	95.941	94.330	-1.611
50	106	96.644	95.114	-1.530
50	107	97.457	95.713	-1.744
50	108	91.955	90.470	-1.485
50	109	96.039	94.551	-1.488
50	110	95.413	93.688	-1.725
50	111	95.910	94.387	-1.523
55	113	96.639	94.705	-1.934
55	114	94.337	92.450	-1.887
55	115	96.351	94.235	-2.116
55	116	96.672	94.702	-1.970
55	126	95.365	93.487	-1.878
55	128	97.363	95.415	-1.948
100	129	96.226	93.232	-2.994
100	131	93.018	90.045	-2.973
100	132	94.793	91.826	-2.967
100	133	92.959	89.894	-3.065
100	134	96.241	93.527	-2.714
100	135	96.621	93.813	-2.808
100	138	96.382	93.426	-2.956
100	139	91.518	89.219	-2.299
100	140	92.791	89.834	-2.957
100	141	96.785	93.853	-2.932
100	142	92.978	90.512	-2.466
100	143	93.485	90.637	-2.848
100	144	93.762	91.088	-2.674
100	220	96.000	93.368	-2.632
100	221	96.022	93.547	-2.475
100	224	93.328	90.890	-2.438
100	225	96.722	93.820	-2.902
100	226	96.664	94.498	-2.166
100	228	96.815	94.253	-2.562
100	232	95.214	92.685	-2.529
100	233	96.342	93.636	-2.706
100	236	92.214	89.790	-2.424
100	238	96.750	94.175	-2.575
100	243	95.258	92.348	-2.910
105	244	92.096	88.842	-3.254
105	246	92.283	89.253	-3.030
105	248	96.319	92.580	-3.739
105	374	92.566	89.192	-3.374
105	376	97.237	93.901	-3.336
	Max	97.533	97.497	0.126
	Average	95.088	93.555	-1.533
	Min	91.518	88.842	-3.739
	Std Dev	1.648	2.044	1.148

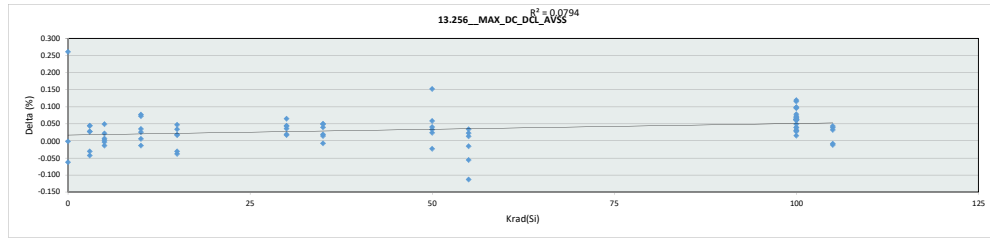


13.80_SPB_DT_100ns_1M_14V											
Test Site											
Tester											
Test Number											
Max Limit	110	ns									
Min Limit	85	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	92.632	92.556	92.117	92.276	91.652	91.602	90.854	90.470	92.450	89.219	88.842
Average	93.879	95.540	94.620	95.327	94.279	94.649	93.281	93.987	94.166	92.247	90.754
Max	94.599	97.497	95.899	97.253	95.383	96.661	94.437	95.713	95.415	94.498	93.901
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

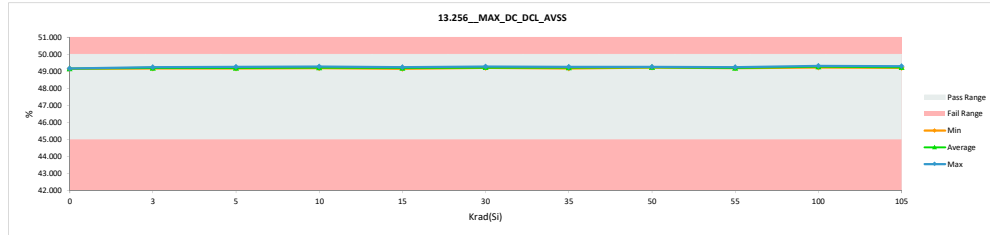


LDR TID Report
TPS7H5001-SP

13.256_MAX_DC_DCL_AVSS				
Test Site				
Tester				
Test Number				
Unit	%	%		
Max Limit	50	50		
Min Limit	45	45		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	49.165	49.163	-0.002
0	118	48.890	49.151	0.261
0	122	49.215	49.152	-0.063
3	61	49.186	49.213	0.027
3	62	49.168	49.212	0.044
3	63	49.194	49.238	0.044
3	64	49.168	49.195	0.027
3	65	49.232	49.201	-0.031
3	66	49.210	49.167	-0.043
5	67	49.186	49.172	-0.014
5	68	49.161	49.163	0.002
5	69	49.198	49.194	-0.004
5	70	49.216	49.265	0.049
5	71	49.211	49.218	0.007
5	72	49.168	49.190	0.022
10	73	49.186	49.258	0.072
10	74	49.168	49.203	0.035
10	75	49.194	49.271	0.077
10	76	49.168	49.174	0.006
10	77	49.232	49.258	0.026
10	78	49.210	49.196	-0.014
15	79	49.186	49.220	0.034
15	80	49.161	49.208	0.047
15	81	49.198	49.215	0.018
15	82	49.216	49.233	0.017
15	83	49.211	49.173	-0.038
15	84	49.168	49.137	-0.031
30	86	49.186	49.205	0.019
30	89	49.168	49.211	0.043
30	91	49.194	49.238	0.044
30	92	49.168	49.233	0.065
30	94	49.232	49.268	0.036
30	95	49.210	49.227	0.017
35	100	49.186	49.226	0.040
35	101	49.161	49.210	0.049
35	102	49.198	49.217	0.019
35	103	49.216	49.230	0.014
35	104	49.211	49.261	0.050
35	105	49.168	49.161	-0.007
50	106	49.180	49.221	0.041
50	107	49.094	49.246	0.152
50	108	49.202	49.235	0.033
50	109	49.197	49.255	0.058
50	110	49.233	49.257	0.024
50	111	49.239	49.216	-0.023
55	113	49.156	49.190	0.034
55	114	49.215	49.199	-0.016
55	115	49.293	49.180	-0.113
55	116	49.268	49.212	-0.056
55	126	49.215	49.238	0.023
55	128	49.179	49.192	0.013
100	129	49.185	49.281	0.096
100	131	49.170	49.269	0.099
100	132	49.208	49.247	0.039
100	133	49.174	49.236	0.062
100	134	49.161	49.223	0.062
100	135	49.198	49.270	0.072
100	138	49.225	49.287	0.062
100	139	49.160	49.279	0.119
100	140	49.210	49.241	0.031
100	141	49.171	49.241	0.070
100	142	49.216	49.256	0.040
100	143	49.256	49.283	0.027
100	144	49.222	49.290	0.068
100	220	49.194	49.310	0.116
100	221	49.240	49.301	0.061
100	224	49.239	49.288	0.049
100	225	49.184	49.247	0.063
100	226	49.215	49.293	0.078
100	228	49.194	49.254	0.060
100	232	49.264	49.294	0.030
100	233	49.188	49.252	0.064
100	236	49.276	49.291	0.015
100	238	49.172	49.270	0.098
100	243	49.176	49.240	0.064
105	244	49.203	49.246	0.043
105	246	49.184	49.216	0.032
105	248	49.222	49.210	-0.012
105	374	49.295	49.287	-0.008
105	376	49.174	49.213	0.039
	Max	49.295	49.310	0.261
	Average	49.195	49.230	0.035
	Min	48.890	49.137	-0.113
	Std Dev	0.048	0.040	0.049

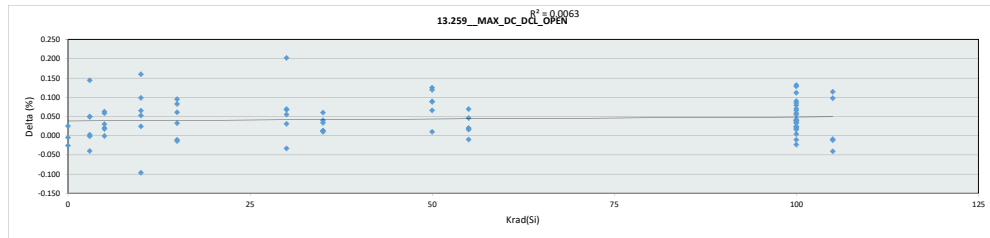


13.256_MAX_DC_DCL_AVSS											
Test Site											
Tester											
Test Number											
Max Limit	50	%									
Min Limit	45	%									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	45.000	45.000	45.000	45.000	45.000	45.000	45.000	45.000	45.000	45.000	45.000
Min	49.151	49.167	49.163	49.174	49.174	49.137	49.161	49.216	49.180	49.223	49.210
Average	49.155	49.204	49.200	49.227	49.198	49.230	49.218	49.238	49.202	49.268	49.234
Max	49.163	49.238	49.265	49.271	49.233	49.268	49.261	49.257	49.238	49.310	49.287
UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000

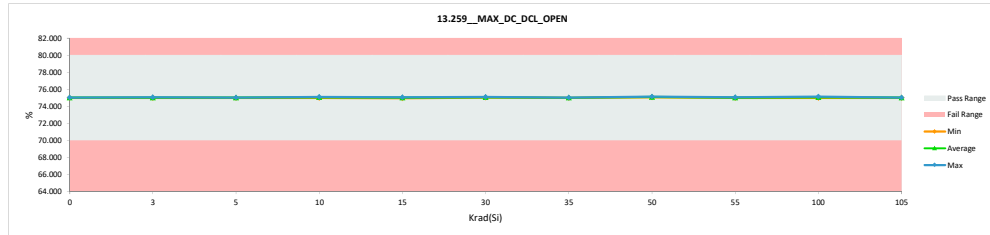


LDR TID Report
TPS7H5001-SP

13.259_MAX_DC_DCL_OPEN				
Test Site	Tester			
Unit	%	%		
Max Limit	80	80		
Min Limit	70	70		
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	74.979	74.974	-0.005
0	118	75.036	75.010	-0.026
0	122	75.004	75.029	0.025
3	61	74.905	75.049	0.144
3	62	75.085	75.045	-0.040
3	63	74.995	75.044	0.049
3	64	75.051	75.049	-0.002
3	65	75.008	75.058	0.050
3	66	74.971	74.973	0.002
5	67	74.984	75.004	0.020
5	68	74.965	75.023	0.058
5	69	74.971	74.988	0.017
5	70	75.022	75.021	-0.001
5	71	74.941	75.003	0.062
5	72	74.970	75.000	0.030
10	73	74.905	75.065	0.160
10	74	75.085	74.988	-0.097
10	75	74.995	75.060	0.065
10	76	75.051	75.075	0.024
10	77	75.008	75.106	0.098
10	78	74.971	75.023	0.052
15	79	74.984	75.066	0.082
15	80	74.965	75.026	0.061
15	81	74.971	75.003	0.032
15	82	75.022	75.011	-0.011
15	83	74.941	75.036	0.095
15	84	74.970	74.956	-0.014
30	86	74.905	75.107	0.202
30	89	75.085	75.052	-0.033
30	91	74.995	75.050	0.055
30	92	75.051	75.082	0.031
30	94	75.008	75.077	0.069
30	95	74.971	75.038	0.067
35	100	74.984	75.017	0.033
35	101	74.965	75.025	0.060
35	102	74.971	74.983	0.012
35	103	75.022	75.035	0.013
35	104	74.941	74.981	0.040
35	105	74.970	74.980	0.010
50	106	74.975	75.064	0.089
50	107	75.014	75.133	0.119
50	108	74.988	75.076	0.088
50	109	75.075	75.085	0.010
50	110	74.971	75.096	0.125
50	111	74.995	75.061	0.066
55	113	75.010	75.000	-0.010
55	114	74.996	75.065	0.069
55	115	74.984	75.030	0.046
55	116	75.015	75.032	0.017
55	126	75.034	75.050	0.016
55	128	74.963	74.983	0.020
100	129	75.045	75.083	0.038
100	131	74.999	75.022	0.023
100	132	74.970	75.002	0.032
100	133	75.066	75.089	0.023
100	134	74.972	74.990	0.018
100	135	75.027	75.004	-0.023
100	138	74.988	75.046	0.058
100	139	75.100	75.116	0.016
100	140	75.022	75.064	0.042
100	141	74.932	75.011	0.079
100	142	75.004	75.043	0.039
100	143	75.008	75.030	0.022
100	144	74.999	75.069	0.070
100	220	75.002	75.092	0.090
100	221	74.980	75.111	0.131
100	224	75.036	75.071	0.035
100	225	75.026	75.015	-0.011
100	226	74.956	75.084	0.128
100	228	74.985	74.989	0.004
100	232	75.009	75.048	0.039
100	233	75.002	75.087	0.085
100	236	75.046	75.101	0.055
100	238	74.956	75.022	0.066
100	243	74.997	75.108	0.111
105	244	75.018	75.006	-0.012
105	246	75.006	74.965	-0.041
105	248	75.038	75.029	-0.009
105	374	74.910	75.007	0.097
105	376	74.913	75.027	0.114
	Max	75.100	75.133	0.202
	Average	74.996	75.039	0.043
	Min	74.905	74.956	-0.097
	Std Dev	0.042	0.040	0.050

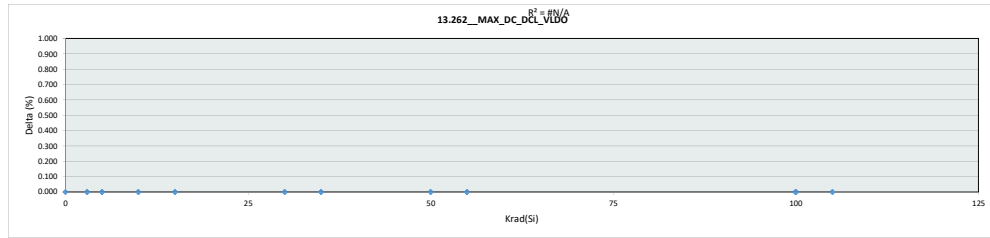


13.259_MAX_DC_DCL_OPEN											
Test Site	Tester										
Unit	%	%									
Max Limit	80	80									
Min Limit	70	70									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	70.000	70.000	70.000	70.000	70.000	70.000	70.000	70.000	70.000	70.000	70.000
Min	74.974	74.973	74.988	74.988	74.956	75.038	74.980	75.061	74.983	74.989	74.965
Average	75.004	75.036	75.007	75.053	75.016	75.068	75.004	75.086	75.027	75.054	75.007
Max	75.029	75.058	75.023	75.106	75.066	75.107	75.035	75.133	75.065	75.116	75.029
UL	80.000	80.000	80.000	80.000	80.000	80.000	80.000	80.000	80.000	80.000	80.000

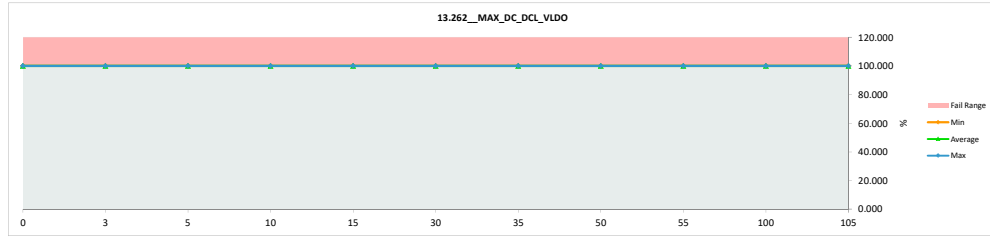


LDR TID Report
TPS7H5001-SP

13.262_MAX_DC_DCL_VLDO				
Test Site	Tester	Unit	Max Limit	Min Limit
13.262			100	100
Krad(Si)	Serial #	PRE_LDR	POST_LDR	Delta
0	117	100.000	100.000	0.000
0	118	100.000	100.000	0.000
0	122	100.000	100.000	0.000
3	61	100.000	100.000	0.000
3	62	100.000	100.000	0.000
3	63	100.000	100.000	0.000
3	64	100.000	100.000	0.000
3	65	100.000	100.000	0.000
3	66	100.000	100.000	0.000
5	67	100.000	100.000	0.000
5	68	100.000	100.000	0.000
5	69	100.000	100.000	0.000
5	70	100.000	100.000	0.000
5	71	100.000	100.000	0.000
5	72	100.000	100.000	0.000
10	73	100.000	100.000	0.000
10	74	100.000	100.000	0.000
10	75	100.000	100.000	0.000
10	76	100.000	100.000	0.000
10	77	100.000	100.000	0.000
10	78	100.000	100.000	0.000
15	79	100.000	100.000	0.000
15	80	100.000	100.000	0.000
15	81	100.000	100.000	0.000
15	82	100.000	100.000	0.000
15	83	100.000	100.000	0.000
15	84	100.000	100.000	0.000
30	86	100.000	100.000	0.000
30	89	100.000	100.000	0.000
30	91	100.000	100.000	0.000
30	92	100.000	100.000	0.000
30	94	100.000	100.000	0.000
30	95	100.000	100.000	0.000
35	100	100.000	100.000	0.000
35	101	100.000	100.000	0.000
35	102	100.000	100.000	0.000
35	103	100.000	100.000	0.000
35	104	100.000	100.000	0.000
35	105	100.000	100.000	0.000
50	106	100.000	100.000	0.000
50	107	100.000	100.000	0.000
50	108	100.000	100.000	0.000
50	109	100.000	100.000	0.000
50	110	100.000	100.000	0.000
50	111	100.000	100.000	0.000
55	113	100.000	100.000	0.000
55	114	100.000	100.000	0.000
55	115	100.000	100.000	0.000
55	116	100.000	100.000	0.000
55	126	100.000	100.000	0.000
55	128	100.000	100.000	0.000
100	129	100.000	100.000	0.000
100	131	100.000	100.000	0.000
100	132	100.000	100.000	0.000
100	133	100.000	100.000	0.000
100	134	100.000	100.000	0.000
100	135	100.000	100.000	0.000
100	138	100.000	100.000	0.000
100	139	100.000	100.000	0.000
100	140	100.000	100.000	0.000
100	141	100.000	100.000	0.000
100	142	100.000	100.000	0.000
100	143	100.000	100.000	0.000
100	144	100.000	100.000	0.000
100	220	100.000	100.000	0.000
100	221	100.000	100.000	0.000
100	224	100.000	100.000	0.000
100	225	100.000	100.000	0.000
100	226	100.000	100.000	0.000
100	228	100.000	100.000	0.000
100	232	100.000	100.000	0.000
100	233	100.000	100.000	0.000
100	236	100.000	100.000	0.000
100	238	100.000	100.000	0.000
100	243	100.000	100.000	0.000
105	244	100.000	100.000	0.000
105	246	100.000	100.000	0.000
105	248	100.000	100.000	0.000
105	374	100.000	100.000	0.000
105	376	100.000	100.000	0.000
Max		100.000	100.000	0.000
Average		100.000	100.000	0.000
Min		100.000	100.000	0.000
Std Dev		0.000	0.000	0.000



13.262_MAX_DC_DCL_VLDO											
Test Site	Tester	Unit	Max Limit	Min Limit							
13.262			100	%							
Min Limit			%								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Average	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Max	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
UL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000



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