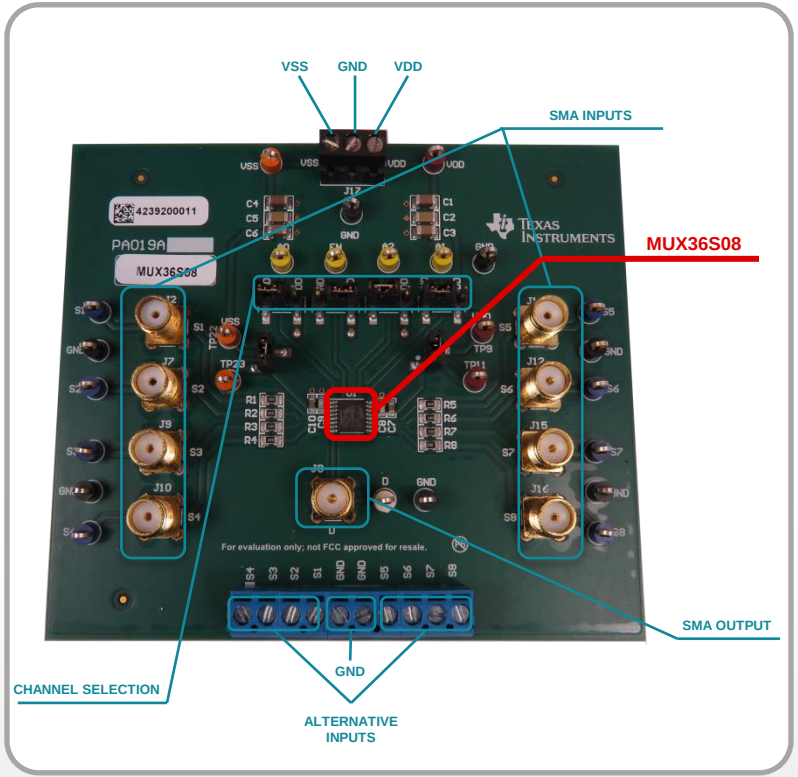
MUX36S08 EVM Board



More information about high voltage MUX's can be found at **http://www.ti.com/hvmux**

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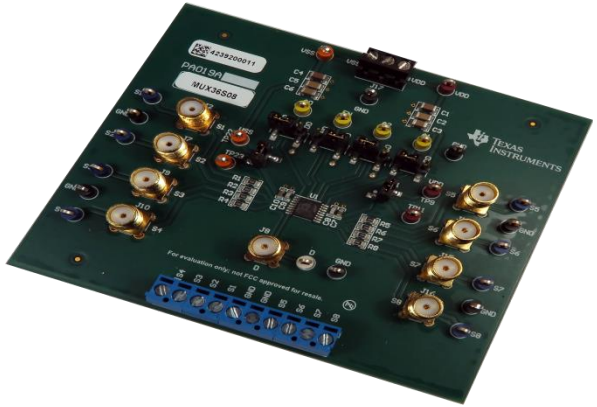
Quick Start Guide:
MUX36S08EVM-PDK

Texas Instruments

The MUX36S08 Evaluation Module Performance Demonstration Kit (PDK) is ideal for evaluating and starting development with the MUX36S08 precision analog multiplexer. This kit is comprised of a MUX evaluation board (EVM) which contains the MUX36S08 single input multiplexer, as well as all required input and output connectors. The EVM features 8 SMA connector/pin inputs and one SMA/pin output. The power supply has a wide range offering ±5V to ±18V, 10V to 36V symmetric or asymmetric supplies. The MUX36S08EVM-PDK contains multiple connectors on input and output pins for ease of evaluation.

MUX36S08EVM-PDK Features:

- 36V input, low-capacitance, low charge injection, low-leakage current analog multiplexer
- 26 compact test points for evaluation
- Bipolar or unipolar inputs with symmetrical or asymmetrical supply
- Header selectable channels



The data sheet for the MUX36S08 can be found at http://www.ti.com/lit/ds/symlink/mux36s08.pdf

Quick Start Guide: MUX36S08 Multiplexer Demonstration Kit

1

A) Watch the kit overview video

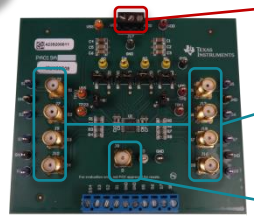
http://www.ti.com/MUX36S08EVM-PDK

B) Download the MUX36S08EVM-PDK users guide

http://www.ti.com/lit/ug/sbau254a/sbau254a.pdf

2

Connect power supply and inputs/output to the MUX36S08EVM-PDK



MUX36S08 EVM Board

Power inputs accept symmetric or asymmetric 36V source.

Unipolar or bipolar input signals can be connected to the EVM's SMA connectors.

Output can be read from the EVM's SMA connector labeled "D".

Channel selection logic to the MUX36S08EVM-PDK

On Channel	EN	A2	A1	A0
All channels are off	0	X	X	X
Channel 1	1	0	0	0
Channel 2	1	0	0	1
Channel 3	1	0	1	0
Channel 4	1	0	1	1
Channel 5	1	1	0	0
Channel 6	1	1	0	1
Channel 7	1	1	1	0
Channel 8	1	1	1	1

CHANNEL CONFIGURATION HEADERS

A0 EN A2 A1

The jumpers can be configured to select the input channel. Please refer to the table above for jumper configuration.

Connecting a middle pin to VDD corresponds to 1 while connecting it to ground gives a 0. Enable must be connected to V_{DD} to receive an output.

TI E2E Community

Technical support for Precision ADCs can be found at **http://e2e.ti.com/support/logic/**

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