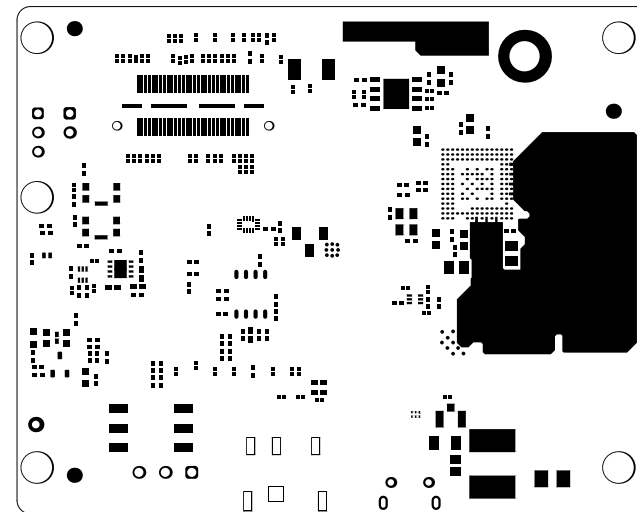
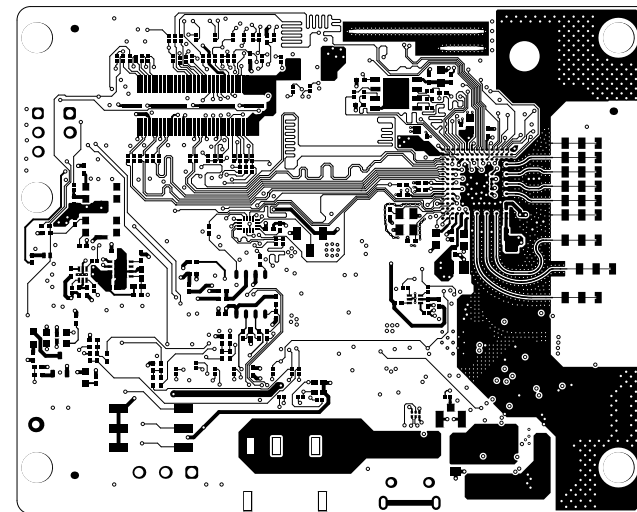


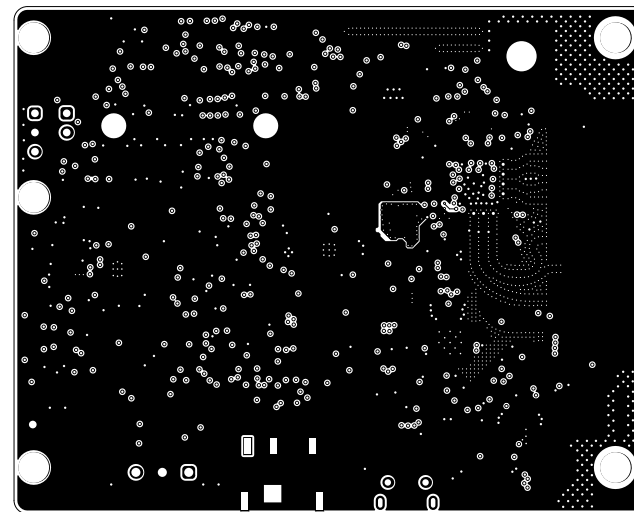
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC051	REV: B	SUN REV: Not In VersionControl
LAYER NAME = Top Overlay	TID #: N/A		
PLOT NAME = Top Overlay	GENERATED : 11/19/2018 3:50:06 PM	TEXAS INSTRUMENTS	



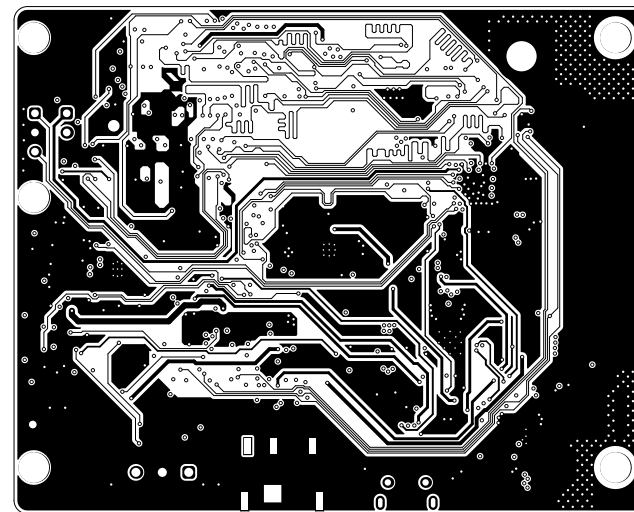
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC051	REV: B	SUN REV: Not In VersionControl
LAYER NAME = Top Solder	TID #: N/A		
PLOT NAME = Top Solder Mask	GENERATED : 11/19/2018 3:50:07 PM	TEXAS INSTRUMENTS	



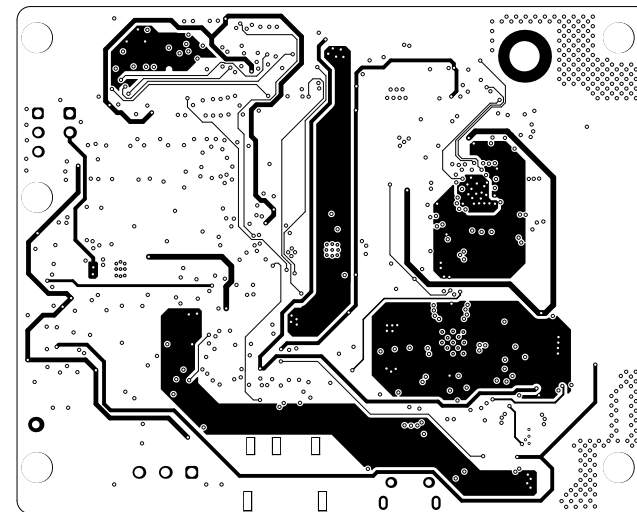
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC051	REV: B	SUN REV: Not In VersionControl
LAYER NAME = TOP	TID #: N/A		
PLOT NAME = Top Layer	GENERATED : 11/19/2018 3:50:09 PM	TEXAS INSTRUMENTS	



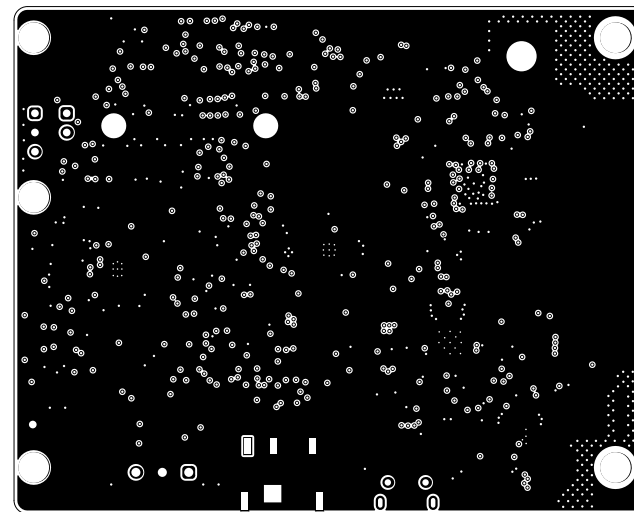
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC051	REV: B	SUN REV: Not In VersionControl
LAYER NAME = L2	TID #: N/A		
PLOT NAME = Signal Layer 1	GENERATED : 11/19/2018 3:50:11 PM		TEXAS INSTRUMENTS



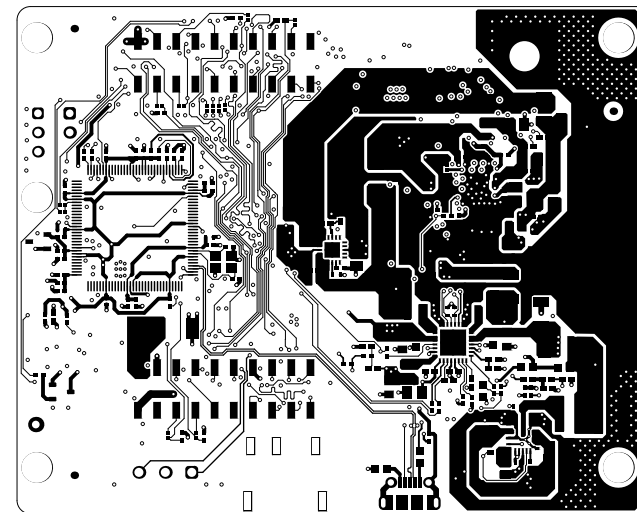
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC051	REV: B	SUN REV: Not In VersionControl
LAYER NAME = L3	TID #: N/A		
PLOT NAME = Signal Layer 2	GENERATED : 11/19/2018 3:50:12 PM		TEXAS INSTRUMENTS



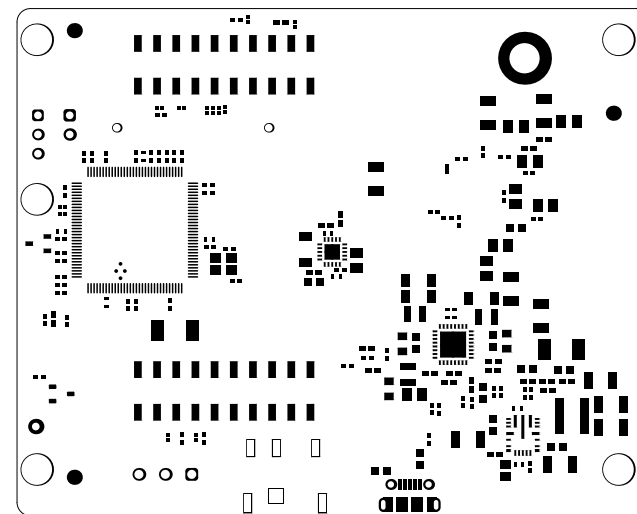
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC051	REV: B	SUN REV: Not In VersionControl
LAYER NAME = L4	TID #: N/A		
PLOT NAME = Signal Layer 3	GENERATED : 11/19/2018 3:50:14 PM		TEXAS INSTRUMENTS



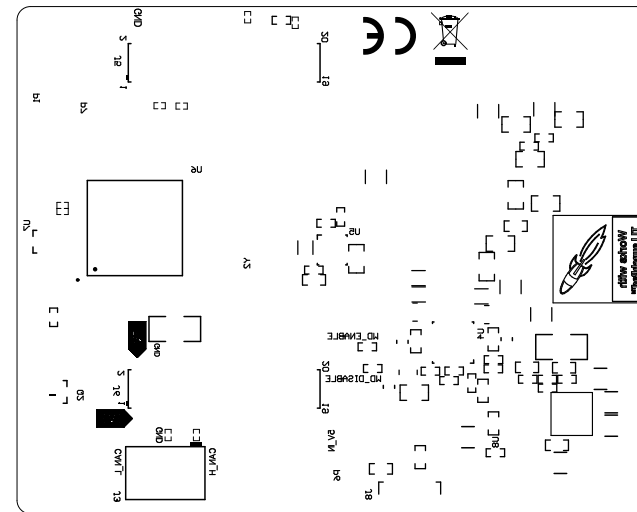
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC051	REV: B	SUN REV: Not In VersionControl
LAYER NAME = L5	TID #: N/A		
PLOT NAME = Signal Layer 4	GENERATED : 11/19/2018 3:50:16 PM	TEXAS INSTRUMENTS	



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC051	REV: B	SUN REV: Not In VersionControl
LAYER NAME = BOTTOM	TID #: N/A		
PLOT NAME = Bottom Layer	GENERATED : 11/19/2018 3:50:17 PM	TEXAS INSTRUMENTS	



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC051	REV: B	SUN REV: Not In VersionControl
LAYER NAME = Bottom Solder	TID #: N/A		
PLOT NAME = Bottom Solder Mask	GENERATED : 11/19/2018 3:50:19 PM	TEXAS INSTRUMENTS	



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC051	REV: B	SUN REV: Not In VersionControl
LAYER NAME = Bottom Overlay	TID #: N/A		
PLOT NAME = Bottom Overlay	GENERATED : 11/19/2018 3:50:21 PM	TEXAS INSTRUMENTS	

Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Pad Shape	Hole Length
□	30	7.87mil (0.200mm)	PTH	Round	TOP - BOTTOM	Rounded	-
✖	4	8.00mil (0.203mm)	PTH	Round	TOP - BOTTOM	Rounded	-
B	7	12.10mil (0.307mm)	PTH	Round	TOP - BOTTOM	Rounded	-
⊙	730	12.20mil (0.310mm)	PTH	Round	TOP - BOTTOM	Rounded	-
D	2	23.62mil (0.600mm)	PTH	Slot	TOP - BOTTOM	Rounded	51.18mil (1.300mm)
⌘	3	31.50mil (0.800mm)	PTH	Rectangle	TOP - BOTTOM	Rounded Rectangle	78.74mil (2.000mm)
▽	2	31.50mil (0.800mm)	PTH	Rectangle	TOP - BOTTOM	Rounded Rectangle	90.55mil (2.300mm)
◇	2	33.47mil (0.850mm)	PTH	Round	TOP - BOTTOM	Rounded	-
☆	3	40.00mil (1.016mm)	PTH	Round	TOP - BOTTOM	<Mixed>	-
F	2	40.16mil (1.020mm)	NPTH	Round	TOP - BOTTOM	Rounded	-
E	3	40.16mil (1.020mm)	PTH	Round	TOP - BOTTOM	<Mixed>	-
■	3	47.24mil (1.200mm)	PTH	Round	TOP - BOTTOM	<Mixed>	-
⊙	1	70.87mil (1.800mm)	NPTH	Rectangle	TOP - BOTTOM	Rounded Rectangle	71.00mil (1.803mm)
▼	1	157.48mil (4.000mm)	PTH	Round	TOP - BOTTOM	Rounded	-
⊙	5	160.00mil (4.064mm)	NPTH	Round	TOP - BOTTOM	Rounded	-
	798 Total						

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

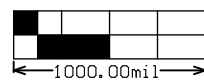
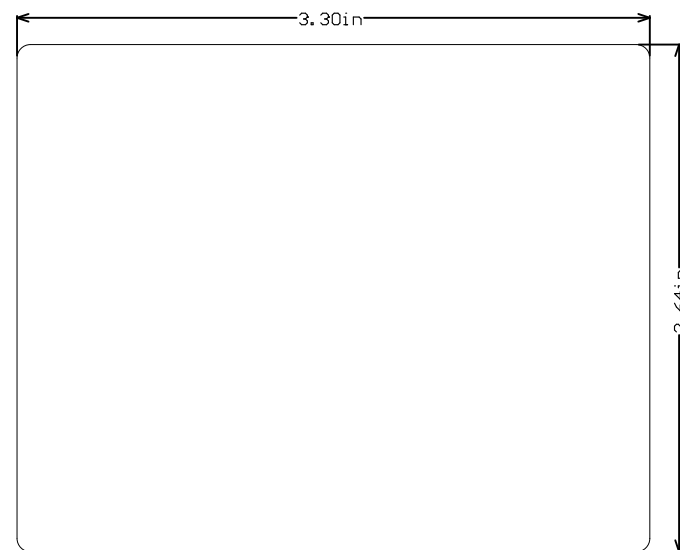
Drill Table
FOR PTH +/- 3MIL
FOR NPTH +/- 2MIL
FOR 7.874MIL DRILL +0/-7.874MIL
FOR 8MIL DRILL +0/-8MIL
FOR 12.1MIL DRILL +0/-12.1MIL
FOR 12.2MIL DRILL +0/-12.2MIL

Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Length	Routed Path Length
○	620	5.90mil (0.150mm)	PTH	Round	TOP - L2	-	-
	620 Total						

FOR 5.9MIL DRILL +0/-5.9MIL



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC051	REV: B	SUN REV: Not In VersionControl
LAYER NAME = Drill Drawing	TID #: N/A		
PLOT NAME = Drill Drawing	GENERATED : 11/19/2018 3:50:23 PM	TEXAS INSTRUMENTS	



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC051	REV: B	SUN REV: Not In VersionControl
LAYER NAME = M2 Board Dimensions	TID #: N/A		
PLOT NAME = Board Dimensions	GENERATED : 11/19/2018 3:50:30 PM	TEXAS INSTRUMENTS	

IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to TI's Terms of Sale (www.ti.com/legal/termsofsale.html) or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
Copyright © 2020, Texas Instruments Incorporated