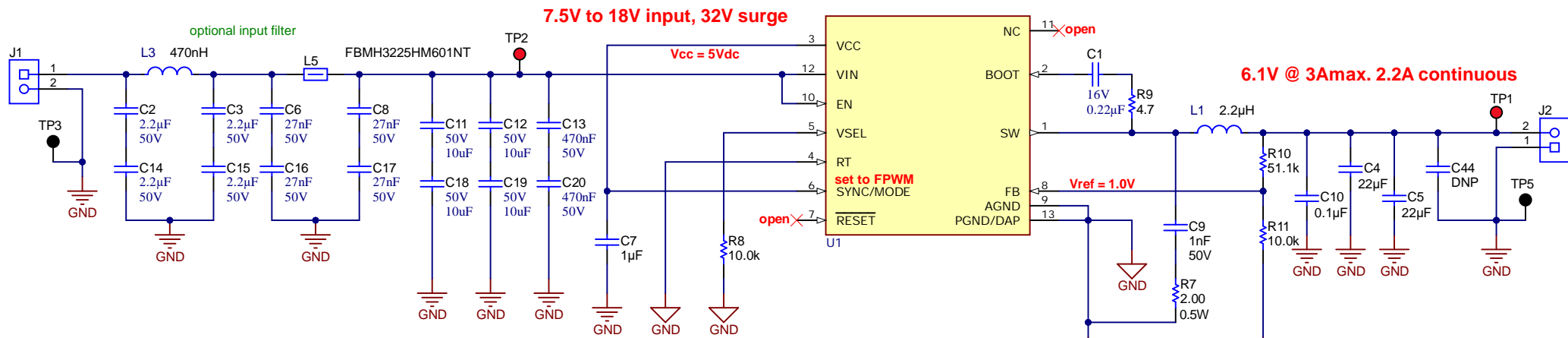


Orderable:	Designed for: Public Release	Mod. Date: 07.10.2020
TID #: N/A	Project Title: Automotive Synchronous Buck	
Number: PMP30930	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 1 of 1
Drawn By:	File: PMP30930RevA_schematic_2_SchDoc	Size: A4
Engineer: B. Görner	Contact: http://www.ti.com/support	

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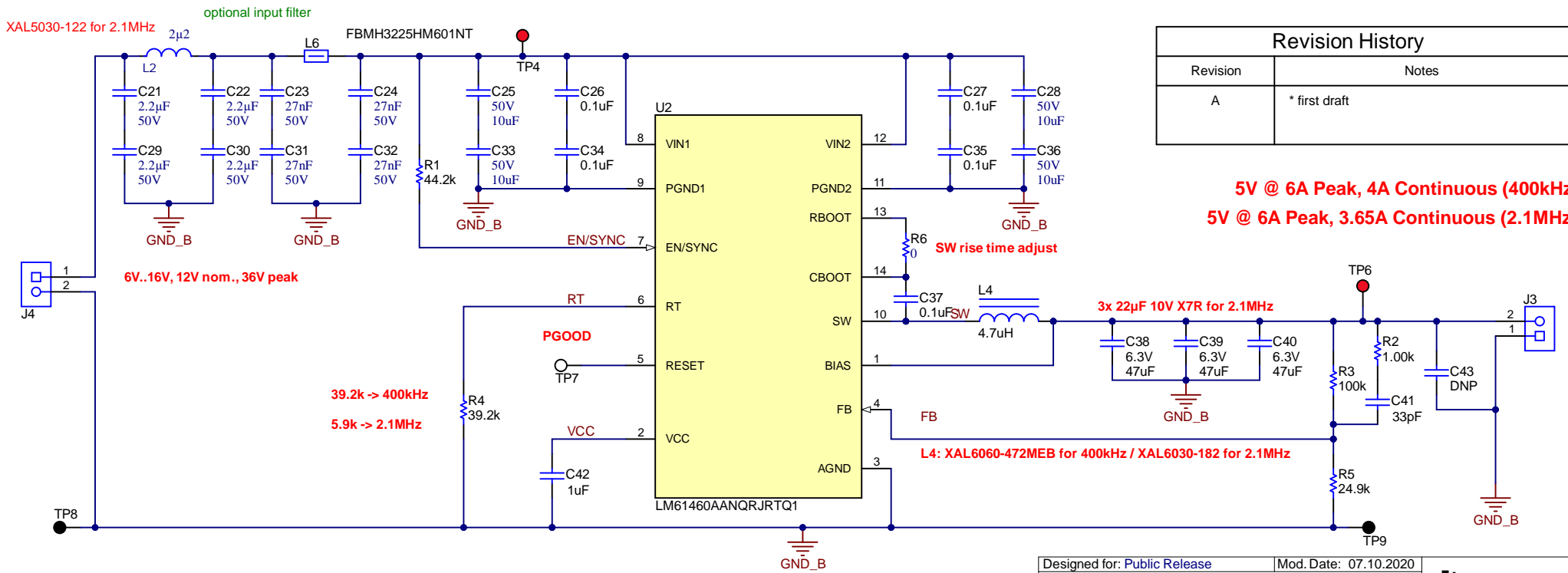
Input voltage limitations at high switching frequency:
 1) min. off time 100ns, max. duty 79% at F_{sw} 2.1MHz, minimum input voltage >6.3V, F_{sw} = 2.1MHz
 2) min. on time >75ns, min. duty 16% at F_{sw} 2.1MHz, max. input voltage <31.75V

R_t tied to AGND

V_{sel} 10k
 V_{out} adjustable

LM63635-Q1 -> I_{OUT} = 3.0A
 LM63625-Q1 -> I_{OUT} = 2.0A
 LM63615-Q1 -> I_{OUT} = 1.0A

Revision History	
Revision	Notes
A	* first draft



Designed for: Public Release	Mod. Date: 07.10.2020
Project Title: Automotive Synchronous Buck	
Number: PMP30930	Rev: A
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