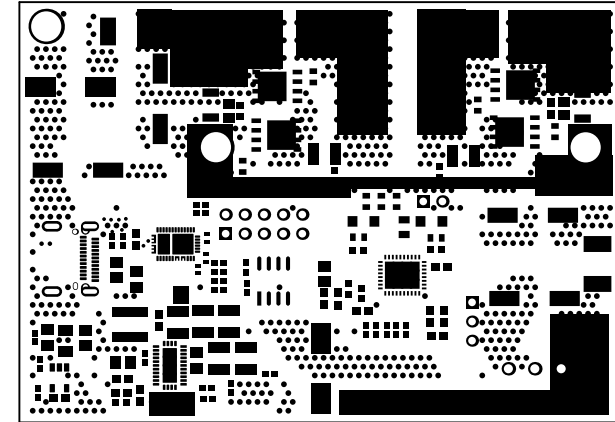
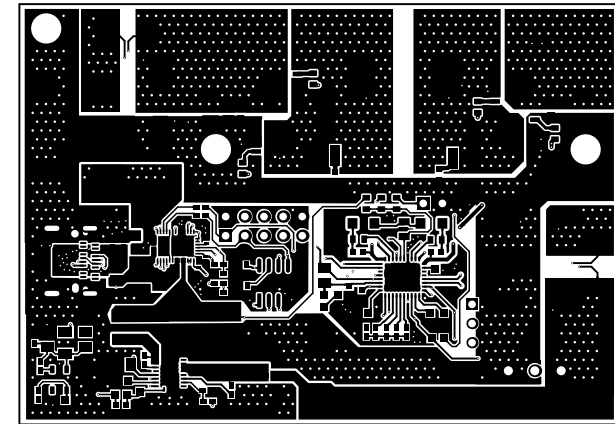


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LAYER NAME	DESCRIPTION		
REF ID	VALUE	PACKAGE	FOOTPRINT



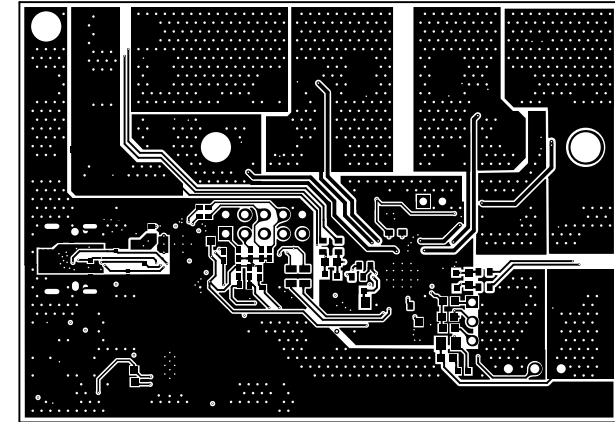
ALL RIGHTS RESERVED FOR THE BOARD	DATE: 11/11/2011	REV: 1.0	DESIGNER: [REDACTED]
PROJECT: [REDACTED]	[REDACTED]		
FILE: [REDACTED]	DATE: 11/11/2011	DESIGNER: [REDACTED]	FILE: [REDACTED]



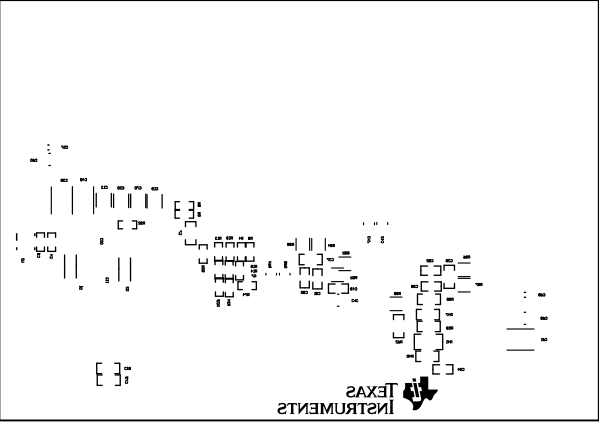
Layer	Name	Material	Thickness	Constant
	Top Overlay			
	Top Solder	Solder Resist	0.010mm	3.5
1	Top Layer		0.036mm	
	Dielectric 1	FR-4 High Tg	0.203mm	4.2
2	Signal Layer 1		0.036mm	
	Dielectric 2	FR-4 High Tg	1.016mm	4.2
3	Signal Layer 2		0.036mm	
	Dielectric 3	FR-4 High Tg	0.203mm	4.2
4	Bottom Layer		0.036mm	
	Bottom Solder	Solder Resist	0.010mm	3.5
	Bottom Overlay			

Total board thickness: 1.585mm

ALL DIMENSIONS UNLESS SPECIFIED OTHERWISE	DATE: 10/10/2010	REV: 02	SCALE: 1:1
DESIGNED BY: [Name]	CHECKED BY: [Name]		
DATE: 10/10/2010	DESIGNED BY: [Name]	CHECKED BY: [Name]	DATE: 10/10/2010



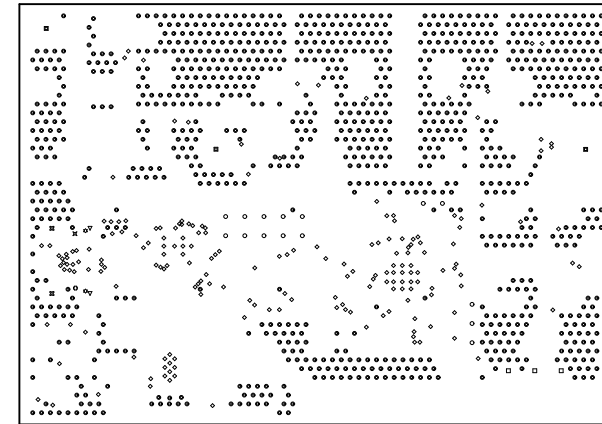
ALL DIMENSIONS UNLESS SPECIFIED OTHERWISE	DATE: 10/10/2023	REV: 01	SCALE: 1:1	DESIGNER: J. SMITH
PROJECT: PCB LAYOUT				CHECKED: M. JONES
APP: 1001	DESIGN: 1001	DATE: 10/10/2023	SCALE: 1:1	FILE: 1001.PCB



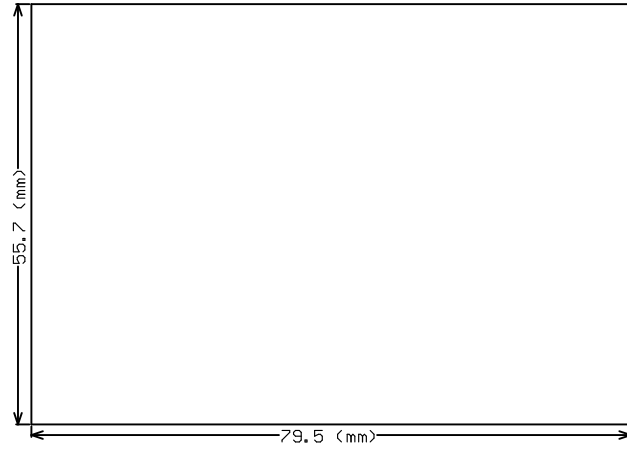
ALL RIGHTS RESERVED FOR THE STATE	STATE OF TEXAS	BY: _____	DATE: _____
JAMES H. _____			
JAMES H. _____	STATE OF TEXAS	DATE: _____	

Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
◇	196	0.200mm (7.87mil)	PTH	Round	Top Layer - Bottom Layer	
●	999	0.330mm (13.00mil)	PTH	Round	Top Layer - Bottom Layer	
⊗	1	0.500mm (19.69mil)	NPTH	Slot	Top Layer - Bottom Layer	+/-0.030mm
▽	1	0.600mm (23.62mil)	NPTH	Round	Top Layer - Bottom Layer	+/-0.030mm
∇	2	0.600mm (23.62mil)	PTH	Slot	Top Layer - Bottom Layer	+/-0.050mm
⊛	2	0.700mm (27.56mil)	PTH	Slot	Top Layer - Bottom Layer	+/-0.050mm
⊗	2	0.700mm (27.56mil)	PTH	Slot	Top Layer - Bottom Layer	+/-0.050mm
○	15	1.016mm (40.00mil)	PTH	Round	Top Layer - Bottom Layer	
□	3	1.200mm (47.24mil)	PTH	Round	Top Layer - Bottom Layer	
■	3	4.000mm (157.48mil)	PTH	Round	Top Layer - Bottom Layer	
	1224 Total					

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size + Slot length as defined in the PCB layout.



ALL RIGHTS RESERVED FOR THE BOARD	DATE: 11/11/2023	REV: 02	DESIGNER: [Name]
USER: [Name]	[Information]		
APP: [Name]	PROJECT: [Name]	DATE: [Date]	TIME: [Time]



ALL RIGHTS RESERVED FOR THE USER	DATE	TIME	FILE	EXT	FILE	EXT
USER NAME	IP ADDRESS					
HOST NAME	MAC ADDRESS	IP ADDRESS	PORT	PROTOCOL	USER COMMENTS	

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