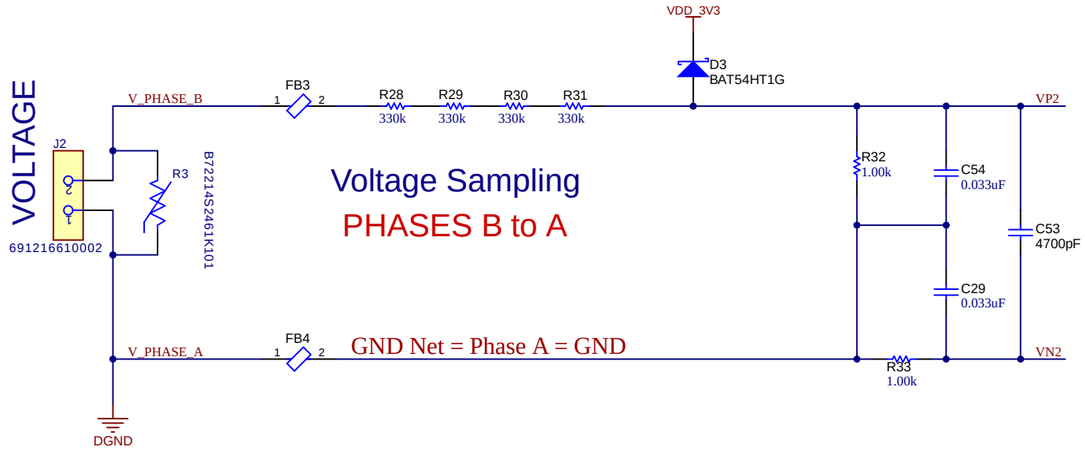
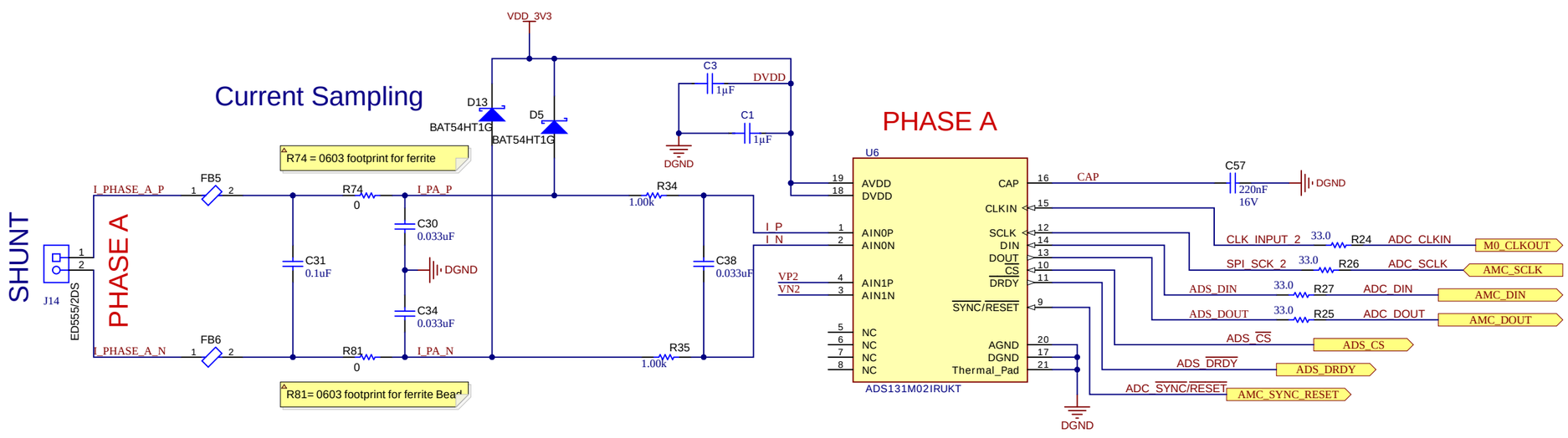
PHASE A





Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 7/3/2024
TID #: 010944	Project Title: Change in menu Project|Project Options|Parameter	
Number: TIDA-010944 Rev: E5	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 1 of 6
Drawn By:	File: 2P_TIDA-10944_ADS_RTM.SchDoc	Size: B
Engineer: Jose Velez - Lopez Jr	Contact: http://www.ti.com/support	

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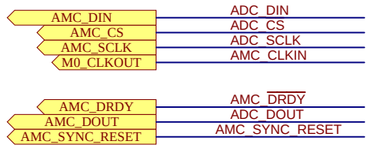
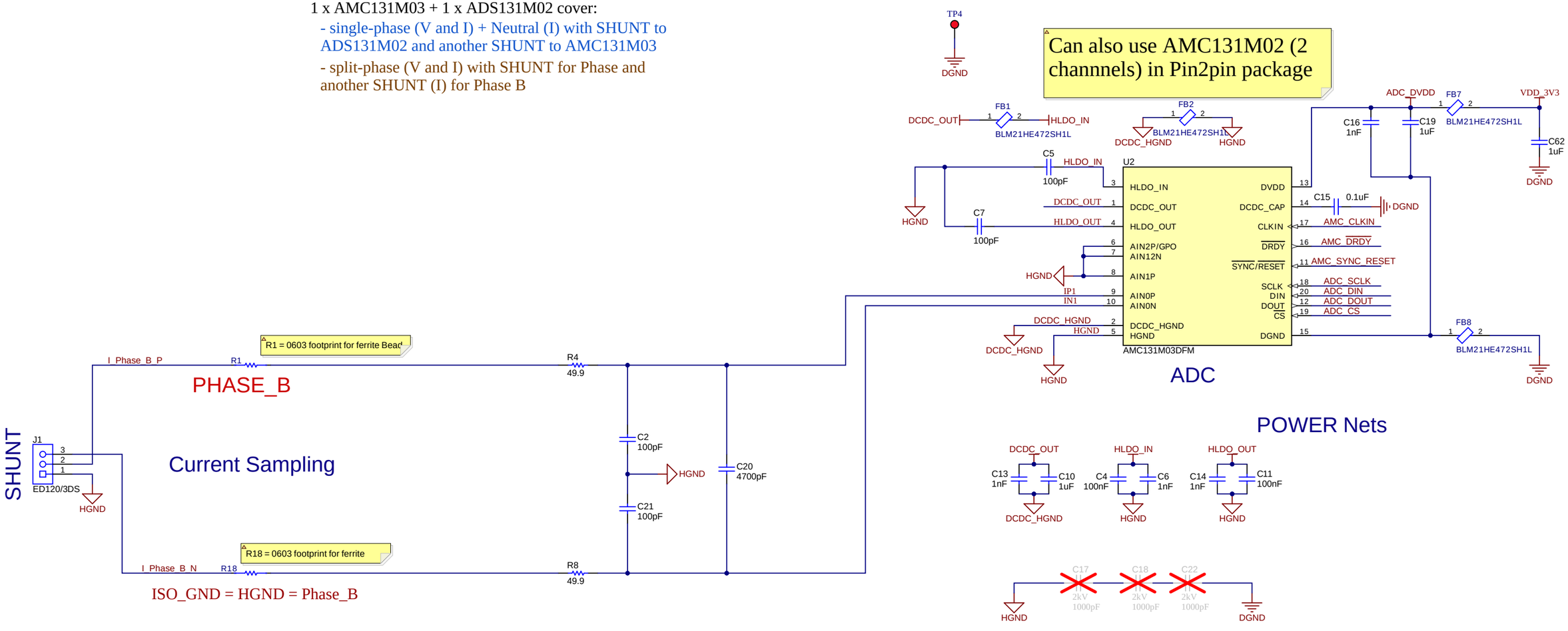
1 x AMC131M03 + 1 x ADS131M02 cover:

- single-phase (V and I) + Neutral (I) with SHUNT to ADS131M02 and another SHUNT to AMC131M03
- split-phase (V and I) with SHUNT for Phase and another SHUNT (I) for Phase B

Digital Interface





Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 7/3/2024
TID #: 010944	Project Title: Change in menu Project|Project Options|Parameter	
Number: TIDA-010944 Rev: E5	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 2 of 6
Drawn By:	File: 2P_TIDA-10944_AMC_RTM.SchDoc	Size: B
Engineer: Jose Velez - Lopez Jr	Contact: http://www.ti.com/support	

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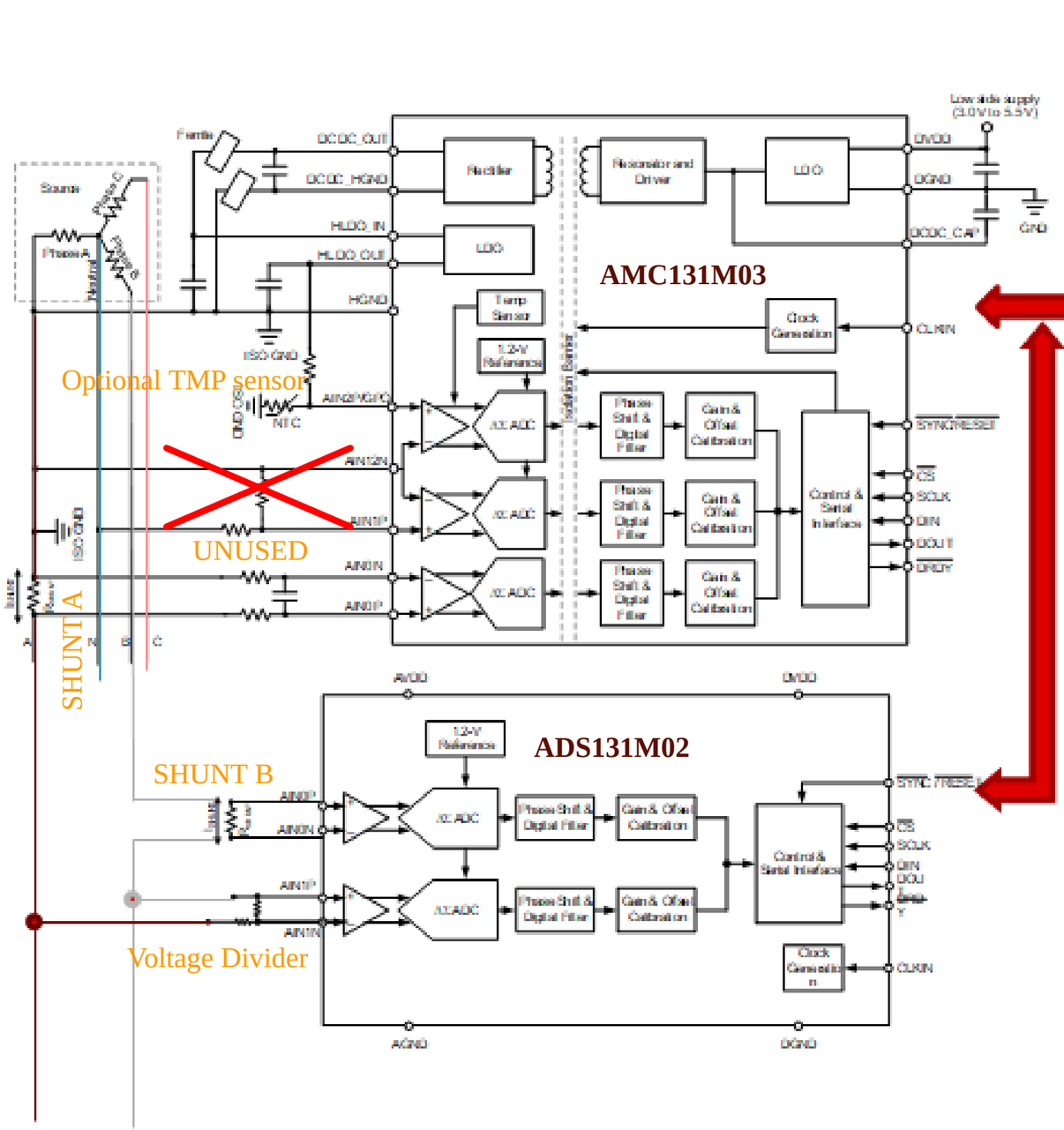

Orderable: ChangeMe in variant		Designed for: Public Release	Mod. Date: 5/3/2024
TID #: 010944		Project Title: Change in menu Project|Project Options|Parameter	
Number: TIDA-010944	Rev: E5	Sheet Title:	
SVN Rev: Not in version control		Assembly Variant: 001	Sheet: 3 of 6
Drawn By:		File: 2P_TIDA-10944_BD.SchDoc	Size: B
Engineer: Jose Velez - Lopez Jr		Contact: http://www.ti.com/support	

H1
5MM HOLE

H2
5MM HOLE

H3
5MM HOLE

H4
5MM HOLE

FID1

FID2

FID3

logo5

logo6

PCB Number: TIDA-010944
PCB Rev: E5

PCB
LOGO
Texas Instruments

CE Mark

PCB
LOGO
FCC disclaimer

PCB
LOGO
WEEE logo

LBL1
PCB Label
THT-14-423-10
Size: 0.65" x 0.20 "

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

Variant/Label Table	
Variant	Label Text
001	TIDA-010944

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 7/31/2024
TID #: 010944	Project Title: Change in menu Project|Project Options|Parameter	
Number: TIDA-010944 Rev: E5	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 4 of 6
Drawn By:	File: 2P_TIDA-10944_Hardware.SchDoc	Size: B
Engineer: Jose Velez - Lopez Jr	Contact: http://www.ti.com/support	

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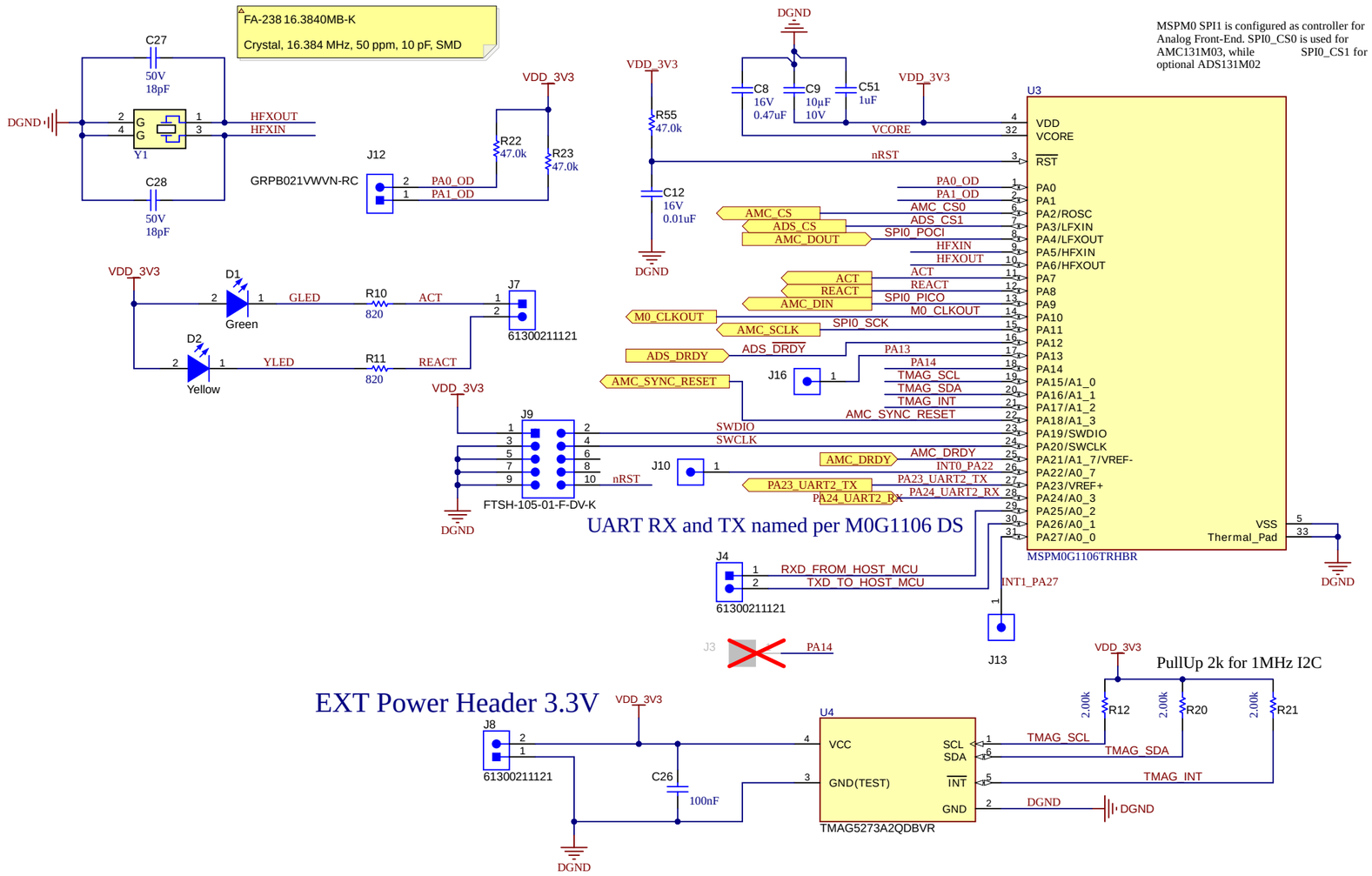
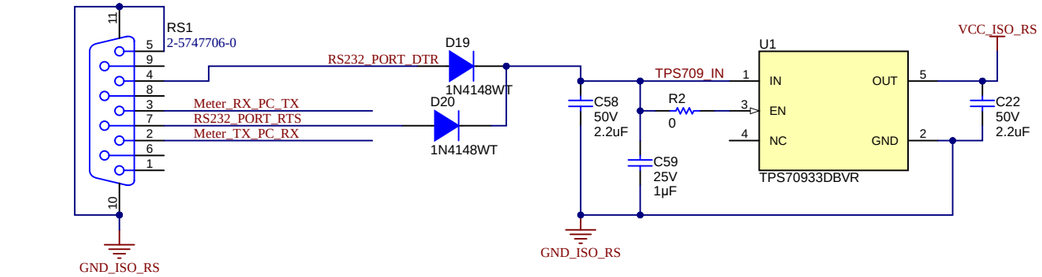
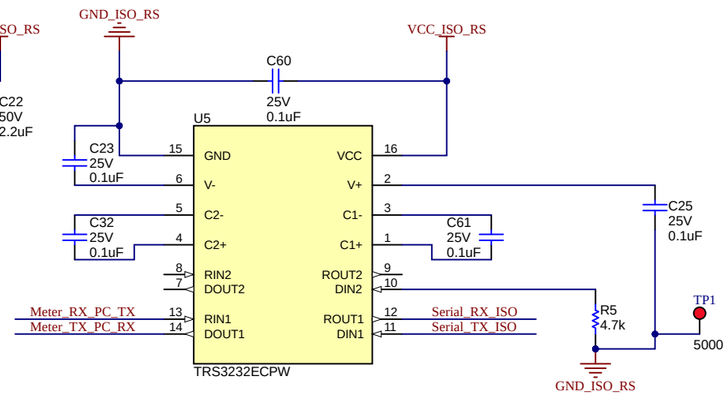
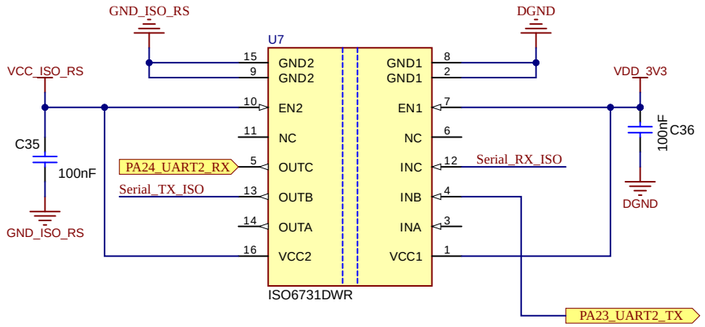


Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 7/31/2024
TID #: 010944	Project Title: Change in menu Project|Project Options|Parameter	
Number: TIDA-010944 Rev: E5	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 5 of 6
Drawn By:	File: 2P_TIDA-10944_M0G1106RHB_RTM.SchDoc	Size: B
Engineer: Jose Velez - Lopez Jr	Contact: http://www.ti.com/support	







UART RX and TX named per M0G1106 DS

RTMrelease: Swapped UART RX and TX on ISO6731

Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 7/31/2024
TID #: 010944	Project Title: Change in menu Project|Project Options|Parameter	
Number: TIDA-010944 Rev: E5	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 6 of 6
Drawn By:	File: 2P_TIDA-010944_Interfaces_RTM.SchDoc	Size: B
Engineer: Jose Velez - Lopez Jr	Contact: http://www.ti.com/support	

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