



**NOTES:**

- 1) R8 for test purposes only
- 2) max. gate charge Q2 <50nC
- 3) 2nd source L1: Bourns 2304-V-RC Pulse PE-51506

**ASSEMBLY:**

- 1) built on PCB PMP4540RevC
- 2) Q3 has to be flipped upside down !

Texas Instruments

Title 48V @ 5A Boost w/ TPS40210		
Size B	Number PMP7118	Rev A
Date 03/12/2012	Drawn by B.Geck	
Filename PMP7118RevA.SCH	Sheet 1 of 1	

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