

Revision History	
Revision	Notes

Designator
Control_TPS53661.SchDoc



Designator
Input_Bias_DynL_Output.SchDoc




Designator
Power_Trains.SchDoc

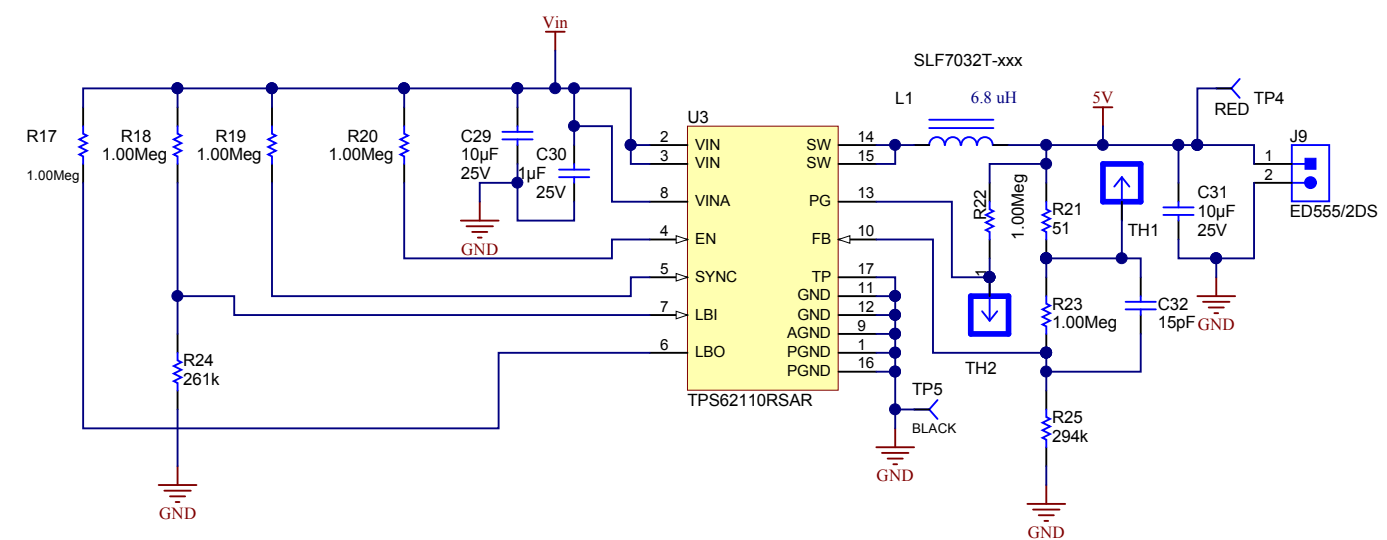
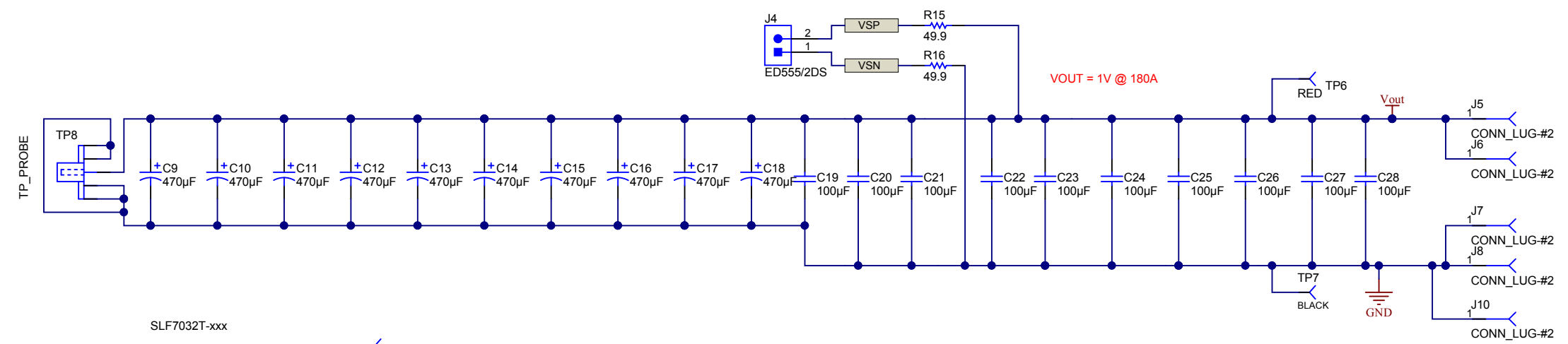
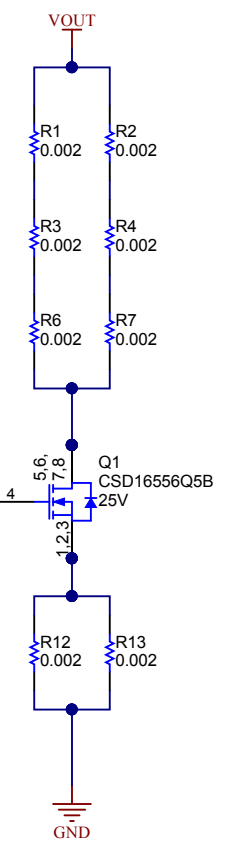
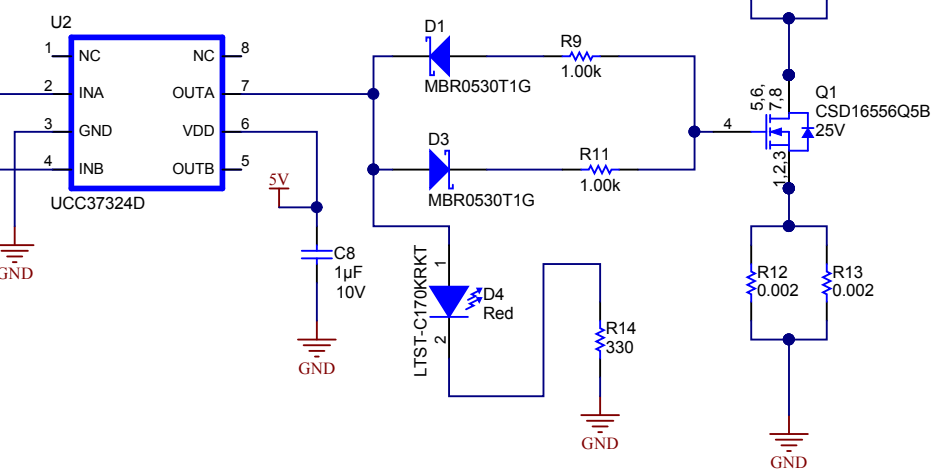
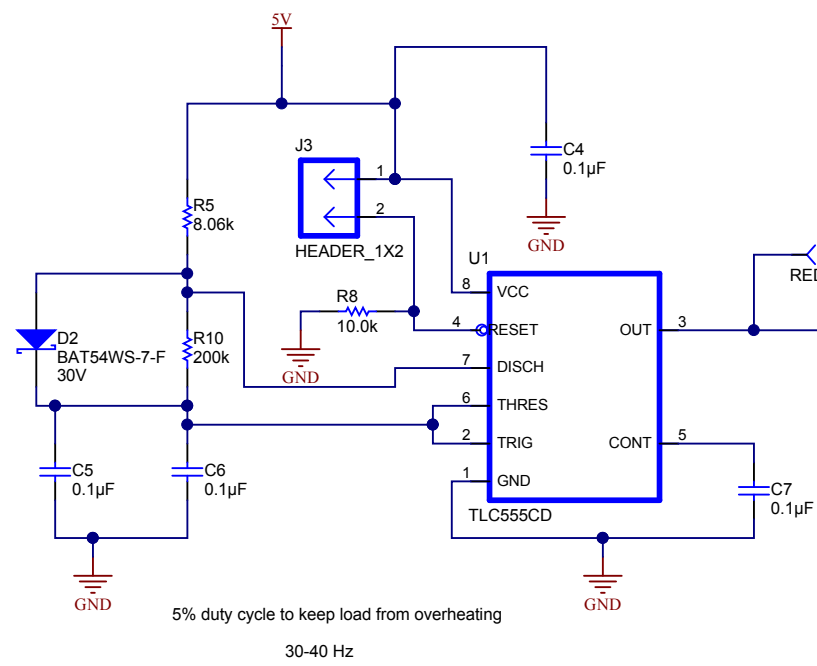
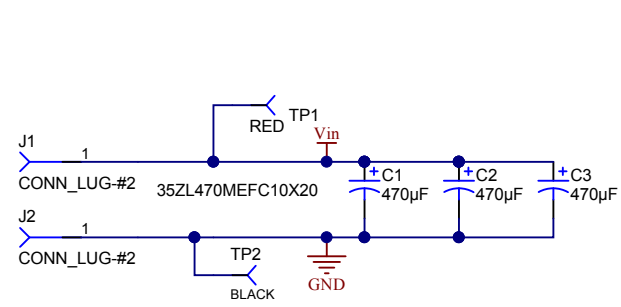


Designator
Hardware.SchDoc



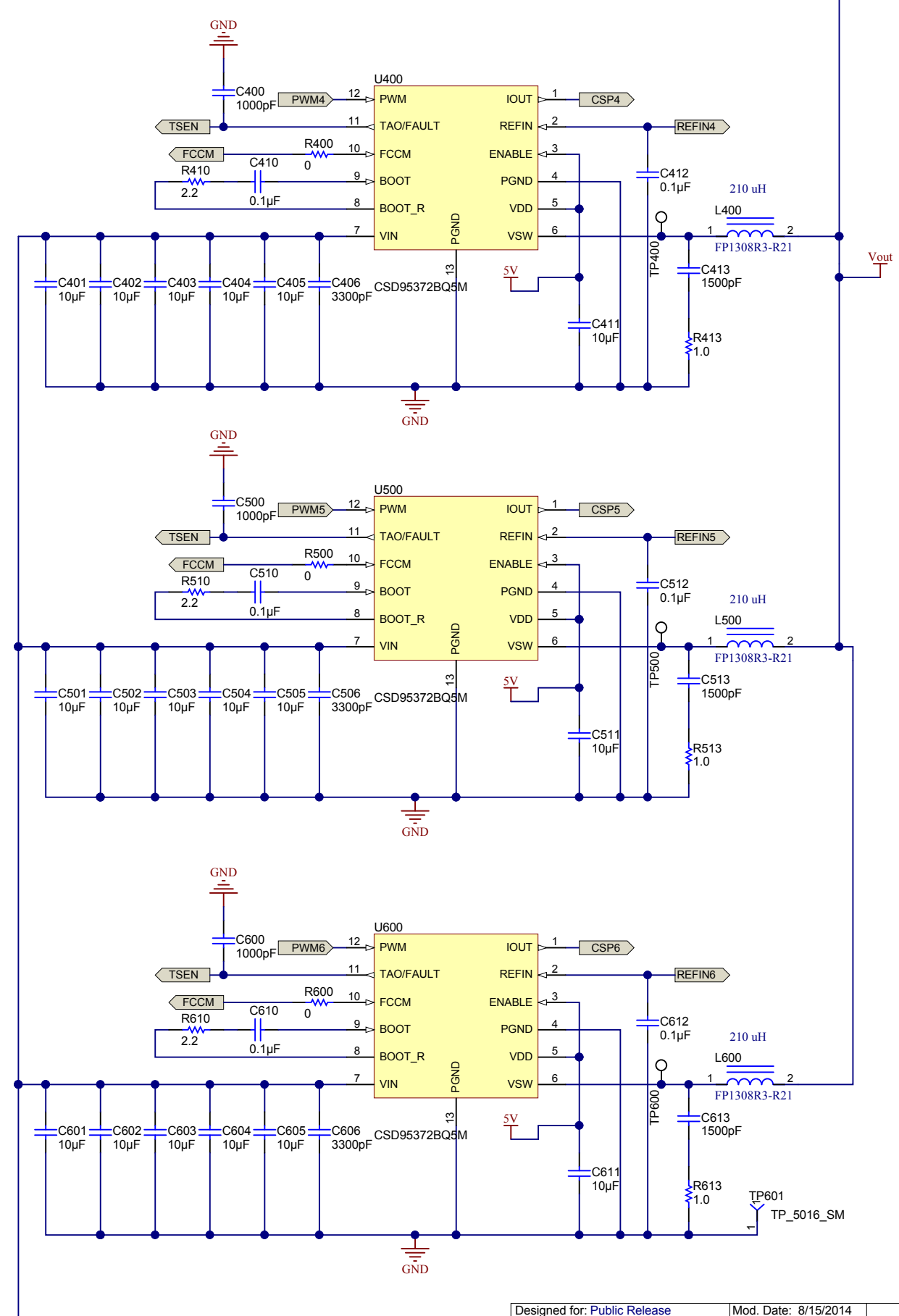
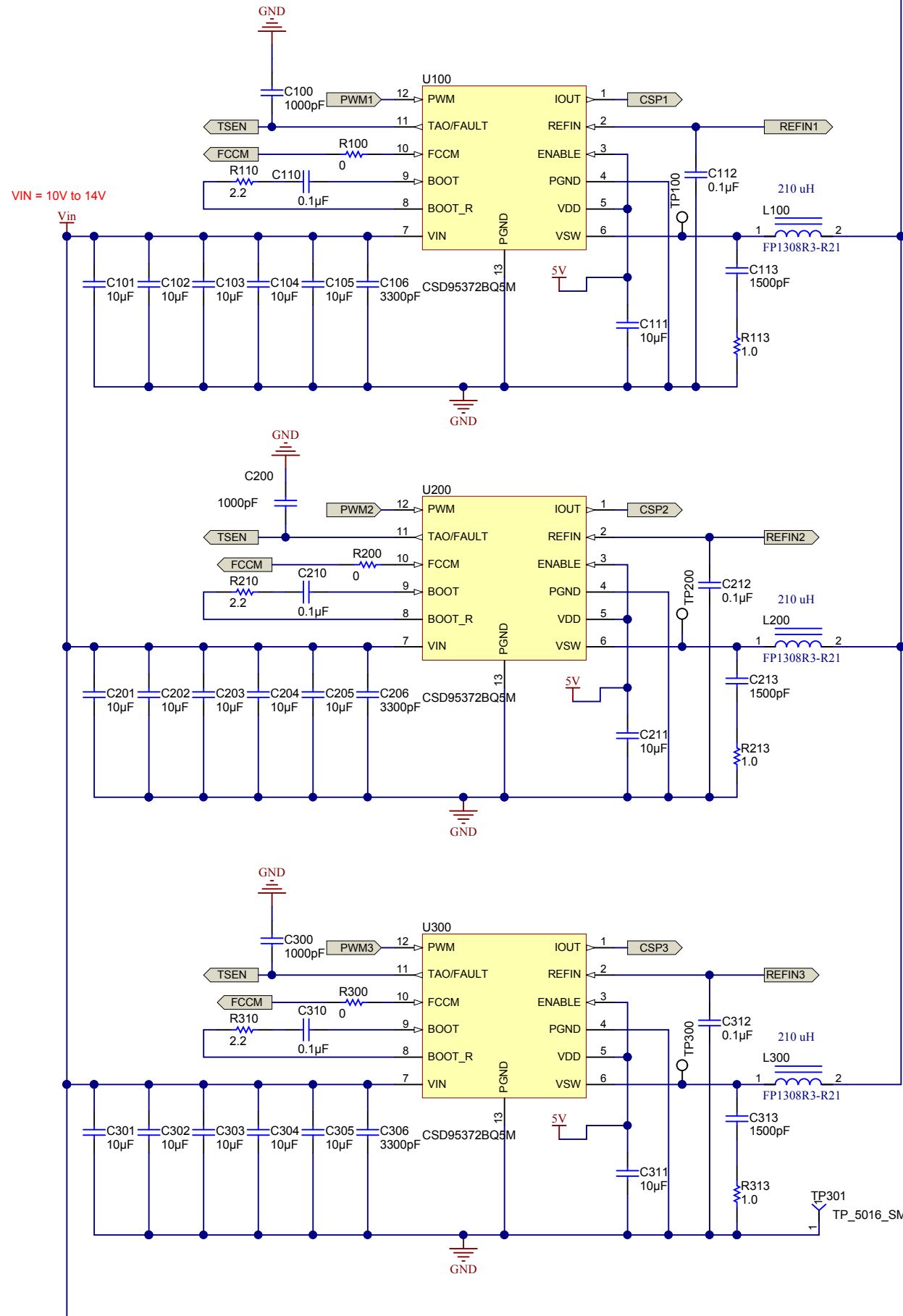
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Number: PMP9738	Rev: A	Designed for: Public Release	Mod. Date: 5/28/2014
SVN Rev: Version control disabled	Assembly Variant: 001	Project Title: 12Vin 1Vout 180/240A 6 Phases TPS53661 / CSD9	
Drawn By: Josh Mandelcorn	Engineer: Josh Mandelcorn	Sheet: 1 of 5	Size: B
Contact: http://www.ti.com/support		 http://www.ti.com © Texas Instruments 2014	



Space output connectors 55mm apart
to allow 132A Kikisui load lugs to be screwed to Vout & GND
Kikisui lugs are 30mm wide and reach 10mm beyond hole centers
With output lugs on right side of board and for board to be top side up then:
Ground lug needs to be toward upper edge of board & Vout lug 55mm further down

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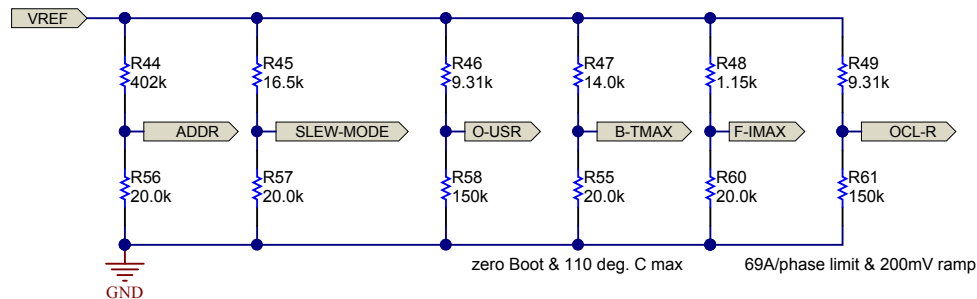


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SVID address 0d & PMBus address 97d

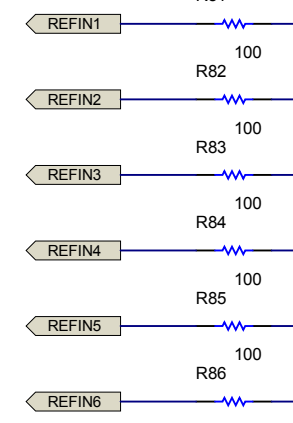
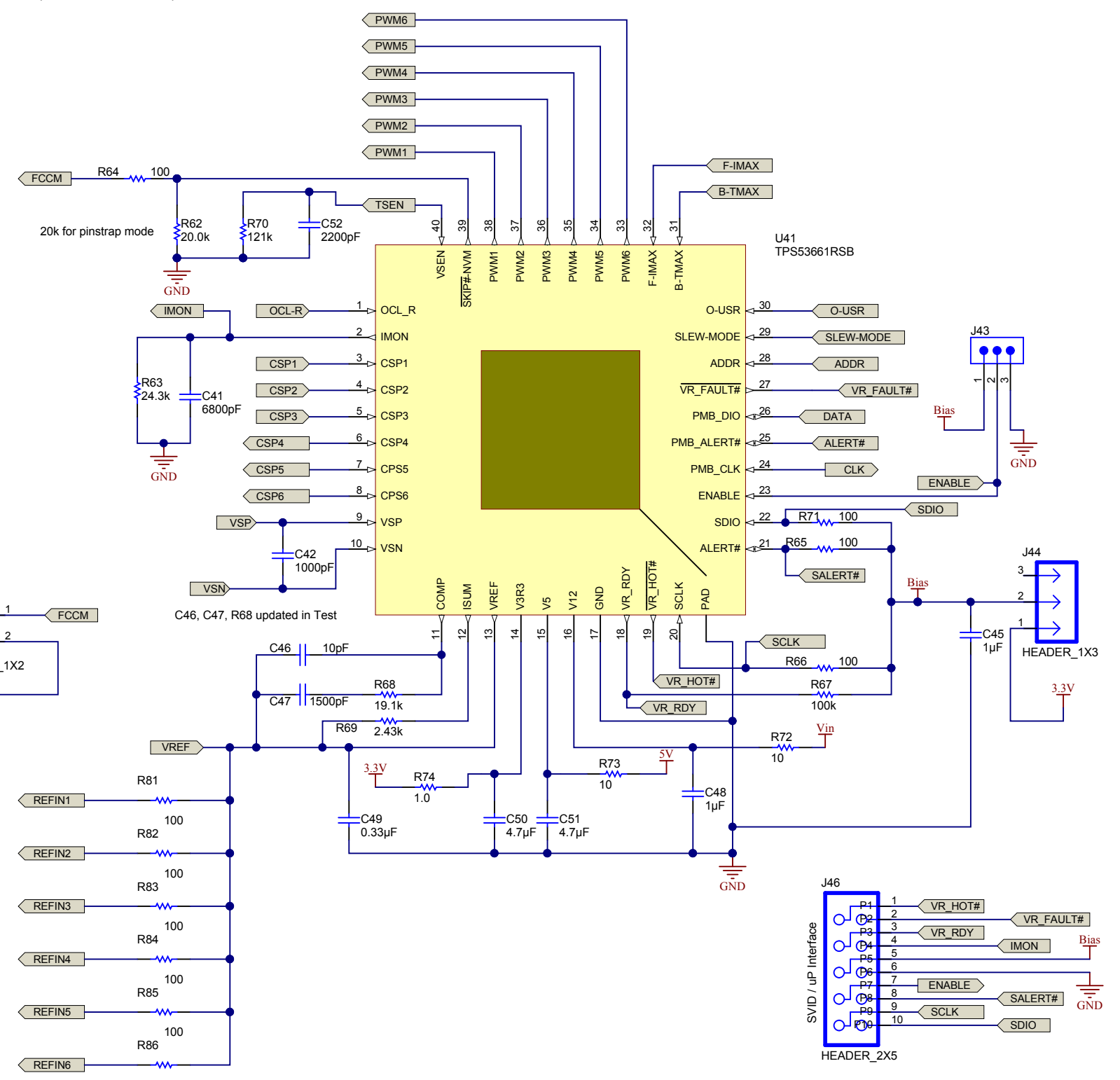
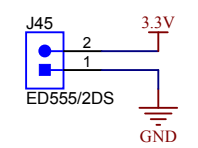
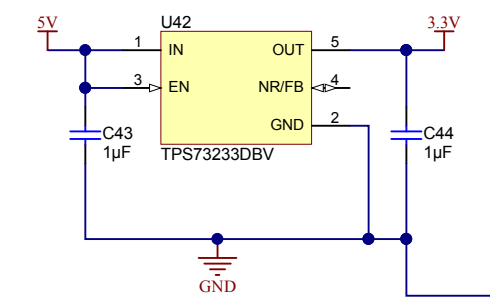
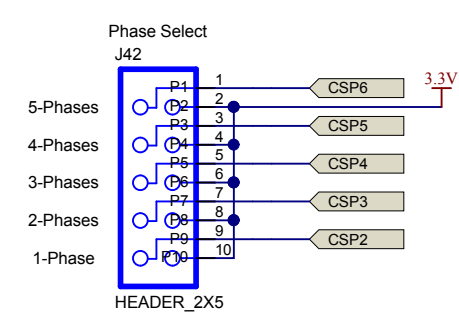
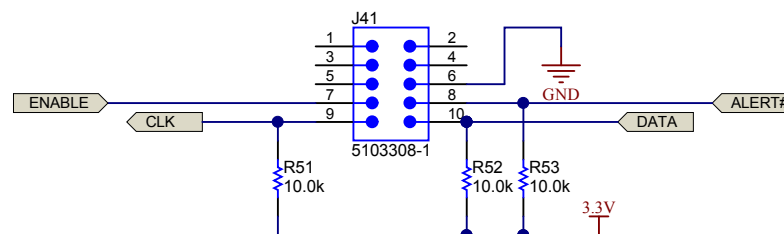
OSR & USR both off

300kHz switching / 240A max



slew = 5mV/us, 1.52VID max, individual phase interleave when in 4 phases

no turbo mode, no phase shedding, zero loadline



LBL41
PCB Label
Size: 0.65" x 0.20"

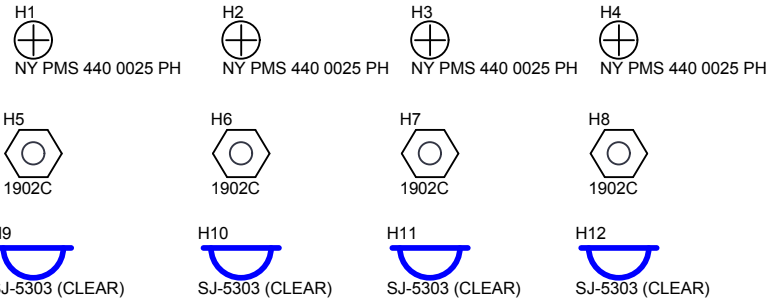
PCB LOGO
Texas Instruments

Preliminary

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Number: PMP9738	Rev: A	Designed for: Public Release	Mod. Date: 1/8/2015
SVN Rev: Version control disabled	Sheet: 4 of 5	Project Title: 12Vin 1Vout 180/240A 6 Phases TPS53661 / CSD9	
Drawn By:	Engineer: Josh Mandelcorn	File: Control_TPS53661.SchDoc	Contact: http://www.ti.com/support
		Sheet: 4 of 5	Size: B

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PCB Number: PMP9738
PCB Rev: A

PCB
LOGO
Texas Instruments

You should delete the nylon screws/standoffs and/or the bumpers as needed for your design (or substitute other parts from Hardware.IntLib). Bumpers are cheaper, but provide less clearance.

Deleting anything else from this page may result in your EVM submission being rejected (until you add them back).

Update the Label Text in the Label Table as needed for each Assembly Variant.

You can delete this note too.

Label Table	
Variant	Label Text
001	ChangeMe!
002	ChangeMe!

LBL1
PCB Label
Size: 0.65" x 0.20"


ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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Number: PMP9738	Rev: A	Designed for: Public Release	Mod. Date: 5/28/2014
SVN Rev: Version control disabled	Sheet: 5 of 5	Project Title: 12Vin 1Vout 180/240A 6 Phases TPS53661 / CSD9	
Drawn By: Josh Mandelcorn	File: Hardware.SchDoc	Sheet: 5 of 5	Size: B
Engineer: Josh Mandelcorn	Contact: http://www.ti.com/support	 http://www.ti.com © Texas Instruments 2014	

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