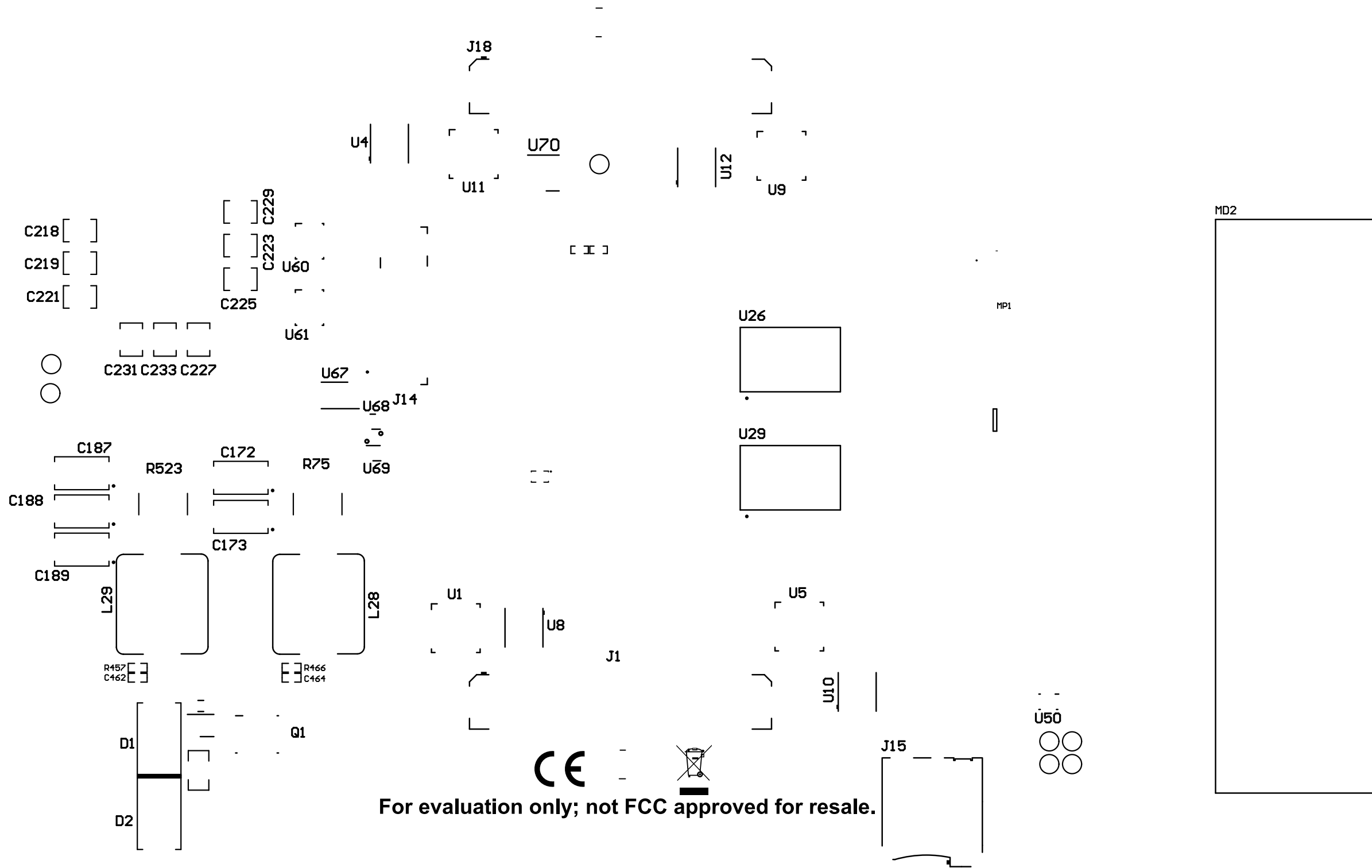


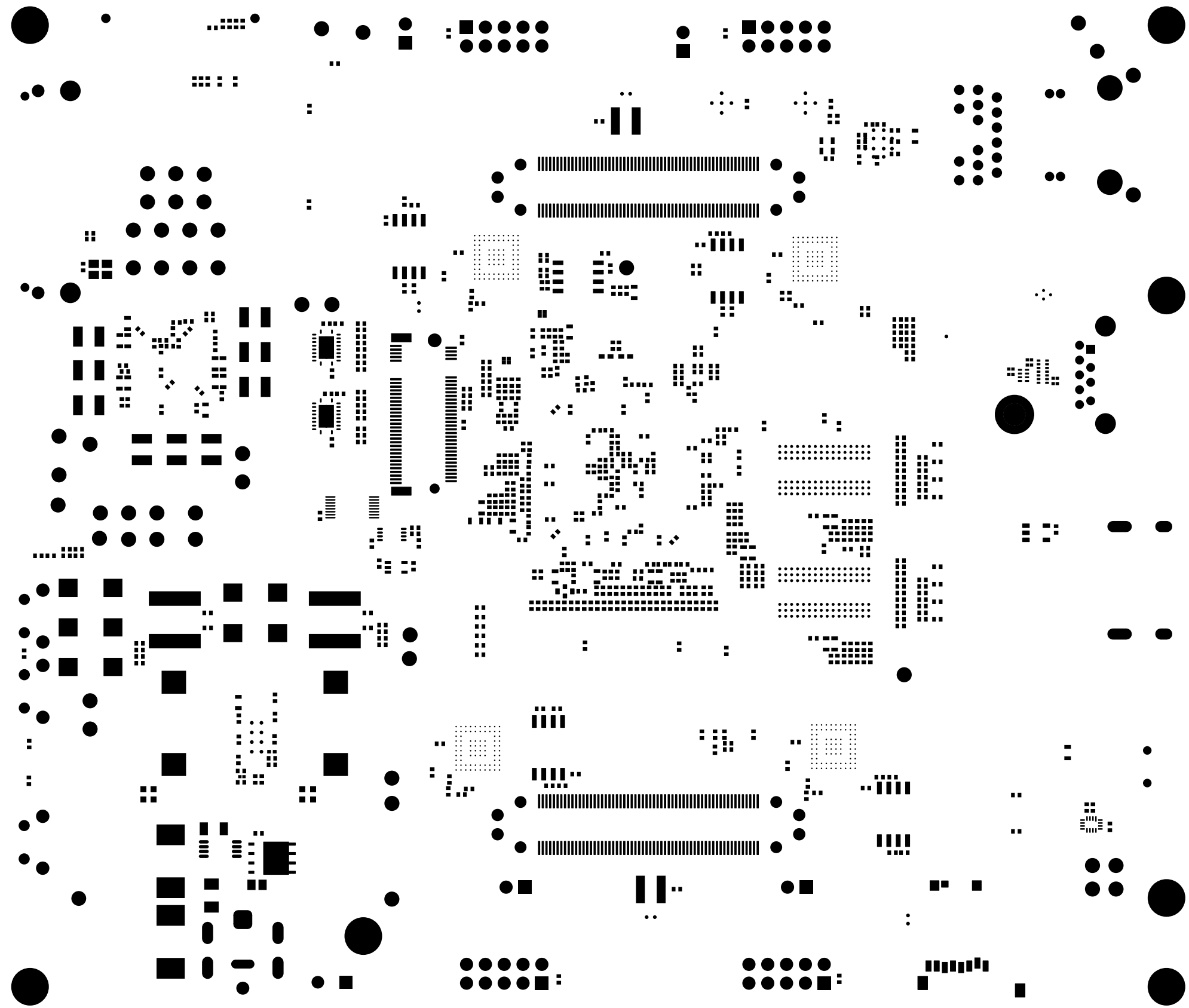
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = Top Paste	TID #: N/A		
	GENERATED : 29-04-2021 18:29:56		TEXAS INSTRUMENTS



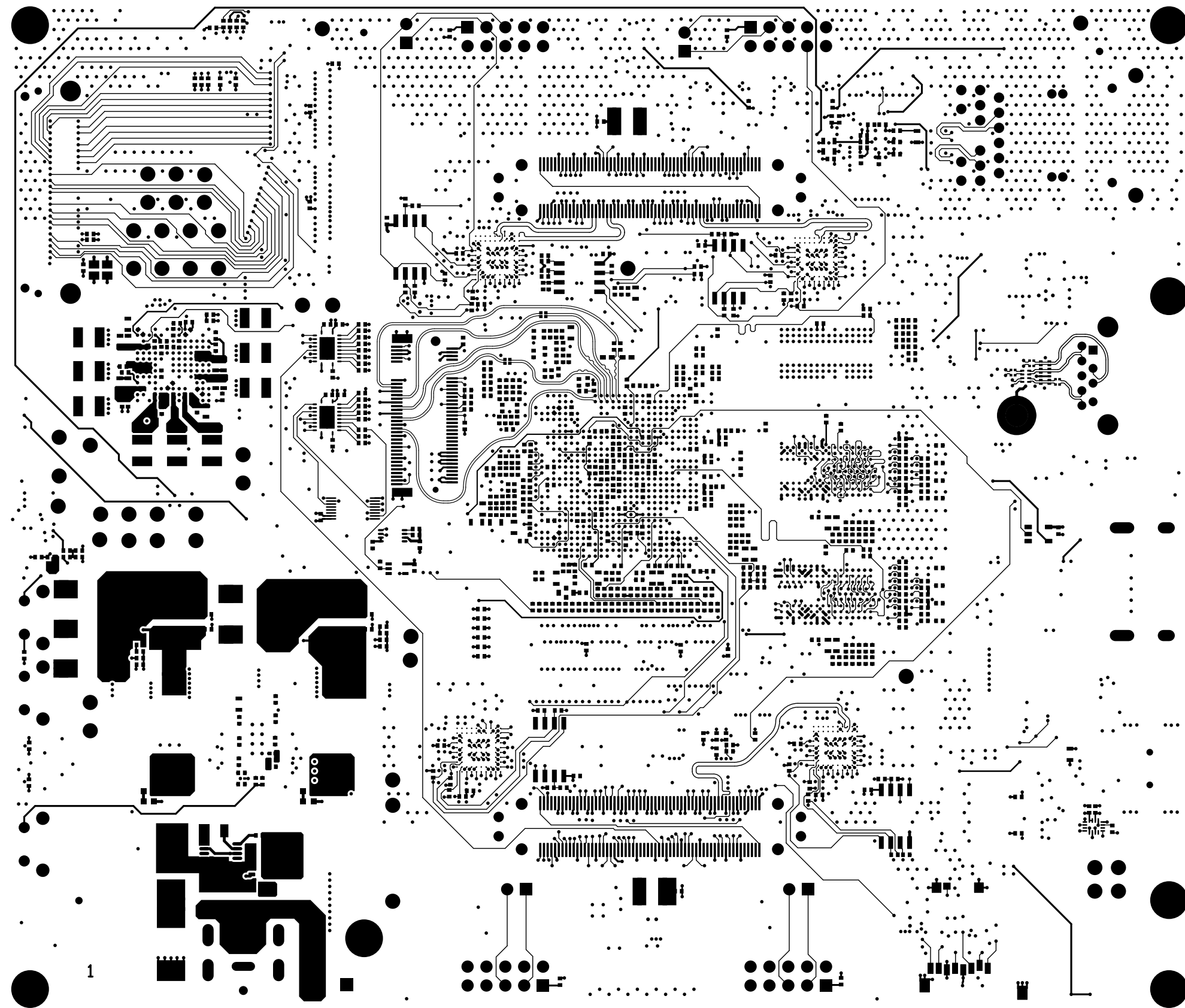


 For evaluation only; not FCC approved for resale.

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = Top Overlay	TID #: N/A		
	GENERATED : 29-04-2021 18:29:57	TEXAS INSTRUMENTS	

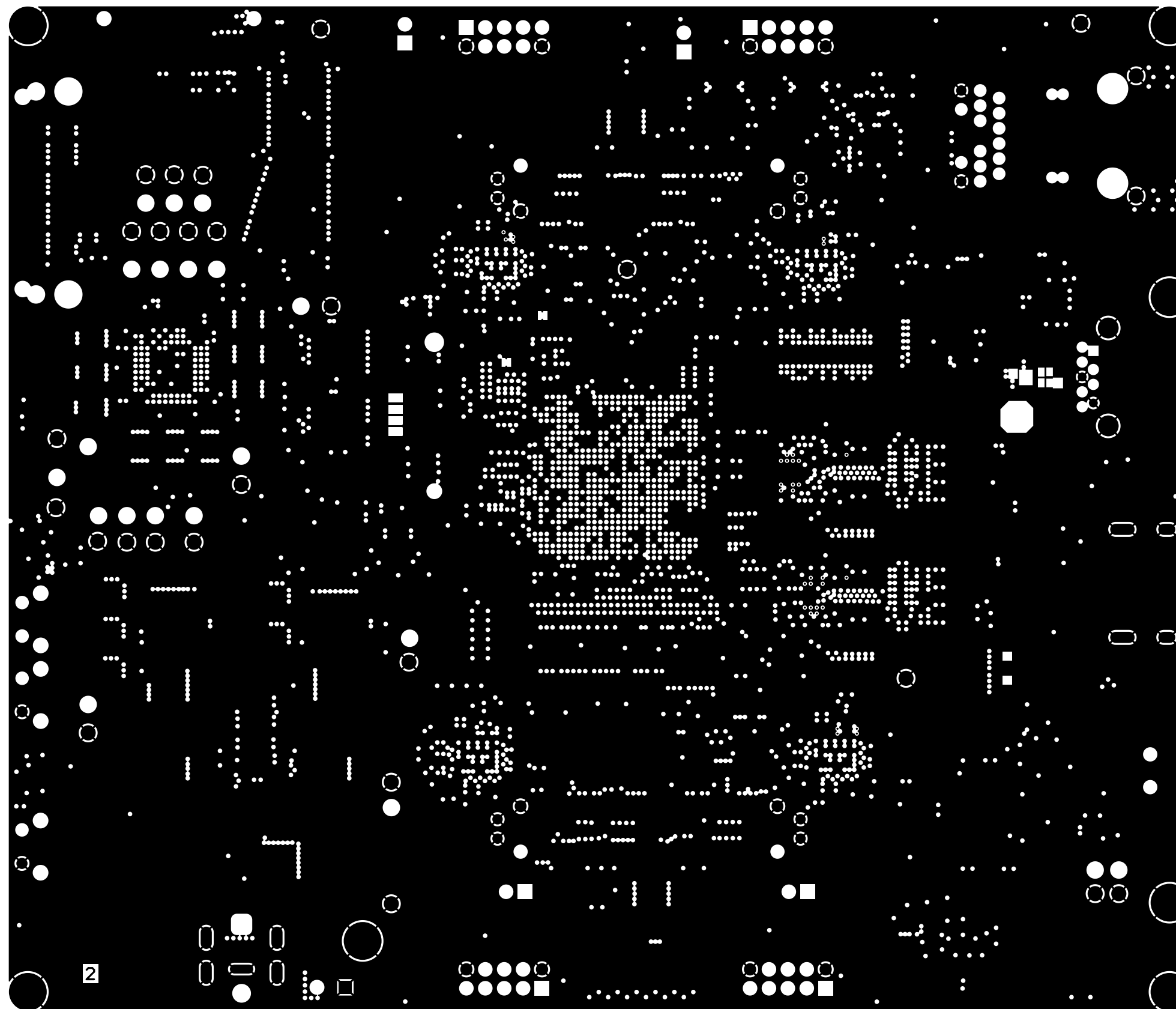


ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = Top Solder	TID #: N/A		
	GENERATED : 29-04-2021 18:29:56	TEXAS INSTRUMENTS	

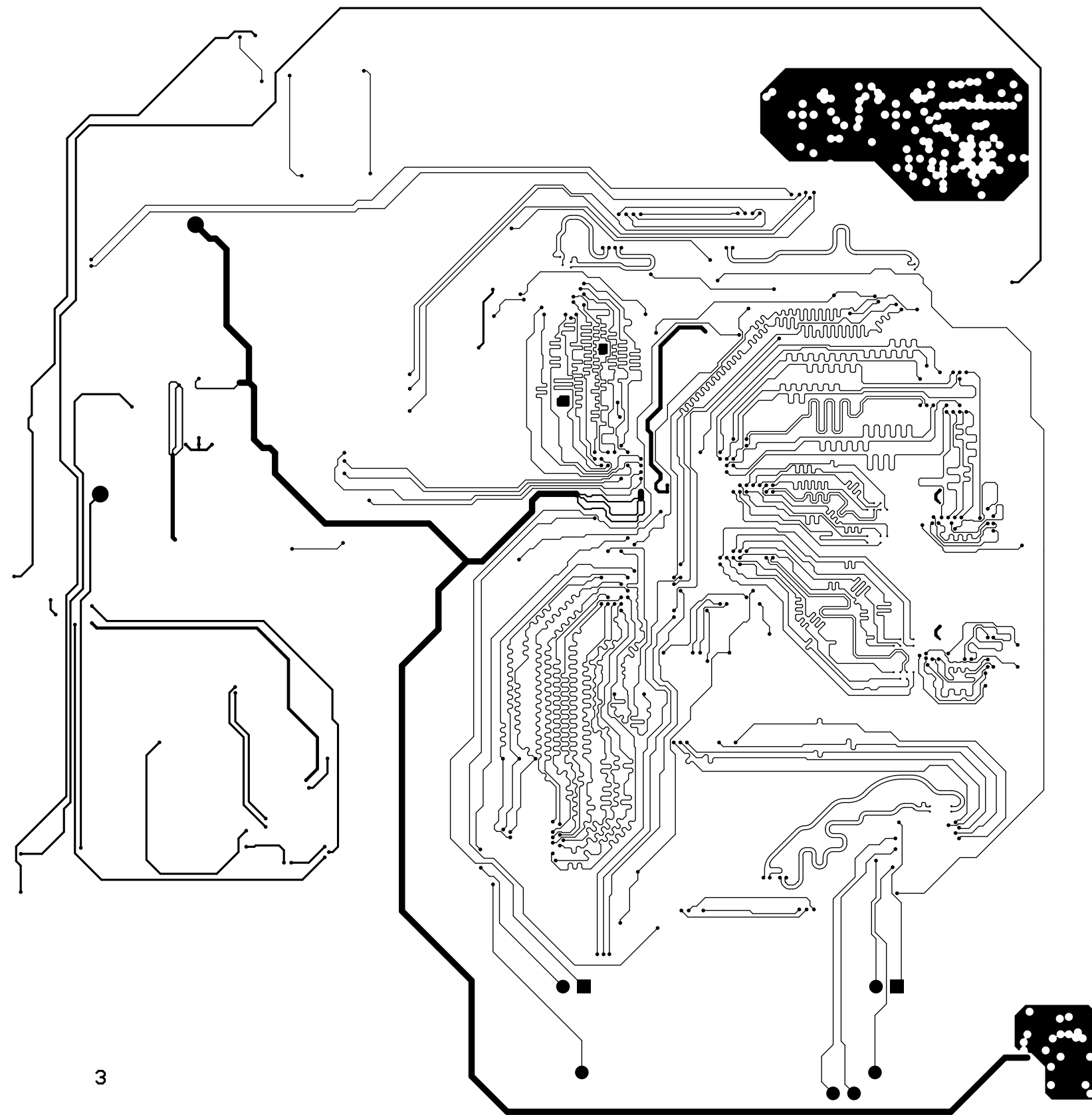


1

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = L1 Top Layer	TID #: N/A		
	GENERATED : 29-04-2021 18:29:52	TEXAS INSTRUMENTS	

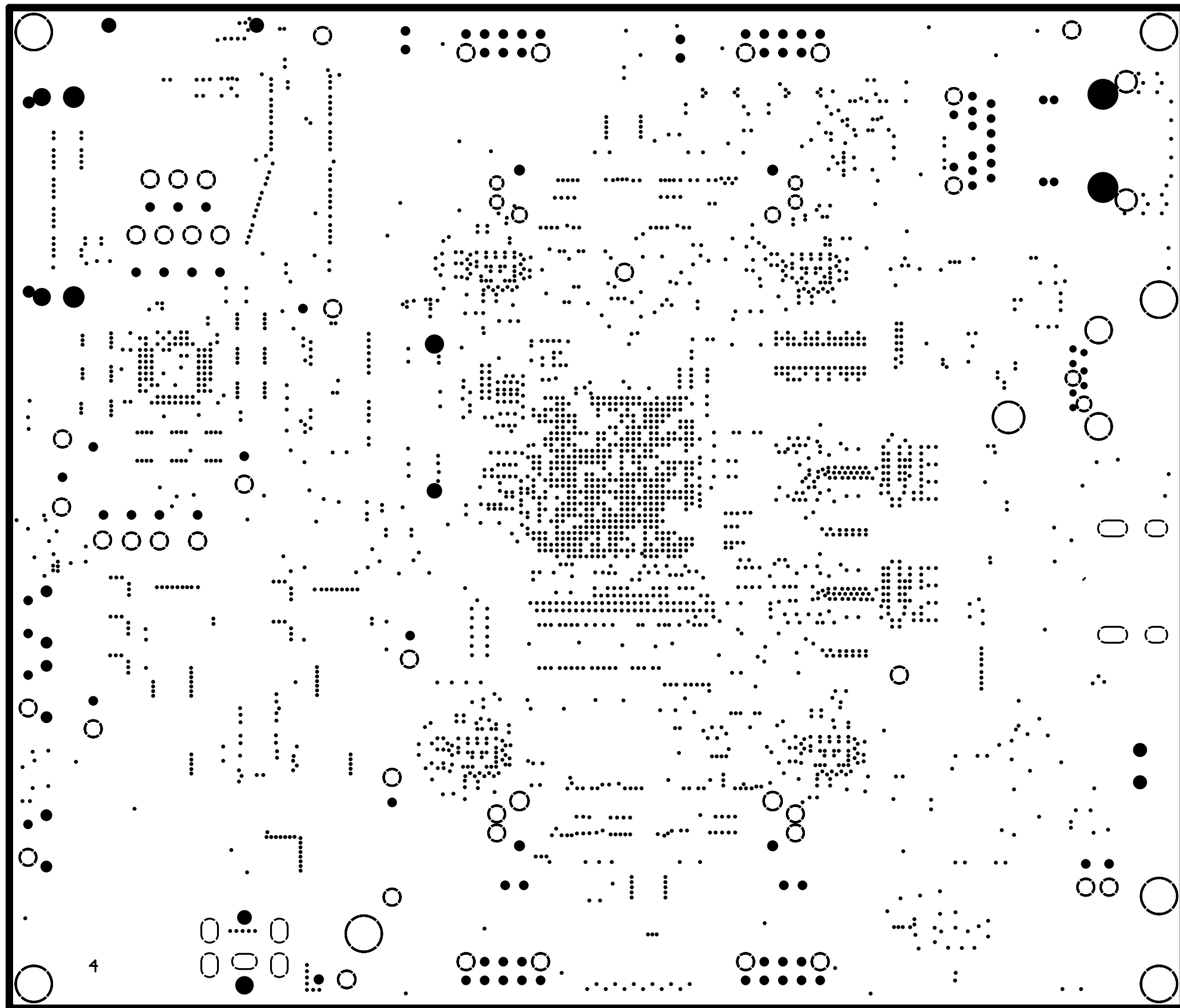


ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = L2 GND	TID #: N/A		
	GENERATED : 29-04-2021 18:29:52	TEXAS INSTRUMENTS	

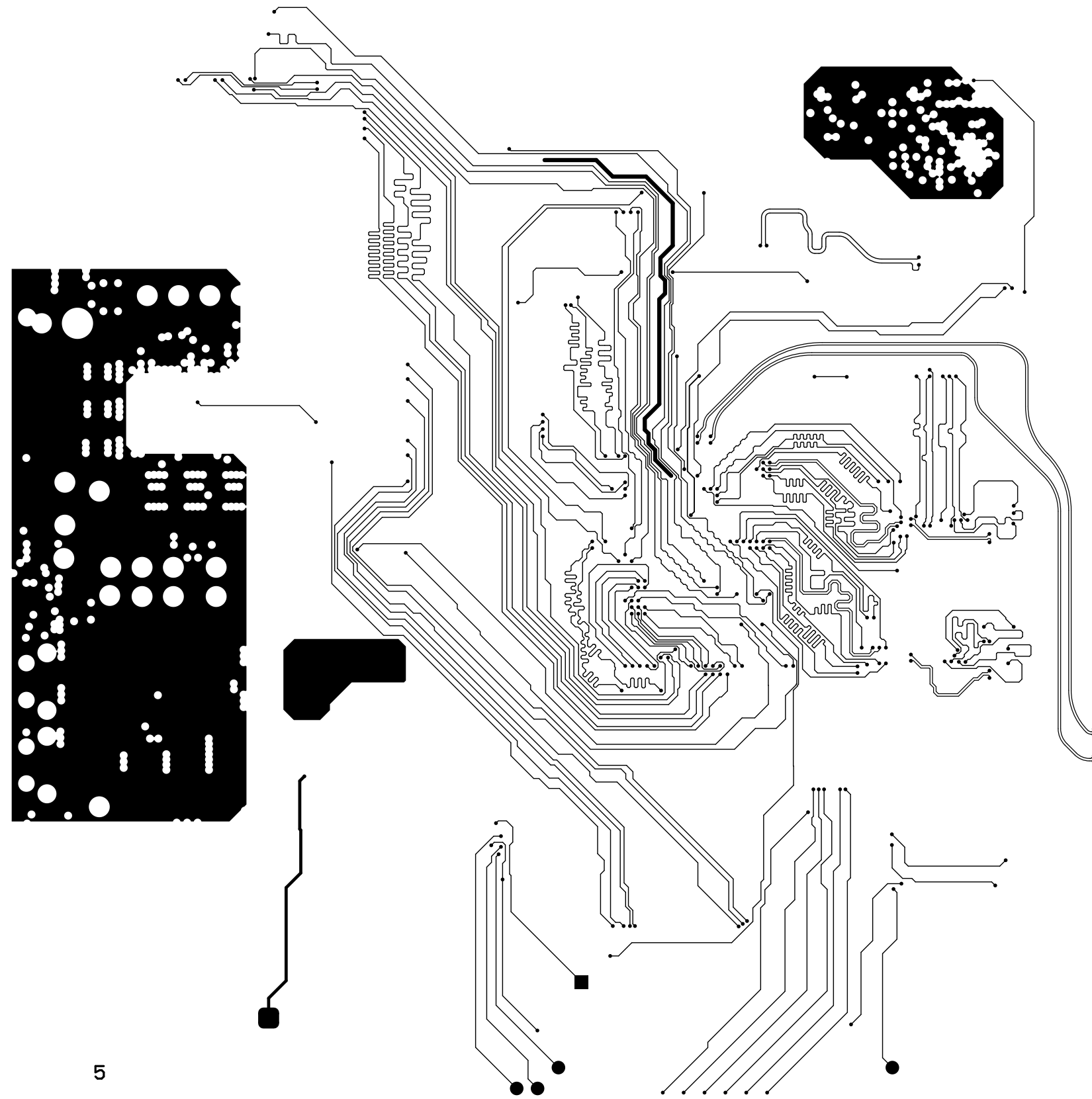


3

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = L3 Signal	TID #: N/A		
GENERATED : 29-04-2021 18:29:52			TEXAS INSTRUMENTS

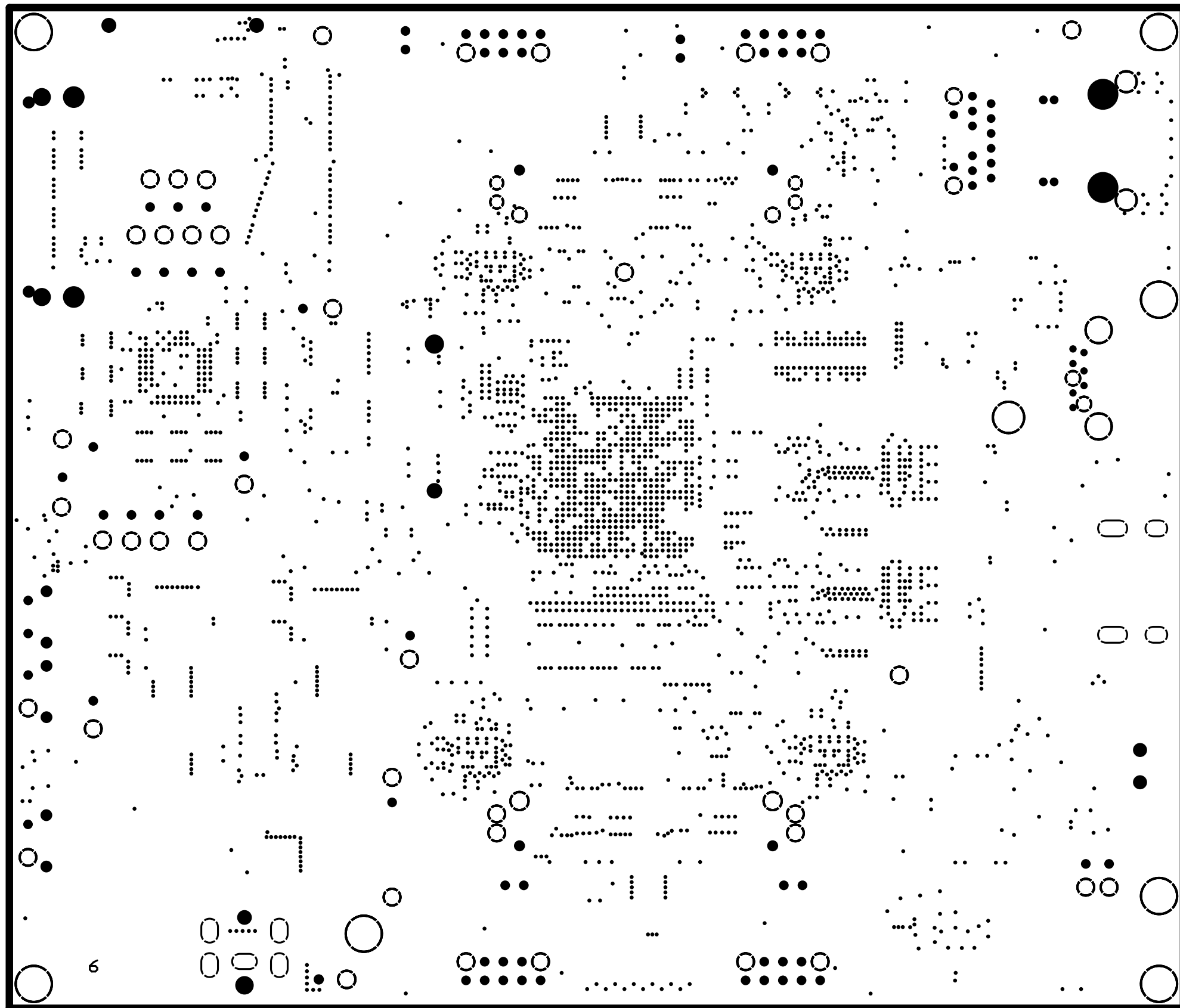


ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = L4 GND	TID #: N/A		
	GENERATED : 29-04-2021 18:29:52	TEXAS INSTRUMENTS	

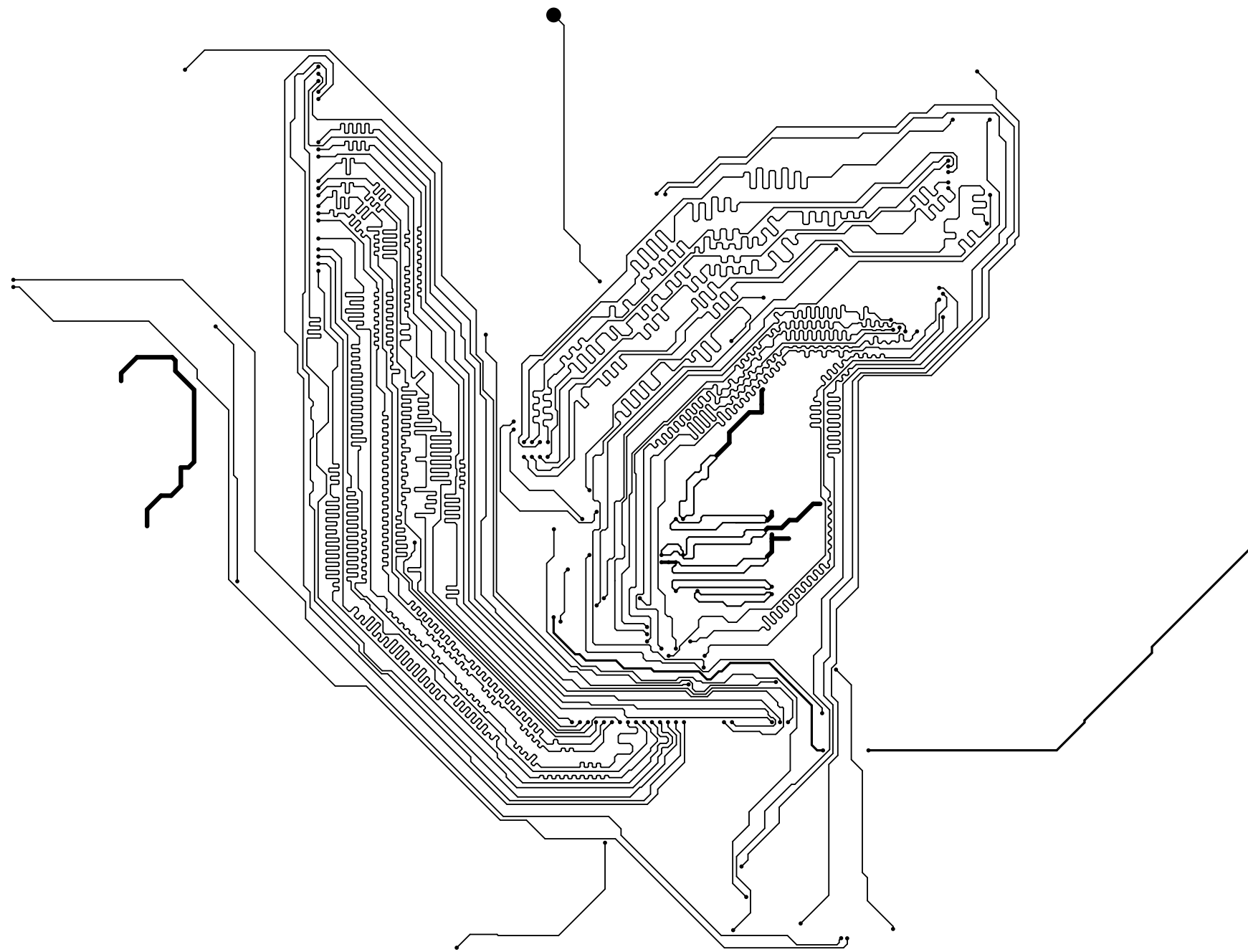


5

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = L5 Signal	TID #: N/A		
GENERATED : 29-04-2021 18:29:52			TEXAS INSTRUMENTS

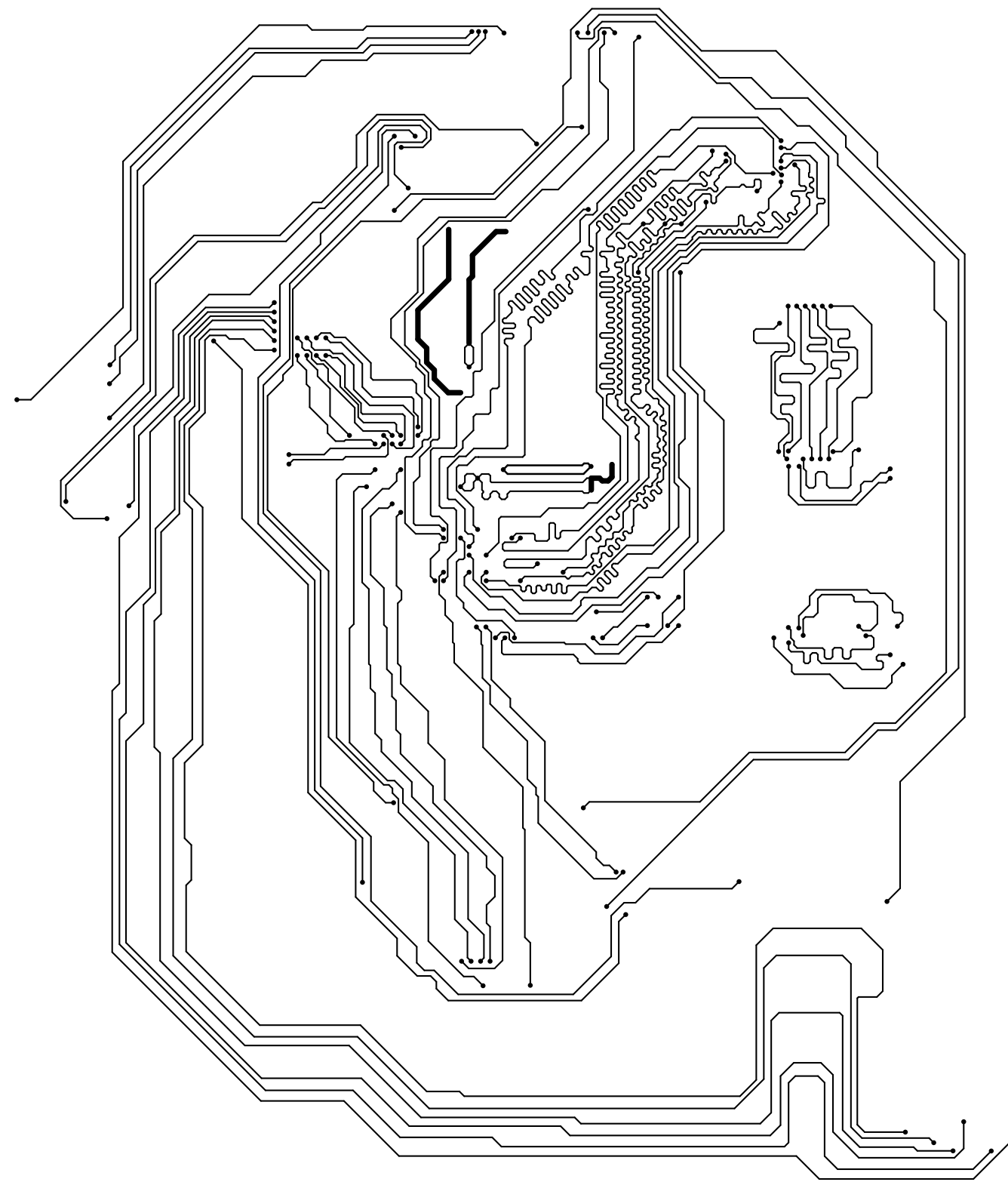


ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = L6 GND	TID #: N/A		
	GENERATED : 29-04-2021 18:29:52	TEXAS INSTRUMENTS	



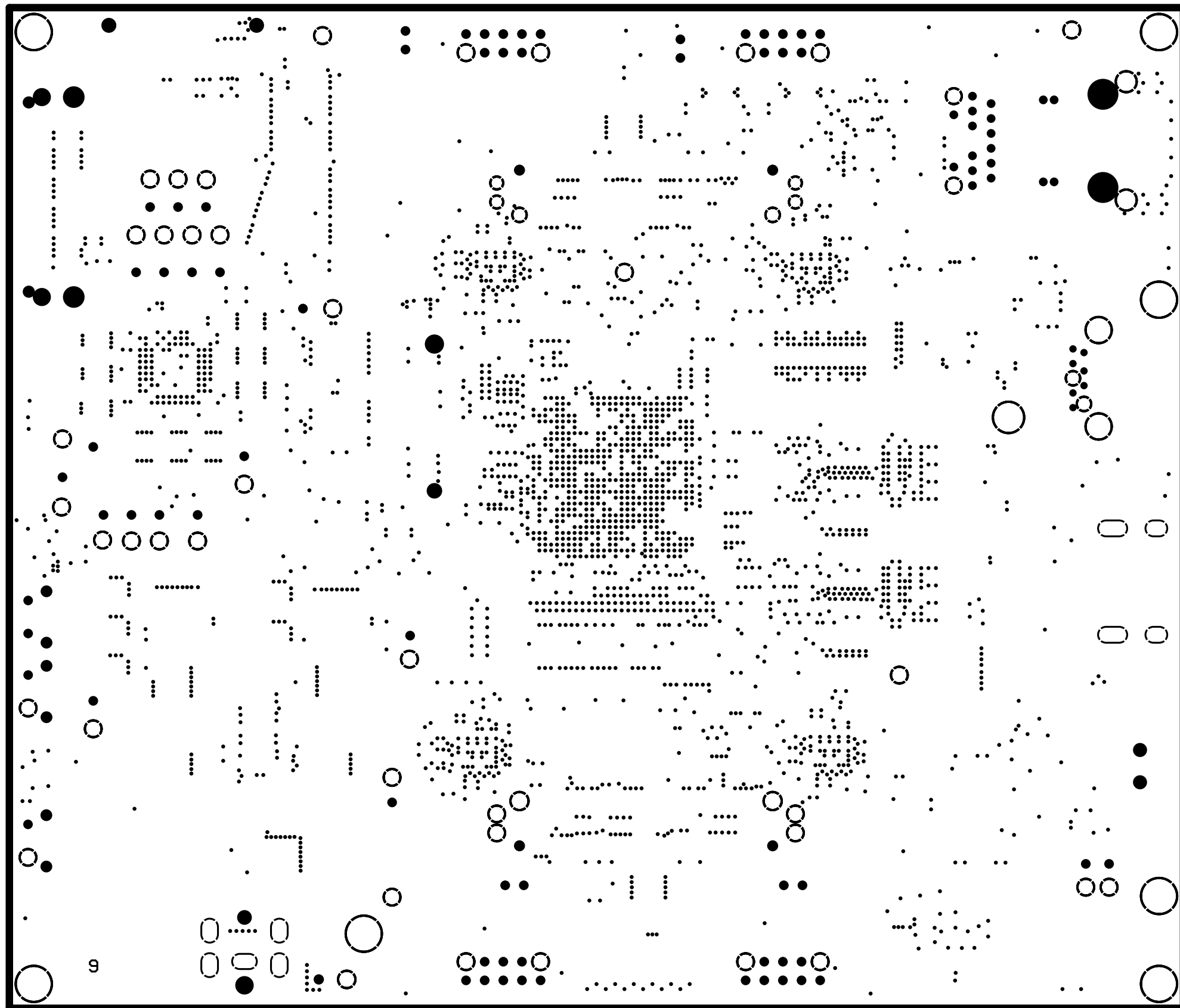
7

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = L7 Signal	TID #: N/A		
	GENERATED : 29-04-2021 18:29:52		TEXAS INSTRUMENTS

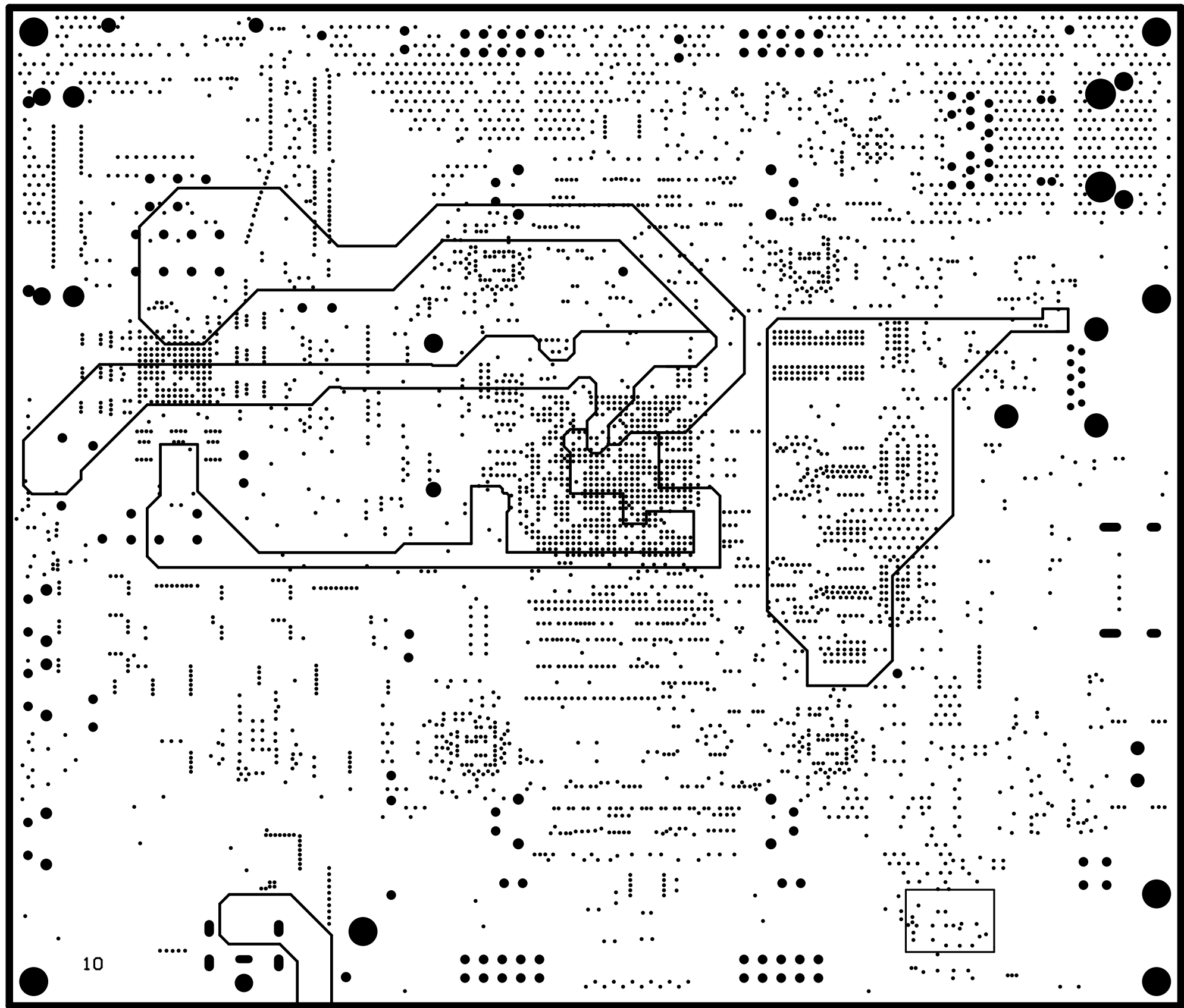


8

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = L8 Signal	TID #: N/A		
	GENERATED : 29-04-2021 18:29:52		TEXAS INSTRUMENTS

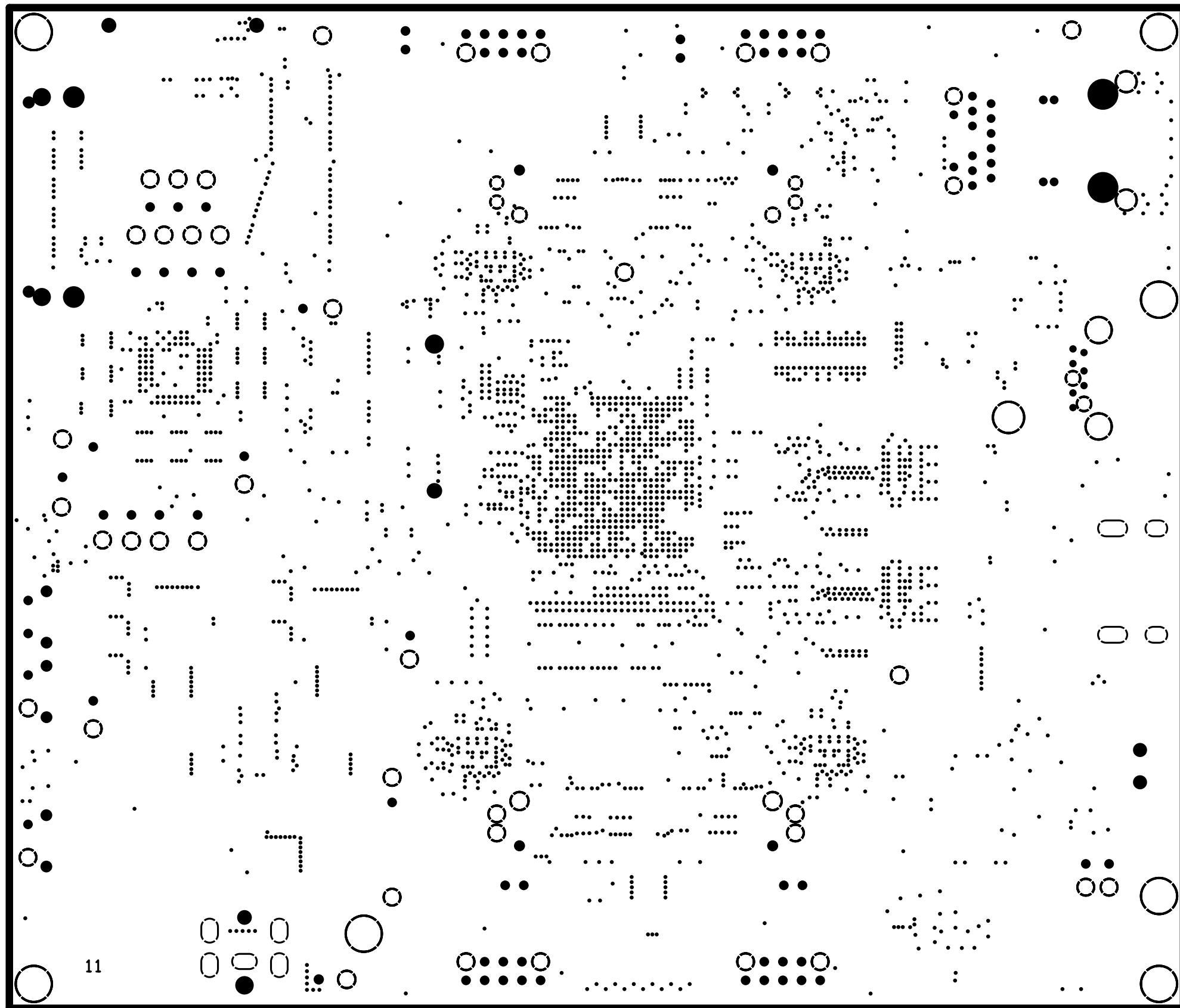


ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = L9 GND	TID #: N/A		
	GENERATED : 29-04-2021 18:29:52	TEXAS INSTRUMENTS	

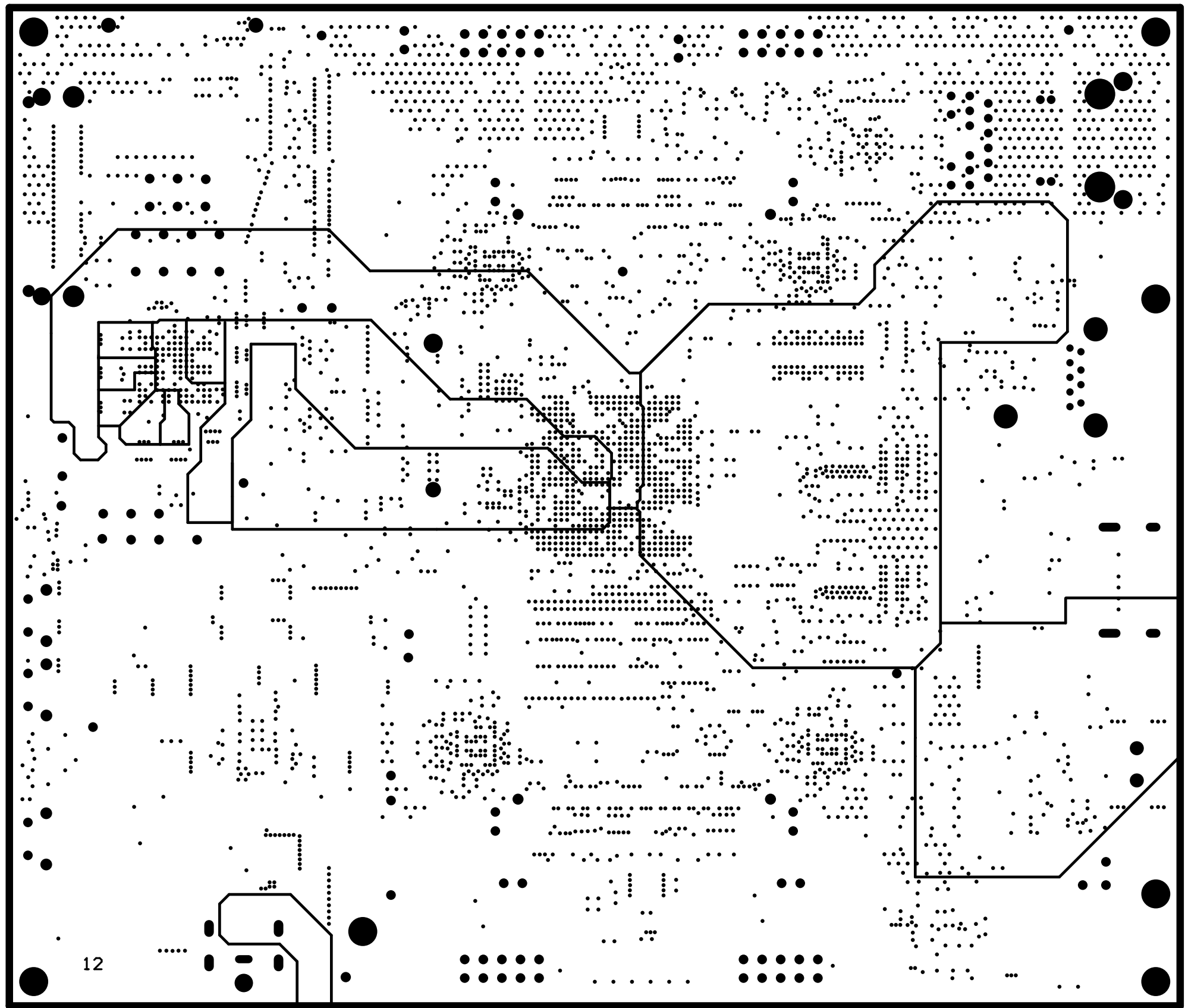


10

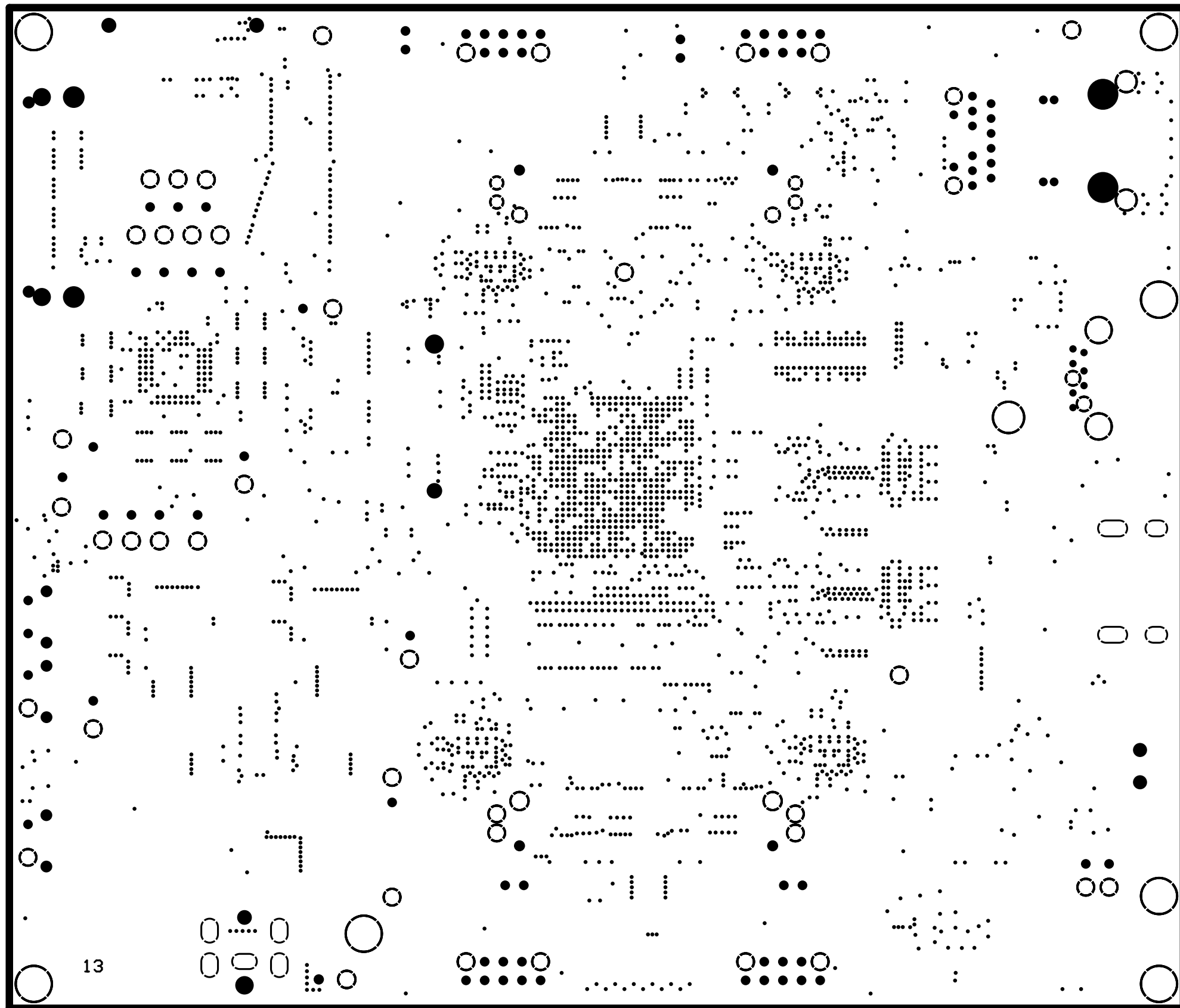
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = L10 PWR	TID #: N/A		
	GENERATED : 29-04-2021 18:29:54	TEXAS INSTRUMENTS	



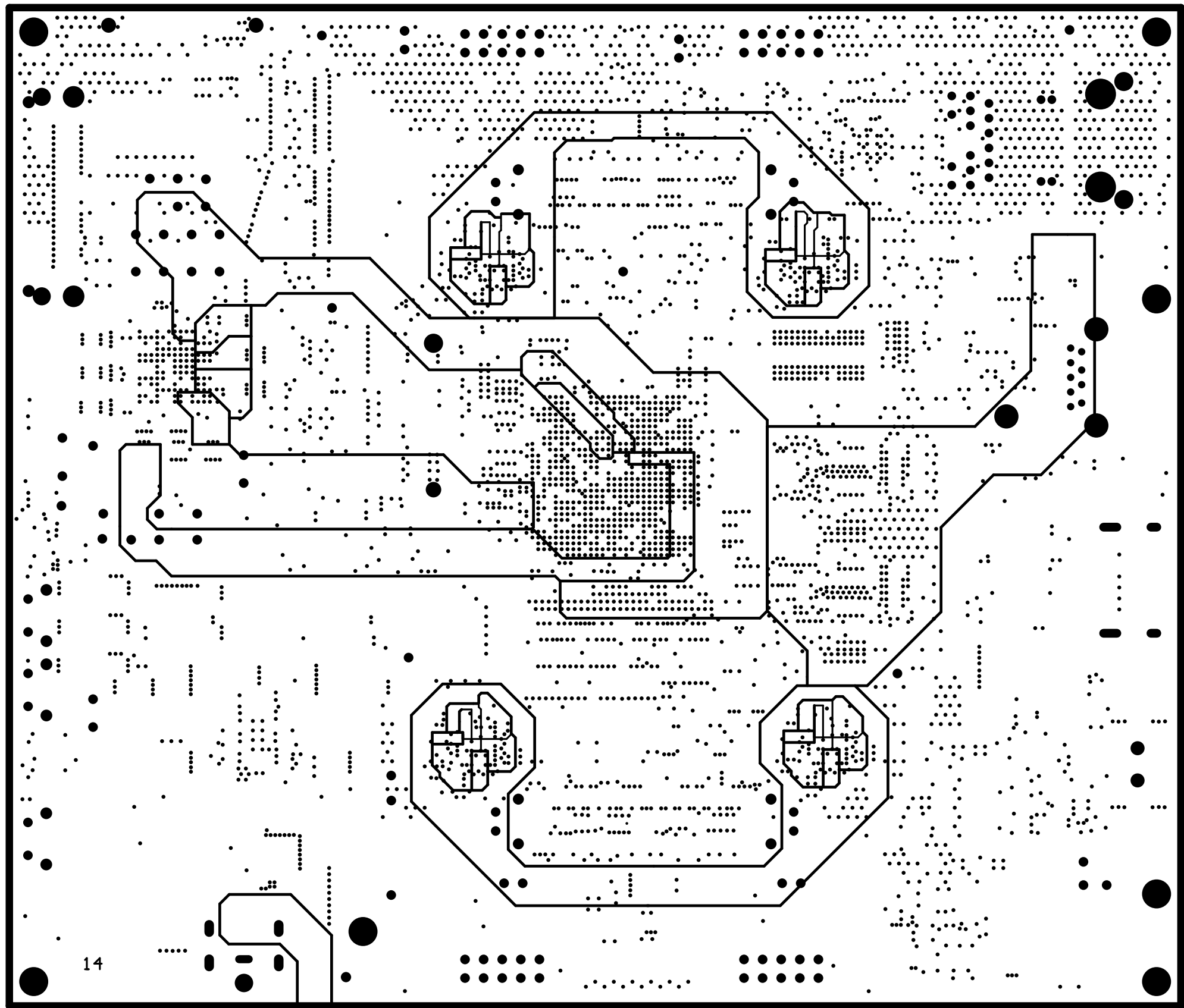
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = L11 GND	TID #: N/A		
	GENERATED : 29-04-2021 18:29:54	TEXAS INSTRUMENTS	



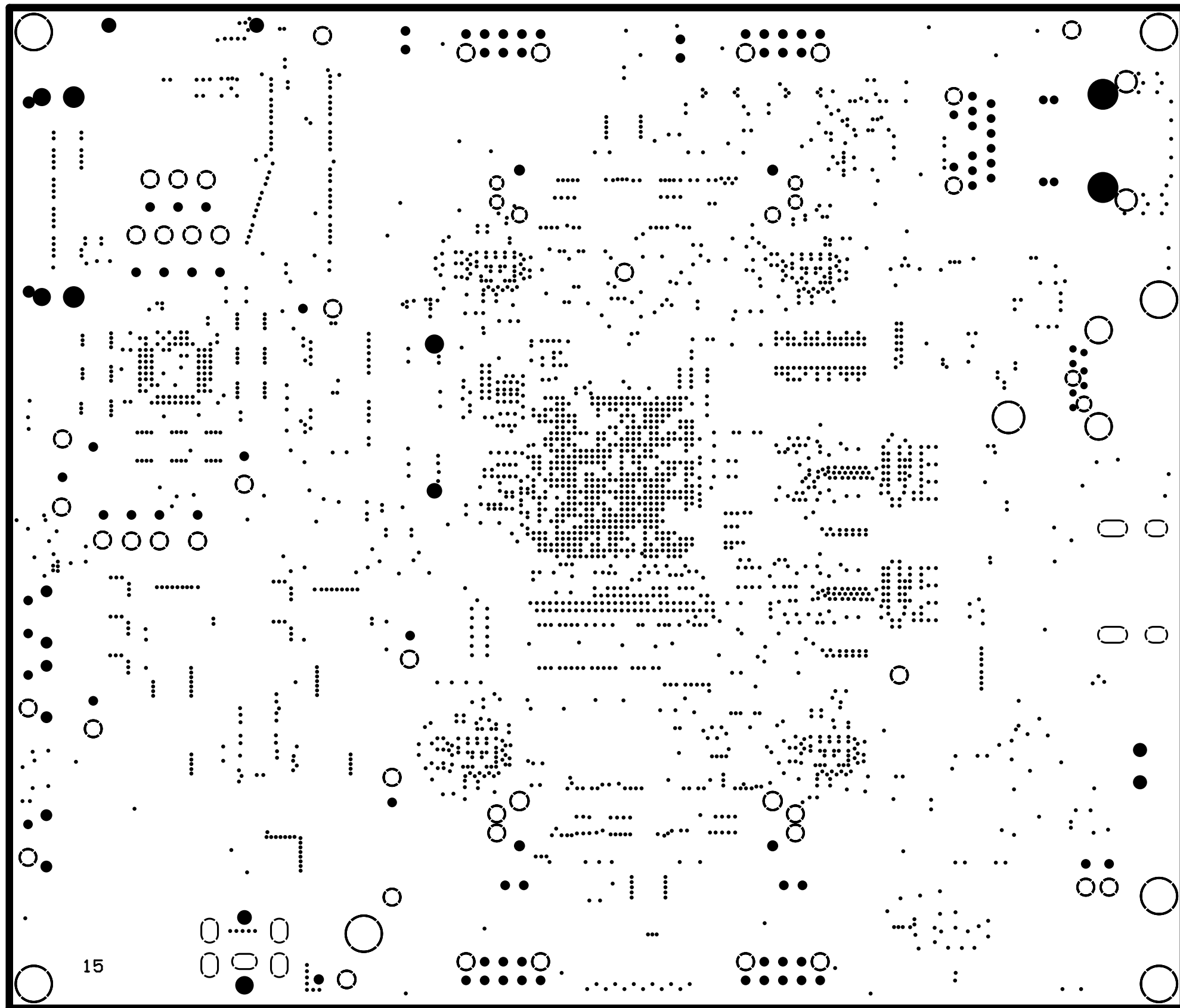
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = L12 PWR	TID #: N/A		
	GENERATED : 29-04-2021 18:29:54	TEXAS INSTRUMENTS	



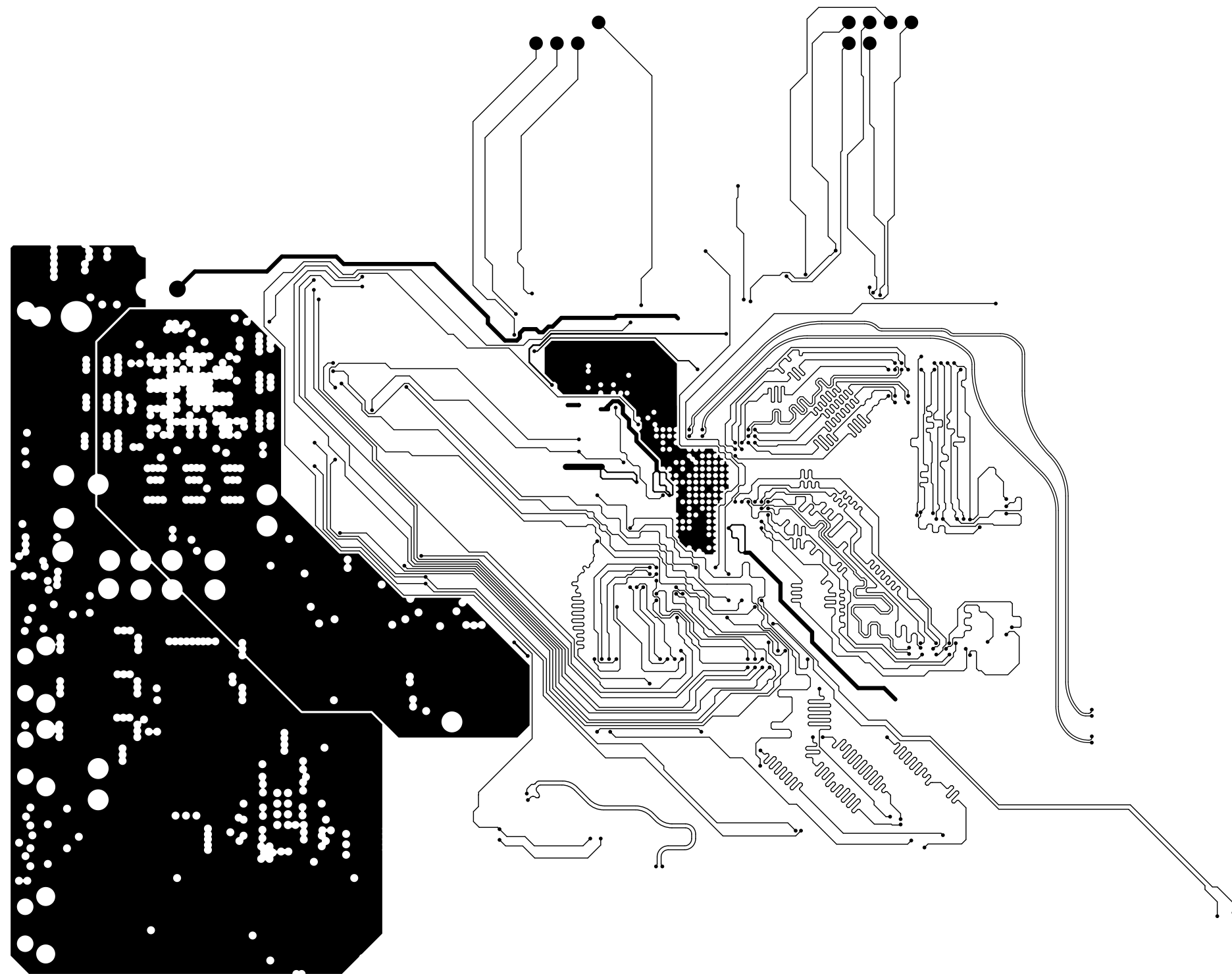
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = L13 GND	TID #: N/A		
	GENERATED : 29-04-2021 18:29:54	TEXAS INSTRUMENTS	



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = L14 PWR	TID #: N/A		
GENERATED : 29-04-2021 18:29:54			TEXAS INSTRUMENTS

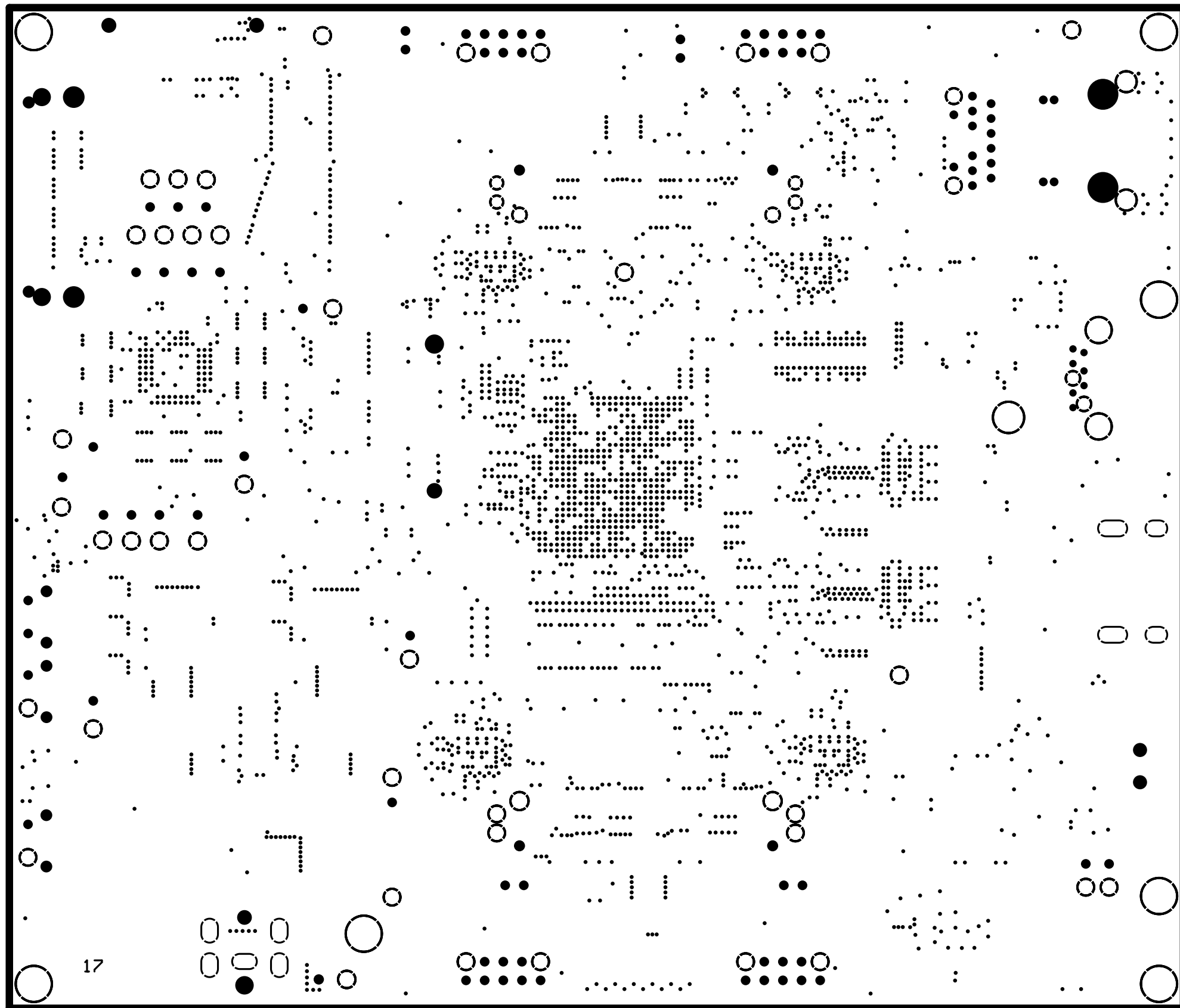


ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = L15 GND	TID #: N/A		
	GENERATED : 29-04-2021 18:29:54	TEXAS INSTRUMENTS	

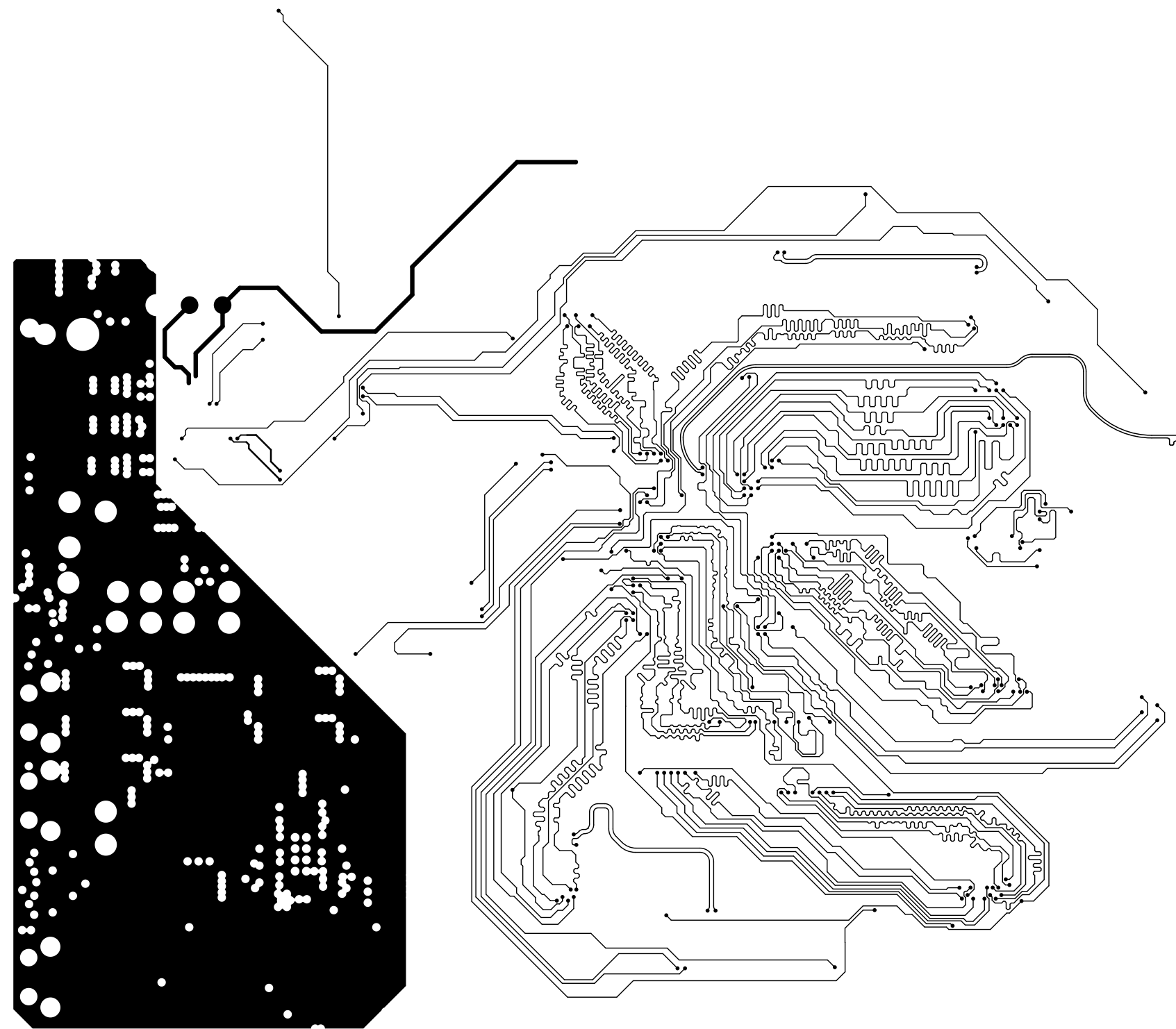


16

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = L16 Signal	TID #: N/A		
	GENERATED : 29-04-2021 18:29:54	TEXAS INSTRUMENTS	

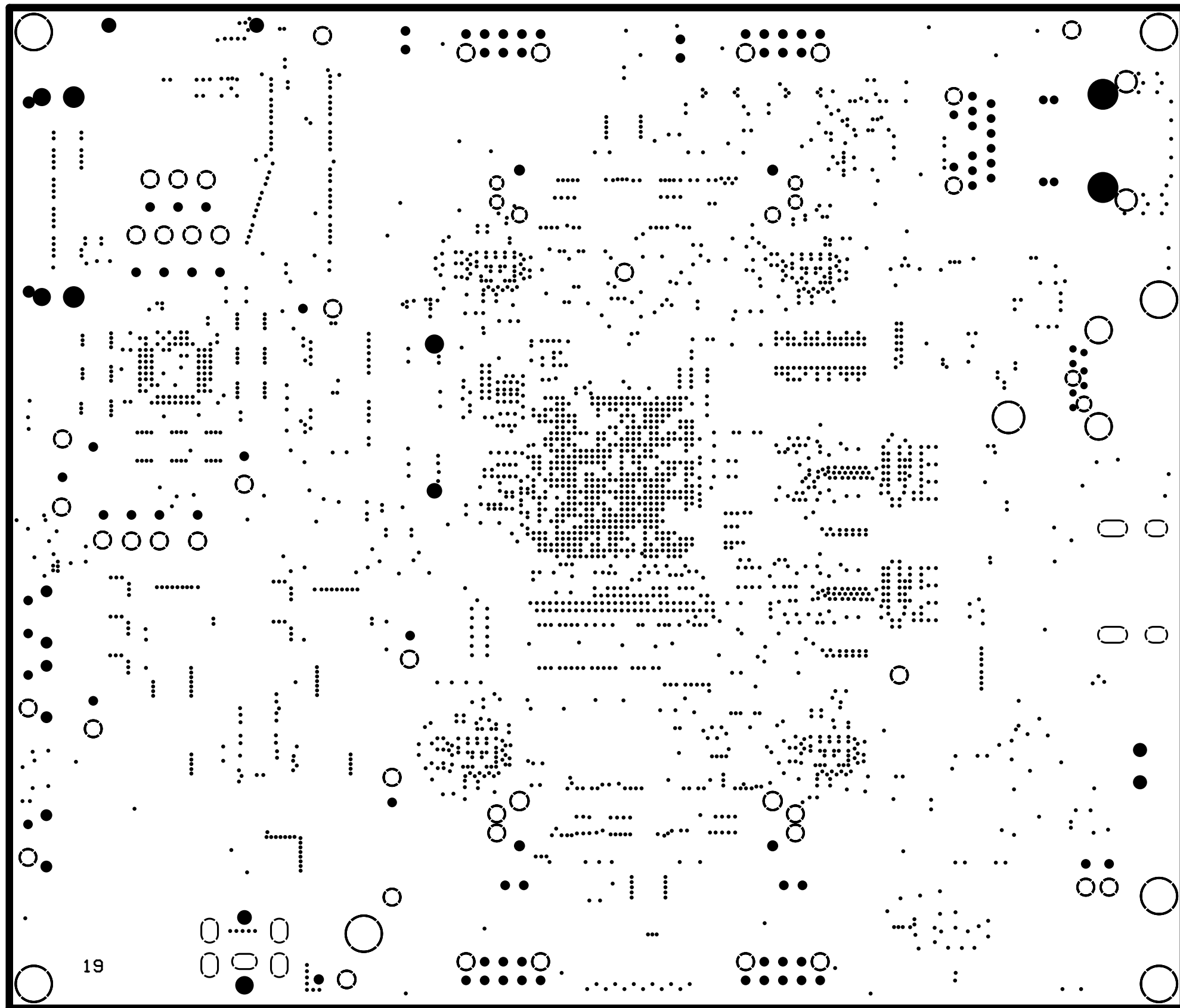


ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = L17 GND	TID #: N/A		
	GENERATED : 29-04-2021 18:29:54	TEXAS INSTRUMENTS	

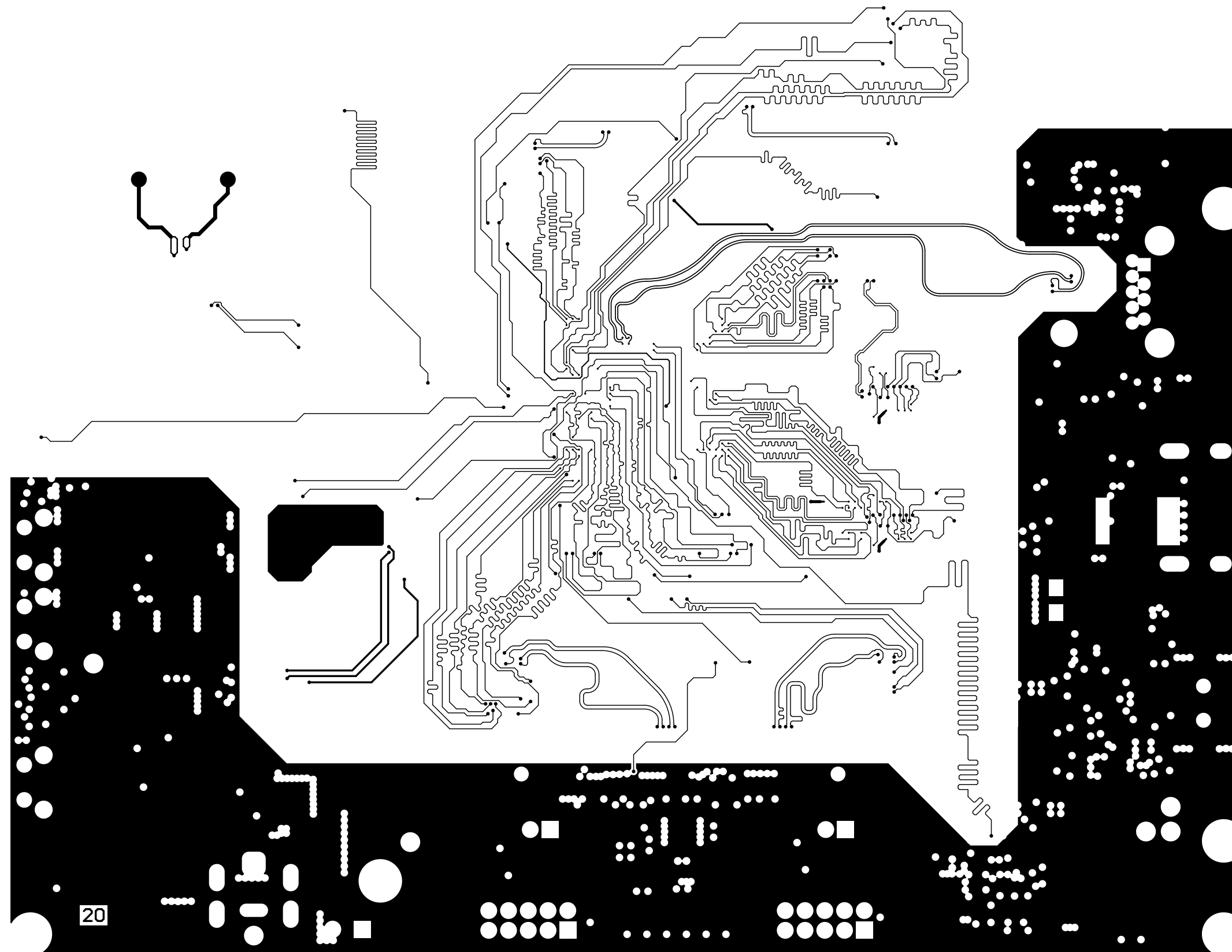


18

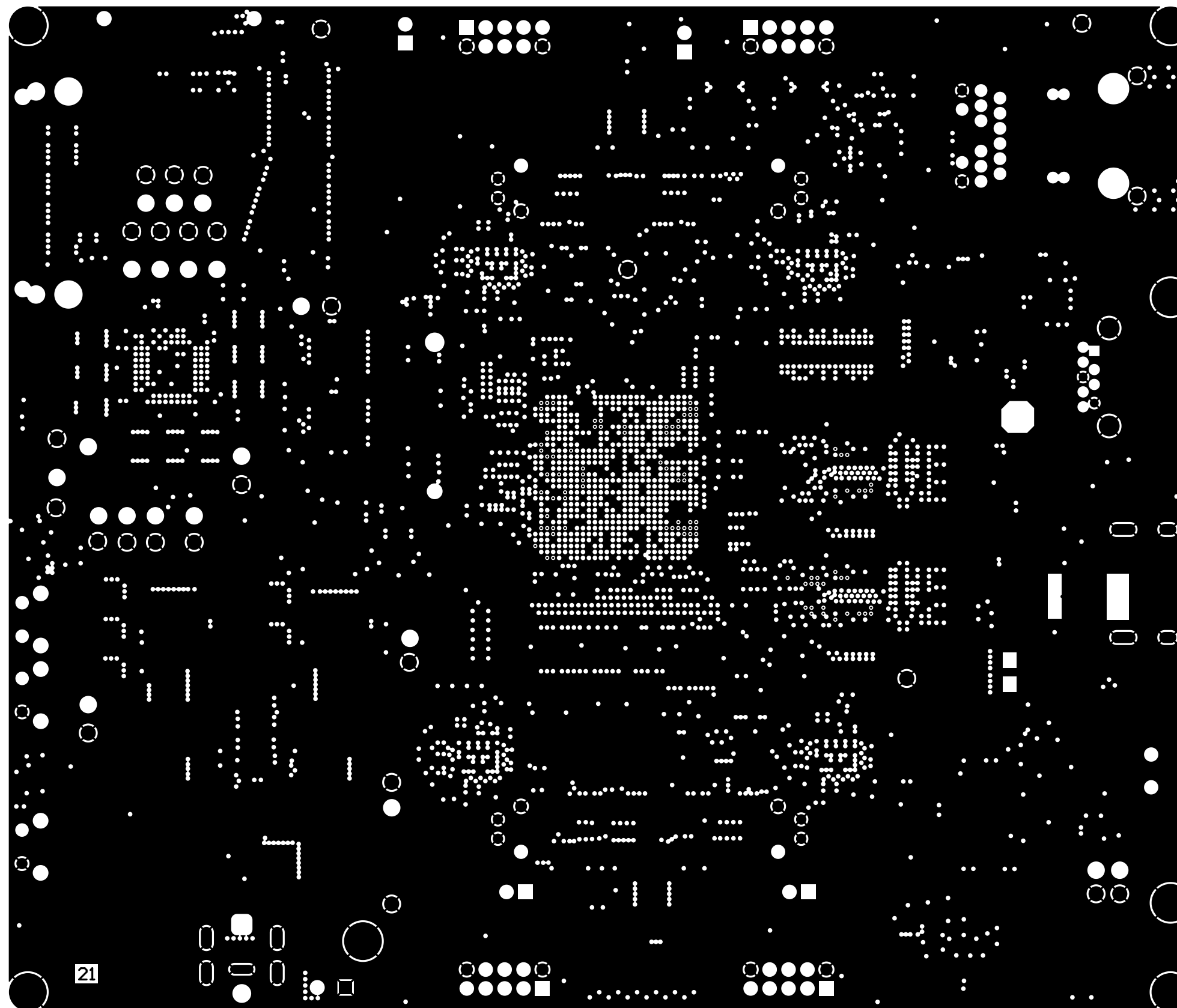
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = L18 Signal	TID #: N/A		
	GENERATED : 29-04-2021 18:29:54	TEXAS INSTRUMENTS	



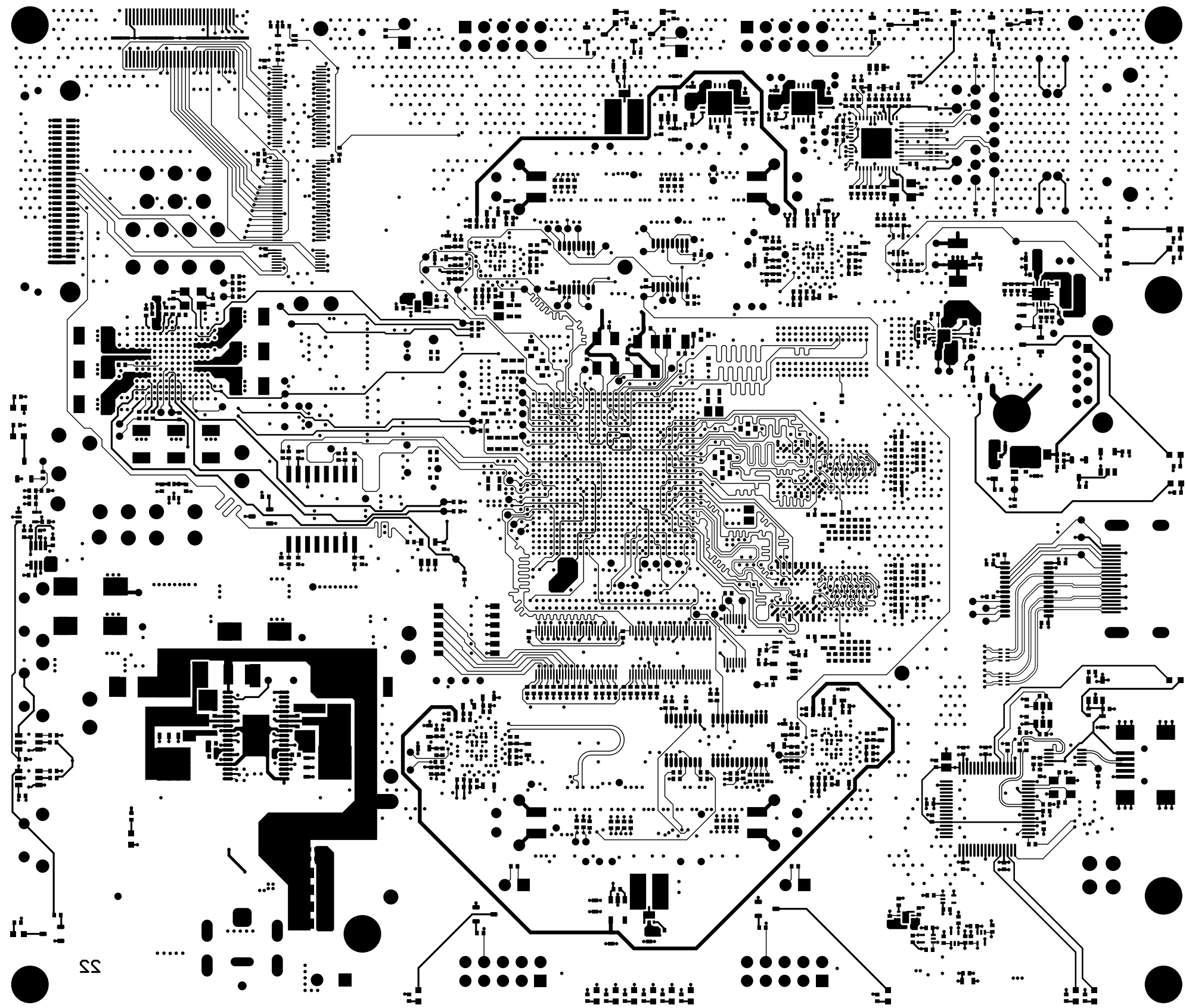
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = L19 GND	TID #: N/A		
	GENERATED : 29-04-2021 18:29:54	TEXAS INSTRUMENTS	



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = L20 Signal	TID #: N/A		
	GENERATED : 29-04-2021 18:29:56	TEXAS INSTRUMENTS	

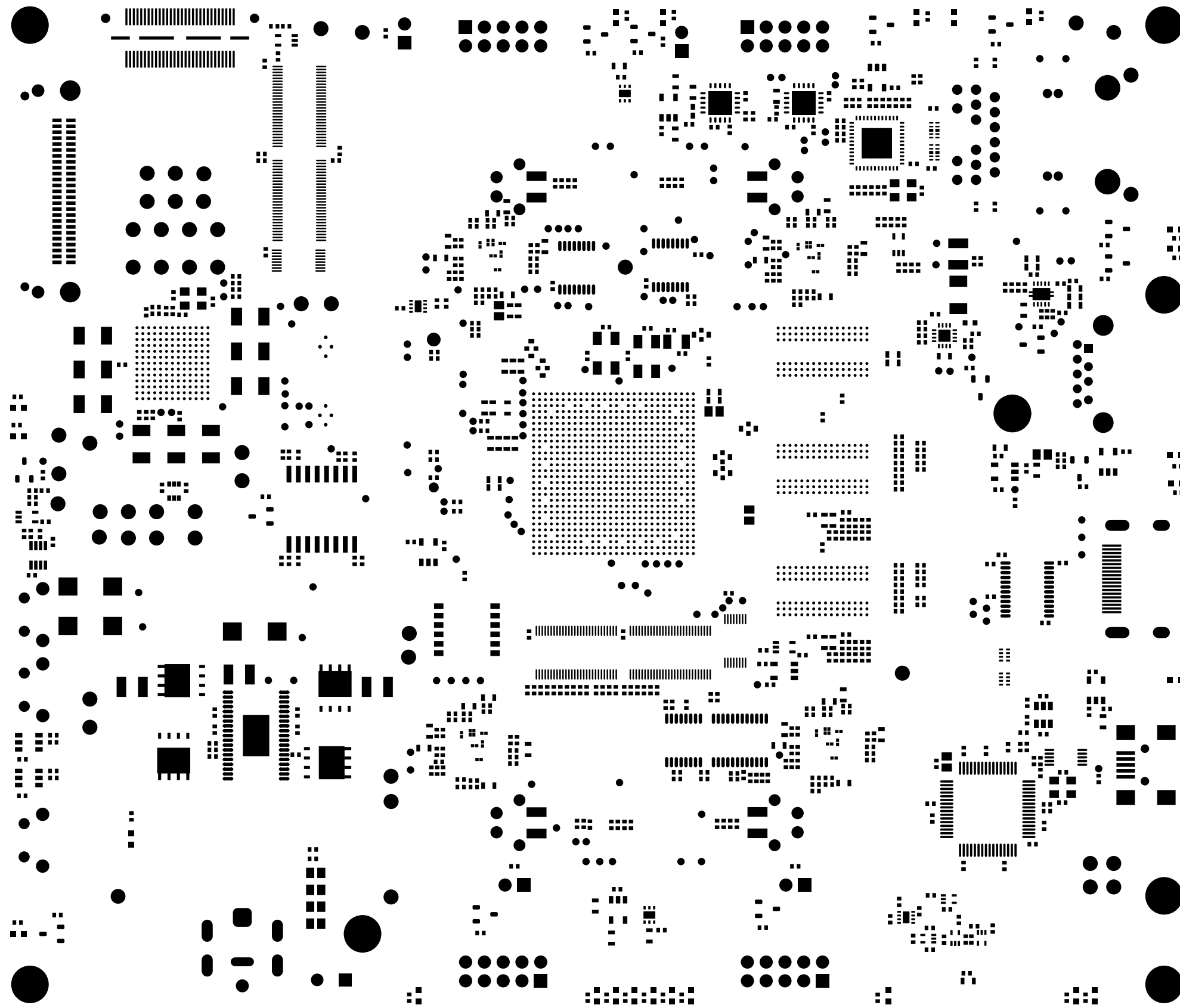


ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = L21 GND	TID #: N/A		
	GENERATED : 29-04-2021 18:29:56	TEXAS INSTRUMENTS	

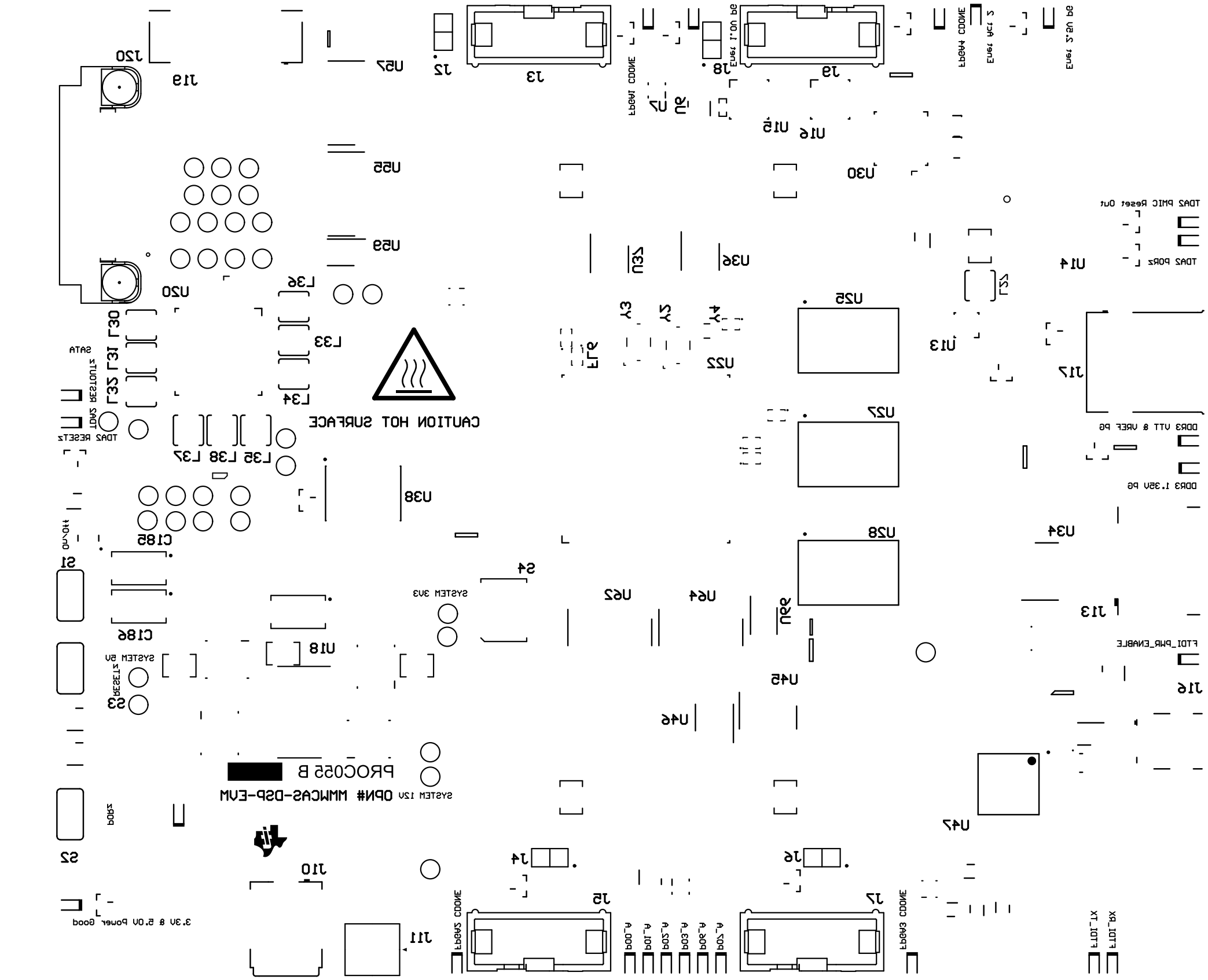


55

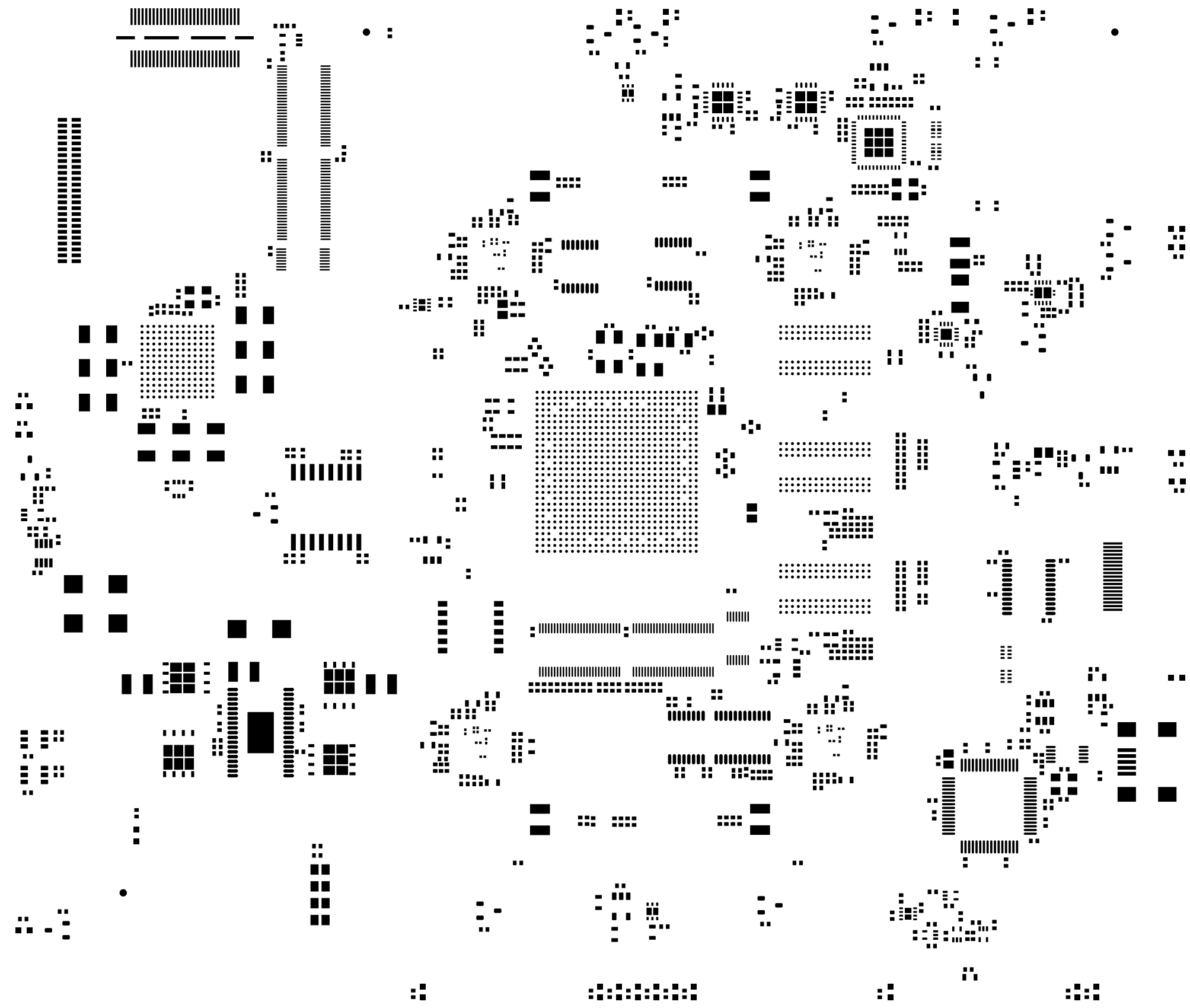
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = L22 Bottom	TID #: N/A		
	GENERATED : 29-04-2021 18:29:56		TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = Bottom Solder	TID #: N/A		
	GENERATED : 29-04-2021 18:29:56	TEXAS INSTRUMENTS	



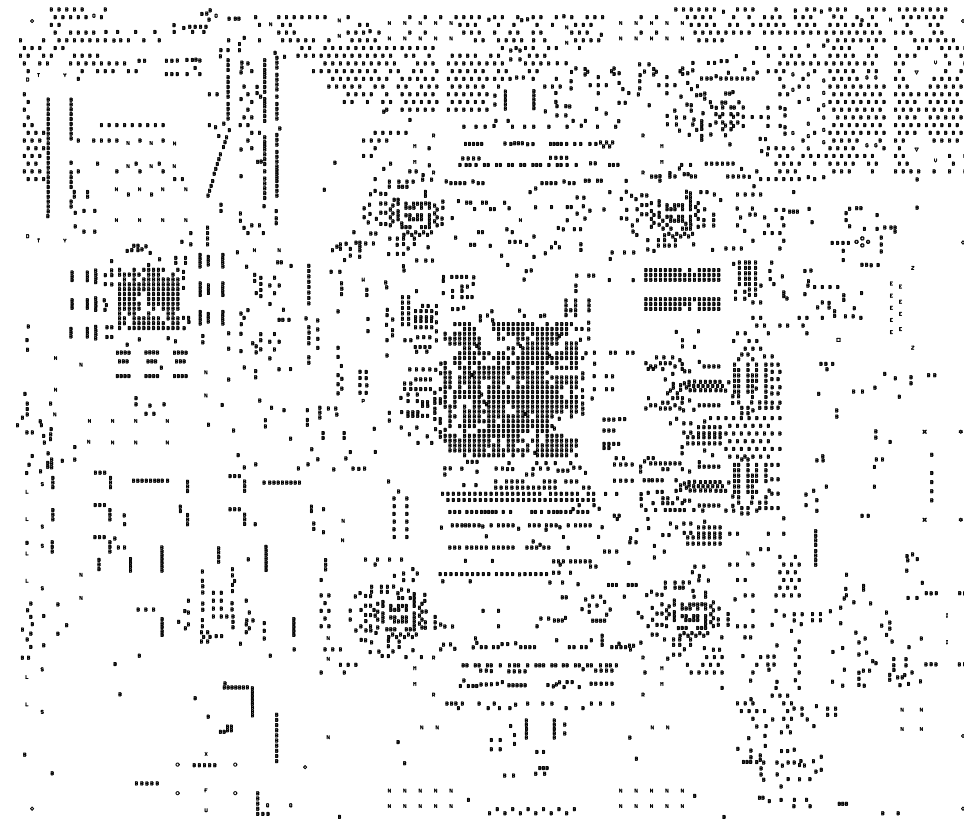
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = Bottom Overlay	TID #: N/A		
	GENERATED : 29-04-2021 18:29:57	TEXAS INSTRUMENTS	



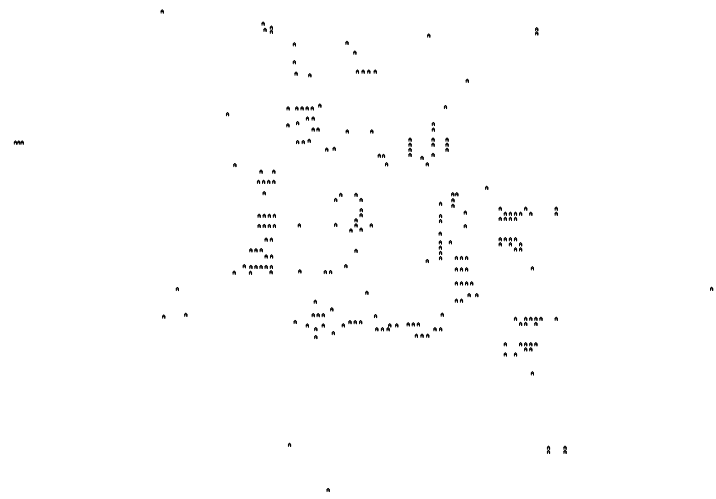
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = Bottom Paste	TID #: N/A		
	GENERATED : 29-04-2021 18:29:56		TEXAS INSTRUMENTS

Symbol	Count	Hole Size	Plated	Hole Type	Hole Length	Drill Layer Pair	Routed Path Length	Hole Tolerance
⊙	4	7.87mil (0.200mm)	PTH	Round	-	L1 Top Layer - L22 Bottom	-	
B	4758	9.10mil (0.231mm)	PTH	Round	-	L1 Top Layer - L22 Bottom	-	+0.00mil/-9.10mil
D	2	25.00mil (0.635mm)	PTH	Round	-	L1 Top Layer - L22 Bottom	-	+/-3.00mil
DRILL CHART :		27.56mil (0.700mm)	PTH	Round	-	L1 Top Layer - L22 Bottom	-	+/-3.00mil
F	1	31.50mil (0.800mm)	PTH	Slot	82.68mil (2.100mm)	L1 Top Layer - L22 Bottom	51.18mil (1.300mm)	+3.94mil/-0.00mil
G	20	35.00mil (0.889mm)	PTH	Round	-	L1 Top Layer - L22 Bottom	-	+/-3.00mil
I	2	35.43mil (0.900mm)	NPTH	Round	-	L1 Top Layer - L22 Bottom	-	+/-2.00mil
⊗	2	35.43mil (0.900mm)	PTH	Slot	66.93mil (1.700mm)	L1 Top Layer - L22 Bottom	31.50mil (0.800mm)	+/-3.00mil
⊗	2	35.43mil (0.900mm)	PTH	Slot	106.30mil (2.700mm)	L1 Top Layer - L22 Bottom	70.87mil (1.800mm)	+/-3.00mil
M	8	39.37mil (1.000mm)	PTH	Round	-	L1 Top Layer - L22 Bottom	-	+3.94mil/-0.00mil
L	6	39.37mil (1.000mm)	PTH	Round	-	L1 Top Layer - L22 Bottom	-	+/-3.00mil
O	4	39.37mil (1.000mm)	PTH	Slot	78.74mil (2.000mm)	L1 Top Layer - L22 Bottom	39.37mil (1.000mm)	+/-3.00mil
N	93	40.00mil (1.016mm)	PTH	Round	-	L1 Top Layer - L22 Bottom	-	+/-3.00mil
O	2	40.16mil (1.020mm)	NPTH	Round	-	L1 Top Layer - L22 Bottom	-	+/-2.00mil
P	1	43.31mil (1.100mm)	NPTH	Round	-	L1 Top Layer - L22 Bottom	-	+/-1.97mil
Q	2	43.31mil (1.100mm)	PTH	Round	-	L1 Top Layer - L22 Bottom	-	+/-3.00mil
R	8	47.24mil (1.200mm)	PTH	Round	-	L1 Top Layer - L22 Bottom	-	+3.94mil/-0.00mil
S	6	51.18mil (1.300mm)	PTH	Round	-	L1 Top Layer - L22 Bottom	-	+/-3.00mil
T	2	57.09mil (1.450mm)	NPTH	Round	-	L1 Top Layer - L22 Bottom	-	+/-2.00mil
U	1	59.06mil (1.500mm)	NPTH	Round	-	L1 Top Layer - L22 Bottom	-	+3.94mil/-0.00mil
V	2	62.50mil (1.587mm)	PTH	Round	-	L1 Top Layer - L22 Bottom	-	+/-3.00mil
W	1	62.99mil (1.600mm)	NPTH	Round	-	L1 Top Layer - L22 Bottom	-	+/-1.97mil
X	1	66.93mil (1.700mm)	PTH	Round	-	L1 Top Layer - L22 Bottom	-	+/-1.97mil
Y	2	76.00mil (1.930mm)	PTH	Round	-	L1 Top Layer - L22 Bottom	-	+/-3.00mil
Z	2	90.55mil (2.300mm)	PTH	Round	-	L1 Top Layer - L22 Bottom	-	+/-2.00mil
□	1	118.11mil (3.000mm)	PTH	Round	-	L1 Top Layer - L22 Bottom	-	+/-3.00mil
▽	2	126.00mil (3.200mm)	NPTH	Round	-	L1 Top Layer - L22 Bottom	-	+2.00mil/-1.00mil
◇	7	144.00mil (3.658mm)	PTH	Round	-	L1 Top Layer - L22 Bottom	-	+/-3.00mil
	4951 Total							

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

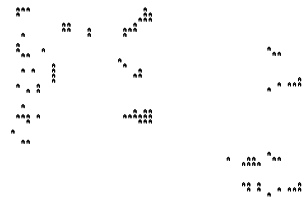


Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
A	224	4.00mil (0.102mm)	PTH	Round	L1 Top Layer - L2 GND	+0.00mil/-4.00mil
	224 Total					



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = FAB	TID #: N/A		
GENERATED : 29-04-2021 18:30:05		TEXAS INSTRUMENTS	

Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
A	93	4.00mil (0.102mm)	PTH	Round	L20 Signal - L21 GND	+0.00mil/-4.00mil
	93 Total					

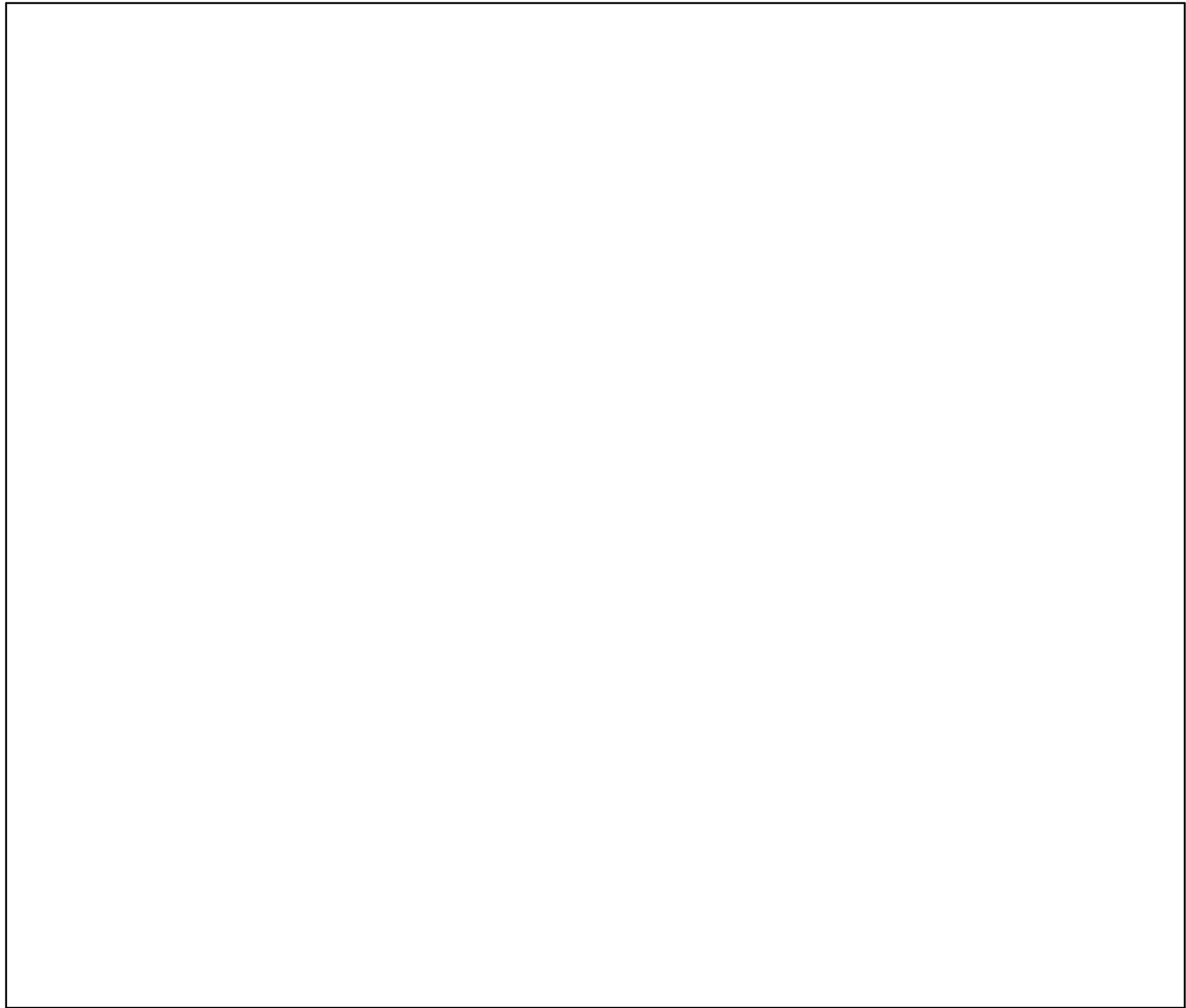


ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = FAB	TID #: N/A		
GENERATED : 29-04-2021 18:30:07		TEXAS INSTRUMENTS	

Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
A	214	4.00mil (0.102mm)	PTH	Round	L21 GND - L22 Bottom	+0.00mil/-4.00mil
	214 Total					



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = FAB	TID #: N/A		
GENERATED : 29-04-2021 18:30:09		TEXAS INSTRUMENTS	



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = M1 Board Outline	TID #: N/A		
	GENERATED : 29-04-2021 18:29:56	TEXAS INSTRUMENTS	




ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = M2 Board Dimensions	TID #: N/A		
	GENERATED : 29-04-2021 18:29:56	TEXAS INSTRUMENTS	



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME =	TID #: N/A		
GENERATED : 29-04-2021 18:29:57		TEXAS INSTRUMENTS	

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 TEXAS INSTRUMENTS	
PROJECT TITLE: Cascade Radar Host Processor Board	
DESIGNED FOR: Public Release	
FILE NAME: PROC055B_Cascade_Radar_Host.PcbDoc	
ENGINEER: Alec Schott	LAYOUT BY: Tessolve
SCALE: 1.00	ALTIM DESIGNER VERSION: 18.1.9.240

FAB NOTES :

1. ALL VIAS ARE TENTED ON TOP AND BOTTOM UNLESS OPENED IN CAD EXPLICITLY.
2. ALL VIAS SHOULD BE FILLED WITH NON CONDUCTIVE EPOXY AND SURFACE FINISHED FLAT VIA IN PAD SURFACE FLATNESS TOLERANCE: +0.000/-0.001 INCHES ON TOP AND BOTTOM LAYER.
3. MANUFACTURER'S IDENTIFICATION AND DATECODE LETTER SHALL BE SILKSCREENED ON TOP LAYER OF BOARD.
4. PLEASE REFER TO THE IMPEDANCE TABLE PROVIDED BELOW FOR THE IMPEDANCE REQUIREMENTS.
5. THIEVING CAN BE PERFORMED AT THE DISCRETION OF THE MANUFACTURER.
6. IF NEEDED, ADD TEAR-DROP TO DRILL HOLES TO GET BETTER ANULAR RING AND REGISTRATION. AT DISCRETION OF MANUFACTURER.
7. No-net vias are intentional at J12.

IMPEDANCE TABLE:

LAYER	TRACK WIDTH(MIL)	SPACING(MIL)	IMPEDANCE(OHM)	TOLERANCE(OHM)
L11	4.5		50	5
L11	4.6	7.4	90	9
L11	3.7	8.3	100	10
L13	4.3		50	5
L13	4	8	100	10
L15	4.5		50	5
L15	4	8	100	10
L17	5.75		50	5
L18	5.75		50	5
L16	4.5		50	5
L16	4.1	7.9	100	10
L18	4.5		50	5
L18	4.75	7.25	90	9
L20	4.3		50	5
L20	5	7	90	9
L20	4	8	100	10
L22	4.5		50	5
L22	4.6	7.4	90	9
L22	3.7	8.3	100	10

DESIGN INFORMATION	
MIN. TRACK WIDTH:	3.2 MIL
MIN. CLEARANCE:	3 MIL
MIN. VIA PAD SIZE:	10 MIL
MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL PER IPC-D-275 CLASS 2 LEVEL C REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL HOLE SIZE TOLERANCE (UNLESS OTHERWISE SPECIFIED): +/- 3 MIL	
MATERIAL:	
<input type="checkbox"/> FR-408	<input type="checkbox"/> FR-4 High Tg
<input checked="" type="checkbox"/> OTHER	370H
THICKNESS:	<input type="checkbox"/> 62 MIL (1.6mm) +/-10% <input checked="" type="checkbox"/> OTHER 132 MIL +/-10 MIL
TOLERANCE:	<input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2 <input type="checkbox"/> OTHER +/-
BOW & TWIST:	<input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2 <input type="checkbox"/> OTHER +/-
DRILLING:	
REFERENCE:	<input checked="" type="checkbox"/> AS SHOWN <input checked="" type="checkbox"/> NC_DRILL FILES
PTH COPPER THICKNESS:	<input checked="" type="checkbox"/> 20-30 um <input type="checkbox"/> OTHER
BOARD FINISH:	
SILKSCREEN:	<input checked="" type="checkbox"/> TOP <input checked="" type="checkbox"/> BOTTOM
SILKSCREEN COLOR:	<input checked="" type="checkbox"/> WHITE <input type="checkbox"/> OTHER
SOLDER RESIST COLOR:	<input checked="" type="checkbox"/> GREEN <input type="checkbox"/> OTHER <input checked="" type="checkbox"/> MATTE <input type="checkbox"/> SEMI-GLOSS
SURFACE FINISH:	
<input checked="" type="checkbox"/> IMMERSION GOLD (ENIG)	<input type="checkbox"/> ENEPIC
<input type="checkbox"/> IMM. TIN/SILVER OR EQUIV	<input type="checkbox"/> OTHER
ARRAY/PANEL:	
<input type="checkbox"/> CUT AND TRM PER M1 BOARD OUTLINE	<input checked="" type="checkbox"/> V. SCORE
<input type="checkbox"/> N.C. ROUTE	<input checked="" type="checkbox"/> V. SCORE
CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF:	
<input checked="" type="checkbox"/> ANSI IPC-A-600F CLASS ->	<input type="checkbox"/> 1 <input checked="" type="checkbox"/> 2 <input type="checkbox"/> 3
<input checked="" type="checkbox"/> RoHS	<input type="checkbox"/> OTHER PER ORDER
ALL BOARDS MUST MEET OR EXCEED UL94-V0 REQUIREMENTS. PCB MUST BEAR THE UL94V-0 UL REGISTERED MATERIAL ID NUMBER	
ADDITIONAL REQUIREMENTS:	
MICROSECTION: <input checked="" type="checkbox"/> YES	
BARE BOARD ELEC. TEST: <input type="checkbox"/> NONE <input checked="" type="checkbox"/> REQUIRED <input type="checkbox"/> PER ORDER	
<input type="checkbox"/> XX MIL VIAS REQUIRE NON-CONDUCTIVE FILL AND PLANARIZE	
<input type="checkbox"/> XX MIL VIAS REQUIRE CONDUCTIVE FILL AND PLANARIZE	
<input type="checkbox"/> OUTER XX MIL VIAS REQUIRE 50 OHM SINGLE-ENDED IMPEDANCE LAYER 2 & 3 (INNER LAYERS) XX MIL WIDE, XX MIL SPACE	
<input type="checkbox"/> TRACES REQUIRE 100 OHM DIFFERENTIAL IMPEDANCE	

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = M10 Fab Notes	TID #: N/A		
GENERATED : 29-04-2021 18:29:57	TEXAS INSTRUMENTS		

LAYER STACKUP :

Layer	Name	Material	Thickness	Constant	Board Layer Stack
1	Top Overlay				
2	Top Solder	Solder Resist	0.50mil	4.7	
3	L1 Top Layer	Copper	2.00mil		
4	Dielectric 1	370H	2.90mil	3.77	
5	L2 GND	Copper	2.00mil		
6	Dielectric 2	370H	4.00mil	3.69	
7	L3 Signal	Copper	0.60mil		
8	Dielectric 3	370H	6.00mil	4.43	
9	L4 GND	Copper	1.20mil		
10	Dielectric 4	370H	7.20mil	3.85	
11	L5 Signal	Copper	0.60mil		
12	Dielectric 5	370H	4.00mil	4.57	
13	L6 GND	Copper	0.60mil		
14	Dielectric 6	370H	4.30mil	3.83	
15	L7 Signal	Copper	0.60mil		
16	Dielectric 7	370H	6.00mil	4.43	
17	L8 Signal	Copper	0.60mil		
18	Dielectric 8	370H	4.20mil	3.83	
19	L9 GND	Copper	1.20mil		
20	Dielectric 9	370H	6.00mil	4.43	
21	L10 PWR	Copper	1.20mil		
22	Dielectric 10	370H	5.30mil	3.87	
23	L11 GND	Copper	1.20mil		
24	Dielectric 11	370H	6.00mil	4.43	
25	L12 PWR	Copper	1.20mil		
26	Dielectric 12	370H	5.30mil	3.87	
27	L13 GND	Copper	1.20mil		
28	Dielectric 13	370H	6.00mil	4.43	
29	L14 PWR	Copper	1.20mil		
30	Dielectric 14	370H	4.50mil	3.83	
31	L15 GND	Copper	0.60mil		
32	Dielectric 15	370H	6.00mil	4.43	
33	L16 Signal	Copper	0.60mil		
34	Dielectric 16	370H	4.40mil	3.83	
35	L17 GND	Copper	0.60mil		
36	Dielectric 17	370H	4.00mil	4.57	
37	L18 Signal	Copper	0.60mil		
38	Dielectric 18	370H	7.20mil	3.85	
39	L19 GND	Copper	1.20mil		
40	Dielectric 19	370H	6.00mil	4.43	
41	L20 Signal	Copper	0.60mil		
42	Dielectric 20	370H	4.10mil	3.69	
43	L21 GND	Copper	2.00mil		
44	Dielectric 21	370H	2.90mil	3.77	
45	L22 Bottom	Copper	2.00mil		
46	Bottom Solder	Solder Resist	0.50mil	4.7	
47	Bottom Overlay				

L01-L02
L02-L03

L01-L22

L20-L21
L21-L22

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC055	REV: B	SUN REV: Not In VersionControl
LAYER NAME = M12 Stackup	TID #: N/A		
	GENERATED : 29-04-2021 18:29:57	TEXAS INSTRUMENTS	

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