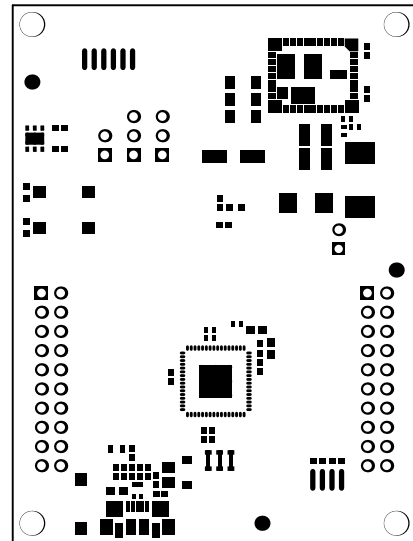
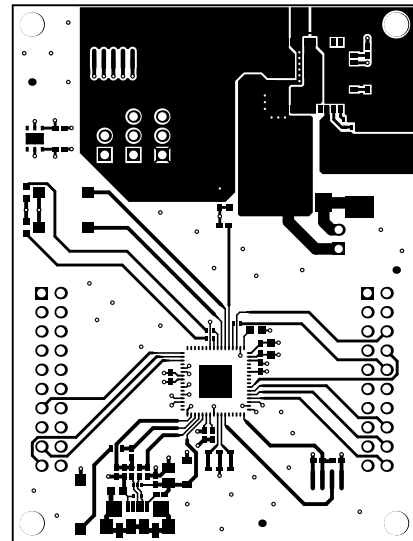


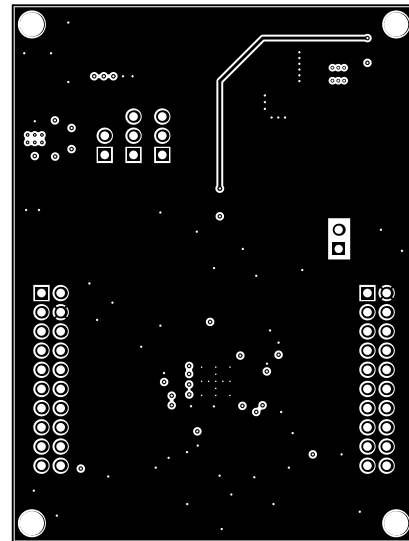
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-00979PW	REV: E1	SUN REV: Not In VersionControl
LAYER NAME = Top Overlay	TID #: TIDA-00979PW		
PLOT NAME = Top Overlay	GENERATED : 5/30/2016	12:40:05 PM	TEXAS INSTRUMENTS



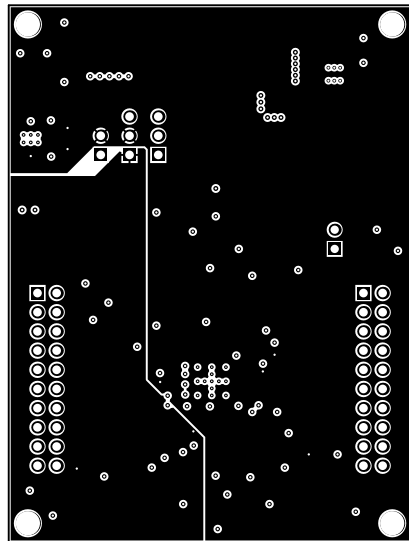
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-00979PW	REV: E1	SUN REV: Not In VersionControl
LAYER NAME = Top Solder	TID #: TIDA-00979PW		
PLOT NAME = Top Solder Mask	GENERATED : 5/30/2016 12:40:06 PM	TEXAS INSTRUMENTS	



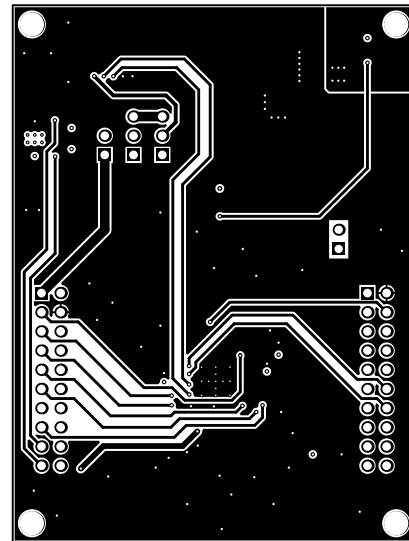
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-00979PW	REV: E1	SUN REV: Not In VersionControl
LAYER NAME = Top Layer	TID #: TIDA-00979PW		
PLOT NAME = Top Layer	GENERATED : 5/30/2016	12:40:07 PM	TEXAS INSTRUMENTS



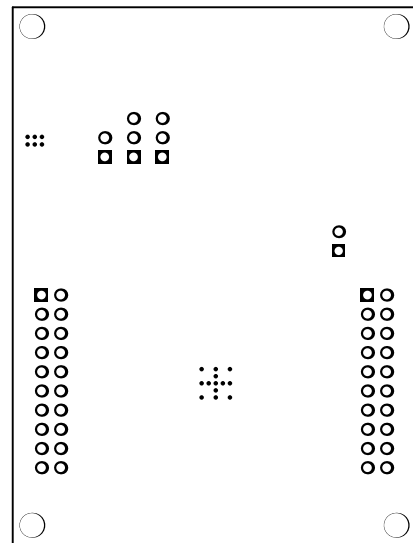
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-00979PW	REV: E1	SUN REV: Not In VersionControl
LAYER NAME = L02 - GND	TID #: TIDA-00979PW		
PLOT NAME = Signal Layer 1	GENERATED : 5/30/2016 12:40:07 PM	TEXAS INSTRUMENTS	



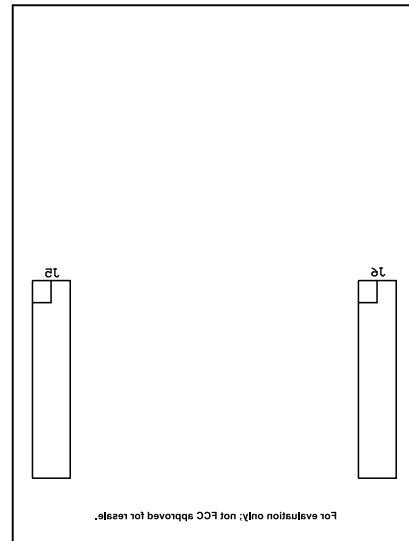
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-00979PW	REV: E1	SUN REV: Not In VersionControl
LAYER NAME = L03 - SUPPLY	TID #: TIDA-00979PW		
PLOT NAME = Signal Layer 2	GENERATED : 5/30/2016 12:40:08 PM	TEXAS INSTRUMENTS	



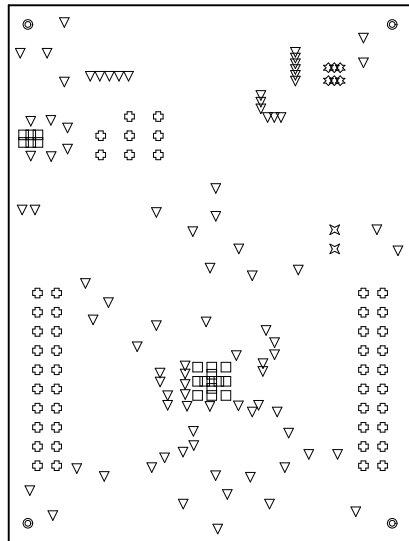
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-00979PW	REV: E1	SUN REV: Not In VersionControl
LAYER NAME = Bottom Layer	TID #: TIDA-00979PW		
PLOT NAME = Bottom Layer	GENERATED : 5/30/2016 12:40:09 PM	TEXAS INSTRUMENTS	



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-00979PW	REV: E1	SUN REV: Not In VersionControl
LAYER NAME = Bottom Solder	TID #: TIDA-00979PW		
PLOT NAME = Bottom Solder Mask	GENERATED : 5/30/2016	12:40:10 PM	TEXAS INSTRUMENTS

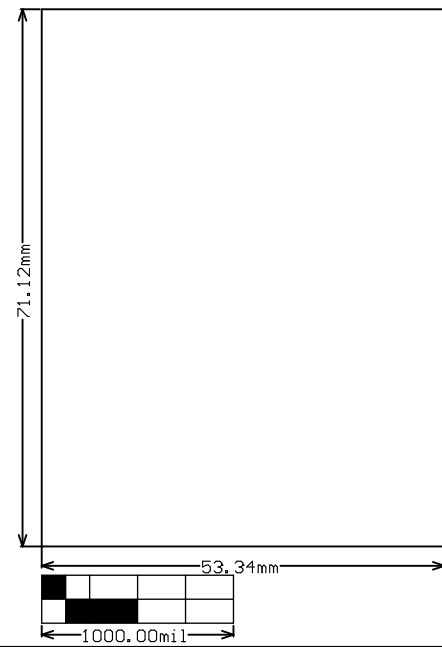


ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-00979PW	REV: E1	SUN REV: Not In VersionControl
LAYER NAME = Bottom Overlay	TID #: TIDA-00979PW		
PLOT NAME = Bottom Overlay	GENERATED : 5/30/2016	12:40:10 PM	TEXAS INSTRUMENTS



Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Drill Layer Pair	Tolerance
●	4	125.98mil (3.200mm)	NPTH	Round	Top Layer - Bottom Layer	
□	19	7.87mil (0.200mm)	PTH	Round	Top Layer - Bottom Layer	
⊗	6	11.81mil (0.300mm)	PTH	Round	Top Layer - Bottom Layer	
▽	87	12.00mil (0.305mm)	PTH	Round	Top Layer - Bottom Layer	
⊗	2	43.31mil (1.100mm)	PTH	Round	Top Layer - Bottom Layer	
⊗	48	45.26mil (1.150mm)	PTH	Round	Top Layer - Bottom Layer	
	166 Total					

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-00979PW	REV: E1	SUN REV: Not In VersionControl
LAYER NAME = Drill Drawing	TID #: TIDA-00979PW		
PLOT NAME = Drill Drawing	GENERATED : 5/30/2016	12:40:11 PM	TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-00979PW	REV: E1	SUN REV: Not In VersionControl
LAYER NAME = M2 Board Dimensions	TID #: TIDA-00979PW		
PLOT NAME = Board Dimensions	GENERATED : 5/30/2016	12:40:12 PM	TEXAS INSTRUMENTS

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