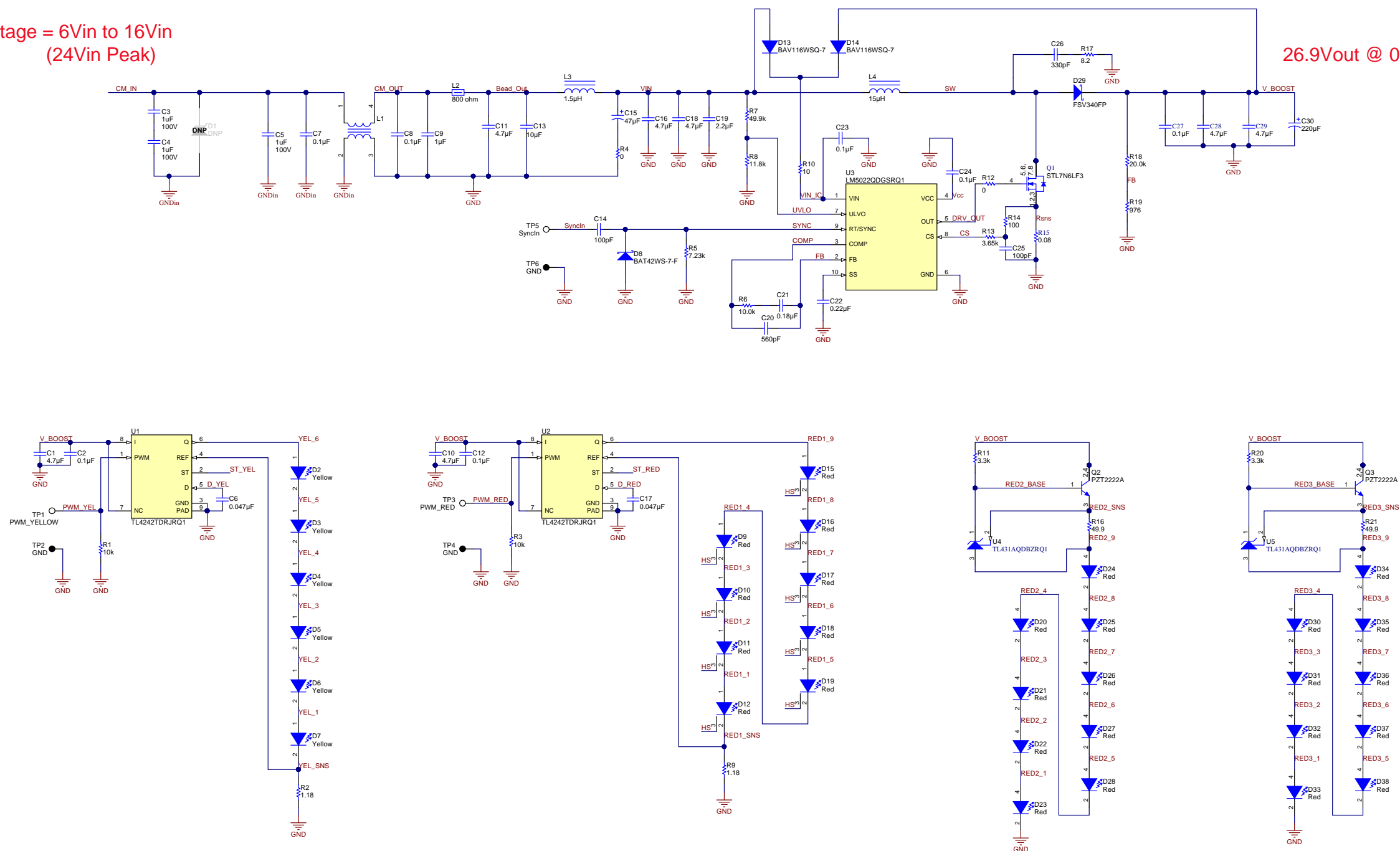


Input Voltage = 6Vin to 16Vin  
(24Vin Peak)

26.9Vout @ 0.4A Max.



**\*NOTES:**

1. Electrical testing for test report were performed with U4, U5, R16, and R21 removed from the board. An electronic load was used instead.
2. Refer to the TL4242-Q1 datasheet for details regarding the use of its PWM input.
3. Refer to the LM5022-Q1 datasheet for details regarding its synchronization to an external clock source.

H1 NY PMS 440 0025 PH  
 H2 NY PMS 440 0025 PH  
 H3 NY PMS 440 0025 PH  
 H4 NY PMS 440 0025 PH

H5 1902C  
 H6 1902C  
 H7 1902C  
 H8 1902C

DNP FID1  
 DNP FID2  
 DNP FID3

PCB Number: PMP20305  
 PCB Rev: A



PCB LOGO  
 FCC disclaimer

LBL1  
 PCB Label  
 Size: 0.65" x 0.20"

Variant/Label Table	
Variant	Label Text
001	

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Orderable: <a href="#">ChangeMe!</a>	Designed for:	Mod. Date: 6/22/2016
TID #: PMP20305	Project Title: LM5022 Non-Sync Boost with LED Drivers	
Number: PMP20305	Rev: A	Sheet Title: Hardware
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 2 of 2
Drawn By:	File: PMP20305_EVM_Hardware.SchDoc	Size: B
Engineer:	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	TEXAS INSTRUMENTS <a href="http://www.ti.com">http://www.ti.com</a> © Texas Instruments 2016

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