

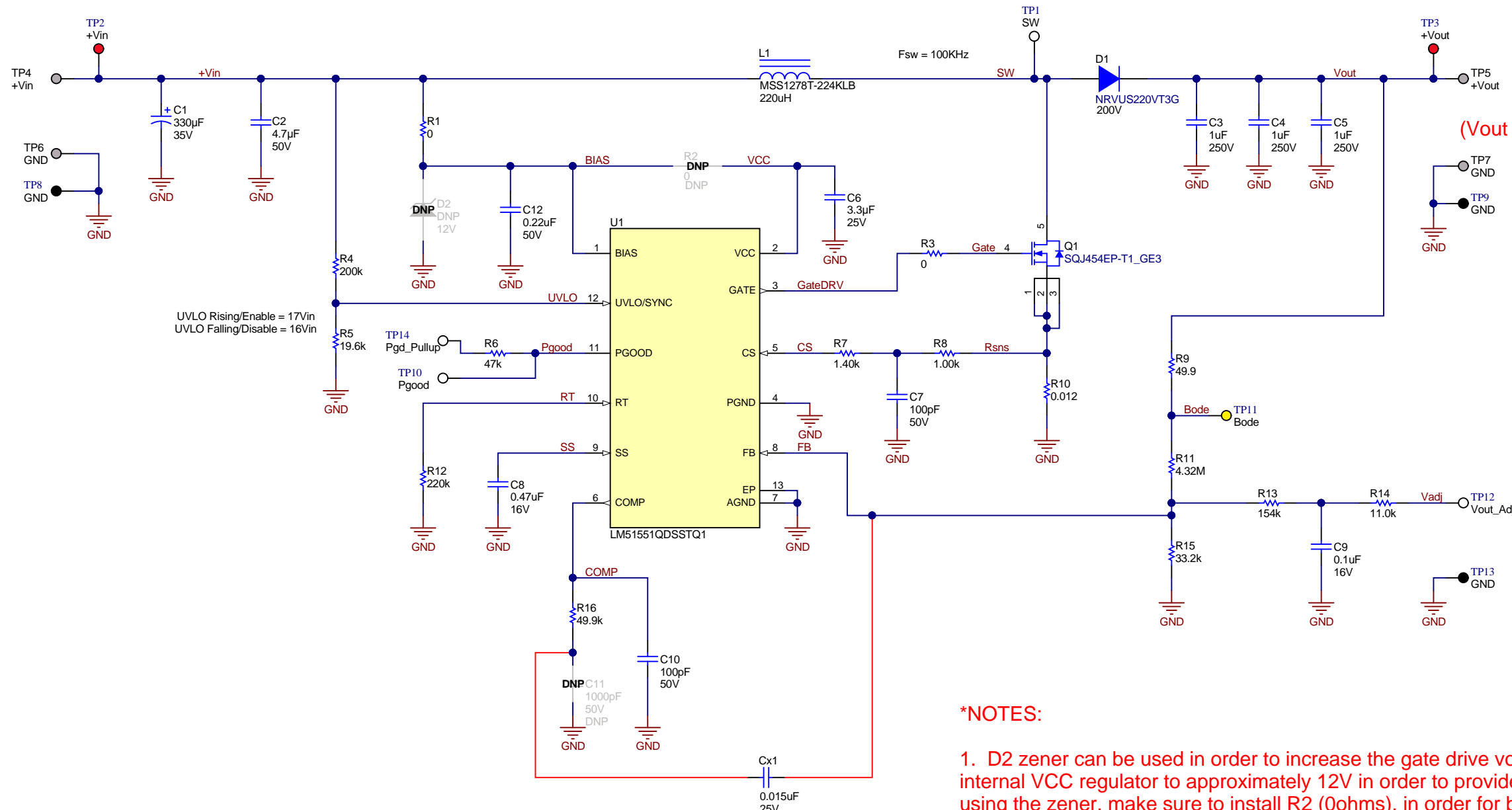
18Vin

35Vout to 150Vout @ 85mA Max.  
(Vout is adjustable using the "Vout\_Adj." testpoint)

"Vout\_Adj" control voltage = 0.3V to 4.7V  
(Vadj. = 0.3V => Vout = 150V)  
(Vadj. = 4.7V => Vout = 35V)

**\*NOTES:**

1. D2 zener can be used in order to increase the gate drive voltage from the approximate 6.8V of the internal VCC regulator to approximately 12V in order to provide higher drive voltage to the FET. When using the zener, make sure to install R2 (0ohms), in order for both the "BIAS" and "VCC" pins to be at the same voltage potential. Also, select an appropriate value for R1 for biasing the zener.
2. Red traces signify wires or traces that were not part of the original PCB layout. These are connections that are air-wired.



H1 1  
NY PMS 440 0025 PH

H2 1  
NY PMS 440 0025 PH

H3 1  
NY PMS 440 0025 PH

H4 1  
NY PMS 440 0025 PH

H5  
1902C

H6  
1902C

H7  
1902C

H8  
1902C

DNP  
FID1

DNP  
FID2

DNP  
FID3

PCB Number: PMP21781  
PCB Rev: A

PCB  
LOGO  
FCC disclaimer

PCB  
LOGO  
WEEE logo

Variant/Label Table	
Variant	Label Text
001	

Orderable: <a href="#">ChangeMe in variant</a>	Designed for:	Mod. Date: 10/31/2018
TID #: PMP21781	Project Title: LM51551-Q1 Non-Synchronous Boost	
Number: PMP21781	Rev: 1	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 2 of 2
Drawn By:	File: PMP21781_Hardware.SchDoc	Size: B
Engineer: Hrag Kasparian	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

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