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*
* Model Usage Notes:
* A. Features have been modelled
*    1.  Output Voltage Setting
*    2.  External Adjustable Soft-start
*    3.  Adjustable UVLO
*    4.  Frequency Selection
*    5.  Low-side FET Zero-Crossing
*    6.  Current Sense and Positive Overcurrent Protection(OCP)
*    7.  Over Voltage Protection(OVP)
*    8.  Under Voltage Protection(UVP)
*    9.  UVLO Protection
*    10. BOOT functionality
*    11. PGOOD functionality
*
* B. Features have not been modelled
*    1. Operating Quiescent Current
*    2. Shutdown Current
*    3. Temperature dependent characteristics
*
* C. Application Notes
*    1. The parameter STEADY_STATE has been used to reach the steady state faster.
*       Keep STEADY_STATE = 0 to observe startup behaviour.
*       Keep STEADY_STATE = 1 and appropriate IC on Inductor and capacitor to observe for faster Steady state.
*    2. The parameter EN_FSS has been used to Enable/Disable FSS behaviour.
*       Keep EN_FSS = 0 to disable Frequency Spread Spectrum
*       Keep EN_FSS = 1 to observe Frequency Spread Spectrum after 1.5*Tss. +
*    3. Ground Pins have been tied to 0V internally and hence model does not support Inverting
*       topologies.
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```