

TPSM82822A PSPICE Transient Model Features and Limitations

```
*****
*
* Model Usage Notes:
*
* A. Features have been modelled
* 1. Output Voltage Setting
* 2. Internal Fixed Soft-Start
* 3. Current Sense and Positive Overcurrent Protection(OCP)
* 4. Low-side FET Zero-Crossing
* 5. Output Voltage Discharge
* 6. Power Good
* 7. UVLO Protection
*
* B. Features have not been modelled
* 1. Operating Quiescent Current
* 2. Shutdown Current
* 3. Temperature dependent characteristics
* 4. Ground pins have been tied to 0V internally and hence model does not support Inverting
*    topologies.
*
* C. Application Notes
* 1. The parameter STEADY_STATE has been used to reach the steady state faster.
*    Keep STEADY_STATE = 0 to observe startup behaviour
*    keep STEADY_STATE = 1 IOUT= load current value and appropriate IC on capacitor to observe
*    for faster Steady state.
*
*****
```