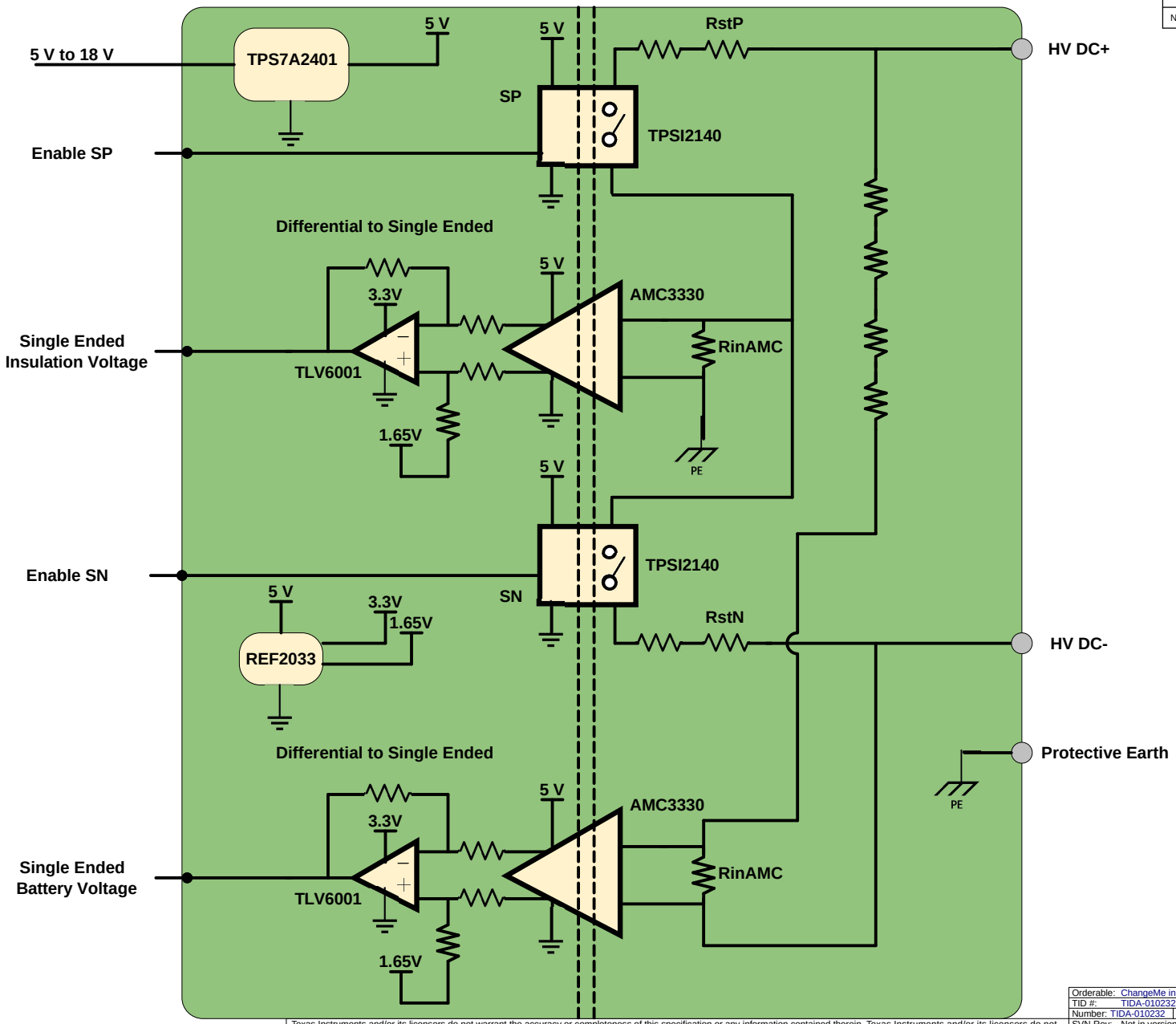
Revision History				
Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A

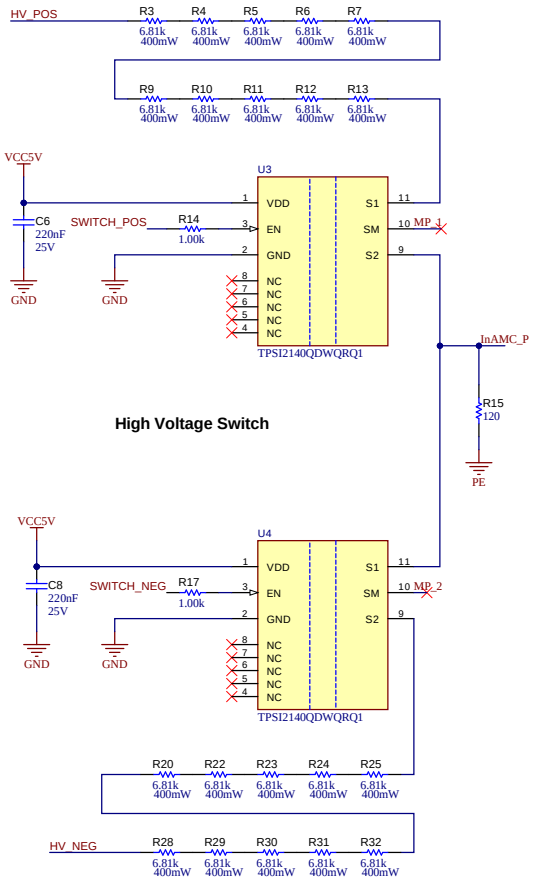


Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 29-Mar-22
TID #: TIDA-010232	Project Title: RisoMonitoringAFE_BoosterPack	
Number: TIDA-010232 Rev: E2	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 1 of 5
Drawn By:	File: TIDA-010232_BlockDiagram.SchDoc	Size: B
Engineer: Juan Aparicio	Contact: http://www.ti.com/support	

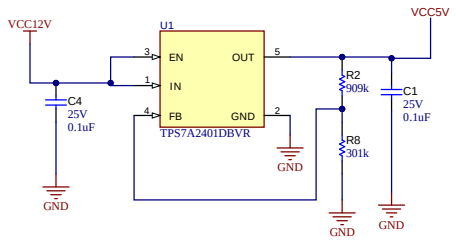
Texas Instruments
http://www.ti.com

Riso Monitoring Through Isolated Bridge Switch

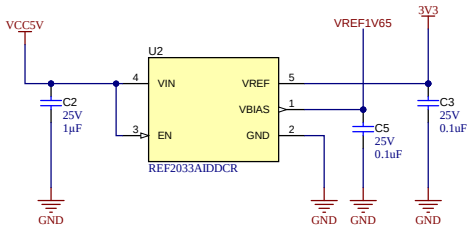
HV Switch



High Voltage Switch



Linear Power Conversion

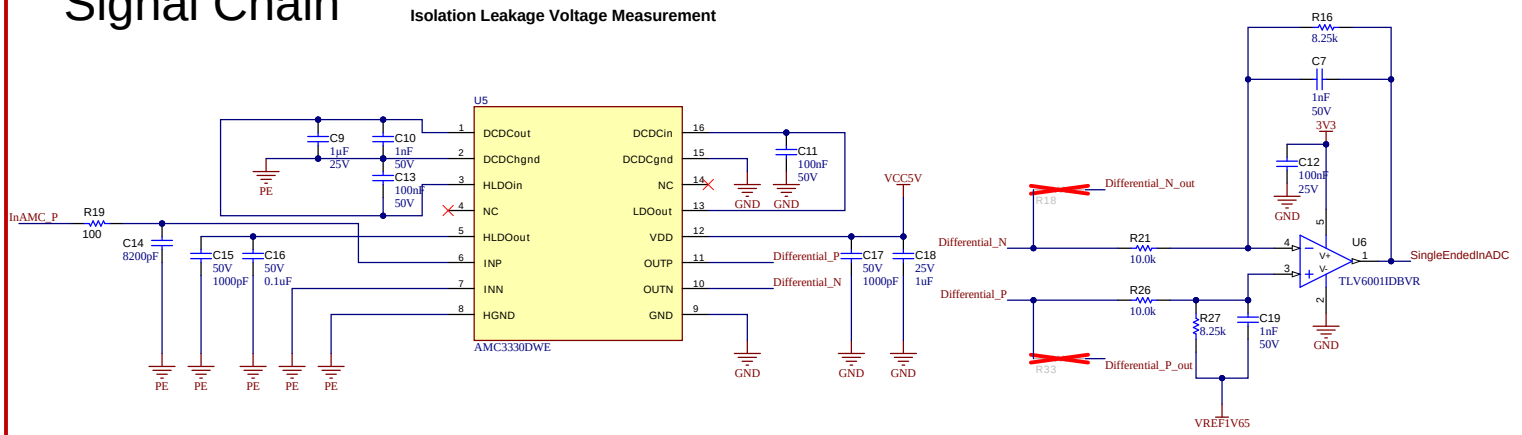


Voltage Reference - Common Mode

Power & Voltage Reference

Signal Chain

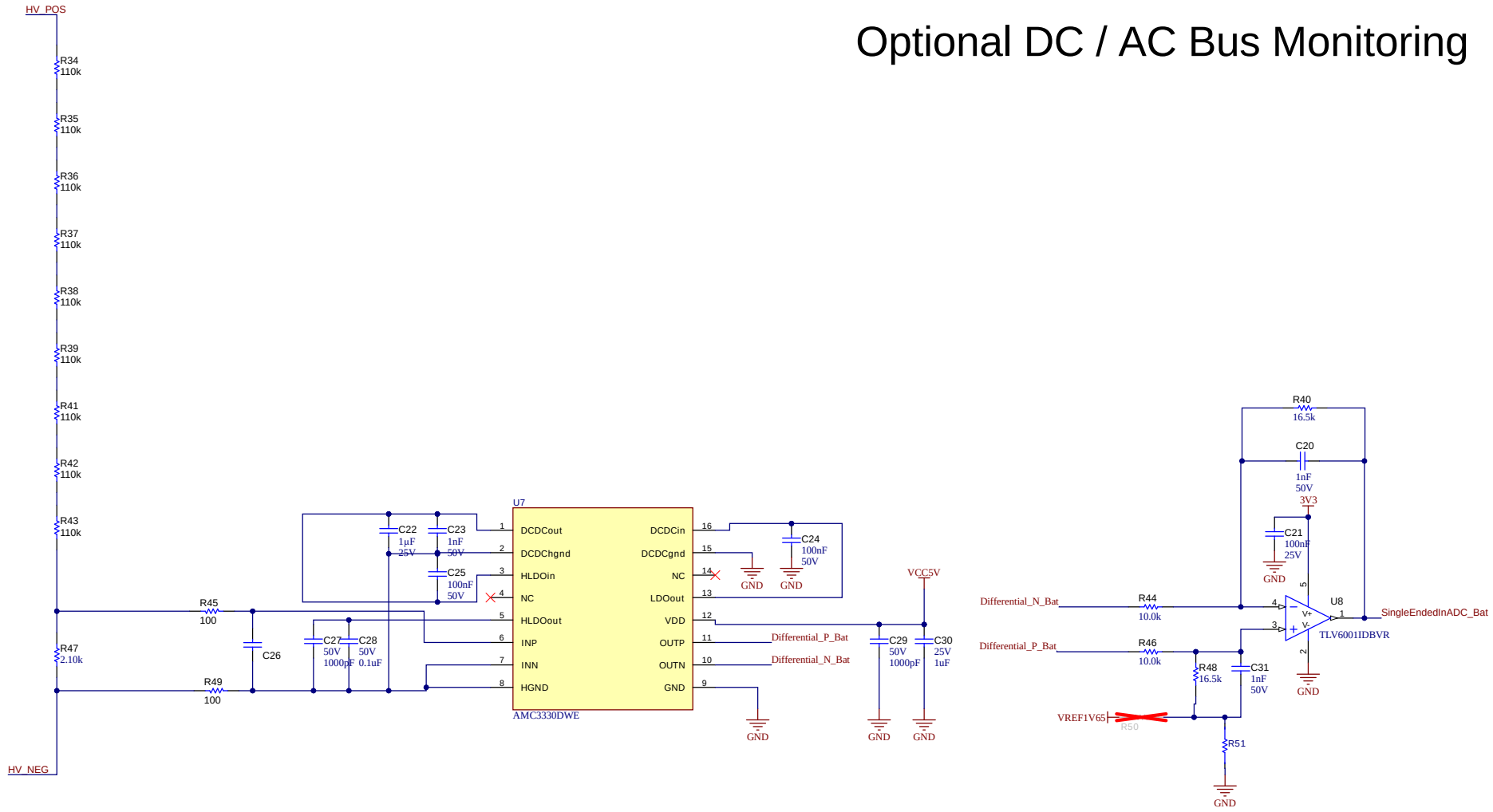
Isolation Leakage Voltage Measurement



Orderable: ChangeMe in variant		Designed for: Public Release	Mod. Date: 30-Mar-22
TID #: TIDA-010232		Project Title: RisoMonitoringAFE_BoosterPack	
Number: TIDA-010232	Rev: E2	Sheet Title:	
SVN Rev: Not in version control		Assembly Variant: 001	Sheet: 2 of 5
Drawn By:		File: TIDA-010232-InsulationMeasurement.SchDoc	Size: B
Engineer: Juan Aparicio		Contact: http://www.ti.com/support	

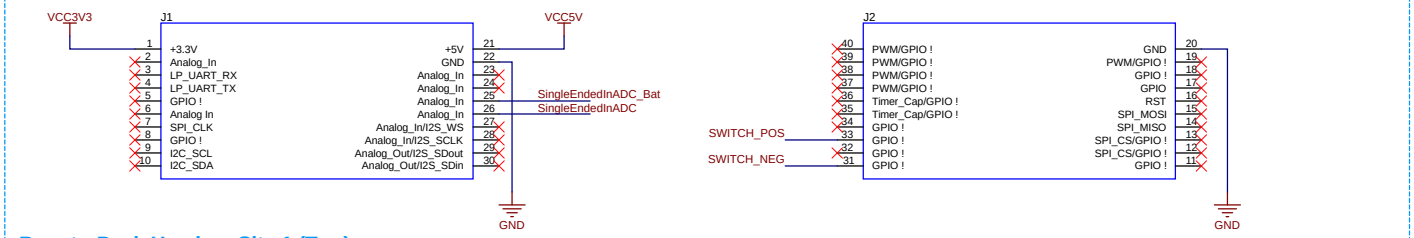
Texas Instruments
http://www.ti.com

Optional DC / AC Bus Monitoring

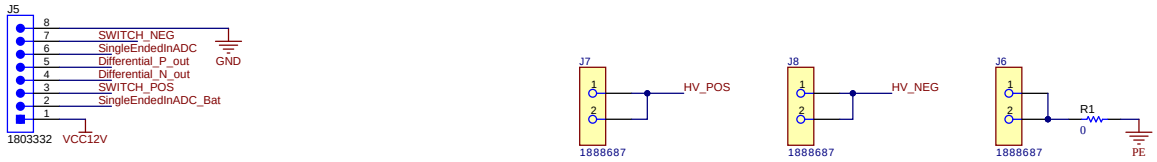


Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 31-Mar-22
TID #: TIDA-010232	Project Title: RisoMonitoringAFE_BoosterPack	
Number: TIDA-010232 Rev: E2	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 3 of 5
Drawn By:	File: TIDA-010232-DCBusMeasurement.SchDoc	Size: A3
Engineer: Juan Aparicio	Contact: http://www.ti.com/support	

Texas Instruments
http://www.ti.com



BoosterPack Headers Site 1 (Top)



External Connections

Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 29-Mar-22	
TID #: TIDA-010232	Project Title: RisoMonitoringAFE_BoosterPack		
Number: TIDA-010232 Rev: E2	Sheet Title:		
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 4 of 5	
Drawn By:	File: TIDA-010232-Connectors.SchDoc	Size: B	
Engineer: Juan Aparicio	Contact: http://www.ti.com/support		

Texas Instruments
http://www.ti.com

LOGO1

DANGER HIGH VOLTAGE

CE Mark

PCB
LOGO
WEEE logo

PCB
LOGO
FCC disclaimer

FID1 FID2 FID3 FID4 FID5 FID6

PCB Number: TIDA-010232
PCB Rev: E2

Variant/Label Table	
Variant	Label Text
001	400V

LBL1
PCB Label
THT-14-423-10
Size: 0.65" x 0.20 "

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Orderable: ChangeMe in variant		Designed for: Public Release	Mod. Date: 31-Mar-22
TID #: TIDA-010232		Project Title: RisoMonitoringAFE_BoosterPack	
Number: TIDA-010232	Rev: E2	Sheet Title:	
SVN Rev: Not in version control		Assembly Variant: 001	Sheet: 5 of 5
Drawn By:		File: TIDA-010232-Hardware.SchDoc	Size: B
Engineer: Juan Aparicio		Contact: http://www.ti.com/support	

Texas Instruments
http://www.ti.com