Revision History

Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A

Orderable: N/A	Designed for: Public Release	Mod. Date: 12/16/2016
TID #: TIDA-00847_AC_DC	Project Title: AC/DC BINARY INPUT	
Number: TIDA-00847_AC_DC_E1	Sheet Title: Block Diagram	
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 1 of 5
Drawn By: Sreenivasa	File: TIDA-00847_AC_DC-E1_Pg 1 Cover Sheet.SchDoc	Size: B
Engineer: Sreenivasa	Contact: http://www.ti.com/support	

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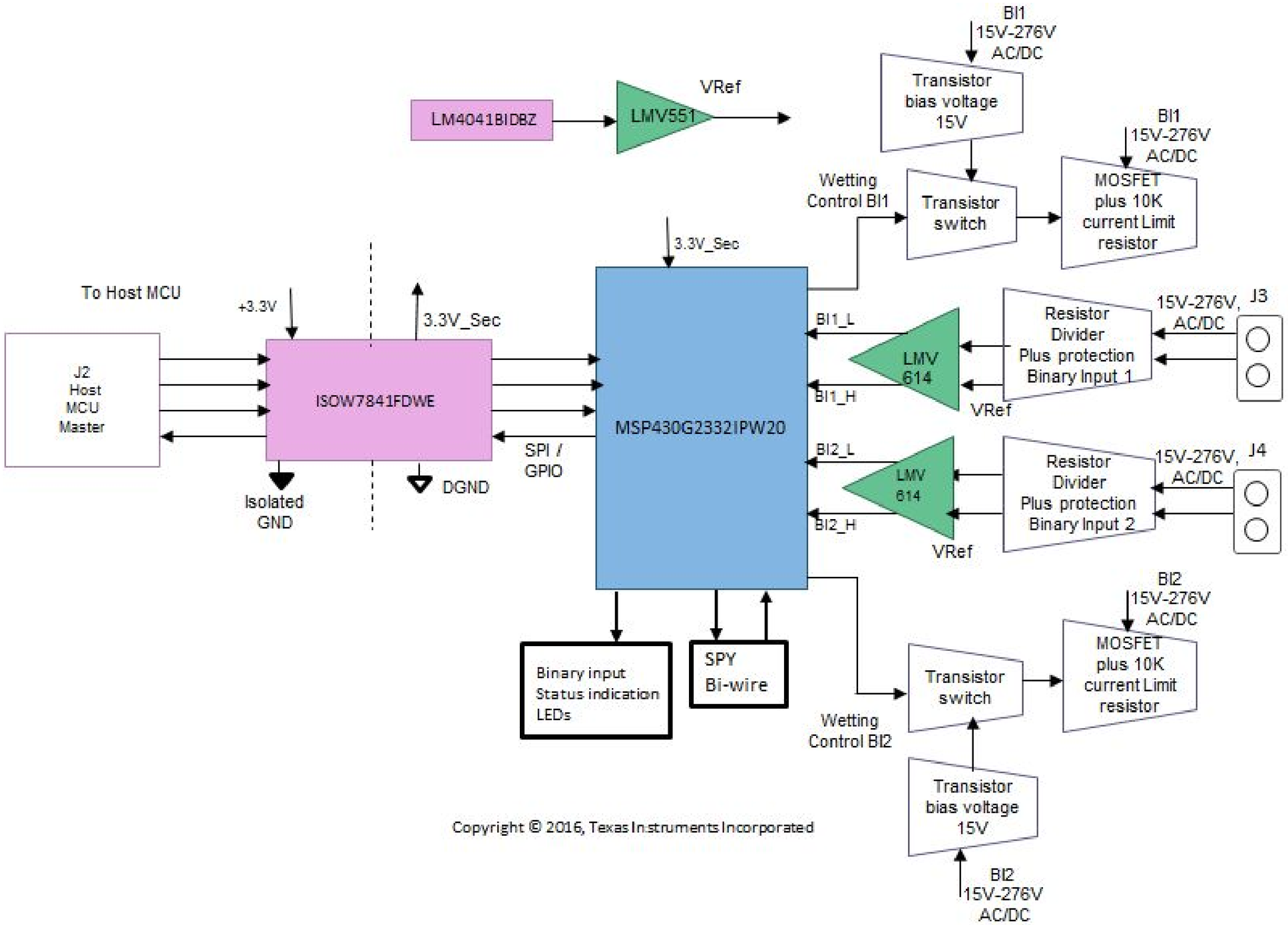
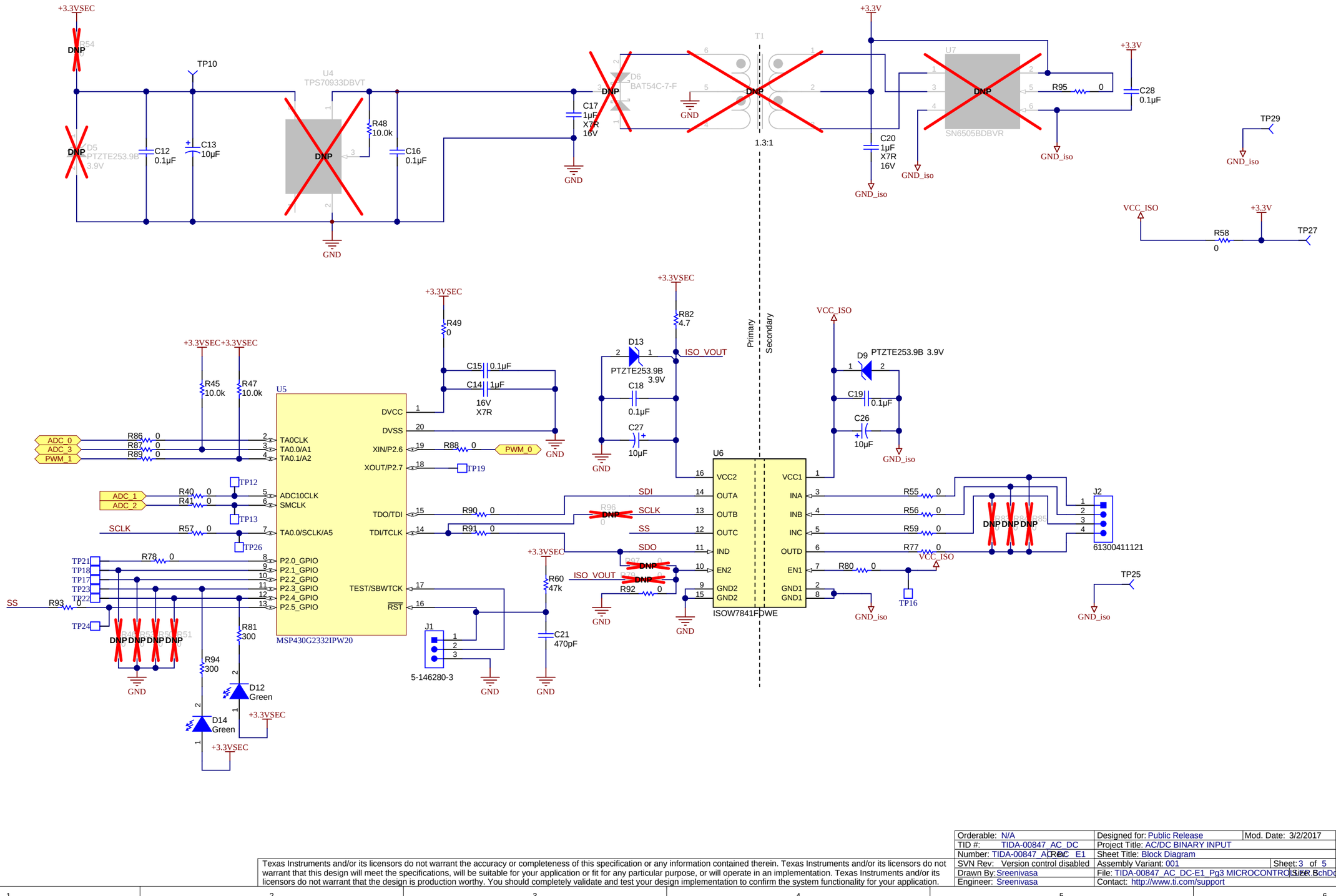
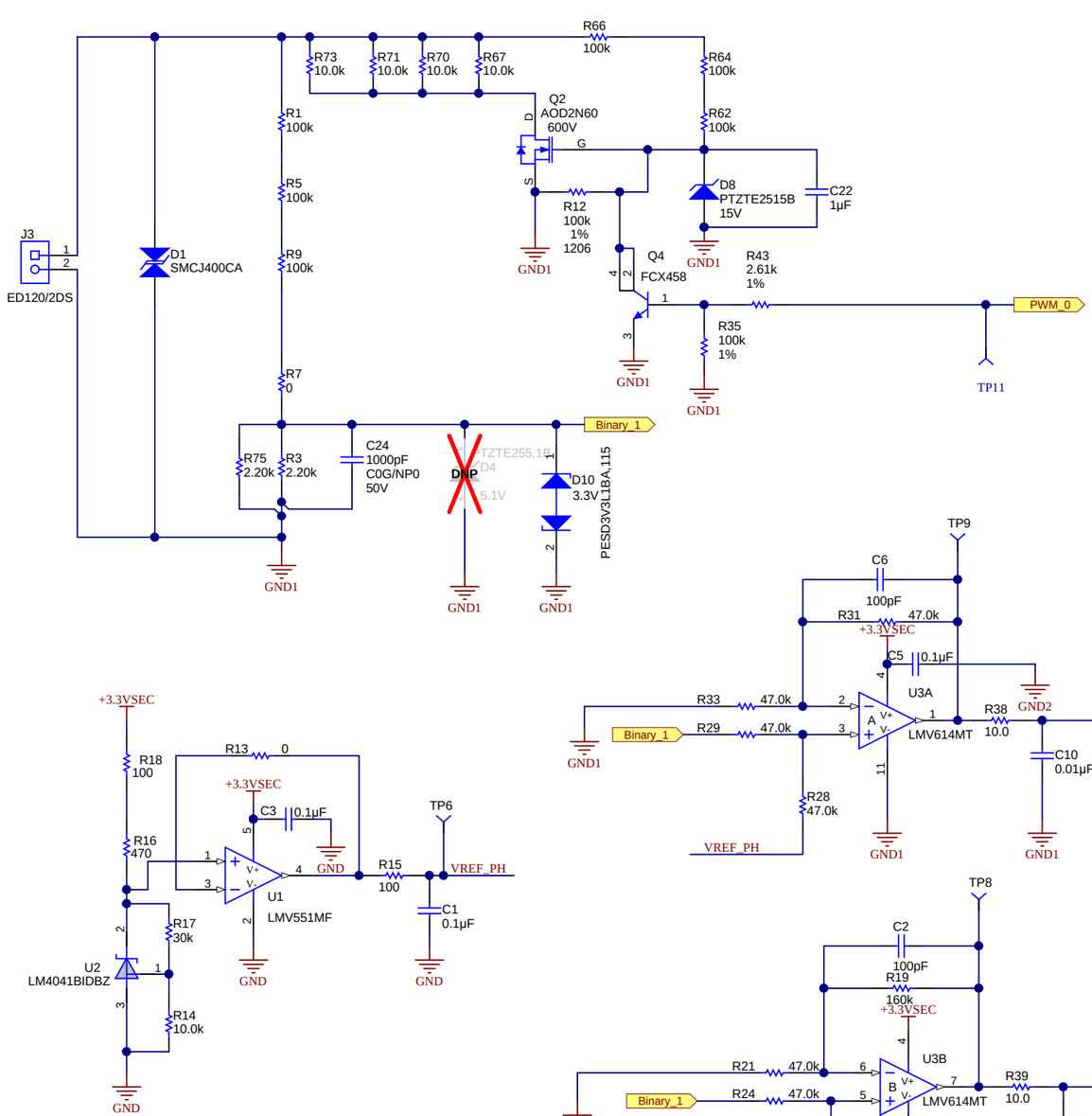


Orderable: N/A	Designed for: Public Release	Mod. Date: 3/1/2017
TID #: TIDA-00847_AC_DC	Project Title: AC/DC BINARY INPUT	
Number: TIDA-00847_AC_DC_E1 Rev C	Sheet Title: Block Diagram	
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 2 of 5
Drawn By: Sreenivasa	File: TIDA-00847_AC_DC-E1_Pg2 Block Diagram.SchDoc	Size: B
Engineer: Sreenivasa	Contact: http://www.ti.com/support	



Orderable: N/A	Designed for: Public Release	Mod. Date: 3/2/2017
TID #: TIDA-00847_AC_DC	Project Title: AC/DC BINARY INPUT	
Number: TIDA-00847_AC_DC_E1	Sheet Title: Block Diagram	
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 3 of 5
Drawn By: Sreenivasa	File: TIDA-00847_AC_DC-E1_Pg3 MICROCONTROLLER.SchDoc	Size: B
Engineer: Sreenivasa	Contact: http://www.ti.com/support	http://www.ti.com



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Orderable: N/A	Designed for: Public Release	Mod. Date: 3/2/2017
TID #: TIDA-00847_AC_DC	Project Title: AC/DC BINARY INPUT	
Number: TIDA-00847_AC_DC_E1	Sheet Title: Block Diagram	
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 4 of 5
Drawn By: Sreenivasa	File: TIDA-00847_AC_DC-E1_Pg 4 BINARY INPUTS.SchDoc	Size: D
Engineer: Sreenivasa	Contact: http://www.ti.com/support	

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DNP FID1 DNP FID3 DNP FID2

DNP FID5 DNP FID6 DNP FID4

Label Table	
Variant	Label Text
001	ChangeMe!
002	ChangeMe!

PCB Number: TIDA-00847_AC_DC
PCB Rev: E1

PCB LOGO
Pb_Free

PCB LOGO
Pb-Free Symbol

PCB LOGO
Texas Instruments

LBL1
PCB Label
Size: 0.65" x 0.20 "

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Orderable: N/A	Designed for: Public Release	Mod. Date: 12/15/2016
TID #: TIDA-00847_AC_DC	Project Title: AC/DC BINARY INPUT	
Number: TIDA-00847_AC_DC_E1	Sheet Title: Block Diagram	
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 5 of 5
Drawn By: Sreenivasa	File: TIDA-00847_AC_DC-E1_Pg 5 Hardware.SchDoc	Size: B
Engineer: Sreenivasa	Contact: http://www.ti.com/support	

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