

TAPE AND REEL INFORMATION

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
SN65LVDS050DR	SOIC	D	16	2500	330.0	16.4	6.5	10.3	2.1	8.0	16.0	Q1
SN65LVDS050PWR	TSSOP	PW	16	2000	330.0	12.4	6.9	5.6	1.6	8.0	12.0	Q1
SN65LVDS051DR	SOIC	D	16	2500	330.0	16.4	6.5	10.3	2.1	8.0	16.0	Q1
SN65LVDS051PWR	TSSOP	PW	16	2000	330.0	12.4	6.9	5.6	1.6	8.0	12.0	Q1
SN65LVDS179DGKR	VSSOP	DGK	8	2500	330.0	12.4	5.3	3.4	1.4	8.0	12.0	Q1
SN65LVDS179DR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
SN65LVDS179DR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
SN65LVDS180DR	SOIC	D	14	2500	330.0	16.4	6.5	9.0	2.1	8.0	16.0	Q1
SN65LVDS180PWR	TSSOP	PW	14	2000	330.0	12.4	6.9	5.6	1.6	8.0	12.0	Q1

TAPE AND REEL BOX DIMENSIONS


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
SN65LVDS050DR	SOIC	D	16	2500	350.0	350.0	43.0
SN65LVDS050PWR	TSSOP	PW	16	2000	350.0	350.0	43.0
SN65LVDS051DR	SOIC	D	16	2500	350.0	350.0	43.0
SN65LVDS051PWR	TSSOP	PW	16	2000	350.0	350.0	43.0
SN65LVDS179DGKR	VSSOP	DGK	8	2500	358.0	335.0	35.0
SN65LVDS179DR	SOIC	D	8	2500	353.0	353.0	32.0
SN65LVDS179DR	SOIC	D	8	2500	350.0	350.0	43.0
SN65LVDS180DR	SOIC	D	14	2500	350.0	350.0	43.0
SN65LVDS180PWR	TSSOP	PW	14	2000	350.0	350.0	43.0

TUBE


*All dimensions are nominal

Device	Package Name	Package Type	Pins	SPQ	L (mm)	W (mm)	T (μm)	B (mm)
SN65LVDS050D	D	SOIC	16	40	505.46	6.76	3810	4
SN65LVDS050DG4	D	SOIC	16	40	505.46	6.76	3810	4
SN65LVDS050PW	PW	TSSOP	16	90	530	10.2	3600	3.5
SN65LVDS051D	D	SOIC	16	40	505.46	6.76	3810	4
SN65LVDS051PW	PW	TSSOP	16	90	530	10.2	3600	3.5
SN65LVDS179D	D	SOIC	8	75	505.46	6.76	3810	4
SN65LVDS179D	D	SOIC	8	75	507	8	3940	4.32
SN65LVDS180D	D	SOIC	14	50	505.46	6.76	3810	4
SN65LVDS180DG4	D	SOIC	14	50	505.46	6.76	3810	4
SN65LVDS180PW	PW	TSSOP	14	90	530	10.2	3600	3.5