# *Radiation Report Single-Event Effects Test Report of the TPS7H4010-SEP Synchronous Step-Down Converter*



### **1 Abstract**

The purpose of this study is to characterize the single-event-effects (SEE) performance due to heavy-ion irradiation of the TPS7H4010-SEP. SEE performance was verified at two common output voltage rails of 1.8V and 3.3V. Heavy-ions ranging from an LET $_{\sf EFF}$  of 1.33 to 43 MeV $\cdot$ cm $^2$ /mg were used to irradiate 10 production devices. Flux of ≈10<sup>4</sup> and 10<sup>5</sup> ions/cm<sup>2</sup> ·s and fluences of ≈10<sup>6</sup> and 10<sup>7</sup> ions/cm<sup>2</sup> per run were used for the characterization. The results demonstrated that the TPS7H4010-SEP is SEL and SEB/SEGR-free up to 43 MeV·cm<sup>2</sup>/mg, at T = 125°C and T = 10°C, respectively, and across the full electrical specifications. SET and SEFI performance for output voltage excursions ≥ |3%| from the nominal voltage and PGOOD ≤ VOUT/2 are presented and discussed.

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# <span id="page-2-0"></span>**2 Introduction**

The TPS7H4010-SEP is a space-enhanced-plastic, 3.5V to 32V input, 6A, synchronous step-down voltage converter. The device provides exceptional efficiency and output accuracy in a very small solution size. Peak current-mode control is employed. Additional features such as adjustable switching frequency, synchronization to an external clock, FPWM option, power-good flag, precision enable, adjustable soft start, and tracking provide both flexible and easy-to-use solutions for a wide range of applications.

The TPS7H4010-SEP requires few external components and has a pinout designed for simple PCB layout with optimal EMI and thermal performance. Protection features include thermal shutdown, input undervoltage lockout, cycle-by-cycle current limiting, and hiccup short-circuit protection. The device is offered in a 30-pin WQFN plastic package. General device information and test conditions are listed in Table 2-1. For more detailed technical specifications, user-guides, and application notes please go to [TPS7H4010-SEP product page.](https://www.ti.com/product/TPS7H4010-SEP)





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# **3 Single-Event Effects (SEE)**

The primary concern for the TPS7H4010-SEP is the robustness against destructive single-event effects (DSEE): single-event latch-up (SEL), single-event burnout (SEB), and single-event gate rupture (SEGR). In mixed technologies such as the BiCMOS process used on the TPS7H4010-SEP, the CMOS circuitry introduces a potential for SEL susceptibility.

SEL can occur if excess current injection caused by the passage of an energetic ion is high enough to trigger the formation of a parasitic cross-coupled PNP and NPN bipolar structure (formed between the p-sub and n-well and n+ and p+ contacts) [[1](#page-22-0),[2\]](#page-22-0). The parasitic bipolar structure initiated by a single-event creates a high-conductance path (inducing a steady-state current that is typically orders-of-magnitude higher than the normal operating current) between power and ground that persists (is "latched") until power is removed, the device is reset, or until the device is destroyed by the high-current state. The TPS7H4010-SEP was tested for SEL at the maximum recommended voltage of 32 V, maximum load current of 6 A, and  $V_{\text{OUT}}$  of 3.3 and 1.8 V. The device exhibited no SEL with heavy-ions with LET<sub>EFF</sub> = 43 MeV·cm<sup>2</sup>/mg at flux ≈10<sup>5</sup> ions/cm<sup>2.</sup>s, fluences of ≈10<sup>7</sup> ions/cm<sup>2</sup>, and a die temperature of 125°C.

Since this device is designed to conduct large currents (up to 6 A) and withstand up to 32 V during the off-state, the power LDMOS introduces a potential susceptibility for SEB and SEGR [[2](#page-22-0)]. The TPS7H4010-SEP was evaluated for SEB/SEGR at full load conditions of 6 A, and a maximum voltage of 32 V in both the enabled and disabled modes. Because it has been shown that MOSFET susceptibility to burnout decreases with temperature [\[2](#page-22-0)], the device was evaluated while operating under sub-ambienttemperatures. The devices were cooled-down (or "chilled") by using VORTEC tube (model 611). During the SEB/SEGR testing, not a single current event was observed, demonstrating that the TPS7H4010-SEP is SEB/SEGR-free up to LET $_{\sf EFF}$  = 43 MeV·cm<sup>2</sup>/mg at a flux of ≈10<sup>5</sup> ions/cm<sup>2.</sup>s, fluences of ≈10<sup>7</sup> ions/cm<sup>2</sup>, and a die temperature of ≈10°C.

The TPS7H4010-SEP was characterized for SET and SEFIs at flux of ≈10<sup>4</sup> ions/cm<sup>2.</sup>s, fluences of 3 × 10<sup>6</sup> ions/cm<sup>2</sup>, at room temperature. The device was characterized at P<sub>VIN</sub> = 12 V to V<sub>OUT</sub> = 3.3 V and P<sub>VIN</sub> = 5 V to  $V_{OUT}$  = 1.8 V at full load of 6-A. Under these conditions the device showed 2 different single-event transients (SET) signatures and 1 single-event functional interrupt (SEFI) under heavy-ion irradiation. All observed types of SETs were self-recoverable without the need of external intervention. The observed transients can be classified as:

- 1. A brief transient of the output voltage (refer here as  $VOUT<sub>SET</sub>$ ). For the purpose of this report the transients were characterized for deviations –3%  $\leq$  V<sub>OUT</sub>  $\leq$  3% from the nominal output voltage of 1.8 V (±54 mV) and 3.3 V (±99 mV). The upsets typically have duration of 30 μs and peak normalized deviation of 4.3% from the nominal voltage. For more details please refer to [Section 9](#page-15-0).
- 2. A soft-start power re-cycle which results in the  $V_{\text{OUT}}$  dropping to zero volts and characterized by a long recovery time governed by the soft start (SS) capacitor. This kind of SEFI is referred to here as  $SS_{SEFI}$ .
- 3. A PGOOD upset ≥ 10% from the nominal output voltage.

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# **4 Device and Test Board Information**

The TPS7H4010-SEP is packaged in a 30-pin WQFN plastic package as shown in Figure 4-1. The TPS7H4010EVM evaluation board was used to evaluate the performance and characteristics of the TPS7H4010-SEP under heavy-ions. [Figure 4-2](#page-5-0) shows the top view of the evaluation board used for the radiation testing. [Figure 4-3](#page-6-0) and [Figure 4-4](#page-6-0) shows the EVM board schematics for the  $V_{OUT}$  = 3.3 V and  $V_{OUT}$  = 1.8 V used for the heavy-ion testing campaign. See the [TPS7H4010-SP Evaluation Module user's guide](https://www.ti.com/lit/pdf/snvu744) for more information about the evaluation board.

The package was delidded to reveal the die face for all heavy-ion testing.



**Figure 4-1. Photograph of Delidded TPS7H4010-SEP [Left] and Pinout Diagram [Right]**



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**Figure 4-2. TPS7H4010-SEP Board Top View**

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Figure 4-4. TPS7H4010EVM Schematic for V<sub>OUT</sub> = 1.8 V

# <span id="page-7-0"></span>**5 Irradiation Facility and Setup**

The heavy-ion species used for the SEE studies on this product were provided and delivered by the TAMU Cyclotron Radiation Effects Facility using a superconducting cyclotron and an advanced electron cyclotron resonance (ECR) ion source. At the fluxes used, ion beams had good flux stability and high irradiation uniformity over a 1-in diameter circular cross-sectional area for the in-air station. Uniformity is achieved by magnetic defocusing. The flux of the beam is regulated over a broad range spanning several orders of magnitude. For the bulk of these studies, ion flux of 10<sup>4</sup> and 10<sup>5</sup> ions/cm<sup>2.</sup>s were used to provide heavy-ion fluences of ≈ 3 × 10<sup>6</sup> and 10<sup>7</sup> ions/cm<sup>2</sup>.

For the experiments conducted in this report the following ions and corresponding angles were used in order to provide a range of LET <sub>EFF</sub> of 1.33 to 43 MeV·cm2 /mg.

- Krypton ( $^{84}$ Kr) at 44.3° for an LET<sub>EFF</sub> = 43 MeV
	- $-$  Range<sub>EFF</sub> = 77.7 µm
	- Total Kinetic Energy = 2.081 GeV (25 MeV/nucleon)
- Krypton ( $^{84}$ Kr) at 42.7° for an LET<sub>EFF</sub> = 43.1 MeV
	- $-$  Range<sub>EFF</sub> = 73.6 µm
	- Total Kinetic Energy = 1.259 GeV (15 MeV/nucleon)
- Krypton (<sup>84</sup>Kr) at 0° for an LET<sub>EFF</sub> = 29.7 MeV
	- $-$  Range<sub>EFF</sub> = 118.2 µm
- Total Kinetic Energy = 1.259 GeV (15 MeV/nucleon)
- Copper ( ${}^{63}$ Cu) at 41.6° for an LET $_{\sf EFF}$  = 28.1 MeV
	- $-$  Range<sub>FFF</sub> = 80.1 µm
	- Total Kinetic Energy = 0.944 GeV (15 MeV/nucleon)
- Copper (<sup>63</sup>Cu) at 0° for an LET<sub>EFF</sub> = 19.7 MeV
	- $-$  Range<sub>EFF</sub> = 124.1 µm
- Total Kinetic Energy = 0.944 GeV (15 MeV/nucleon)
- Argon (<sup>40</sup>Ar) at 42.3° for an LET<sub>EFF</sub> = 12 MeV
	- $-$  Range<sub>EFF</sub> = 120.8 µm
	- Total Kinetic Energy = 0.599 GeV (15 MeV/nucleon)
- Argon ( $40$ Ar) at 0° for an LET $_{\sf EFF}$  = 8.44 MeV
	- $-$  Range<sub>FFF</sub> = 180.9 µm
- Total Kinetic Energy = 0.599 GeV (15 MeV/nucleon)
- Neon  $(^{20}$ Ne) at 43.1° for an LET<sub>EFF</sub> = 3.89 MeV
	- Range $_{EFF}$  = 177 µm
	- Total Kinetic Energy = 0.300 GeV (15 MeV/nucleon)
- Neon (<sup>20</sup>Ne) at 0° for an LET<sub>EFF</sub> = 2.74 MeV
	- $-$  Range<sub>EFF</sub> = 261.3 µm
	- Total Kinetic Energy = 0.300 GeV (15 MeV/nucleon)
- Nitrogen ( $14N$ ) at 0° for an LET<sub>EFF</sub> = 1.33 MeV
	- $-$  Range<sub>EFF</sub> = 371.8 µm
	- Total Kinetic Energy = 0.210 GeV (15 MeV/nucleon)

Ion uniformity for these experiments was between 88 and 98%.

[Figure 5-1](#page-8-0) shows the TPS7H4010-SEP test board used for the experiments at the TAMU facility. Although not visible in this photo, the beam port has a 1-mil Aramica window to allow in-air testing while maintaining the vacuum within the accelerator with only minor ion energy loss. All through-hole test points were soldered backwards for easy access of the signals while having enough room to change the angle of incidence and maintaining the 40-mm distance to the die. The in-air gap between the device and the ion beam port window was maintained at 40 mm for all runs.

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**Figure 5-1. Photograph of the TPS7H4010-SEP Evaluation Board Mounted in Front of the Heavy-Ion Beam Exit Port at the Texas A&M Cyclotron**

### <span id="page-9-0"></span>**6 Depth, Range, and LETEFF Calculation**



#### **Figure 6-1. Generalized Cross-Section of the LBC8 Technology BEOL Stack on the TPS7H4010-SEP [Left] and SEUSS 2020 Application Used to Determine Key Ion Parameters [Right]**

The TPS7H4010-SEP is fabricated in the TI Linear BiCMOS 180-nm process with a back-end-of-line (BEOL) stack consisting of 4 levels of standard thickness aluminum metal on a 0.6-μm pitch. The total stack height from the surface of the passivation to the silicon surface is 5.46 μm based on nominal layer thickness as shown in Figure 6-1. Accounting for energy loss through the 1-mil thick Aramica beam port window, the 40-mm air gap, and the BEOL stack over the TPS7H4010-SEP, the effective LET (LET<sub>EFF</sub>) at the surface of the silicon substrate, the depth, and the ion range was determined with the SEUSS 2020 Software (provided by the Texas A&M Cyclotron Institute and based on the latest SRIM-2013 [[7](#page-22-0)] models). The results are shown in Table 6-1. The LET<sub>FFF</sub> vs range for the <sup>84</sup>Kr heavy-ion is shown on [Figure 6-2.](#page-10-0) The stack was modeled as a homogeneous layer of silicon dioxide (valid since  $SiO<sub>2</sub>$  and aluminum density are similar).



#### **Table 6-1. Krypton Ion LET**<sub>EFF</sub>, Depth, and Range in Silicon

<span id="page-10-0"></span>



Figure 6-2. LET<sub>EFF</sub> vs Range for <sup>84</sup>Kr at the Conditions Used for the SEE Test Campaign

<span id="page-11-0"></span>

# **7 Test Setup and Procedures**

SEE testing was performed on a TPS7H4010-SEP device mounted on a TPS7H4010EVM. The device power was provided using the J19 (PVIN) and (GND) inputs with the N6766A PS Module mounted on a N6705 precision power supply in a 4-wire configuration. Discrete power resistors of 0.3 Ω (VOUT = 1.8 V) and 0.55 Ω (VOUT = 3.3 V) were used to load the device to 6 A for the SEE testing campaign. The device was tested under Auto Mode for all runs by tying the SYNC to GND using the J7 jumper.

For SEL, SEB, and SEGR testing, the device was powered up to the maximum recommended operating voltage of 32 V and loaded with the maximum load of 6 A. Two commonly used output voltages, 3.3 and 1.8 V, were used for the SEE testing campaign. For the SEB/SEGR characterization, the device was tested under enabled and disabled modes. The device was disabled by using jumper J6, connecting EN to GND. The discrete load resistor was connected even when the device was disabled to help differentiate if an SET momentarily activated the device under the heavy-ion irradiation. During the SEB/SEGR testing with the device in disabled mode, not a single  $V_{\text{OUT}}$  transient or input current event was observed.

For the SET characterization, the device was powered up to 12 V for the 3.3-V V<sub>OUT</sub> case and 5 V for the 1.8-V case. The SET events were monitored using two National Instruments (NI) PXIe-5172 scope cards. One scope was used to monitor and trigger from  $V_{\text{OUT}}$ , using a window trigger around  $\pm 3\%$  from the nominal output voltage. The second scope was used to monitor and trigger from the PGOOD at 1.5 V (for the V<sub>OUT</sub> = 1.8 V) and 3 V (for the  $V_{\text{OUT}}$  = 3.3 V), using a edge/negative trigger. Both scopes were mounted on a NI PXIe-1095 chassis.

All equipment was controlled and monitored using a custom-developed LabVIEW program (PXI-RadTest) running on a HP-Z4 desktop computer. The computer communicates with the PXI chassis via an MXI controller and NI PXIe-8381 remote control module.

[Figure 7-1](#page-12-0) shows a block diagram of the setup used for SEE testing of the TPS7H4010-SEP. Table 7-1 shows the connections, limits, and compliance values used during the testing. A die temperature of 125°C was used for SEL and was achieved with the use of a convection heat gun aimed at the die. For the SEB/SEGR testing, the device was cooled-down to ≈10°C using a VORTEC tube (model: 611), aimed at the die. For SET testing, the device was tested at room temperature (no cooling or heating was applied to the DUT). The die temperature was monitored during all the testing using a T-Type thermocouple attached to the thermal pad vias (on the bottom side of the EVM) with thermal paste. The thermocouple was held in place by using high temperature tape (kapton-tape). Die-to-thermocouple temperature was verified using a IR-camera prior to the SEE test campaign.





All boards used for SEE testing were fully checked for functionality. Dry runs were also performed to ensure that the test system was stable under all bias and load conditions prior to being taken to the TAMU facility. During the heavy-ion testing, the LabVIEW control program powered up the TPS7H4010-SEP device and set the external sourcing and monitoring functions of the external equipment. After functionality and stability was confirmed, the beam shutter was opened to expose the device to the heavy-ion beam. The shutter remained open until the target fluence was achieved (determined by external detectors and counters). During irradiation, the NI scope cards continuously monitored the signals. When the output voltage exceeded the pre-defined 3% window trigger, or when the PG signal changed from High to Low (using a negative edge trigger), a data capture was initiated. In addition to monitoring the voltage levels of the two scopes, VIN current and the +5-V signal from TAMU were

<span id="page-12-0"></span>monitored at all times. No sudden increases in current were observed (outside of normal fluctuations) on any of the test runs and indicated that no SEL or SEB/SEGR events occurred during any of the tests.

The lumped components in the block diagram represent the EVM set for  $V_{OUT}$  = 1.8 V. For details on the EVM components regarding  $V_{\text{OUT}}$  = 3.3 V, please refer to [Figure 4-3.](#page-6-0)







# <span id="page-13-0"></span>**8 Destructive Single-Event Effects (DSEE)**

## **8.1 Single-Event Latch-up (SEL) Results**

During SEL characterization, the device was heated using forced hot air, maintaining the DUT temperature at 125°C. The die temperature was monitored during testing using a T-Type thermocouple attached to the thermal pad vias (on the bottom side of the EVM) with thermal paste. The thermocouple was held in-place by using high temperature tape (kapton-tape). Die-to-thermocouple temperature was verified using a IR-camera.

The species used for the SEL testing was a Krypton  $(^{84}$ Kr) ion with an angle-of-incidence of 44.38°, degraded by 3-steps of a degrader angle of 47.75° for an LET<sub>EFF</sub> = 43 MeV·cm<sup>2</sup>/mg (for more details refer to <mark>Section</mark> [6](#page-9-0)). The kinetic energy in the vacuum for this ion is 2.081 GeV (25-MeV/amu line). Flux of approximately 10<sup>5</sup> ions/cm<sup>2.</sup>s and a fluence of approximately 10<sup>7</sup> ions/cm<sup>2</sup> were used for the four runs. Run duration to achieve this fluence was approximately 2 minutes. The two devices were powered up and exposed to the heavy-ions using the maximum recommended voltage of 32 V and maximum load of 6 A. No SEL events were observed during all four runs, indicating that the TPS7H4010-SEP is SEL-free. Table 8-1 shows the SEL test conditions and results. Figure 8-1 shows a plot of the current vs time for run # 1.



### **Table 8-1. Summary of TPS7H4010-SEP SEL Test Condition and Results**

Using the MFTF method described in *[Single-Event Effects \(SEE\) Confidence Interval Calculations](https://www.ti.com/lit/pdf/SLVK047)* application [report](https://www.ti.com/lit/pdf/SLVK047) and combining (or summing) the fluences of the four runs @ 125°C (3.99  $\times$  10<sup>7</sup>), the upper-bound cross-section (using a 95% confidence level) is calculated as:

 $\sigma_{\sf SEL}$  ≤ 9.23 × 10<sup>–8</sup> cm<sup>2</sup>/device for LET<sub>EFF</sub> = 43 MeV·cm<sup>2</sup>/mg and T = 125°C.

NOTE: The current spikes are due to the output transients on the output voltage.





### **8.2 Single-Event Burnout (SEB) and Single-Event Gate Rupture (SEGR) Results**

During the SEB/SEGR characterization, the device was cooled-down using a VORTEC tube (model: 611) to ≈10°C. The die temperature was monitored during the testing using a T-Type thermocouple attached to the thermal pad vias (on the bottom side of the EVM) with thermal paste. The thermocouple was held in place by using high temperature tape (kapton-tape). Die-to-thermocouple temperature was verified using a IR-camera.

The species used for the SEB/SEGR testing was a Krypton  $(^{84}Kr)$  ion with an angle-of-incidence of 44.38°, degraded by 3-steps with a degrader angle of 47.75°, for an LET $_{\sf EFF}$  = 43 MeV·cm<sup>2</sup>/mg (for more details refer to [Section 6](#page-9-0)). The kinetic energy in the vacuum for this ion is 2.081 GeV (25-MeV/amu line). Flux of approximately 10<sup>5</sup> ions/cm<sup>2</sup>·s and a fluence of approximately 10<sup>7</sup> ions/cm<sup>2</sup> were used for the four runs. Run duration to achieve this fluence was approximately 2 minutes. The two devices were powered up using the recommended maximum voltage of 32 V and the maximum load of 6 A. The TPS7H4010-SEP was tested under enabled and disabled modes, the device was disabled by using jumper J6 connecting EN to GND. The discrete load resistor was connected, even when the device was disabled, to help differentiate if an SET momentarily activated the device under the heavy-ion irradiation. During SEB/SEGR testing with the device "disabled," no  $V_{OUT}$  transient or input

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current events were observed. No SEB/SEGR events were observed during all four runs, indicating that the TPS7H4010-SEP is SEB/SEGR-free up to LET $_{\sf EFF}$  = 43 MeV $\cdot$ cm<sup>2</sup>/mg and across the full electrical specifications. Table 8-2 shows the SEB test conditions and results. Figure 8-2 shows a plot of the current vs time for run # 5 (Enabled) and Figure 8-3 for run # 6 (Disabled).



#### **Table 8-2. Summary of TPS7H4010-SEP SEB Test Condition and Results**

Using the MFTF method described in *[Single-Event Effects \(SEE\) Confidence Interval Calculations](https://www.ti.com/lit/pdf/SLVK047)* application [report](https://www.ti.com/lit/pdf/SLVK047) and combining (or summing) the fluences of the four runs @ 10°C (3.06  $\times$  10<sup>7</sup>), the upper-bound cross-section (using a 95% confidence level) is calculated as:

 $\sigma_{\rm SEB}$  ≤ 1.21 × 10<sup>–7</sup> cm<sup>2</sup>/device for LET<sub>EFF</sub> = 43 MeV·cm<sup>2</sup>/mg and T = 10°C.

The current spikes are due to the output transients on the output voltage.



#### **Figure 8-2. Current vs Time for Run # 5 (Enabled) for the TPS7H4010-SEP at T = 10°C**



**Figure 8-3. Current vs Time for Run # 6 (Disabled) for the TPS7H4010-SEP at T = 10°C**



# <span id="page-15-0"></span>**9 Single-Event Transients (SET)**

SETs are defined as heavy-ion-induced transients upsets on the  $V_{OUT}$  and the PGOOD flag of the TPS7H4010-SEP. SET testing was performed at room temperature (no external temperature control applied). The species used for the SET testing was  $^{84}$ Kr,  $^{63}$ Cu,  $^{40}$ Ar,  $^{20}$ Ne, and  $^{14}$ N for a range of LET $_{\sf EFF}$  = 1.33 to 43 MeV·cm $^2$ /mg, for more details refer to <mark>Section 6</mark>. Flux of approximately 10<sup>4</sup> ions/cm<sup>2.</sup>s and a fluence of approximately 3 × 10<sup>6</sup> ions/cm<sup>2</sup> were used for the SET runs.

 $V_{\text{OUT}}$  SETs were characterized using a window trigger of ±3% around the nominal output voltage (≈1.8 and 3.3 V). The devices were characterized at P<sub>VIN</sub> = 5 V (for VOUT = 1.8 V) and P<sub>VIN</sub> = 12 V (for V<sub>OUT</sub> = 3.3 V). The output load was set to 6 A for both runs by using discrete power resistors of 0.55 Ω (for V<sub>OUT</sub> = 3.3 V) and 0.3 Ω (for  $V_{\text{OUT}}$  = 1.8 V). To capture the SETs two NI-PXI-5172 scope card were used to continuously monitor the V<sub>OUT</sub> and the PGOOD. Each scope was operated independently from each other. The output voltage was monitored by using the TP1 and the TP15 test points on the EVM, while the PGOOD was monitored using the TP11 and TP25 test points. The scope triggering from PGOOD was also monitoring the output voltage. The PGOOD and  $V_{\text{OUT}}$  were tied together through a 100-kΩ resistor (pull-up).

The scope triggering from  $V_{OUT}$  was programmed to record 80k samples with a sample rate of 10 M samples per second (S/s) in case of an event (trigger). The scope triggering from PGOOD was programmed with 30k samples and 5 MS/s. Both scopes were programmed to record 20% of the data before the trigger occured.

Under heavy-ions, the TPS7H4010-SEP exhibits three transient upsets that were fully recoverable without the need for external intervention. Two distinct signatures were observed on the output voltage.

- 1. A fast transient of both polarities (positive and negative) with typical deviation of ≈4.3% and transient times of ≈30 µs. This transients are referred here as VOUT $_{\rm SET}$ .
- 2. A slow transient that drops to zero volts and recover within the programmed soft start time, refer here as SS<sub>SEFI</sub>.

The  $SS_{SEF1}$  is associated with a disturbance on the soft start circuit. During the  $SS_{SEF1}$  the soft start circuit discharges the SS capacitor (through an internal FET) and restarted the device. Since no external soft start capacitor was used on the EVM, the internal soft start time of ≈6 ms was observed during this kind of  $SS_{\text{SEFI}}$ . PGOOD SETs at trigger levels of 300 mV below the nominal output voltage were observed. Since the PGOOD was pulled-up to the output voltage; with each transient on  $V_{\text{OUT}}$  that droops 300 mV a capture was initiated on the scope triggering from the PGOOD signal. However this was not a real transient on the PGOOD circuit. Since PGOOD circuit is designed to pull-down when the output voltage is not within 10% of the final value, each VOUT<sub>SET</sub> >  $|10\%|$  was also captured on the scope triggering from PGOOD. All the data that was captured on this scope was processed to eliminate the intrinsic electronic behavior and the coupling with the output voltage. Those SETs that were true transients on the PGOOD circuitry were compared to half the nominal output voltage. This is valid since PGOOD is typically connected to digital circuits with  $V_{IL}$  = 200 mV. After processing, there were no captures for PGOOD that met this condition. Weibull FITs were conducted for each output case (1.8V and 3.3V) as well as each transient type (VOUT $_{\text{SET}}$  and  $SS_{\text{SEFI}}$ ). The weibull parameters and FIT plots are shown in table 9-5 and figure 9-1.

Test conditions and results are summarized in Table 9-1. Histograms for the VOUT $_{\text{SET}}$  peak normalized deviation and the transient time for run # 9 and # 10 are shown from [Figure 9-2](#page-18-0) to [Figure 9-5](#page-19-0), since all the  $SS_{\text{SEFI}}$  drop to zero. Only the Tt histogram is shown in [Figure 9-6](#page-19-0) and [Figure 9-7. Figure 9-8](#page-19-0) to [Figure 9-13](#page-20-0) show typical time domain plots for all the different types of the observed SETs for  $V_{OUT} = 1.8 V$  and  $V_{OUT} = 3.3 V$ .





#### <span id="page-16-0"></span>Table 9-1. Summary of TPS7H4010-SEP SET Test Condition and Results (V<sub>IN</sub> = 5V and V<sub>OUT</sub> = 1.8V) **(continued)**



### Table 9-2. Summary of TPS7H4010-SEP SET Test Condition and Results (V<sub>IN</sub> = 12V & V<sub>OUT</sub> = 3.3V)



Using the MFTF method described in *[Single-Event Effects \(SEE\) Confidence Interval Calculations](https://www.ti.com/lit/pdf/SLVK047)* application [report,](https://www.ti.com/lit/pdf/SLVK047) the upper-bound cross-section (using a 95% confidence level) is calculated for the different SETs as shown in Table 9-3 and [Table 9-4](#page-17-0).

#### Table 9-3. Upper Bound Cross Section for V<sub>OUT</sub> = 1.8 V at 95% Confidence Interval and Room **Temperature**





#### Table 9-3. Upper Bound Cross Section for V<sub>OUT</sub> = 1.8 V at 95% Confidence Interval and Room **Temperature (continued)**

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#### Table 9-4. Upper Bound Cross Section for V<sub>OUT</sub> = 3.3 V at 95% Confidence Interval and Room **Temperature**



#### Table 9-5. Weibull Parameters for V<sub>OUT</sub> = 1.8V and 3.3V Cases





<span id="page-18-0"></span>

#### X-axis is  $LET_{EFF}$  and Y-axis is respective upper bound cross section.



#### **Figure 9-1. Weibull FITs for each V<sub>OUT</sub> case**



**Figure 9-2. Histogram of the Normalized Amplitude Figure 9-3. Histogram of the Normalized Amplitude 9**



**for the Positive and Negative VOUT SETs on Run # for the Positive and Negative VOUT SETs on Run # 10**

<span id="page-19-0"></span>

**Figure 9-4. Histogram of the Transient Time for VOUT SETs on Run # 9**



**Figure 9-6. Histogram of the Transient Time for SS Figure 9-7. Histogram of the Transient Time for SS SETs on Run # 9**



**Figure 9-8. Worst Case Positive and Negative Polarity VOUT**<sub>SET</sub> for Run # 9



**Figure 9-10. Typical PGOODSET ≤ 0.9 V for Run # 9**



Texas

**TRUMENTS** 

**Figure 9-5. Histogram of the Transient Time for VOUT SETs on Run # 10**



**SETs on Run # 10**



**Figure 9-9. Typical V<sub>OUT</sub> During a SS<sub>SEFI</sub>** for Run # **9**



**Figure 9-11. Worst Case Positive and Negative Polarity VOUT**<sub>SET</sub> for Run # 10

<span id="page-20-0"></span>



**Figure 9-12. Typical VOUT During a SSSET for Run # Figure 9-13. Typical PGOODSET ≤ 1.6 V for Run # 10 10**

### **10 Event Rate Calculations**

Event rates were calculated for LEO(ISS) and GEO environments by combining CREME96 orbital integral flux estimations and simplified SEE cross-sections according to methods described in *[Heavy Ion Orbital Environment](https://www.ti.com/lit/pdf/slvk046)  [Single-Event Effects Estimations](https://www.ti.com/lit/pdf/slvk046)* application report. We assume a minimum shielding configuration of 100 mils (2.54 mm) of aluminum, and "worst-week" solar activity (this is similar to a 99% upper bound for the environment). Using the 95% upper-bounds for the SEL and the SEB/SEGR, the event rate calculation for the SEL and the SEB/SEGR is shown on Table 10-1 and Table 10-2, respectively. **It is important to note that this number is for reference since no SEL or SEB/SEGR events were observed.**

TADIC TV-T. OLL LYCIIL INALC VAILUIALIUIIS TUL YYUISL-YYCCN LLV AITU ULV VIDILS						
<b>Orbit Type</b>	Onset LET <sub>FFF</sub> $(MeV-cm^2/mq)$	<b>CREME96</b> <b>Integral FLUX</b> $\ell$ / day/cm <sup>2</sup> )	$\sigma$ SAT (cm <sup>2</sup> )	Event Rate (/day)	<b>Event Rate (FIT)</b>	<b>MTBE (Years)</b>
LEO (ISS)	43	$6.4 \times 10^{-4}$	$9.23 \times 10^{-8}$	$5.91 \times 10^{-11}$	$2.5 \times 10^{-3}$	$4.63 \times 10^{7}$
GEO		$2.2 \times 10^{-3}$		$2 \times 10^{-10}$	$8.4 \times 10^{-3}$	$1.36 \times 10^{7}$

**Table 10-1. SEL Event Rate Calculations for Worst-Week LEO and GEO Orbits**

#### **Table 10-2. SEB/SEGR Event Rate Calculations for Worst-Week LEO and GEO Orbits**



#### **Table 10-3. Combined SET (1.8V) Event Rate Calculations for Worst-Week LEO and GEO Orbits**



#### **Table 10-4. VOUT SET (1.8V) Event Rate Calculations for Worst-Week LEO and GEO Orbits**



#### **Table 10-5. SS SEFI (1.8V) Event Rate Calculations for Worst-Week LEO and GEO Orbits**



<span id="page-21-0"></span>

#### **Table 10-6. Combined SET (3.3V) Event Rate Calculations for Worst-Week LEO and GEO Orbits**



#### **Table 10-7. VOUT SET (3.3V) Event Rate Calculations for Worst-Week LEO and GEO Orbits**



#### **Table 10-8. SS SEFI (3.3V) Event Rate Calculations for Worst-Week LEO and GEO Orbits**



# <span id="page-22-0"></span>**11 Summary**

The purpose of this study was to characterize the effect of heavy-ion irradiation on the single-event effect (SEE) performance of the TPS7H4010-SEP synchronous step-down POL converter. Heavy-ions with LET<sub>FFF</sub> = 43 MeV·cm<sup>2</sup>/mg were were used for the SEE characterization campaign. Flux of 10<sup>4</sup> and 10<sup>5</sup> ions/cm<sup>2.</sup>s and fluences ranging from 3 × 10<sup>6</sup> to 1 × 10<sup>7</sup> ions/cm<sup>2</sup> per run were used for the characterization. The SEE results demonstrated that the TPS7H4010-SEP POL is free of destructive SEB events and SEL-free up to LET<sub>FFF</sub> = 43 MeV $\cdot$ cm $^2$ /mg and across the full electrical specifications. Transients at LET $_{\sf EFF}$  = 43 MeV $\cdot$ cm $^2$ /mg on V $_{\sf OUT}$  and PGOOD are presented and discussed. CREME96-based worst-week event-rate calculations for LEO(ISS) and GEO orbits for the DSEE are presented for reference.

### **12 Revision History**

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.





# **A Total Ionizing Dose from SEE Experiments**

The production TPS7H4010-SEP POL is rated to a total ionizing dose (TID) of 20 krad(Si). In the course of the SEE testing, the heavy-ion exposures delivered ≈10 krad(Si) per 10<sup>7</sup> ions/cm<sup>2</sup> run. The cumulative TID exposure over all runs was determined to be between 3 krad(Si) to 20 krad(Si), for each device. All eight production TPS7H4010-SEP devices used in the studies described in this report stayed within specification and were fully-functional after the heavy-ion SEE testing was completed.

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