Radiation Report

Single-Event Effects Test Report of the TPS7H4010-SEP Synchronous Step-Down Converter



1 Abstract

The purpose of this study is to characterize the single-event-effects (SEE) performance due to heavy-ion irradiation of the TPS7H4010-SEP. SEE performance was verified at two common output voltage rails of 1.8V and 3.3V. Heavy-ions ranging from an LET_{EFF} of 1.33 to 43 MeV·cm²/mg were used to irradiate 10 production devices. Flux of $\approx 10^4$ and 10^5 ions/cm² ·s and fluences of $\approx 10^6$ and 10^7 ions/cm² per run were used for the characterization. The results demonstrated that the TPS7H4010-SEP is SEL and SEB/SEGR-free up to 43 MeV·cm²/mg, at T = 125°C and T = 10°C, respectively, and across the full electrical specifications. SET and SEFI performance for output voltage excursions \geq |3%| from the nominal voltage and PGOOD \leq VOUT/2 are presented and discussed.

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2 Introduction

The TPS7H4010-SEP is a space-enhanced-plastic, 3.5V to 32V input, 6A, synchronous step-down voltage converter. The device provides exceptional efficiency and output accuracy in a very small solution size. Peak current-mode control is employed. Additional features such as adjustable switching frequency, synchronization to an external clock, FPWM option, power-good flag, precision enable, adjustable soft start, and tracking provide both flexible and easy-to-use solutions for a wide range of applications.

The TPS7H4010-SEP requires few external components and has a pinout designed for simple PCB layout with optimal EMI and thermal performance. Protection features include thermal shutdown, input undervoltage lockout, cycle-by-cycle current limiting, and hiccup short-circuit protection. The device is offered in a 30-pin WQFN plastic package. General device information and test conditions are listed in Table 2-1. For more detailed technical specifications, user-guides, and application notes please go to TPS7H4010-SEP product page.

Table 2-1. Overview Information

DESCRIPTION ⁽¹⁾	DEVICE INFORMATION					
TI Part Number	TPS7H4010-SEP					
Orderable Number	TPS7H4010MRNPTSEP					
Device Function	Point-of-load (POL) Switching Regulator					
Technology	Linear BiCMOS 8 (LBC8					
Exposure Facility	Radiation Effects Facility, Cyclotron Institute, Texas A&M University (25 MeV/nucleon)					
Heavy Ion Fluence per Run	$5.65 \times 10^5 - 1 \times 10^7 \text{ ions/cm}^2$					
Irradiation Temperature	10°C (for SEB/SEGR testing), 25°C (for SET testing), and 125°C (for SEL testing)					

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Single-Event Effects (SEE) Www.ti.com

3 Single-Event Effects (SEE)

The primary concern for the TPS7H4010-SEP is the robustness against destructive single-event effects (DSEE): single-event latch-up (SEL), single-event burnout (SEB), and single-event gate rupture (SEGR). In mixed technologies such as the BiCMOS process used on the TPS7H4010-SEP, the CMOS circuitry introduces a potential for SEL susceptibility.

SEL can occur if excess current injection caused by the passage of an energetic ion is high enough to trigger the formation of a parasitic cross-coupled PNP and NPN bipolar structure (formed between the p-sub and n-well and n+ and p+ contacts) [1,2]. The parasitic bipolar structure initiated by a single-event creates a high-conductance path (inducing a steady-state current that is typically orders-of-magnitude higher than the normal operating current) between power and ground that persists (is "latched") until power is removed, the device is reset, or until the device is destroyed by the high-current state. The TPS7H4010-SEP was tested for SEL at the maximum recommended voltage of 32 V, maximum load current of 6 A, and V_{OUT} of 3.3 and 1.8 V. The device exhibited no SEL with heavy-ions with LET_{EFF} = 43 MeV·cm²/mg at flux $\approx 10^5$ ions/cm²·s, fluences of $\approx 10^7$ ions/cm², and a die temperature of 125°C.

Since this device is designed to conduct large currents (up to 6 A) and withstand up to 32 V during the off-state, the power LDMOS introduces a potential susceptibility for SEB and SEGR [2]. The TPS7H4010-SEP was evaluated for SEB/SEGR at full load conditions of 6 A, and a maximum voltage of 32 V in both the enabled and disabled modes. Because it has been shown that MOSFET susceptibility to burnout decreases with temperature [2], the device was evaluated while operating under sub-ambienttemperatures. The devices were cooled-down (or "chilled") by using VORTEC tube (model 611). During the SEB/SEGR testing, not a single current event was observed, demonstrating that the TPS7H4010-SEP is SEB/SEGR-free up to LET_{EFF} = 43 MeV·cm²/mg at a flux of $\approx 10^5$ ions/cm²·s, fluences of $\approx 10^7$ ions/cm², and a die temperature of $\approx 10^\circ$ C.

The TPS7H4010-SEP was characterized for SET and SEFIs at flux of $\approx 10^4$ ions/cm²·s, fluences of 3 × 10⁶ ions/cm², at room temperature. The device was characterized at P_{VIN} = 12 V to V_{OUT} = 3.3 V and P_{VIN} = 5 V to V_{OUT} = 1.8 V at full load of 6-A. Under these conditions the device showed 2 different single-event transients (SET) signatures and 1 single-event functional interrupt (SEFI) under heavy-ion irradiation. All observed types of SETs were self-recoverable without the need of external intervention. The observed transients can be classified as:

- 1. A brief transient of the output voltage (refer here as $VOUT_{SET}$). For the purpose of this report the transients were characterized for deviations $-3\% \le V_{OUT} \le 3\%$ from the nominal output voltage of 1.8 V (±54 mV) and 3.3 V (±99 mV). The upsets typically have duration of 30 μ s and peak normalized deviation of 4.3% from the nominal voltage. For more details please refer to Section 9.
- 2. A soft-start power re-cycle which results in the V_{OUT} dropping to zero volts and characterized by a long recovery time governed by the soft start (SS) capacitor. This kind of SEFI is referred to here as SS_{SEFI}.
- 3. A PGOOD upset ≥ 10% from the nominal output voltage.



4 Device and Test Board Information

The TPS7H4010-SEP is packaged in a 30-pin WQFN plastic package as shown in Figure 4-1. The TPS7H4010EVM evaluation board was used to evaluate the performance and characteristics of the TPS7H4010-SEP under heavy-ions. Figure 4-2 shows the top view of the evaluation board used for the radiation testing. Figure 4-3 and Figure 4-4 shows the EVM board schematics for the $V_{OUT} = 3.3 \text{ V}$ and $V_{OUT} = 1.8 \text{ V}$ used for the heavy-ion testing campaign. See the TPS7H4010-SP Evaluation Module user's guide for more information about the evaluation board.

The package was delidded to reveal the die face for all heavy-ion testing.



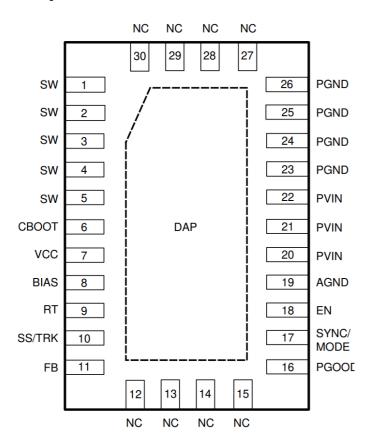


Figure 4-1. Photograph of Delidded TPS7H4010-SEP [Left] and Pinout Diagram [Right]





Figure 4-2. TPS7H4010-SEP Board Top View

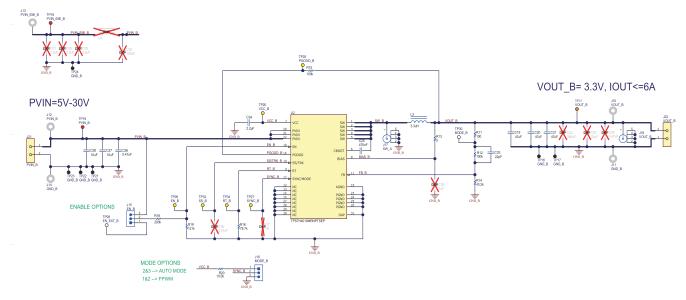


Figure 4-3. TPS7H4010EVM Schematic for V_{OUT} = 3.3 V

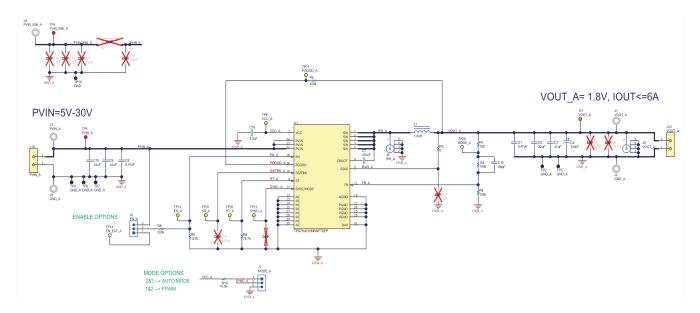


Figure 4-4. TPS7H4010EVM Schematic for V_{OUT} = 1.8 V

5 Irradiation Facility and Setup

The heavy-ion species used for the SEE studies on this product were provided and delivered by the TAMU Cyclotron Radiation Effects Facility using a superconducting cyclotron and an advanced electron cyclotron resonance (ECR) ion source. At the fluxes used, ion beams had good flux stability and high irradiation uniformity over a 1-in diameter circular cross-sectional area for the in-air station. Uniformity is achieved by magnetic defocusing. The flux of the beam is regulated over a broad range spanning several orders of magnitude. For the bulk of these studies, ion flux of 10^4 and 10^5 ions/cm²·s were used to provide heavy-ion fluences of $\approx 3 \times 10^6$ and 10^7 ions/cm².

For the experiments conducted in this report the following ions and corresponding angles were used in order to provide a range of LET _{EFF} of 1.33 to 43 MeV·cm2 /mg.

- Krypton (84Kr) at 44.3° for an LET_{EEE} = 43 MeV
 - Range_{EFF} = $77.7 \mu m$
 - Total Kinetic Energy = 2.081 GeV (25 MeV/nucleon)
- Krypton (⁸⁴Kr) at 42.7° for an LET_{FFF} = 43.1 MeV
 - Range_{FF} = 73.6 μm
 - Total Kinetic Energy = 1.259 GeV (15 MeV/nucleon)
- Krypton (⁸⁴Kr) at 0° for an LET_{FFF} = 29.7 MeV
 - Range_{FFF} = $118.2 \mu m$
 - Total Kinetic Energy = 1.259 GeV (15 MeV/nucleon)
- Copper (⁶³Cu) at 41.6° for an LET_{EFF} = 28.1 MeV
 - Range_{FFF} = $80.1 \mu m$
 - Total Kinetic Energy = 0.944 GeV (15 MeV/nucleon)
- Copper (⁶³Cu) at 0° for an LET_{EFF} = 19.7 MeV
 - Range_{FFF} = $124.1 \mu m$
 - Total Kinetic Energy = 0.944 GeV (15 MeV/nucleon)
- Argon (⁴⁰Ar) at 42.3° for an LET_{EFF} = 12 MeV
 - Range_{FFF} = 120.8 μm
 - Total Kinetic Energy = 0.599 GeV (15 MeV/nucleon)
- Argon (⁴⁰Ar) at 0° for an LET_{EFF} = 8.44 MeV
 - Range_{FFF} = $180.9 \mu m$
 - Total Kinetic Energy = 0.599 GeV (15 MeV/nucleon)
- Neon (²⁰Ne) at 43.1° for an LET_{EFF} = 3.89 MeV
 - Range_{EFF} = 177 μ m
 - Total Kinetic Energy = 0.300 GeV (15 MeV/nucleon)
- Neon (²⁰Ne) at 0° for an LET_{EFF} = 2.74 MeV
 - Range_{FFF} = $261.3 \mu m$
 - Total Kinetic Energy = 0.300 GeV (15 MeV/nucleon)
- Nitrogen (¹⁴N) at 0° for an LET_{EFF} = 1.33 MeV
 - Range_{FFF} = $371.8 \mu m$
 - Total Kinetic Energy = 0.210 GeV (15 MeV/nucleon)

Ion uniformity for these experiments was between 88 and 98%.

Figure 5-1 shows the TPS7H4010-SEP test board used for the experiments at the TAMU facility. Although not visible in this photo, the beam port has a 1-mil Aramica window to allow in-air testing while maintaining the vacuum within the accelerator with only minor ion energy loss. All through-hole test points were soldered backwards for easy access of the signals while having enough room to change the angle of incidence and maintaining the 40-mm distance to the die. The in-air gap between the device and the ion beam port window was maintained at 40 mm for all runs.



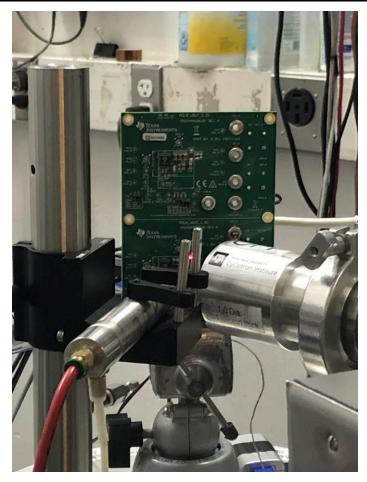


Figure 5-1. Photograph of the TPS7H4010-SEP Evaluation Board Mounted in Front of the Heavy-Ion Beam Exit Port at the Texas A&M Cyclotron

6 Depth, Range, and LET_{EFF} Calculation

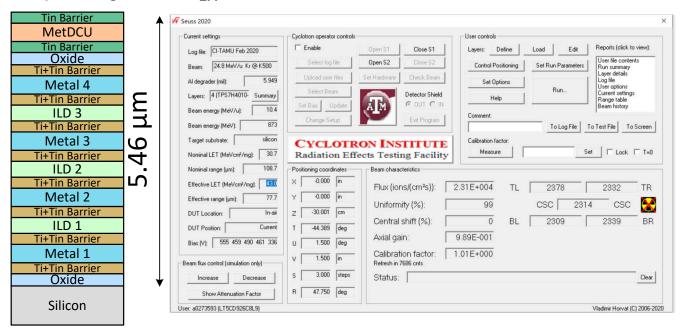


Figure 6-1. Generalized Cross-Section of the LBC8 Technology BEOL Stack on the TPS7H4010-SEP [Left] and SEUSS 2020 Application Used to Determine Key Ion Parameters [Right]

The TPS7H4010-SEP is fabricated in the TI Linear BiCMOS 180-nm process with a back-end-of-line (BEOL) stack consisting of 4 levels of standard thickness aluminum metal on a 0.6- μ m pitch. The total stack height from the surface of the passivation to the silicon surface is 5.46 μ m based on nominal layer thickness as shown in Figure 6-1. Accounting for energy loss through the 1-mil thick Aramica beam port window, the 40-mm air gap, and the BEOL stack over the TPS7H4010-SEP, the effective LET (LET_{EFF}) at the surface of the silicon substrate, the depth, and the ion range was determined with the SEUSS 2020 Software (provided by the Texas A&M Cyclotron Institute and based on the latest SRIM-2013 [7] models). The results are shown in Table 6-1. The LET_{EFF} vs range for the ⁸⁴Kr heavy-ion is shown on Figure 6-2. The stack was modeled as a homogeneous layer of silicon dioxide (valid since SiO₂ and aluminum density are similar).

Table 6-1. Krypton Ion LET_{EFF}, Depth, and Range in Silicon

	ANGLE OF DEGRADER STEPS RANGE IN SILICON LET-										
ION TYPE	ANGLE OF INCIDENCE	DEGRADER STEPS (#)	PEGDADED ANGLE		LET _{EFF} (MeV·cm²/mg)						
⁸⁴ Kr	44.38	3	47.75	77.7	43						
⁸⁴ Kr	42.7	0	0	73.6	43.1						
⁸⁴ Kr	0	0	0	118.2	29.7						
⁶³ Cu	41.6	0	0	80.1	28.1						
⁶³ Cu	0	0	0	124.1	19.7						
⁴⁰ Ar	42.3	0	0	120.8	12						
⁴⁰ Ar	0	0	0	180.9	8.44						
²⁰ Ne	43.1	0	0	177	3.89						
²⁰ Ne	0	0	0	261.3	2.74						
14 N	0	0	0	371.8	1.33						

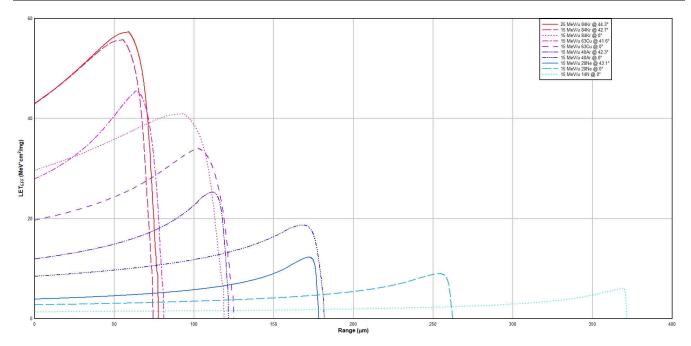


Figure 6-2. LET_{EFF} vs Range for ⁸⁴Kr at the Conditions Used for the SEE Test Campaign

Test Setup and Procedures www.ti.com

7 Test Setup and Procedures

SEE testing was performed on a TPS7H4010-SEP device mounted on a TPS7H4010EVM. The device power was provided using the J19 (PVIN) and (GND) inputs with the N6766A PS Module mounted on a N6705 precision power supply in a 4-wire configuration. Discrete power resistors of 0.3 Ω (VOUT = 1.8 V) and 0.55 Ω (VOUT = 3.3 V) were used to load the device to 6 A for the SEE testing campaign. The device was tested under Auto Mode for all runs by tying the SYNC to GND using the J7 jumper.

For SEL, SEB, and SEGR testing, the device was powered up to the maximum recommended operating voltage of 32 V and loaded with the maximum load of 6 A. Two commonly used output voltages, 3.3 and 1.8 V, were used for the SEE testing campaign. For the SEB/SEGR characterization, the device was tested under enabled and disabled modes. The device was disabled by using jumper J6, connecting EN to GND. The discrete load resistor was connected even when the device was disabled to help differentiate if an SET momentarily activated the device under the heavy-ion irradiation. During the SEB/SEGR testing with the device in disabled mode, not a single V_{OUT} transient or input current event was observed.

For the SET characterization, the device was powered up to 12 V for the 3.3-V V_{OUT} case and 5 V for the 1.8-V case. The SET events were monitored using two National Instruments (NI) PXIe-5172 scope cards. One scope was used to monitor and trigger from V_{OUT} , using a window trigger around ±3% from the nominal output voltage. The second scope was used to monitor and trigger from the PGOOD at 1.5 V (for the V_{OUT} = 1.8 V) and 3 V (for the V_{OUT} = 3.3 V), using a edge/negative trigger. Both scopes were mounted on a NI PXIe-1095 chassis.

All equipment was controlled and monitored using a custom-developed LabVIEW program (PXI-RadTest) running on a HP-Z4 desktop computer. The computer communicates with the PXI chassis via an MXI controller and NI PXIe-8381 remote control module.

Figure 7-1 shows a block diagram of the setup used for SEE testing of the TPS7H4010-SEP. Table 7-1 shows the connections, limits, and compliance values used during the testing. A die temperature of 125°C was used for SEL and was achieved with the use of a convection heat gun aimed at the die. For the SEB/SEGR testing, the device was cooled-down to ≈10°C using a VORTEC tube (model: 611), aimed at the die. For SET testing, the device was tested at room temperature (no cooling or heating was applied to the DUT). The die temperature was monitored during all the testing using a T-Type thermocouple attached to the thermal pad vias (on the bottom side of the EVM) with thermal paste. The thermocouple was held in place by using high temperature tape (kapton-tape). Die-to-thermocouple temperature was verified using a IR-camera prior to the SEE test campaign.

1abic 1-1.	Table 7-1. Equipment Set and I arameters osed for SEE Testing the 11 S711-010-SEI											
PIN NAME	EQUIPMENT USED	CAPABILITY	COMPLIANCE	RANGE OF VALUES USED								
VIN	Agilen N6700 PS (Channel #3)	15 A	10 A	5, 12, and 32 V								
Oscilloscope Card on V _{OUT}	NI-PXIe 5172	100 MS/s	_	10 MS/s								
Oscilloscope Card on V _{OUT}	NI-PXIe 5172	100 MS/s	_	50 MS/s								
Digital I/O	NI-PXIe 6589	_	_	Interrupt Based								

Table 7-1, Equipment Set and Parameters Used for SEE Testing the TPS7H4010-SEP

All boards used for SEE testing were fully checked for functionality. Dry runs were also performed to ensure that the test system was stable under all bias and load conditions prior to being taken to the TAMU facility. During the heavy-ion testing, the LabVIEW control program powered up the TPS7H4010-SEP device and set the external sourcing and monitoring functions of the external equipment. After functionality and stability was confirmed, the beam shutter was opened to expose the device to the heavy-ion beam. The shutter remained open until the target fluence was achieved (determined by external detectors and counters). During irradiation, the NI scope cards continuously monitored the signals. When the output voltage exceeded the pre-defined 3% window trigger, or when the PG signal changed from High to Low (using a negative edge trigger), a data capture was initiated. In addition to monitoring the voltage levels of the two scopes, VIN current and the +5-V signal from TAMU were

monitored at all times. No sudden increases in current were observed (outside of normal fluctuations) on any of the test runs and indicated that no SEL or SEB/SEGR events occurred during any of the tests.

The lumped components in the block diagram represent the EVM set for V_{OUT} = 1.8 V. For details on the EVM components regarding V_{OUT} = 3.3 V, please refer to Figure 4-3.

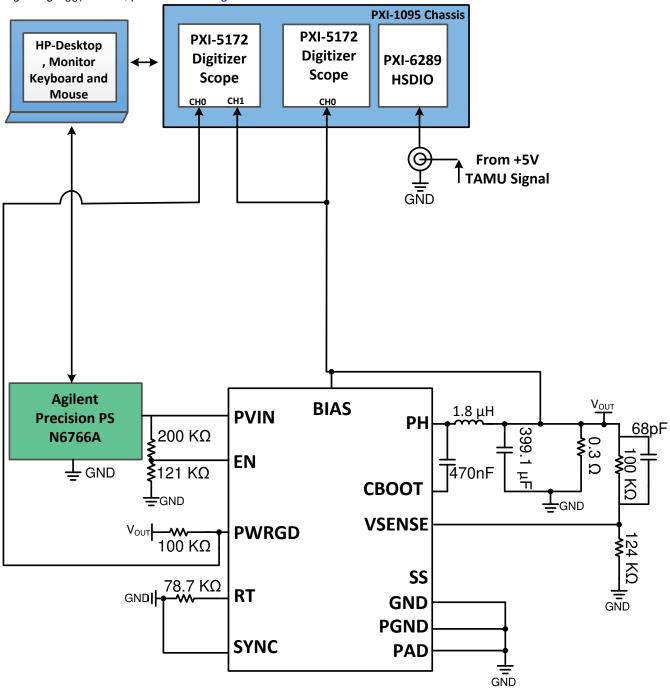


Figure 7-1. Block Diagram of SEE Test Setup With the TPS7H4010-SEP

8 Destructive Single-Event Effects (DSEE)

8.1 Single-Event Latch-up (SEL) Results

During SEL characterization, the device was heated using forced hot air, maintaining the DUT temperature at 125°C. The die temperature was monitored during testing using a T-Type thermocouple attached to the thermal pad vias (on the bottom side of the EVM) with thermal paste. The thermocouple was held in-place by using high temperature tape (kapton-tape). Die-to-thermocouple temperature was verified using a IR-camera.

The species used for the SEL testing was a Krypton (⁸⁴Kr) ion with an angle-of-incidence of 44.38°, degraded by 3-steps of a degrader angle of 47.75° for an LET_{EFF} = 43 MeV·cm²/mg (for more details refer to Section 6). The kinetic energy in the vacuum for this ion is 2.081 GeV (25-MeV/amu line). Flux of approximately 10⁵ ions/cm²·s and a fluence of approximately 10⁷ ions/cm² were used for the four runs. Run duration to achieve this fluence was approximately 2 minutes. The two devices were powered up and exposed to the heavy-ions using the maximum recommended voltage of 32 V and maximum load of 6 A. No SEL events were observed during all four runs, indicating that the TPS7H4010-SEP is SEL-free. Table 8-1 shows the SEL test conditions and results. Figure 8-1 shows a plot of the current vs time for run # 1.

Table 8-1. Sum	imary of IPS	5/H4010-SEP	SEL Test Condition	on and Results
			F 1.11V	FLUENOF

RUN#	UNIT #	ION	LET _{EFF} (MeV·cm ² /mg)	FLUX (ions·cm²·s)	FLUENCE (# ions)	V _{OUT} (V)
1	1	⁸⁴ Kr	43	9.79 x 10 ⁴	9.99 x 10 ⁶	1.8
2	2	⁸⁴ Kr	43	7.06 x 10 ⁴	1 x 10 ⁷	3.3
3	3	⁸⁴ Kr	43	8.56 x 10 ⁴	9.98 x 10 ⁶	1.8
4	4	⁸⁴ Kr	43	1.1 x 10 ⁵	9.97 x 10 ⁶	3.3

Using the MFTF method described in *Single-Event Effects (SEE) Confidence Interval Calculations* application report and combining (or summing) the fluences of the four runs @ 125° C (3.99×10^{7}), the upper-bound cross-section (using a 95% confidence level) is calculated as:

 $\sigma_{\rm SEL} \le 9.23 \times 10^{-8} \, \rm cm^2/device$ for LET_{EFF} = 43 MeV·cm²/mg and T = 125°C.

NOTE: The current spikes are due to the output transients on the output voltage.

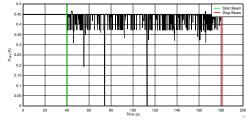


Figure 8-1. Current vs Time for Run # 1 of the TPS7H4010-SEP at T = 125°C

8.2 Single-Event Burnout (SEB) and Single-Event Gate Rupture (SEGR) Results

During the SEB/SEGR characterization, the device was cooled-down using a VORTEC tube (model: 611) to ≈10°C. The die temperature was monitored during the testing using a T-Type thermocouple attached to the thermal pad vias (on the bottom side of the EVM) with thermal paste. The thermocouple was held in place by using high temperature tape (kapton-tape). Die-to-thermocouple temperature was verified using a IR-camera.

The species used for the SEB/SEGR testing was a Krypton (⁸⁴Kr) ion with an angle-of-incidence of 44.38°, degraded by 3-steps with a degrader angle of 47.75°, for an LET_{EFF} = 43 MeV·cm²/mg (for more details refer to Section 6). The kinetic energy in the vacuum for this ion is 2.081 GeV (25-MeV/amu line). Flux of approximately 10⁵ ions/cm²·s and a fluence of approximately 10⁷ ions/cm² were used for the four runs. Run duration to achieve this fluence was approximately 2 minutes. The two devices were powered up using the recommended maximum voltage of 32 V and the maximum load of 6 A. The TPS7H4010-SEP was tested under enabled and disabled modes, the device was disabled by using jumper J6 connecting EN to GND. The discrete load resistor was connected, even when the device was disabled, to help differentiate if an SET momentarily activated the device under the heavy-ion irradiation. During SEB/SEGR testing with the device "disabled," no V_{OUT} transient or input

current events were observed. No SEB/SEGR events were observed during all four runs, indicating that the TPS7H4010-SEP is SEB/SEGR-free up to LET_{EFF} = $43 \text{ MeV} \cdot \text{cm}^2/\text{mg}$ and across the full electrical specifications. Table 8-2 shows the SEB test conditions and results. Figure 8-2 shows a plot of the current vs time for run # 5 (Enabled) and Figure 8-3 for run # 6 (Disabled).

Table 8-2. Summary of TPS7H4010-SEP SEB Test Condition and Results

RUN#	UNIT#	ION	LET _{EFF} (MeV·cm ² /mg)	FLUX (ions·cm²·s)	FLUENCE (# ions)	V _{OUT} (V)	ENABLED STATUS
5	5	⁸⁴ Kr	43	1.05 x 10 ⁵	1 x 10 ⁷	1.8	Enabled
6	5	⁸⁴ Kr	43	1.06 x 10 ⁵	9.99 x 10 ⁶	1.8	Disabled
7	6	⁸⁴ Kr	43	1.12 x 10 ⁵	9.95 x 10 ⁶	3.3	Enabled
46	6	⁸⁴ Kr	43	1.24 x 10 ⁵	1 x 10 ⁷	3.3	Disabled

Using the MFTF method described in *Single-Event Effects (SEE) Confidence Interval Calculations* application report and combining (or summing) the fluences of the four runs @ 10° C (3.06×10^{7}), the upper-bound cross-section (using a 95% confidence level) is calculated as:

 $\sigma_{SEB} \le 1.21 \times 10^{-7} \text{ cm}^2/\text{device for LET}_{EFF} = 43 \text{ MeV} \cdot \text{cm}^2/\text{mg}$ and T = 10°C.

The current spikes are due to the output transients on the output voltage.

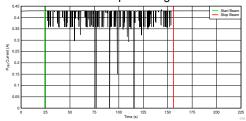


Figure 8-2. Current vs Time for Run # 5 (Enabled) for the TPS7H4010-SEP at T = 10°C

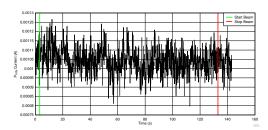


Figure 8-3. Current vs Time for Run # 6 (Disabled) for the TPS7H4010-SEP at T = 10°C

9 Single-Event Transients (SET)

SETs are defined as heavy-ion-induced transients upsets on the V_{OUT} and the PGOOD flag of the TPS7H4010-SEP. SET testing was performed at room temperature (no external temperature control applied). The species used for the SET testing was ⁸⁴Kr, ⁶³Cu, ⁴⁰Ar, ²⁰Ne, and ¹⁴N for a range of LET_{EFF} = 1.33 to 43 MeV·cm²/mg, for more details refer to Section 6. Flux of approximately 10^4 ions/cm²·s and a fluence of approximately 3×10^6 ions/cm² were used for the SET runs.

 V_{OUT} SETs were characterized using a window trigger of ±3% around the nominal output voltage (≈1.8 and 3.3 V). The devices were characterized at P_{VIN} = 5 V (for VOUT = 1.8 V) and P_{VIN} = 12 V (for V_{OUT} = 3.3 V). The output load was set to 6 A for both runs by using discrete power resistors of 0.55 Ω (for V_{OUT} = 3.3 V) and 0.3 Ω (for V_{OUT} = 1.8 V). To capture the SETs two NI-PXI-5172 scope card were used to continuously monitor the V_{OUT} and the PGOOD. Each scope was operated independently from each other. The output voltage was monitored by using the TP1 and the TP15 test points on the EVM, while the PGOOD was monitored using the TP11 and TP25 test points. The scope triggering from PGOOD was also monitoring the output voltage. The PGOOD and V_{OUT} were tied together through a 100-k Ω resistor (pull-up).

The scope triggering from V_{OUT} was programmed to record 80k samples with a sample rate of 10 M samples per second (S/s) in case of an event (trigger). The scope triggering from PGOOD was programmed with 30k samples and 5 MS/s. Both scopes were programmed to record 20% of the data before the trigger occured.

Under heavy-ions, the TPS7H4010-SEP exhibits three transient upsets that were fully recoverable without the need for external intervention. Two distinct signatures were observed on the output voltage.

- 1. A fast transient of both polarities (positive and negative) with typical deviation of ≈4.3% and transient times of ≈30 µs. This transients are referred here as VOUT_{SET}.
- 2. A slow transient that drops to zero volts and recover within the programmed soft start time, refer here as SS_{SEFI}.

The SS_{SEFI} is associated with a disturbance on the soft start circuit. During the SS_{SEFI} the soft start circuit discharges the SS capacitor (through an internal FET) and restarted the device. Since no external soft start capacitor was used on the EVM, the internal soft start time of ≈6 ms was observed during this kind of SS_{SEFI}. PGOOD SETs at trigger levels of 300 mV below the nominal output voltage were observed. Since the PGOOD was pulled-up to the output voltage; with each transient on V_{OUT} that droops 300 mV a capture was initiated on the scope triggering from the PGOOD signal. However this was not a real transient on the PGOOD circuit. Since PGOOD circuit is designed to pull-down when the output voltage is not within 10% of the final value, each $VOUT_{SET} > |10\%|$ was also captured on the scope triggering from PGOOD. All the data that was captured on this scope was processed to eliminate the intrinsic electronic behavior and the coupling with the output voltage. Those SETs that were true transients on the PGOOD circuitry were compared to half the nominal output voltage. This is valid since PGOOD is typically connected to digital circuits with $V_{IL} = 200$ mV. After processing, there were no captures for PGOOD that met this condition. Weibull FITs were conducted for each output case (1.8V and 3.3V) as well as each transient type (VOUT_{SET} and SS_{SEFI}). The weibull parameters and FIT plots are shown in table 9-5 and figure 9-1.

Test conditions and results are summarized in Table 9-1. Histograms for the VOUT_{SET} peak normalized deviation and the transient time for run # 9 and # 10 are shown from Figure 9-2 to Figure 9-5, since all the SS_{SEFI} drop to zero. Only the Tt histogram is shown in Figure 9-6 and Figure 9-7. Figure 9-8 to Figure 9-13 show typical time domain plots for all the different types of the observed SETs for $V_{OUT} = 1.8 \text{ V}$ and $V_{OUT} = 3.3 \text{ V}$.

Table 9-1. Summary of TPS7H4010-SEP SET Test Condition and Results (V_{IN} = 5V and V_{OUT} = 1.8V)

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RUN#	UNIT#	ION	LET _{EFF} (MeV·cm ² / mg)	FLUX (ions·cm²·s)	FLUENCE (# ions)	VOUT _{SET} (#) ≥ 3%	SS _{SEFI} (#)	PGOOD SET ≤ V _{OUT} / 2
9	7	⁸⁴ Kr	43	1.17 × 10 ⁴	3 × 10 ⁶	686	216	0
10	9	⁸⁴ Kr	43.1	1.18 x 10 ⁴	3 × 10 ⁶	251	248	0
11	9	⁸⁴ Kr	29.7	1.09 x 10 ⁴	2.99 × 10 ⁶	192	166	0
12	9	⁶³ Cu	28.1	1.07 x 10 ⁴	3 × 10 ⁶	174	165	0
13	9	⁶³ Cu	19.7	1.08 x 10 ⁴	2.99 × 10 ⁶	184	159	0



Table 9-1. Summary of TPS7H4010-SEP SET Test Condition and Results (V_{IN} = 5V and V_{OUT} = 1.8V) (continued)

RUN#	UNIT#	ION	LET _{EFF} (MeV·cm ² / mg)	FLUX (ions·cm²·s)	FLUENCE (# ions)	VOUT _{SET} (#) ≥ 3%	SS _{SEFI} (#)	PGOOD SET ≤ V _{OUT} / 2
14	9	⁴⁰ Ar	12	1.22 x 10 ⁴	2.99 × 10 ⁶	187	72	0
15	9	⁴⁰ Ar	8.44	1.18 x 10 ⁴	2.99 × 10 ⁶	153	22	0
16	9	⁴⁰ Ar	8.44	1.07 x 10 ⁴	3 × 10 ⁶	150	19	0
17	9	²⁰ Ne	3.89	1.12 x 10 ⁴	3 × 10 ⁶	92	11	0
18	9	²⁰ Ne	2.74	1.11 x 10 ⁴	2.99 × 10 ⁶	43	5	0
19	9	¹⁴ N	1.33	1.11 x 10 ⁴	2.99 × 10 ⁶	18	0	0

Table 9-2. Summary of TPS7H4010-SEP SET Test Condition and Results (V_{IN} = 12V & V_{OUT} = 3.3V)

Run #	Unit #	ION	LET _{EFF} (MeV·cm²/mg)	FLUX (ions·cm²·s)	FLUENCE (# ions)	VOUT _{SET} (#) ≥ 3%	SS _{SEFI} (#)	PGOOD SET ≤ V _{OUT} / 2
20	8	⁸⁴ Kr	43	1.05 x 10 ⁴	3 × 10 ⁶	251	218	0
21	10	⁸⁴ Kr	43.1	1.28 x 10 ⁴	3 × 10 ⁶	448	189	0
22	10	⁸⁴ Kr	29.7	1.03 x 10 ⁴	2.99 × 10 ⁶	362	167	0
23	10	⁶³ Cu	28.1	9.16 x 10 ³	2.99 × 10 ⁶	398	178	0
24	10	⁶³ Cu	19.7	1.28 x 10 ⁴	3 × 10 ⁶	322	145	0
25	10	⁴⁰ Ar	12	1.28 x 10 ⁴	2.99 × 10 ⁶	301	82	0
26	10	⁴⁰ Ar	8.44	1.28 x 10 ⁴	2.99 × 10 ⁶	189	31	0
27	10	²⁰ Ne	3.89	1.13 x 10 ⁴	3 × 10 ⁶	108	6	0
28	10	²⁰ Ne	2.74	1.12 x 10 ⁴	3 × 10 ⁶	84	12	0
29	10	¹⁴ N	1.33	9.07 x 10 ³	2.99 × 10 ⁶	24	0	0

Using the MFTF method described in *Single-Event Effects (SEE) Confidence Interval Calculations* application report, the upper-bound cross-section (using a 95% confidence level) is calculated for the different SETs as shown in Table 9-3 and Table 9-4.

Table 9-3. Upper Bound Cross Section for V_{OUT} = 1.8 V at 95% Confidence Interval and Room Temperature

Run#	SET TYPE	# UPSETS	UPPER BOUND CROSS SECTION (cm²/ device)	SET TYPE	# UPSETS	UPPER BOUND CROSS SECTION (cm²/ device)	SET TYPE	# UPSETS	UPPER BOUND CROSS SECTION (cm ² / device)
9	VOUT _{SET} ≥ 3%	686	2.46 × 10 ⁻⁴	SS _{SEFI}	216	8.23 × 10 ⁻⁵	PGOOD ≤ 0.9 – V	0	1.23 × 10 ⁻⁶
10	VOUT _{SET} ≥ 3%	251	9.47 x 10 ⁻⁵	SS _{SEFI}	248	9.36 x 10 ⁻⁵	PGOOD ≤ 0.9 – V	0	1.23 x 10 ⁻⁶
11	VOUT _{SET} ≥ 3%	192	7.40 x 10 ⁻⁵	SS _{SEFI}	166	6.45 x 10 ⁻⁵	PGOOD ≤ 0.9 – V	0	1.23 x 10 ⁻⁶
12	VOUT _{SET} ≥ 3%	174	6.74 x 10 ⁻⁵	SS _{SEFI}	165	6.41 x 10 ⁻⁵	PGOOD ≤ 0.9 – V	0	1.23 x 10 ⁻⁶
13	VOUT _{SET} ≥ 3%	184	7.11 x 10 ⁻⁵	SS _{SEFI}	159	6.21 x 10 ⁻⁵	PGOOD ≤ 0.9 – V	0	1.23 x 10 ⁻⁶
14	VOUT _{SET} ≥ 3%	187	7.22 x 10 ⁻⁵	SS _{SEFI}	72	3.03 x 10 ⁻⁵	PGOOD ≤ 0.9 – V	0	1.23 x 10 ⁻⁶

Table 9-3. Upper Bound Cross Section for V_{OUT} = 1.8 V at 95% Confidence Interval and Room Temperature (continued)

iomporatar (continuou)									
Run#	SET TYPE	# UPSETS	UPPER BOUND CROSS SECTION (cm²/ device)	SET TYPE	# UPSETS	UPPER BOUND CROSS SECTION (cm²/ device)	SET TYPE	# UPSETS	UPPER BOUND CROSS SECTION (cm ² / device)
15	VOUT _{SET} ≥ 3%	153	6.00 x 10 ⁻⁵	SS _{SEFI}	22	1.11 x 10 ⁻⁵	PGOOD ≤ 0.9 – V	0	1.23 x 10 ⁻⁶
16	VOUT _{SET} ≥ 3%	150	5.87 x 10 ⁻⁵	SS _{SEFI}	19	9.89 x 10 ⁻⁶	PGOOD ≤ 0.9 – V	0	1.23 x 10 ⁻⁶
17	VOUT _{SET} ≥ 3%	92	3.76 x 10 ⁻⁵	SS _{SEFI}	11	6.56 x 10 ⁻⁶	PGOOD ≤ 0.9 – V	0	1.23 x 10 ⁻⁶
18	VOUT _{SET} ≥ 3%	43	1.94 x 10 ⁻⁵	SS _{SEFI}	5	3.90 x 10 ⁻⁶	PGOOD ≤ 0.9 – V	0	1.23 x 10 ⁻⁶
19	VOUT _{SET} ≥ 3%	18	9.51 x 10 ⁻⁶	SS _{SEFI}	0	1.23 x 10 ⁻⁶	PGOOD ≤ 0.9 – V	0	1.23 x 10 ⁻⁶

Table 9-4. Upper Bound Cross Section for V_{OUT} = 3.3 V at 95% Confidence Interval and Room Temperature

remperature										
Run#	SET TYPE	# UPSETS	UPPER BOUND CROSS SECTION (cm²/ device)	SET TYPE	# UPSETS	UPPER BOUND CROSS SECTION (cm²/ device)	SET TYPE	# UPSETS	UPPER BOUND CROSS SECTION (cm²/ device)	
20	VOUT _{SET} ≥ 3%	251	9.47 × 10 ⁻⁵	SS _{SEFI}	218	8.3 × 10 ⁻⁵	PGOOD ≤ 0.9 – V	0	1.23 × 10 ⁻⁶	
21	VOUT _{SET} ≥ 3%	448	1.54 x 10 ⁻⁴	SS _{SEFI}	189	7.25 x 10 ⁻⁵	PGOOD ≤ 0.9 – V	0	1.23 × 10 ⁻⁶	
22	VOUT _{SET} ≥ 3%	362	1.34 x 10 ⁻⁴	SS _{SEFI}	167	6.50 x 10 ⁻⁵	PGOOD ≤ 0.9 – V	0	1.23 × 10 ⁻⁶	
23	VOUT _{SET} ≥ 3%	398	1.47 x 10 ⁻⁴	SS _{SEFI}	178	6.89 x 10 ⁻⁵	PGOOD ≤ 0.9 – V	0	1.23 × 10 ⁻⁶	
24	VOUT _{SET} ≥ 3%	322	1.20 x 10 ⁻⁴	SS _{SEFI}	145	5.69 x 10 ⁻⁵	PGOOD ≤ 0.9 – V	0	1.23 × 10 ⁻⁶	
25	VOUT _{SET} ≥ 3%	301	1.13 x 10 ⁻⁴	SS _{SEFI}	82	3.40 x 10 ⁻⁵	PGOOD ≤ 0.9 – V	0	1.23 × 10 ⁻⁶	
26	VOUT _{SET} ≥ 3%	189	7.29 x 10 ⁻⁵	SS _{SEFI}	31	1.47 x 10 ⁻⁵	PGOOD ≤ 0.9 – V	0	1.23 × 10 ⁻⁶	
27	VOUT _{SET} ≥ 3%	108	4.35 x 10 ⁻⁵	SS _{SEFI}	6	4.35 x 10 ⁻⁶	PGOOD ≤ 0.9 – V	0	1.23 × 10 ⁻⁶	
28	VOUT _{SET} ≥ 3%	84	3.46 x 10 ⁻⁵	SS _{SEFI}	12	6.98 x 10 ⁻⁶	PGOOD ≤ 0.9 – V	0	1.23 × 10 ⁻⁶	
29	VOUT _{SET} ≥ 3%	24	1.19 x 10 ⁻⁵	SS _{SEFI}	0	1.23 × 10 ⁻⁶	PGOOD ≤ 0.9 – V	0	1.23 × 10 ⁻⁶	

Table 9-5. Weibull Parameters for V_{OUT} = 1.8V and 3.3V Cases

Parameters	1.8V VOUT _{SET}	1.8V SS _{SEFI}	3.3V VOUT _{SET}	3.3 SS _{SEFI}
Upper Bound Cross Section (cm ²)	9.47x10 ⁻⁵	9.36x10 ⁻⁵	1.64x10 ⁻⁴	7.27x10 ⁻⁵
Cross-Saturation (cm ²)	8.37x10 ⁻⁵	8.27x10 ⁻⁵	1.49x10 ⁻⁴	6.3x10 ⁻⁵
Onset (MeV·cm²/mg)	1	1.33	1	1.33
w	11	20	12	16



Table 9-5. Weibull Parameters for V_{OUT} = 1.8V and 3.3V Cases (continued)

Parameters	1.8V VOUT _{SET}	1.8V SS _{SEFI}	3.3V VOUT _{SET}	3.3 SS _{SEFI}
S	1	1.6	1	2.2

X-axis is $\mathsf{LET}_{\mathsf{EFF}}$ and Y-axis is respective upper bound cross section.

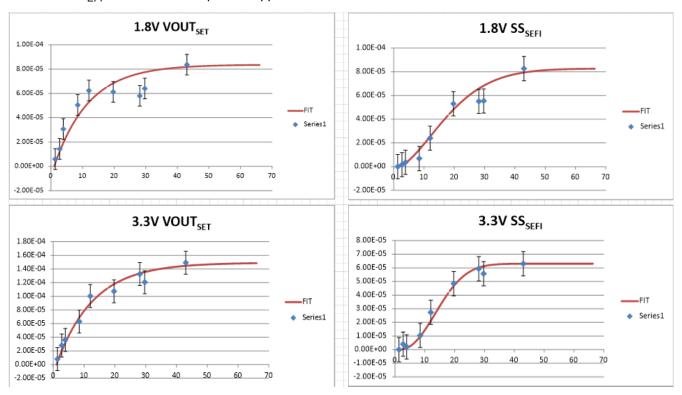
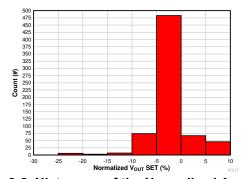


Figure 9-1. Weibull FITs for each V_{OUT} case



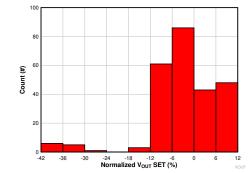


Figure 9-2. Histogram of the Normalized Amplitude Figure 9-3. Histogram of the Normalized Amplitude for the Positive and Negative V_{OUT} SETs on Run # for the Positive and Negative V_{OUT} SETs on Run #

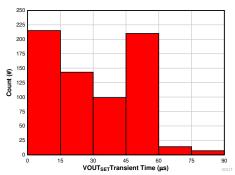


Figure 9-4. Histogram of the Transient Time for V_{OUT} SETs on Run # 9

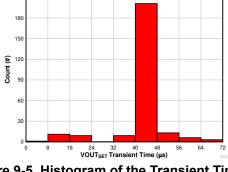


Figure 9-5. Histogram of the Transient Time for V_{OUT} SETs on Run # 10

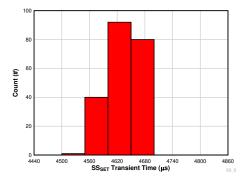
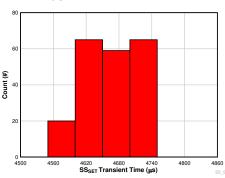


Figure 9-6. Histogram of the Transient Time for SS Figure 9-7. Histogram of the Transient Time for SS SETs on Run #9



SETs on Run # 10

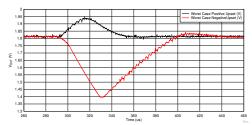


Figure 9-8. Worst Case Positive and Negative Polarity VOUT_{SET} for Run # 9

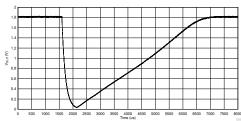


Figure 9-9. Typical V_{OUT} During a SS_{SEFI} for Run #

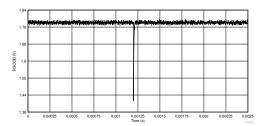


Figure 9-10. Typical PGOOD_{SET} ≤ 0.9 V for Run # 9

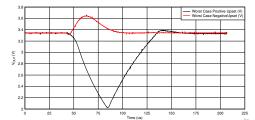
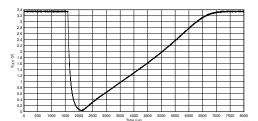


Figure 9-11. Worst Case Positive and Negative Polarity VOUT_{SET} for Run # 10

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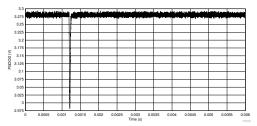


Figure 9-12. Typical V_{OUT} During a SS_{SET} for Run # Figure 9-13. Typical PGOOD_{SET} ≤ 1.6 V for Run # 10

10 Event Rate Calculations

Event rates were calculated for LEO(ISS) and GEO environments by combining CREME96 orbital integral flux estimations and simplified SEE cross-sections according to methods described in *Heavy Ion Orbital Environment Single-Event Effects Estimations* application report. We assume a minimum shielding configuration of 100 mils (2.54 mm) of aluminum, and "worst-week" solar activity (this is similar to a 99% upper bound for the environment). Using the 95% upper-bounds for the SEL and the SEB/SEGR, the event rate calculation for the SEL and the SEB/SEGR is shown on Table 10-1 and Table 10-2, respectively. It is important to note that this number is for reference since no SEL or SEB/SEGR events were observed.

Table 10-1. SEL Event Rate Calculations for Worst-Week LEO and GEO Orbits

Orbit Type	Onset LET _{EFF} (MeV-cm ² /mg)	CREME96 Integral FLUX (/day/cm²)	σSAT (cm²)	Event Rate (/day)	Event Rate (FIT)	MTBE (Years)
LEO (ISS)	43	6.4 × 10 ⁻⁴	9.23 × 10 ^{−8}	5.91 × 10 ⁻¹¹	2.5 × 10 ^{−3}	4.63 × 10 ⁷
GEO	73	2.2 × 10 ⁻³	9.20 ^ 10 *	2 × 10 ⁻¹⁰	8.4 × 10 ⁻³	1.36 × 10 ⁷

Table 10-2. SEB/SEGR Event Rate Calculations for Worst-Week LEO and GEO Orbits

Orbit Type	Onset LET _{EFF} (MeV-cm ² /mg)	CREME96 Integral FLUX (/day/cm²)	σSAT (cm²)	Event Rate (/day)	Event Rate (FIT)	MTBE (Years)
LEO (ISS)	43	6.4 × 10 ⁻⁴	1.21 × 10 ^{−7}	7.74 × 10 ⁻¹¹	3.2 × 10 ^{−3}	3.53 × 10 ⁷
GEO	45	2.2 × 10 ^{−3}	1.21 ~ 10	2.62 × 10 ⁻¹⁰	11 × 10 ^{−3}	1.04 × 10 ⁷

Table 10-3. Combined SET (1.8V) Event Rate Calculations for Worst-Week LEO and GEO Orbits

Orbit Type	Onset LET _{EFF} (MeV-cm ² /mg)	CREME96 Integral FLUX (/day/cm²)	σSAT (cm²)	Event Rate (/day)	Event Rate (FIT)	MTBE (Years)
LEO (ISS)	4	2.24 x 10 ³	1.66 x 10 ⁻⁴	3.72 x 10 ⁻¹	1.55 x 10 ⁷	7.36 x 10 ⁻³
GEO	l	2.70 x 10 ⁴	1.00 X 10	4.49 x 10 ⁰	1.87 x 10 ⁸	6.10 x 10 ⁻⁴

Table 10-4. VOUT SET (1.8V) Event Rate Calculations for Worst-Week LEO and GEO Orbits

Orbit Type	Onset LET _{EFF} (MeV-cm ² /mg)	CREME96 Integral FLUX (/day/cm²)	σSAT (cm²)	Event Rate (/day)	Event Rate (FIT)	MTBE (Years)
LEO (ISS)	1	2.24 x 10 ³	8.37 x 10 ⁻⁵	1.87 x 10 ⁻¹	7.81 x 10 ⁶	1.46 x 10 ⁻²
GEO		2.70 x 10 ⁴		2.26 x 10 ⁰	9.42 x 10 ⁷	1.21 x 10 ⁻³

Table 10-5. SS SEFI (1.8V) Event Rate Calculations for Worst-Week LEO and GEO Orbits

Orbit Type	Onset LET _{EFF} (MeV-cm ² /mg)	CREME96 Integral FLUX (/day/cm²)	σSAT (cm²)	Event Rate (/day)	Event Rate (FIT)	MTBE (Years)
LEO (ISS)	2	5.25 x 10 ²	8.27 x 10 ⁻⁵	4.34 x 10 ⁻²	1.81 x 10 ⁶	6.31 x 10 ⁻²
GEO	2	5.22 x 10 ³	0.27 x 10 -	4.31 x 10 ⁻¹	1.80 x 10 ⁷	6.35 x 10 ⁻³



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Table 10-6. Combined SET (3.3V) Event Rate Calculations for Worst-Week LEO and GEO Orbits

Orbit Type	Onset LET _{EFF} (MeV-cm ² /mg)	CREME96 Integral FLUX (/day/cm²)	σSAT (cm²)	Event Rate (/day)	Event Rate (FIT)	MTBE (Years)
LEO (ISS)	1	2.24 x 10 ³	1.49 x 10 ⁻⁴	3.34 x 10 ⁻¹	1.39 x 10 ⁷	8.20 x 10 ⁻³
GEO	'	2.70 x 10 ⁴	1.43 X 10	4.03 x 10 ⁰	1.68 x 10 ⁸	6.79 x 10 ⁻⁴

Table 10-7. VOUT SET (3.3V) Event Rate Calculations for Worst-Week LEO and GEO Orbits

Orbit Type	Onset LET _{EFF} (MeV-cm ² /mg)	CREME96 Integral FLUX (/day/cm²)	σSAT (cm²)	Event Rate (/day)	Event Rate (FIT)	MTBE (Years)
LEO (ISS)	1	2.24 x 10 ³	8.63 x 10 ⁻⁵	1.93 x 10 ⁻¹	8.05 x 10 ⁶	1.42 x 10 ⁻²
GEO	'	2.70 x 10 ⁴	0.03 X 10 °	2.33 x 10 ⁰	9.71 x 10 ⁷	1.18 x 10 ⁻³

Table 10-8. SS SEFI (3.3V) Event Rate Calculations for Worst-Week LEO and GEO Orbits

Orbit Type	Onset LET _{EFF} (MeV-cm ² /mg)	CREME96 Integral FLUX (/day/cm²)	σSAT (cm²)	Event Rate (/day)	Event Rate (FIT)	MTBE (Years)
LEO (ISS)	2	5.25 x 10 ²	6.30 x 10 ⁻⁵	3.31 x 10 ⁻²	1.38 x 10 ⁶	8.28 x 10 ⁻²
GEO	2	5.22 x 10 ³	0.30 X 10 ·	3.29 x 10 ⁻¹	1.37 x 10 ⁷	8.34 x 10 ⁻³

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11 Summary

The purpose of this study was to characterize the effect of heavy-ion irradiation on the single-event effect (SEE) performance of the TPS7H4010-SEP synchronous step-down POL converter. Heavy-ions with LET_{EFF} = $43 \, \text{MeV} \cdot \text{cm}^2/\text{mg}$ were were used for the SEE characterization campaign. Flux of 10^4 and 10^5 ions/cm²·s and fluences ranging from 3×10^6 to 1×10^7 ions/cm² per run were used for the characterization. The SEE results demonstrated that the TPS7H4010-SEP POL is free of destructive SEB events and SEL-free up to LET_{EFF} = $43 \, \text{MeV} \cdot \text{cm}^2/\text{mg}$ and across the full electrical specifications. Transients at LET_{EFF} = $43 \, \text{MeV} \cdot \text{cm}^2/\text{mg}$ on V_{OUT} and PGOOD are presented and discussed. CREME96-based worst-week event-rate calculations for LEO(ISS) and GEO orbits for the DSEE are presented for reference.

12 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Revision * (December 2020) to Revision A (April 2023)		Page
•	Changes made throughout explaining the ions used while testing	3
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Changes from Revision A (April 2023) to Revision B (November 2024)		
•	Changes SSET to SSEFI throughout the document	
•	Updated to show all ions used during testing	10
	Updated Summary of TPS7H4010-SEP SEB Test Condition and Results table	
•	Added Weibull Parameters for V _{OUT} = 1.8V and 3.3V Cases table	16
•	Added Weibull FITs for each V _{OUT} case figure	16
	Updated Event Rate Calculations table data (added tables 11-3 through 11-8)	
	•	

A Total Ionizing Dose from SEE Experiments

The production TPS7H4010-SEP POL is rated to a total ionizing dose (TID) of 20 krad(Si). In the course of the SEE testing, the heavy-ion exposures delivered ≈10 krad(Si) per 10⁷ ions/cm² run. The cumulative TID exposure over all runs was determined to be between 3 krad(Si) to 20 krad(Si), for each device. All eight production TPS7H4010-SEP devices used in the studies described in this report stayed within specification and were fully-functional after the heavy-ion SEE testing was completed.

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