

# Application Note

## TPSI3050-Q1 Change List



### ABSTRACT

Pre-production samples of the TPSI3050-Q1 are available for customers using orderable PTPSI3050QDWZRQ1. Pre-production samples can not be fully representative of the final revision of the production device. This errata summarizes any known discrepancies that have been identified with pre-production samples and the final product.

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### 1 Introduction

The TPSI3050-Q1 is a fully integrated isolated switch driver, which when combined with an external power switch, forms a complete isolated solid state relay solution. With a nominal gate drive voltage of 10 V 1.5-A peak source current and 3-A peak sink current, a large variety of external power switches can be chosen to meet a wide range of applications.

### 2 Errata Details

The following summarizes known errata.

**Table 2-1. PTPSI3050QDWZRQ1 Errata Summary**

Errata Number	Description
1	In two-wire mode, under some conditions, the falling edge of EN can not be recognized, leading to VDRV being asserted low after a duration of $t_{HL\_VDRV\_PD}$ .

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