

TMS320C674x/OMAP-L1x Processor Security

The generic secure environment is supported by hardware within the C674x DSP core. This allows critical code to be executed in a secure environment, hiding sensitive information from the outside world with the help of the following hardware features:

- Secure thread support – hardware support that allows trusted code to be executed from secure ROM or secure RAM.
- Protected transitions between the secure and non-secure world via Secure Kernel software provided in the secure ROM.
- Ability to secure portions of internal and external RAM. Note that external RAM is only secure from a software point of view and is still vulnerable to physical device attacks if its contents are not encrypted.
- Secure ROM, containing secure boot, cryptographic, and Secure Kernel support.

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Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
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