



Vin +4Vdc to +42Vdc  
(startup needs >5Vin)

Vout +5V @ 200mA

Vout -5V @ -200mA

RHPZ @ 8.22kHz, set Fco to 2.5kHz  
load pole 531Hz, set ZERO here  
set POLE to 10x Fco

controller is negative referenced

max sw current 0.93Arms / 1.28Apk

tss 10ms

Fsw 300kHz

Vref +800mV

Vref -1.193V

Vref -1.186V

connect NWA Gnd here

<< GROUND is here

Revision History

| Revision | Notes                            |
|----------|----------------------------------|
| A        | *** preliminary ***              |
| B        | *** changed U2, added PI filter  |
| C        | *** changed U2 CMOS to U2 BiCMOS |

NOTES:

- 1) R8 for test purposes only
- 2) design is able to handle down to 3Vin w/ limited performance

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 SVN Rev: Version control disabled  
 Drawn By: B.Geck  
 Engineer: B.Geck

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 Project Title: low noise dual bias supply  
 Sheet Title:  
 Assembly Variant: [No Variations] | Sheet: 1 of 1  
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 Contact: <http://www.ti.com/support>



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Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265  
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