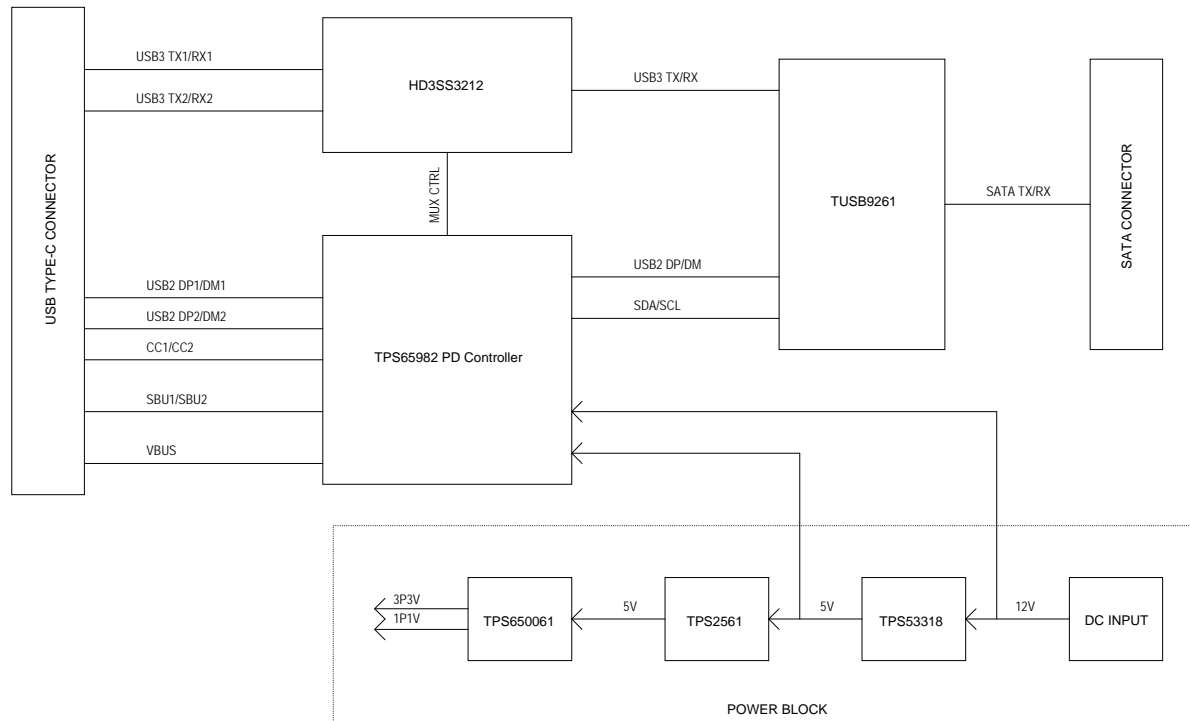
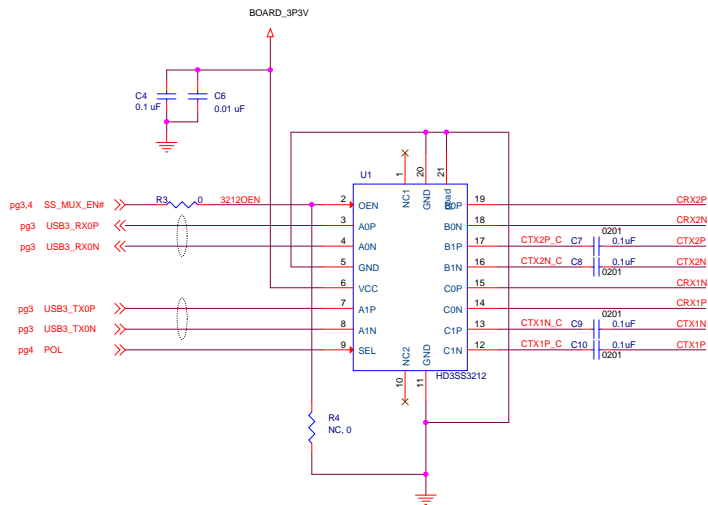


TIDA-00882: USB TYPE-C HDD WITH USB POWER DELIVERY

REV. A

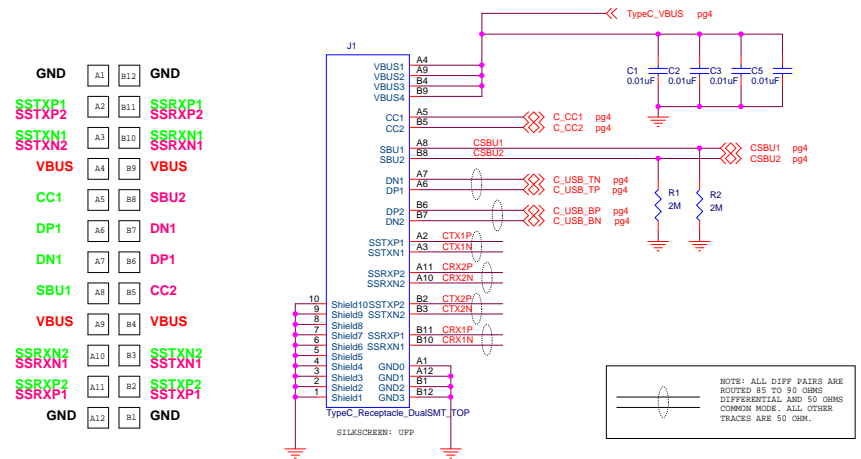


USB3.1 Mux



Port select pin (SEL). Internally tied to GND via 100k resistor.
 L: Port A to Port B
 H: Port A to Port C

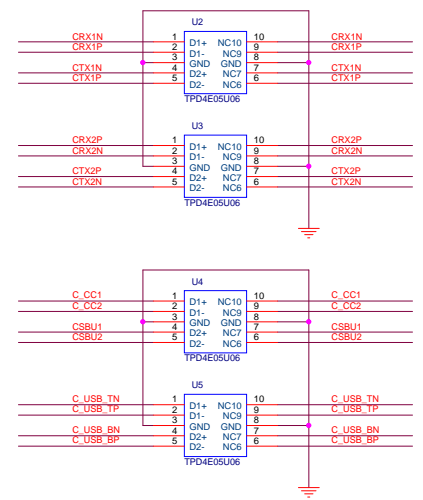
Type-C Receptacle



NOTE: ALL DIFF PAIRS ARE ROUTED 85 TO 90 OHMS DIFFERENTIAL AND 50 OHMS COMMON MODE. ALL OTHER TRACES ARE 50 OHM.

ESD Components

Place near UFP connector in pass-through position with no stub

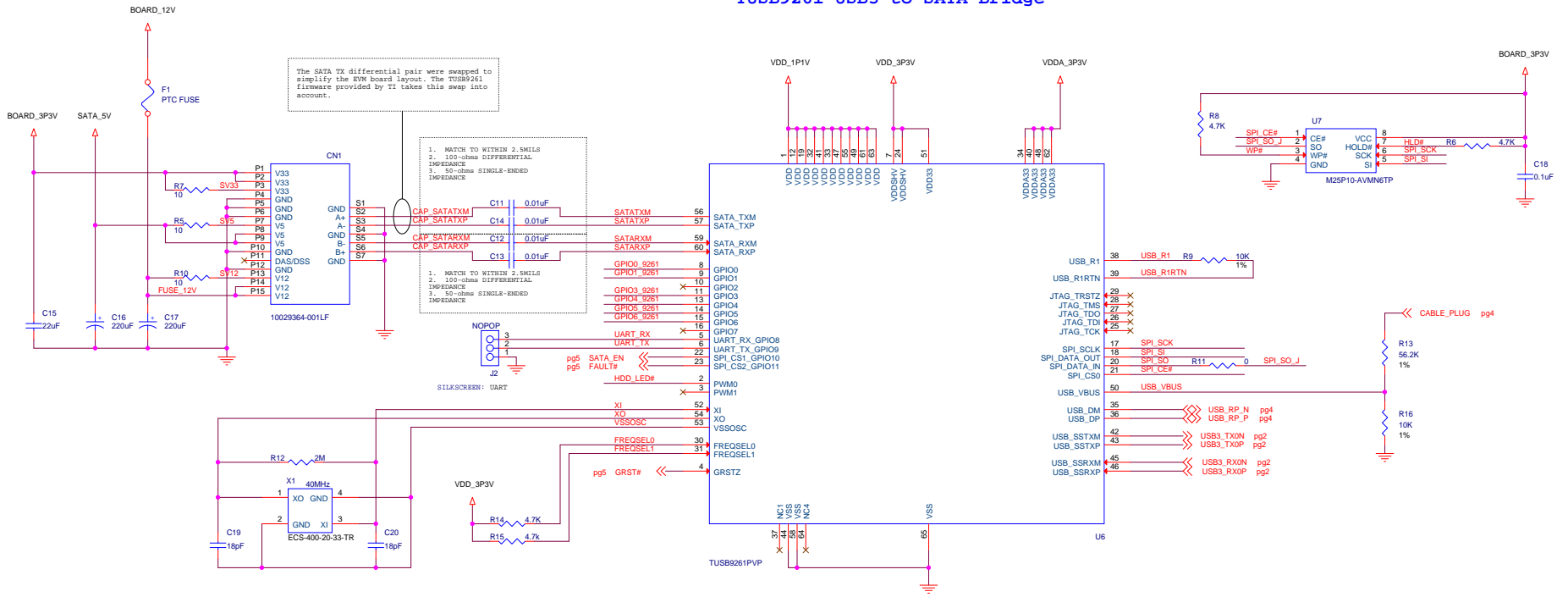


TEXAS INSTRUMENTS

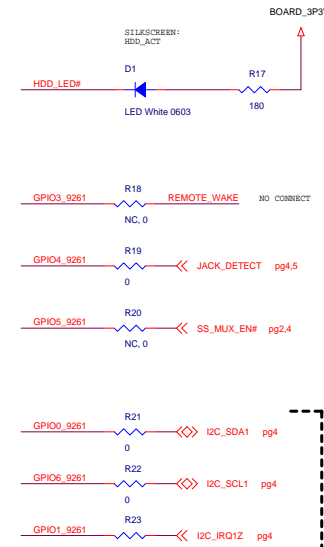
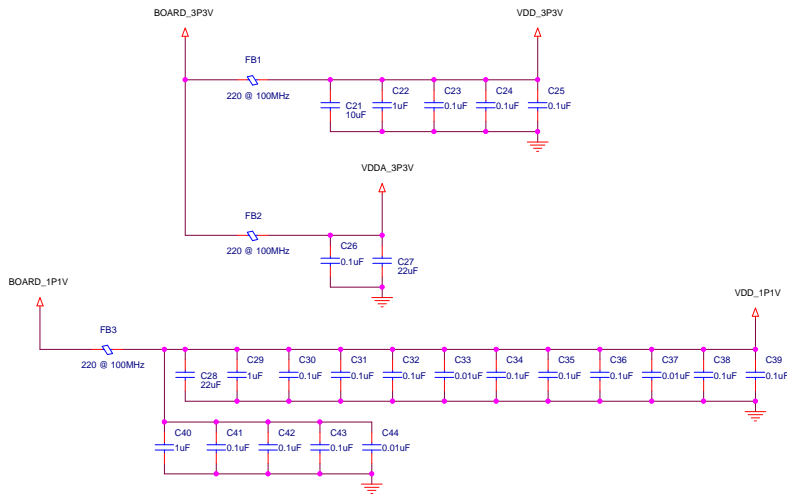
TIDA-00882: USB Type-C HDD: HD3SS3212

SIZE C	DWG NO:
SCALE: NONE	Monday, November 30, 2015 Sheet 2 of 5

TUSB9261 USB3-to-SATA Bridge



DECOUPLING

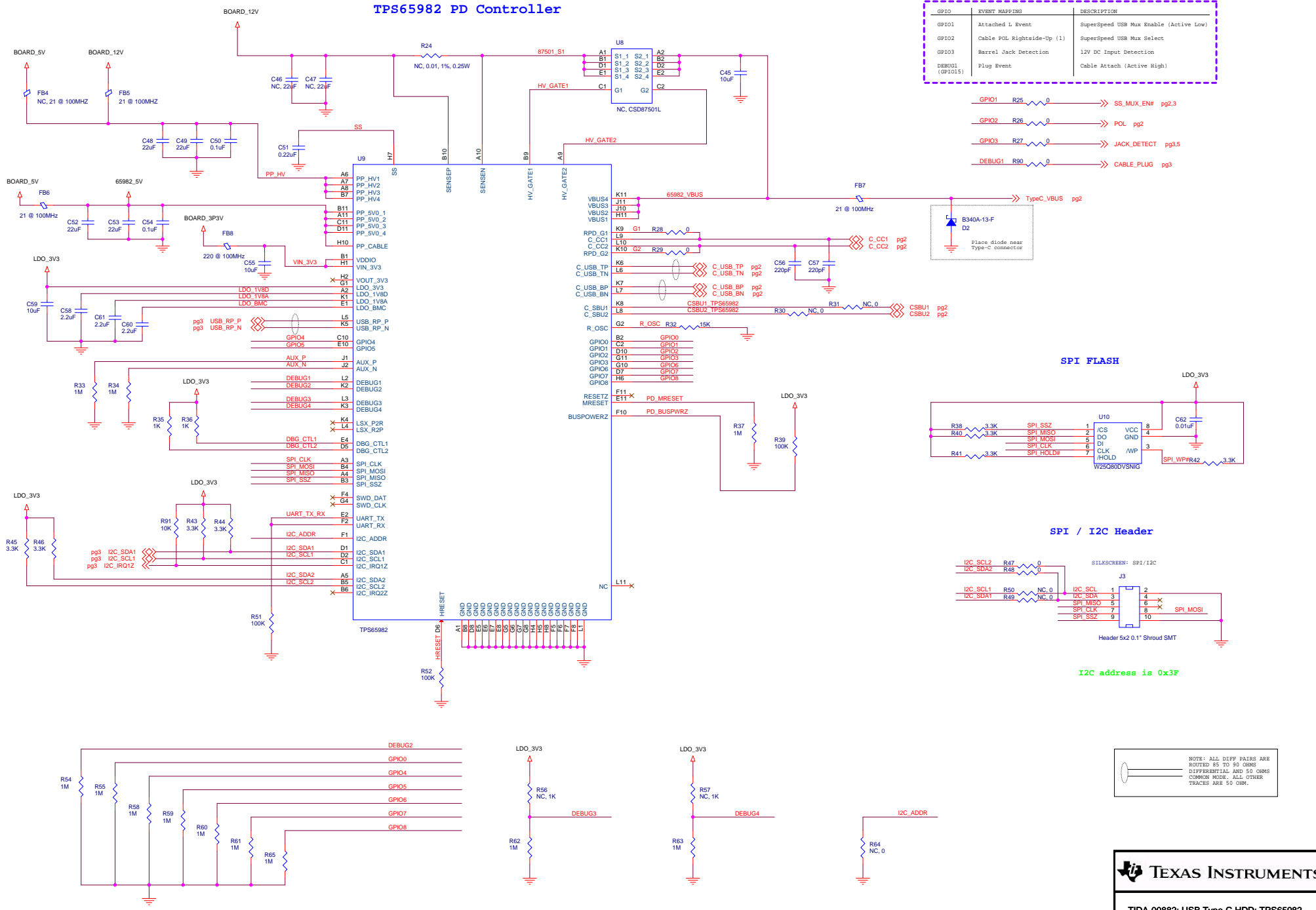


TEXAS INSTRUMENTS

TIDA-00882: USB Type-C HDD: TUSB9261

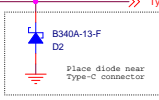
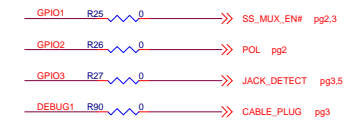
SIZE C	DWG NO:
SCALE: NONE	Monday, November 30, 2015 Sheet 3 of 5

TPS65982 PD Controller

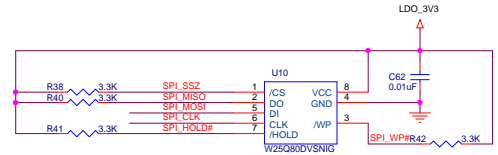


GPIO CONFIGURATION

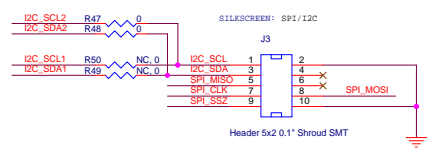
GPIO	EVENT MAPPING	DESCRIPTION
GPIO1	Attached L Event	SuperSpeed USB Mux Enable (Active Low)
GPIO2	Cable POL Rightside-Up (1)	SuperSpeed USB Mux Select
GPIO3	Barrel Jack Detection	12V DC Input Detection
DEBUG1 (GPIO15)	Plug Event	Cable Attach (Active High)



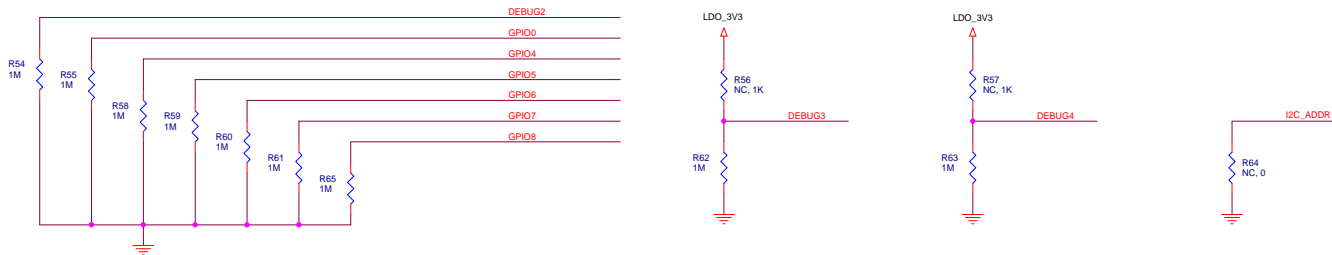
SPI FLASH



SPI / I2C Header



I2C address is 0x3F



Note: If GPIOs are configured as "No Event Mapped" (i.e. outputs), the 1Mohm pull-down resistors can be omitted.

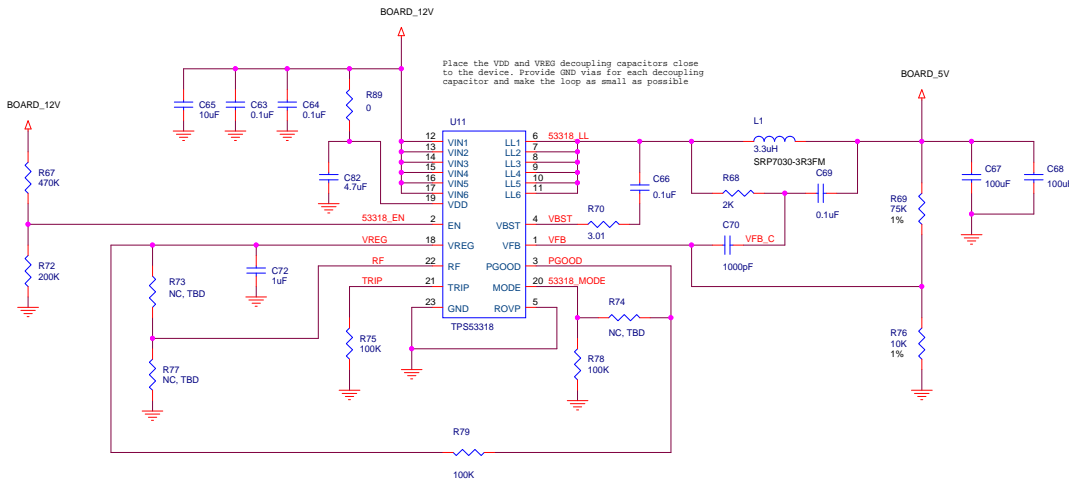
NOTE: ALL DIFF PAIRS ARE ROUTED 85 TO 90 OHMS DIFFERENTIAL AND 50 OHMS COMMON MODE. ALL OTHER TRACES ARE 50 OHM.

TIDA-00882: USB Type-C HDD: TPS65982

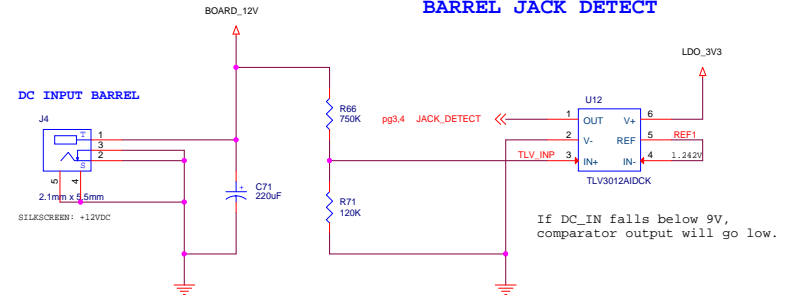
SIZE C	DWG NO:
SCALE: NONE	Monday, November 30, 2015 Sheet 4 of 5

5.25V (7A) Buck Converter

Input Range 4.5 - 22V.
Auto-Skip Mode, 500kHz, 1.4ms soft start.

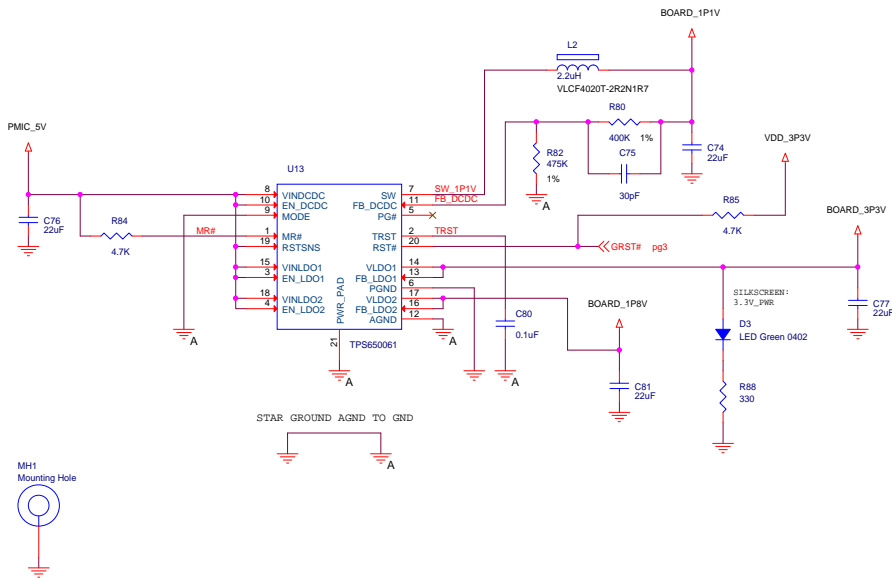


BARREL JACK DETECT

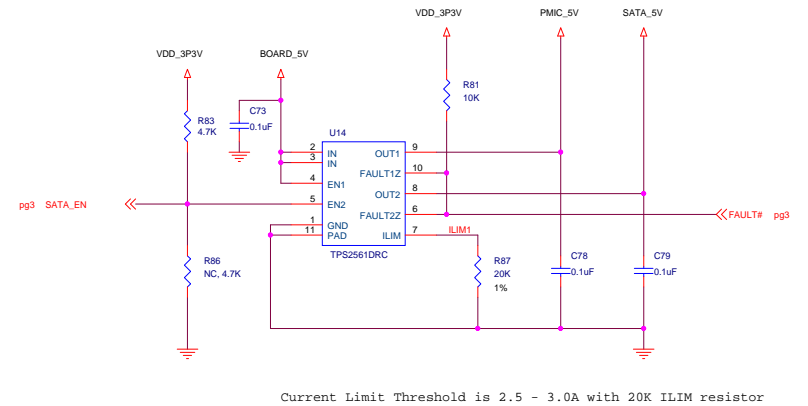


3.3V (300mA) LDO, 1.8V (300mA) LDO, and 1.1V (1A) Step-Down Converter

[Note: 1.8V is not utilized in this design]



Current Limiting Power Switch



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