

# LMR38020S-Q1 PSPICE Transient Model Features and Limitations

```
*****
*
* Model Usage Notes:
*
* A. The following features have been modeled
* 1. Output Voltage Setting
* 2. Frequency Selection
* 3. Internal Fixed Soft-Start
* 4. Minimum ON-Time, Minimum OFF-Time and Frequency Foldback
* 5. Low-side FET Zero-Crossing
* 6. Current Sense and Positive Overcurrent Protection(OCP)
* 7. Over Voltage Protection(OVP)
* 8. Power Good
* 9. UVLO Protection
* 10. BOOT functionality
*
*
* B. Features have not been modelled
* 1. Operating Quiescent Current
* 2. Shutdown Current
* 3. Temperature dependent characteristics
* 4. Steady State
* 5. Ground pins have been tied to 0V internally. Therefore, this model cannot
*    be used for inverting topologies.
*
*****
```