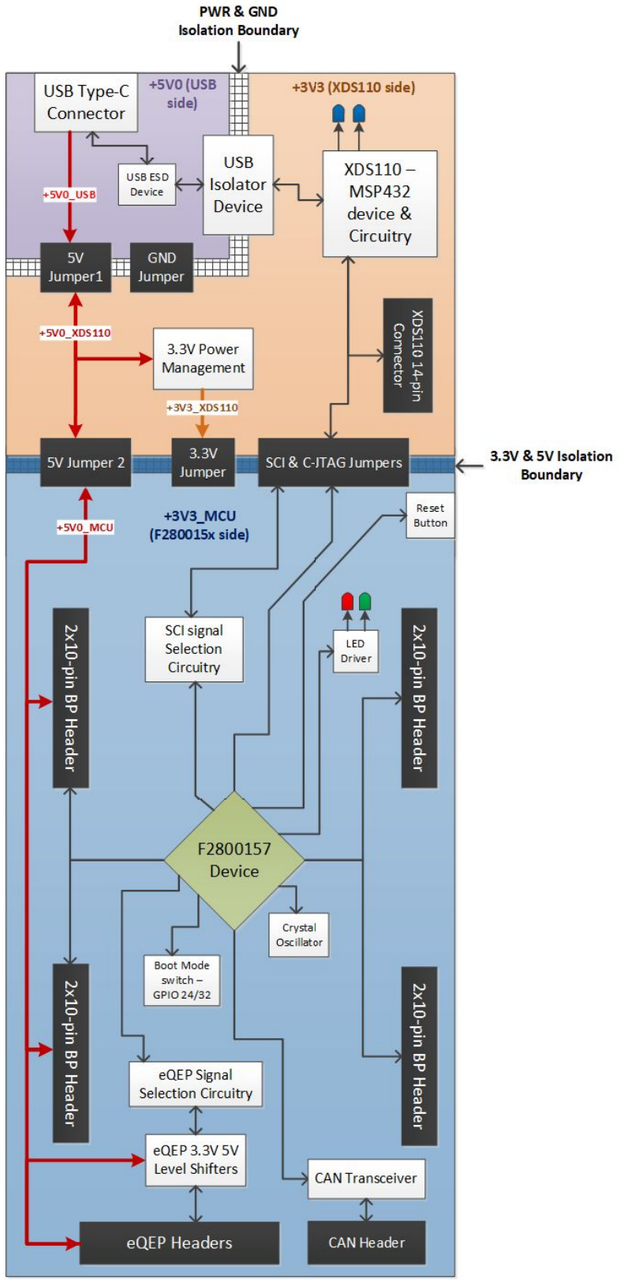

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TID #:		Project Title: LAUNCHXL-F2800157	
Number: MCU110	Rev: A	Sheet Title:	
SVN Rev: 42c87052b884e9ec54069[illegible]1			Sheet: 1 of 7
Drawn By:		File: MCU110A_Block_Diagram.SchDoc	Size: B
Engineer: Peter Luong		Contact: http://www.ti.com/support	

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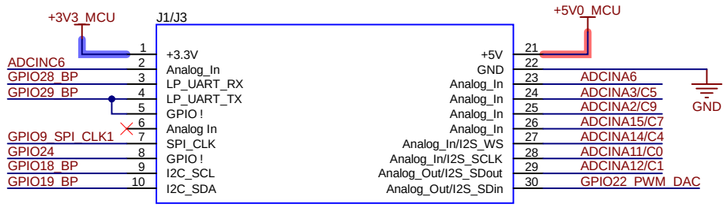
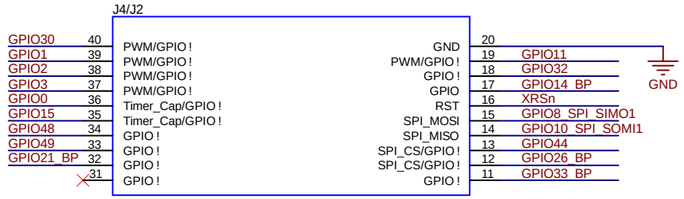
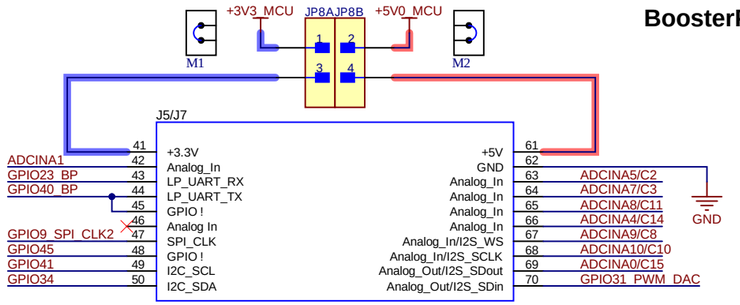
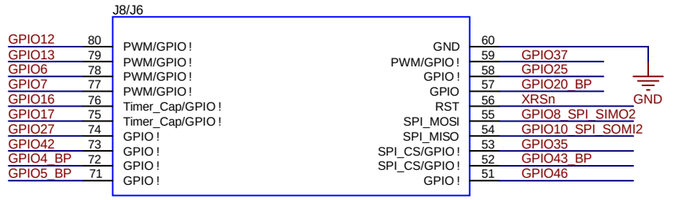
BoosterPack Headers Site 1 (Top)



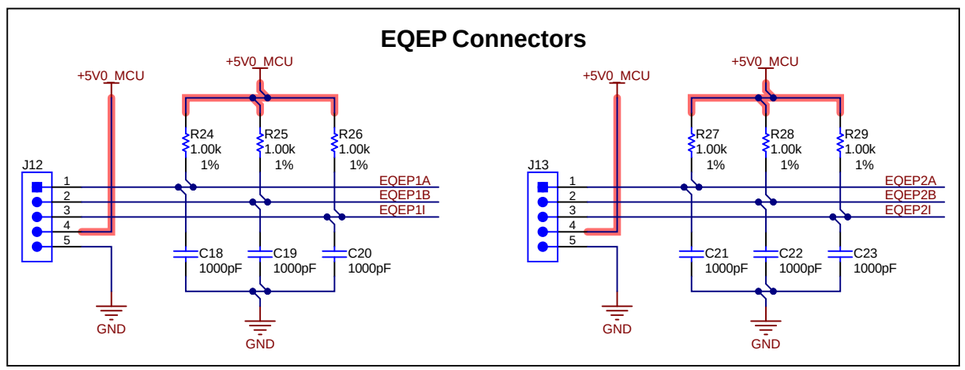


BoosterPack Headers Site 2 (Bottom)

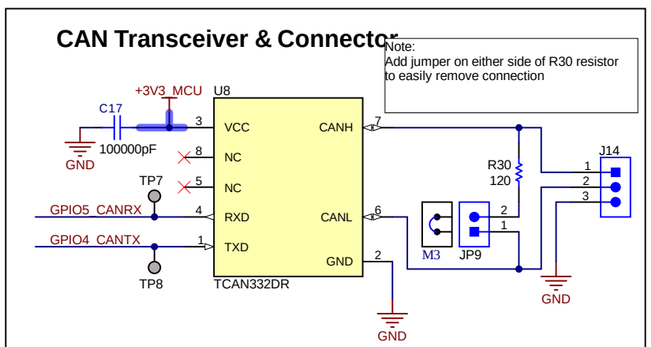




EQEP Connectors



CAN Transceiver & Connector



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Number: MCU110	Rev: A	Sheet Title:	
SVN Rev: 42c87052b884e9ec54069			Sheet: 2 of 7
Drawn By:		File: MCU110A_Connectors.SchDoc	Size: B
Engineer: Peter Luong		Contact: http://www.ti.com/support	

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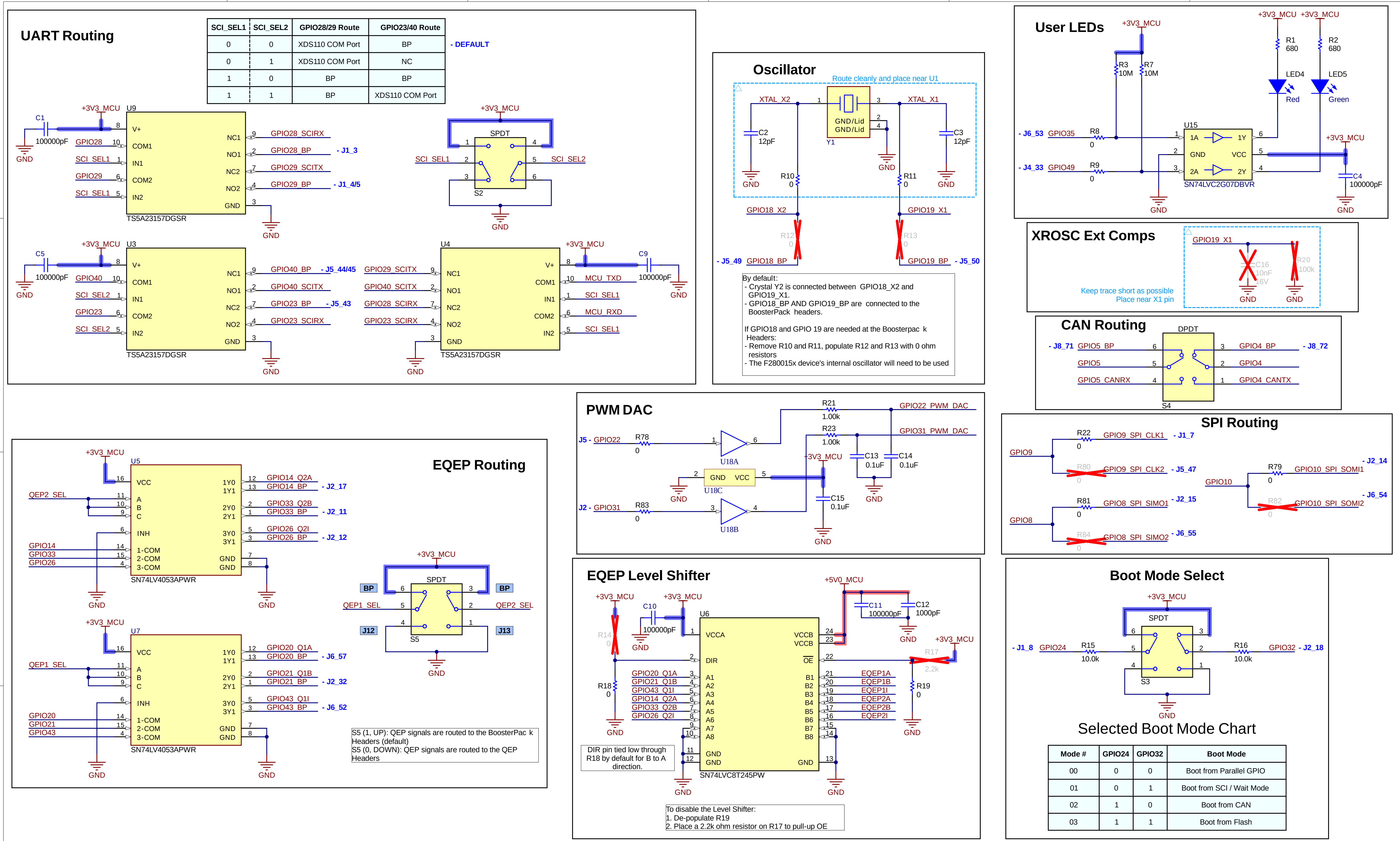
SCI_SEL1	SCI_SEL2	GPIO28/29 Route	GPIO23/40 Route	
0	0	XDS110 COM Port	BP	- DEFAULT
0	1	XDS110 COM Port	NC	
1	0	BP	BP	
1	1	BP	XDS110 COM Port	



Selected Boot Mode Chart

Mode #	GPIO24	GPIO32	Boot Mode
00	0	0	Boot from Parallel GPIO
01	0	1	Boot from SCI / Wait Mode
02	1	0	Boot from CAN
03	1	1	Boot from Flash

Orderable: LAUNCHXL-F2800157
TID #:
Number: MCU110 Rev: A
SVN Rev: 42c87052b884e9ec54069dfa59ebcfda6a162901
Drawn By:
Engineer: Peter Luong
Designed for: Public Release
Project Title: LAUNCHXL-F2800157
Sheet Title:
File: MCU110A_AltRouting_Misc.SchDoc
Contact: http://www.ti.com/support
Mod. Date: 3/17/2023
Sheet: 3 of 7
Size: B
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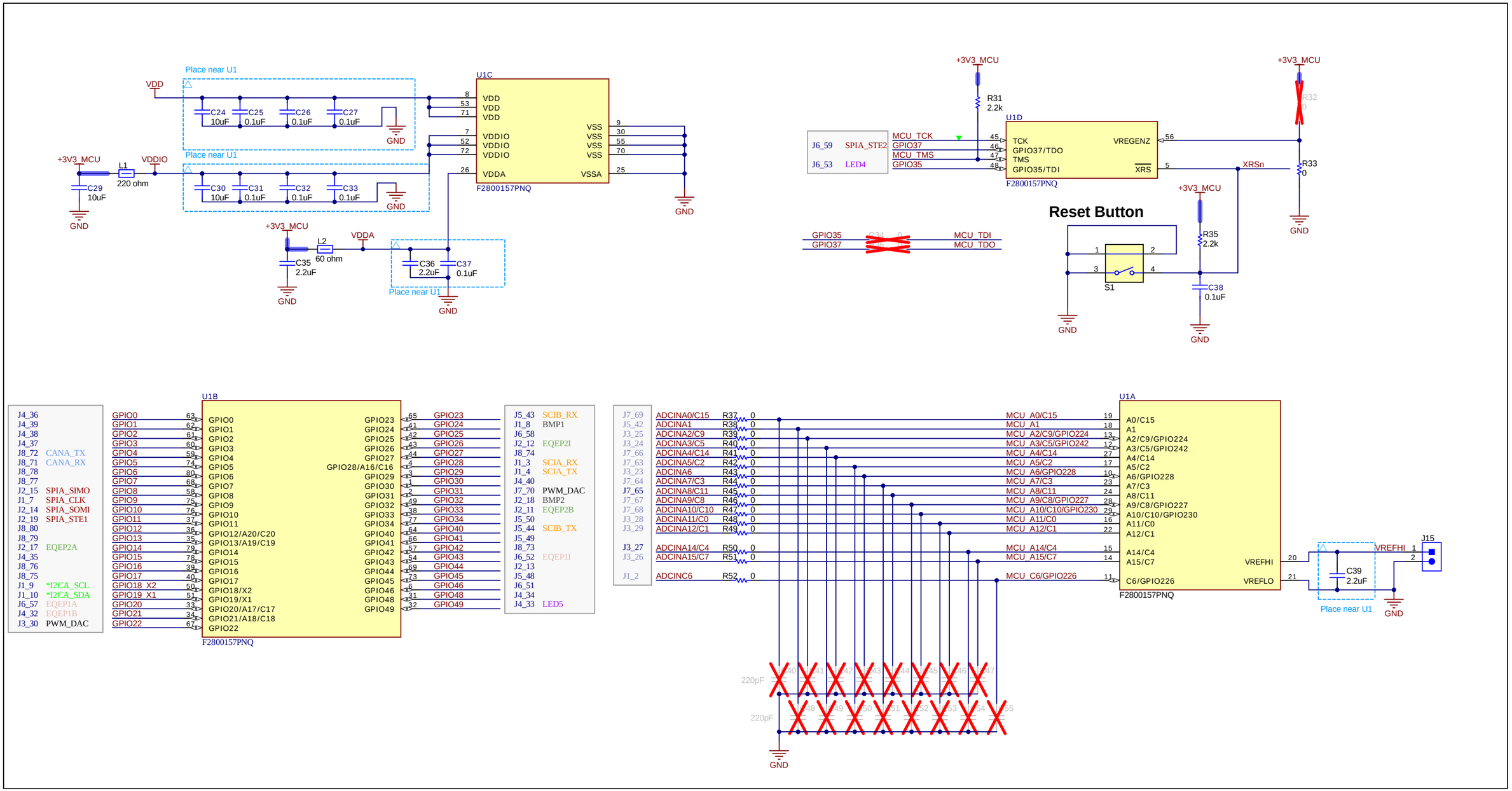

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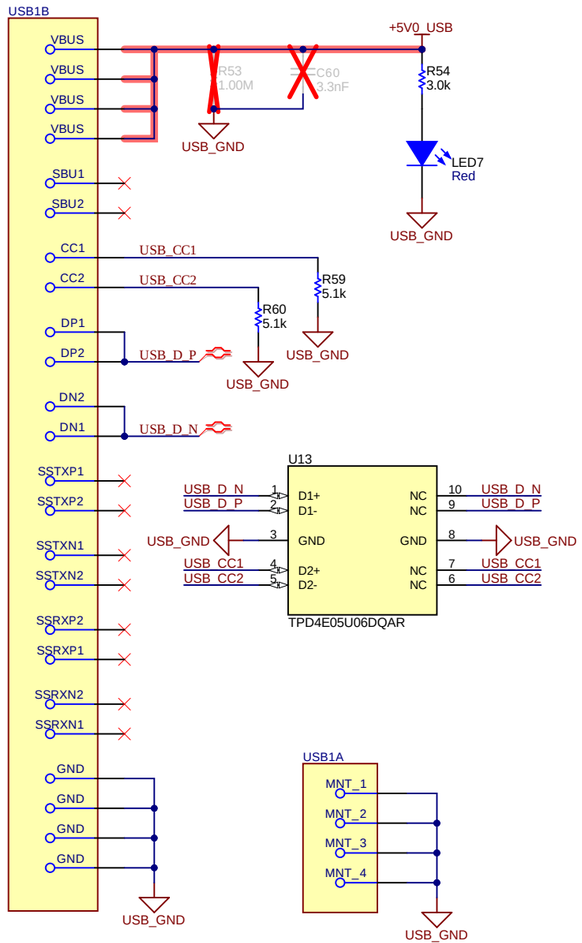
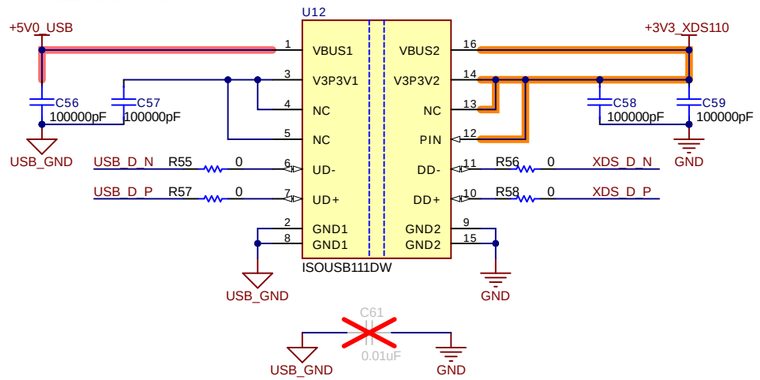
TID #:		Project Title: LAUNCHXL-F2800157	
Number: MCU110	Rev: A	Sheet Title:	
SVN Rev: 42c87052b884e9ec54069			Sheet: 4 of 7
Drawn By:		File: MCU110A_F2800157_Device.SchDoc	Size: B
Engineer: Peter Luong		Contact: http://www.ti.com/support	

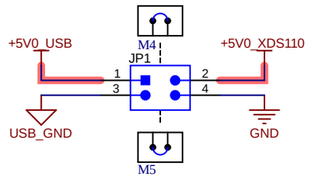
USB-C Connector



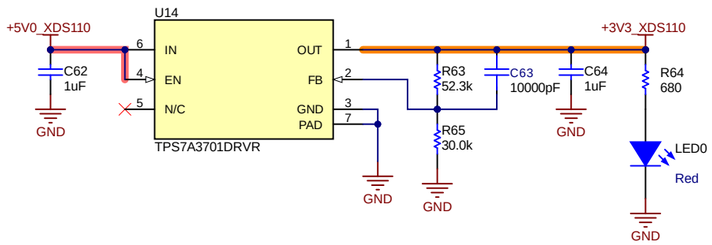
USB Isolation



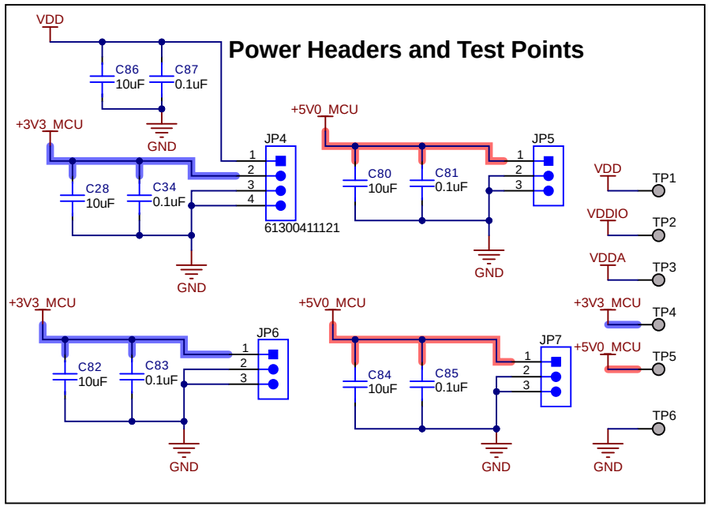
PWR & GND Isolation Boundary



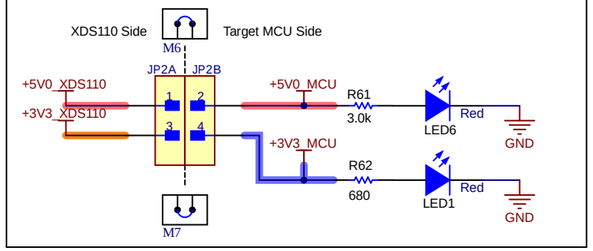
5V to 3.3V



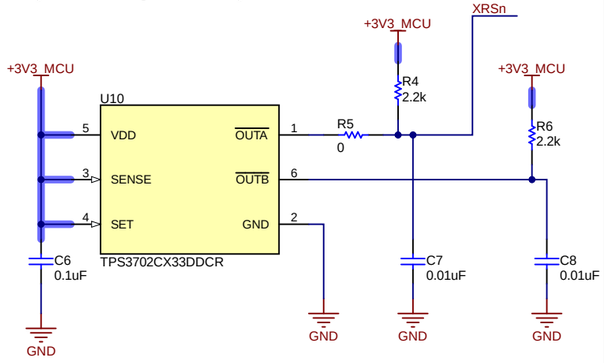
Power Headers and Test Points



5V & 3.3V Isolation Boundary



System Supervisory Circuit



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TID #:		Project Title: LAUNCHXL-F2800157	
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SVN Rev: 42c87052b884e9ec54069d8a53ed8d0dbaa16201			Sheet: 5 of 7
Drawn By:		File: MCU110A_USB_and_Power.SchDoc	Size: B
Engineer: Peter Luong		Contact: http://www.ti.com/support	

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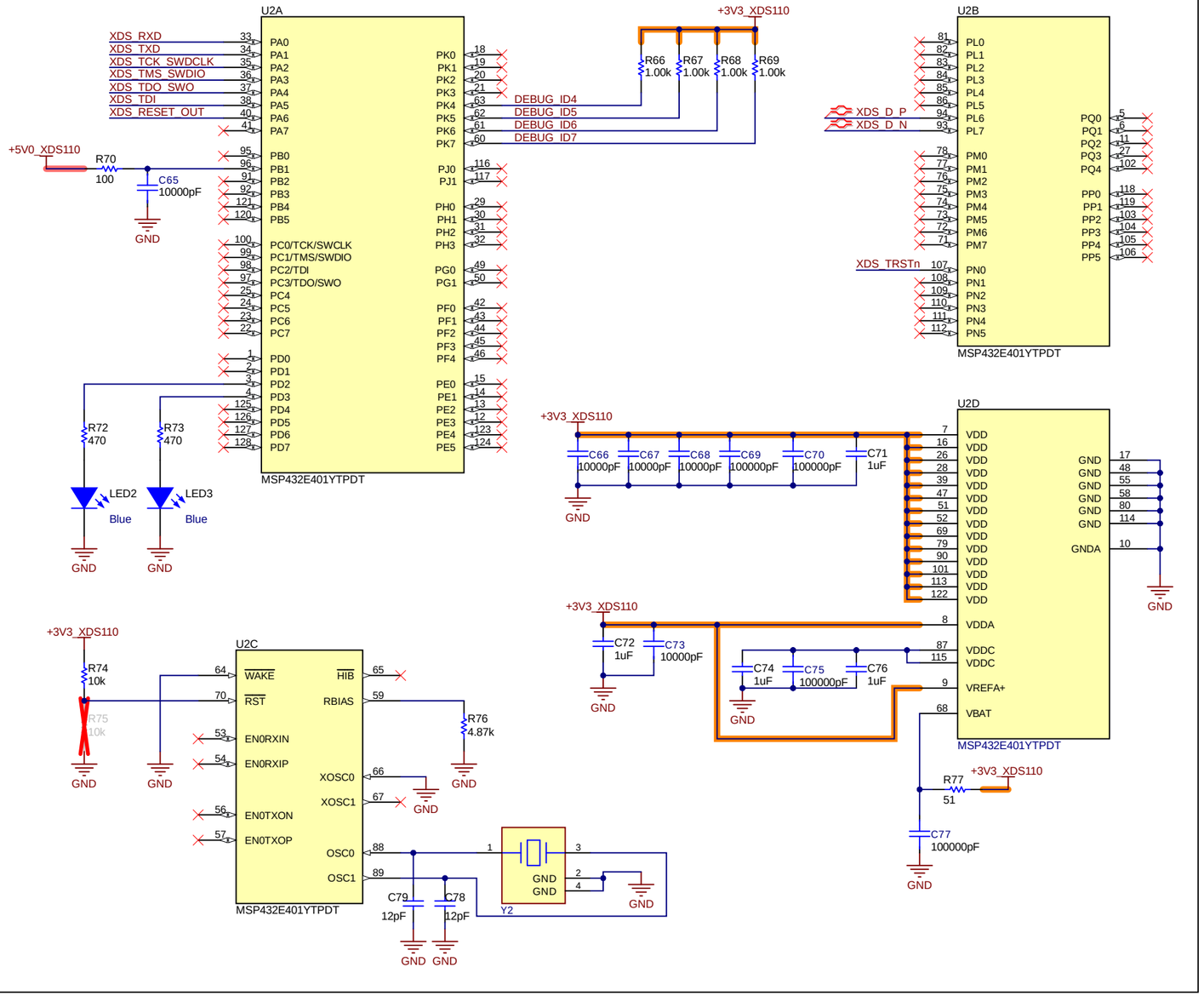
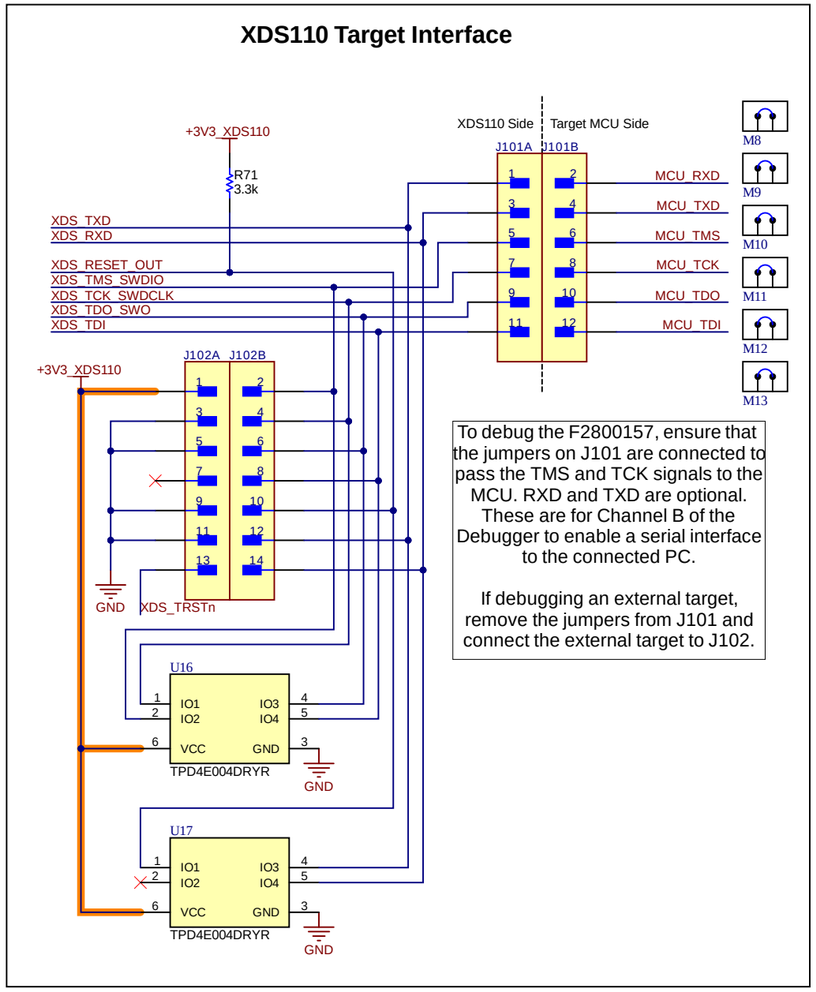
XDS110 Device



XDS110 Target Interface



To debug the F2800157, ensure that the jumpers on J101 are connected to pass the TMS and TCK signals to the MCU. RXD and TXD are optional. These are for Channel B of the Debugger to enable a serial interface to the connected PC.

If debugging an external target, remove the jumpers from J101 and connect the external target to J102.

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SVN Rev: 42c87052b884e9ec54069[illegible]6201			Sheet: 6 of 7
Drawn By:		File: MCU110A_XDS110_MCU.SchDoc	Size: B
Engineer: Peter Luong		Contact: http://www.ti.com/support	

FID1 FID2 FID3

FID4 FID5 FID6

MH1 MH2

MH3 MH4

PCB Number: MCU110
PCB Rev: A

Logo1
PCB
LOGO
Texas Instruments

CE Logo1

Logo2
PCB
LOGO
FCC disclaimer

Logo3
PCB
LOGO
WEEE logo

Logo4
PCB
LOGO
Texas Instruments

ZZ1
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ2
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ3
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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SVN Rev:		Assembly Variant: 001	Sheet: 7 of 7
Drawn By:		File: MCU110A_Hardware.SchDoc	Size: B
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