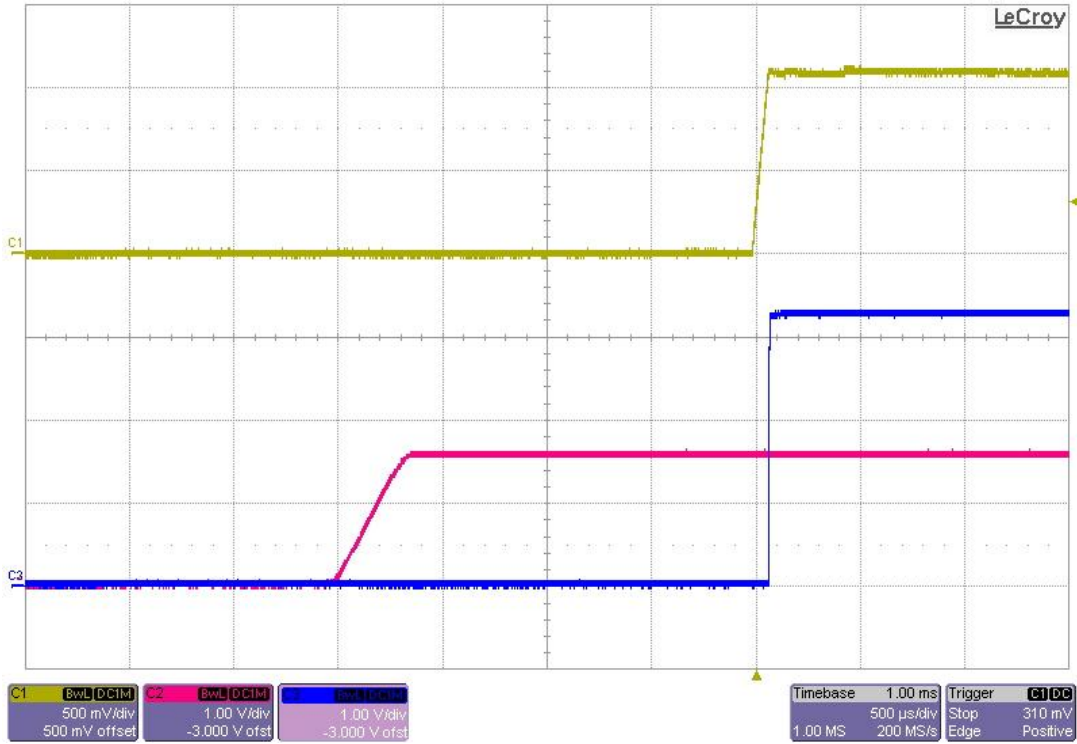
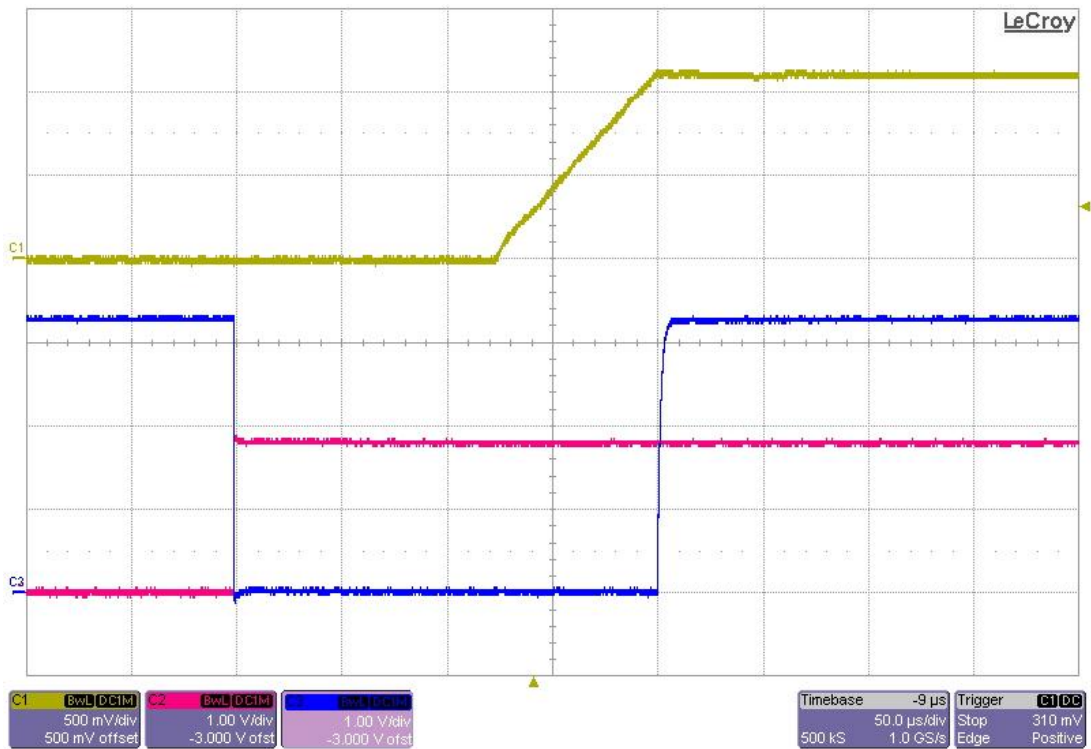


## 1 Turn On

The photo below shows Vout (Yellow, Ch1), EN (Pink, Ch2), and PGood (Blue, Ch3), during a cold boot.

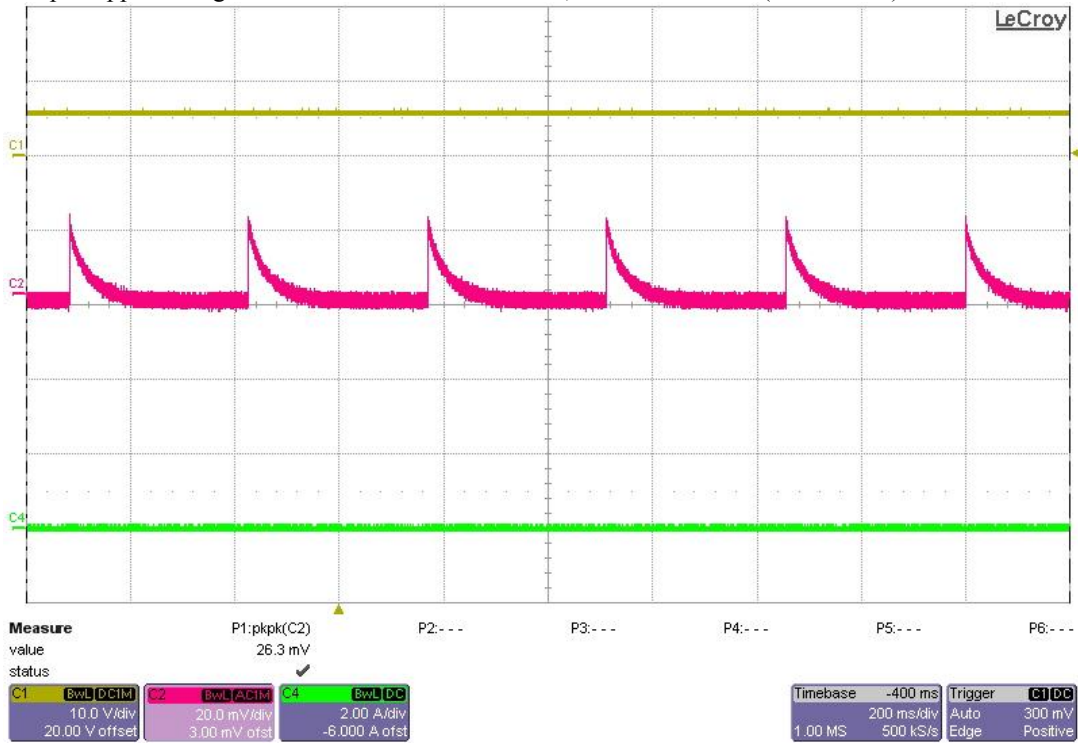


The photo below shows Vout (Yellow, Ch1), EN (Pink, Ch2), and PGood (Blue, Ch3), when EN is toggled.

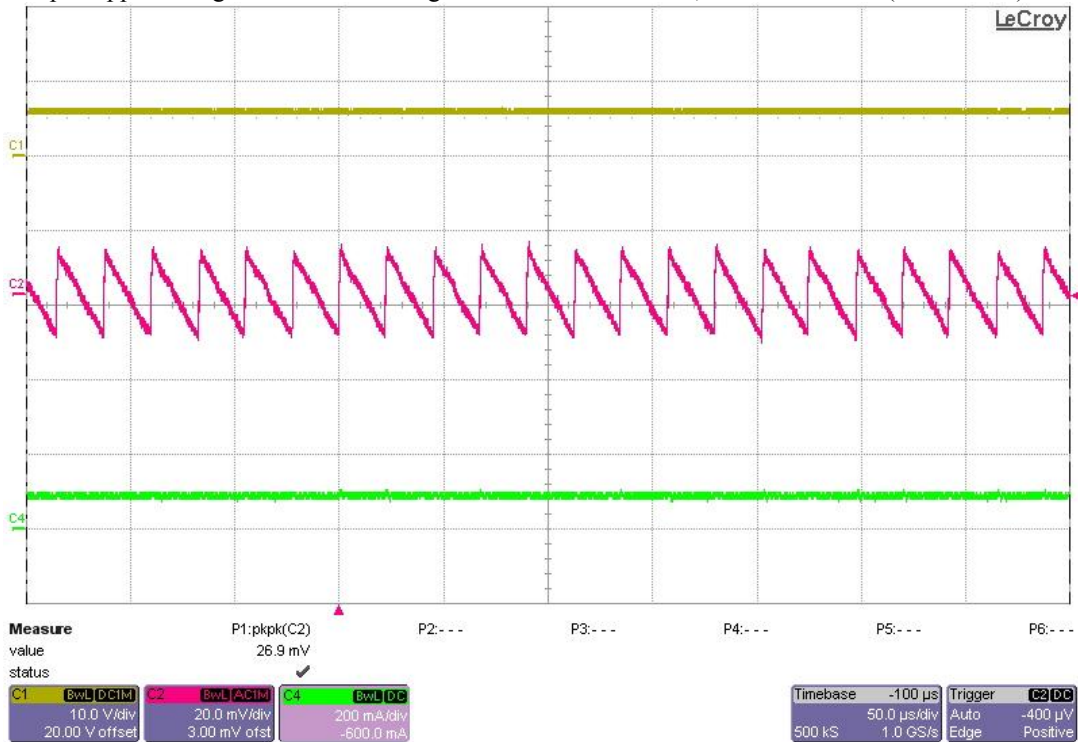


## 2 Output Ripple Voltage - 6Vin

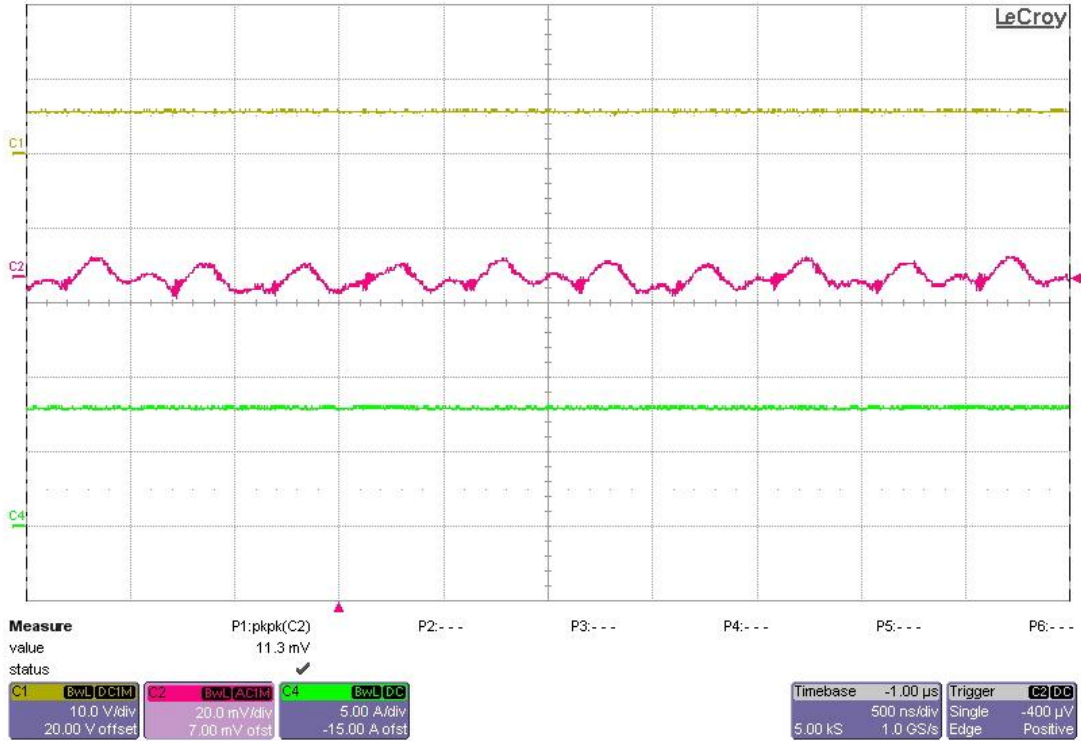
The output ripple voltage is shown below with  $V_{in}=6V$ ,  $I_{out} = \text{No load}$ . (20mV/DIV)



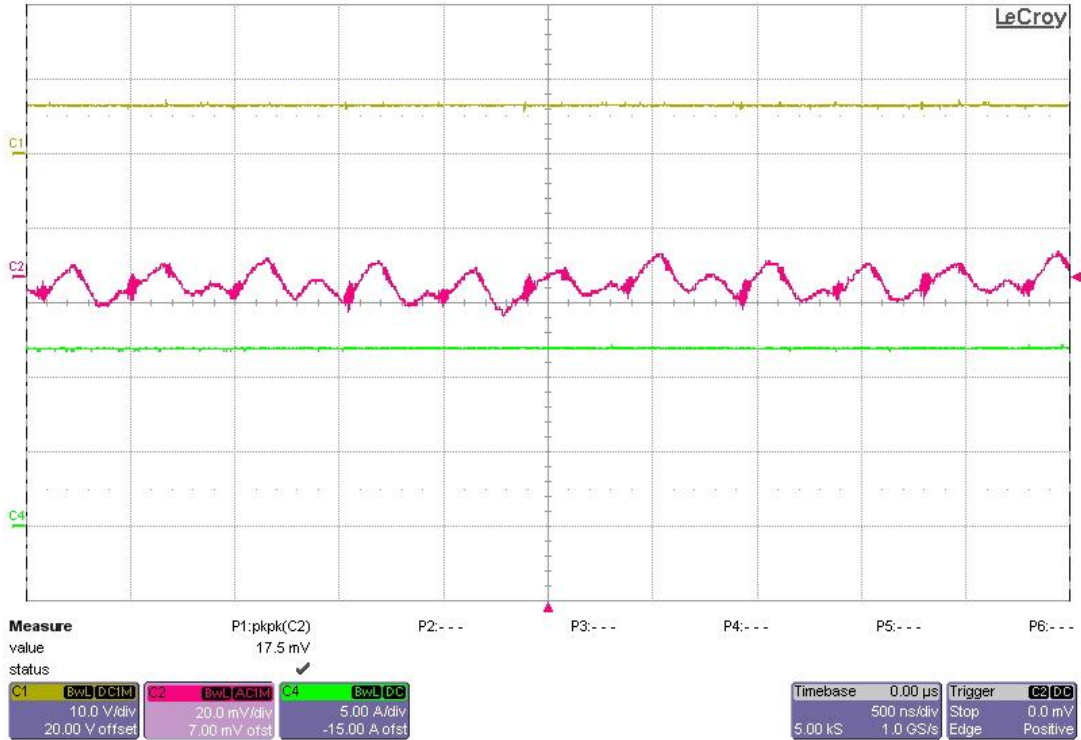
The output ripple voltage is shown in the figure below with  $V_{in}=6V$ ,  $I_{out} = 100mA$ . (20mV/DIV)



The output ripple voltage is shown below with  $V_{in} = 6V$ ,  $I_{out} = 8A$ . (20mV/DIV)

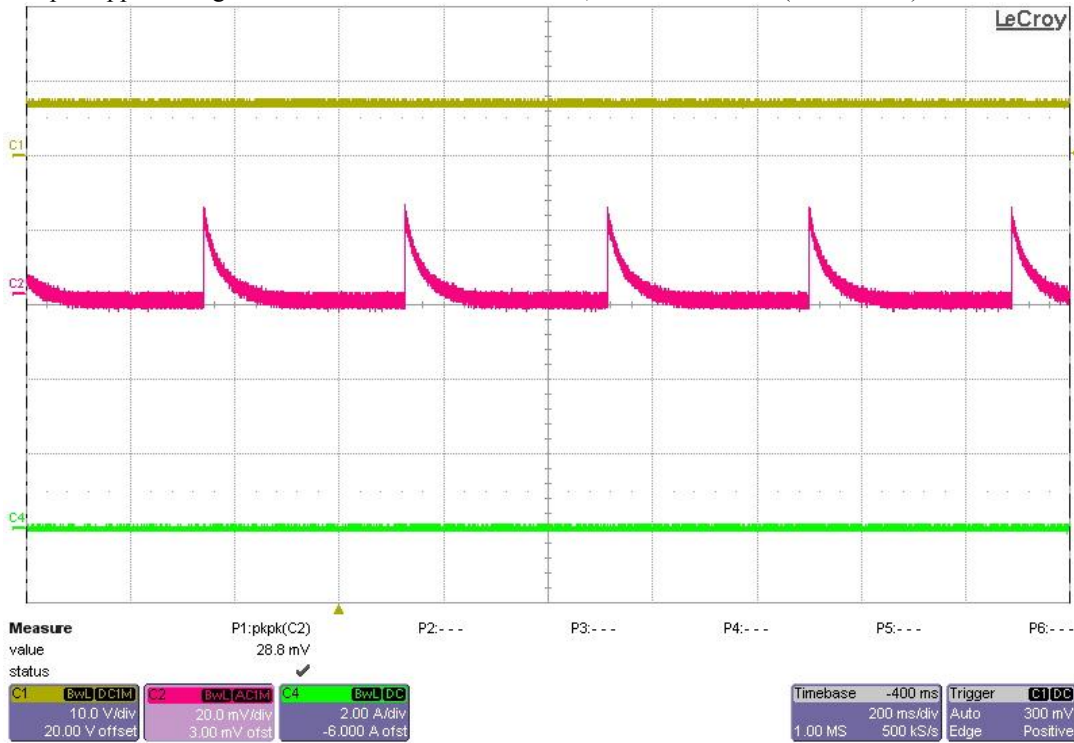


The output ripple voltage is shown in the figure below with  $V_{in} = 6V$ ,  $I_{out} = 12A$ . (20mV/DIV)

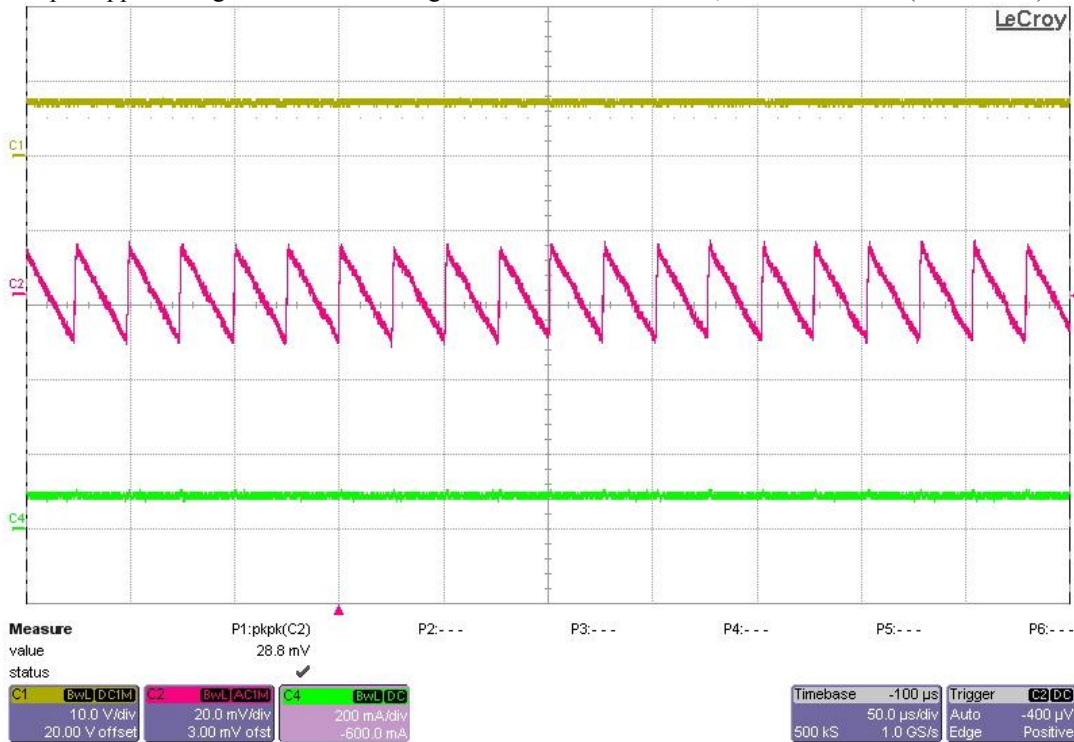


### 3 Output Ripple Voltage – 7.2Vin

The output ripple voltage is shown below with  $V_{in} = 7.2V$ ,  $I_{out} = \text{No load}$ . (20mV/DIV)

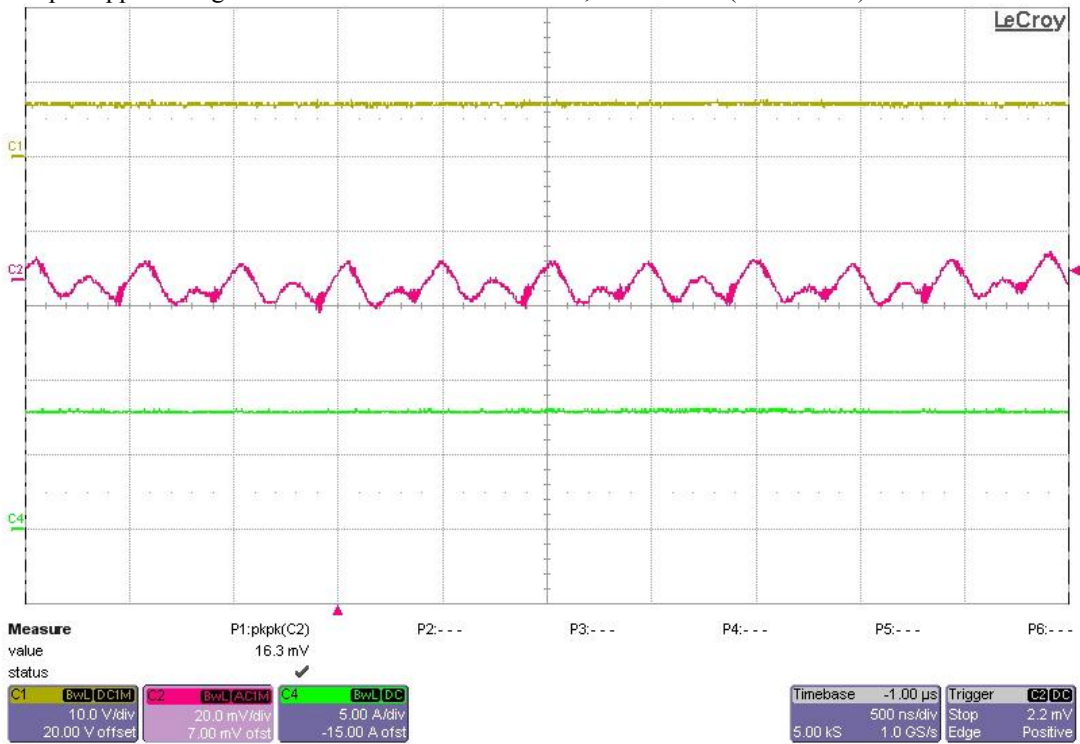


The output ripple voltage is shown in the figure below with  $V_{in} = 7.2V$ ,  $I_{out} = 100mA$ . (20mV/DIV)

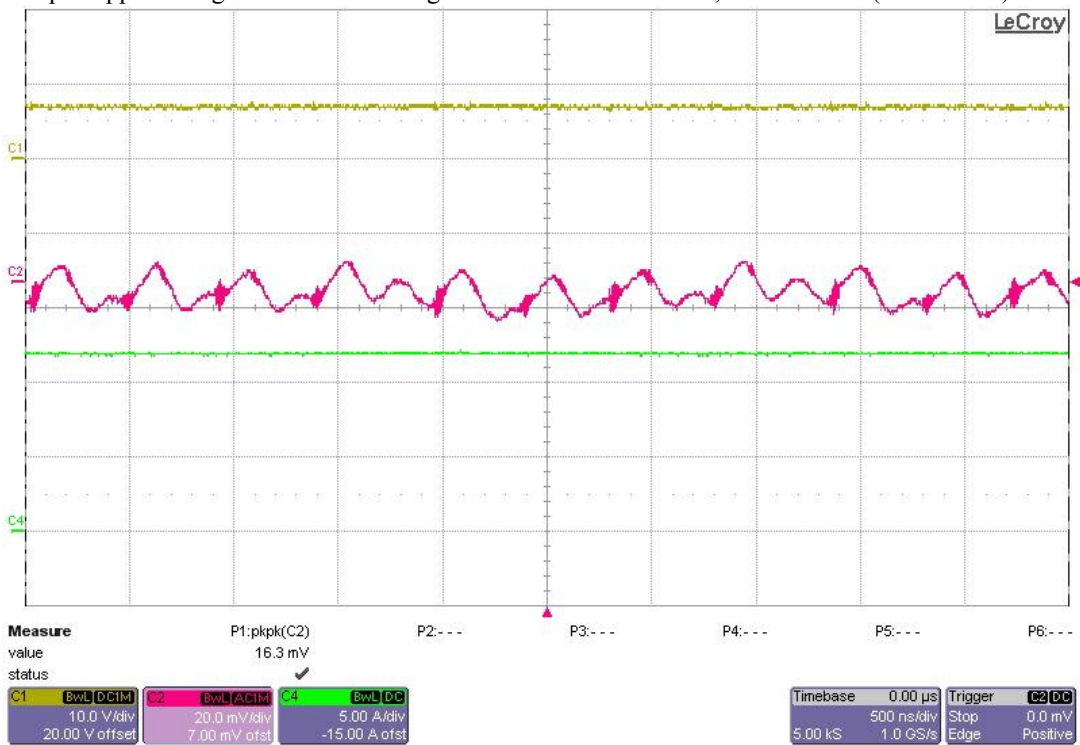




The output ripple voltage is shown below with  $V_{in} = 7.2V$ ,  $I_{out} = 8A$ . (20mV/DIV)

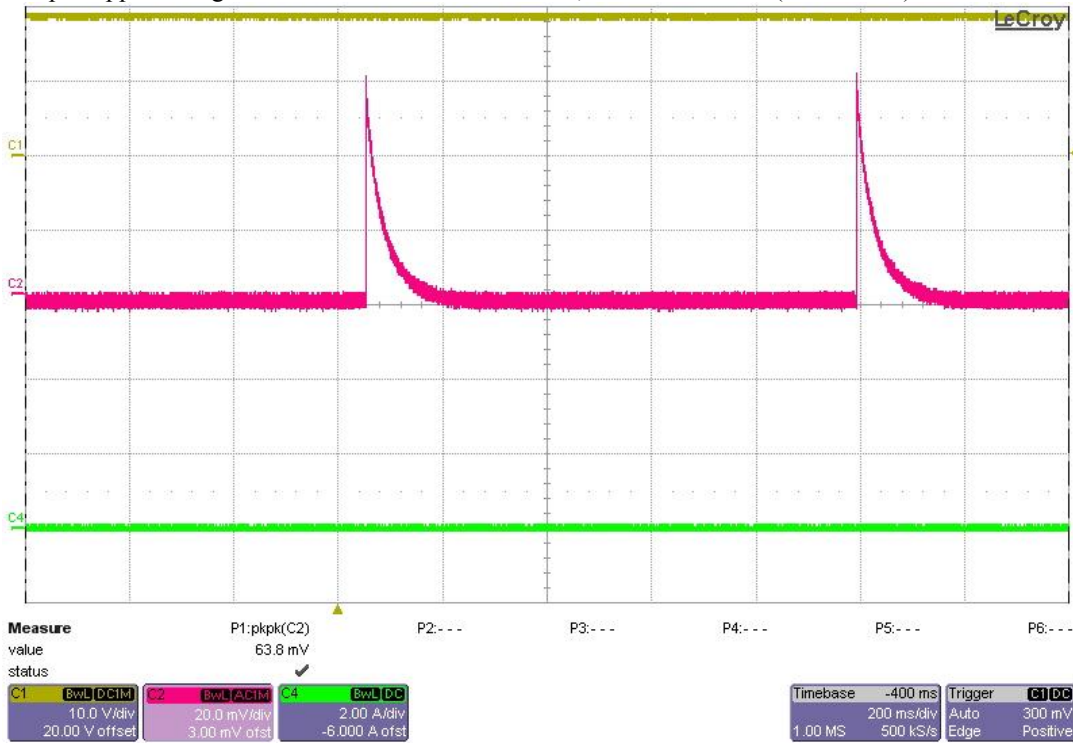


The output ripple voltage is shown in the figure below with  $V_{in} = 7.2V$ ,  $I_{out} = 12A$ . (20mV/DIV)

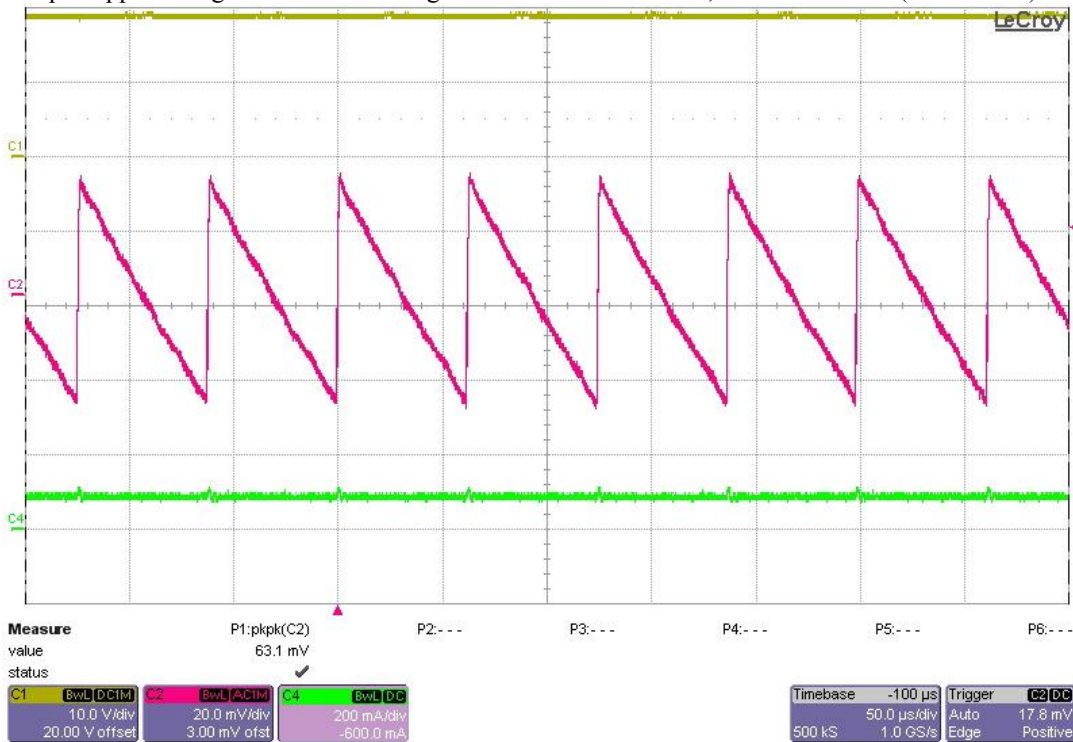


## 4 Output Ripple Voltage – 19Vin

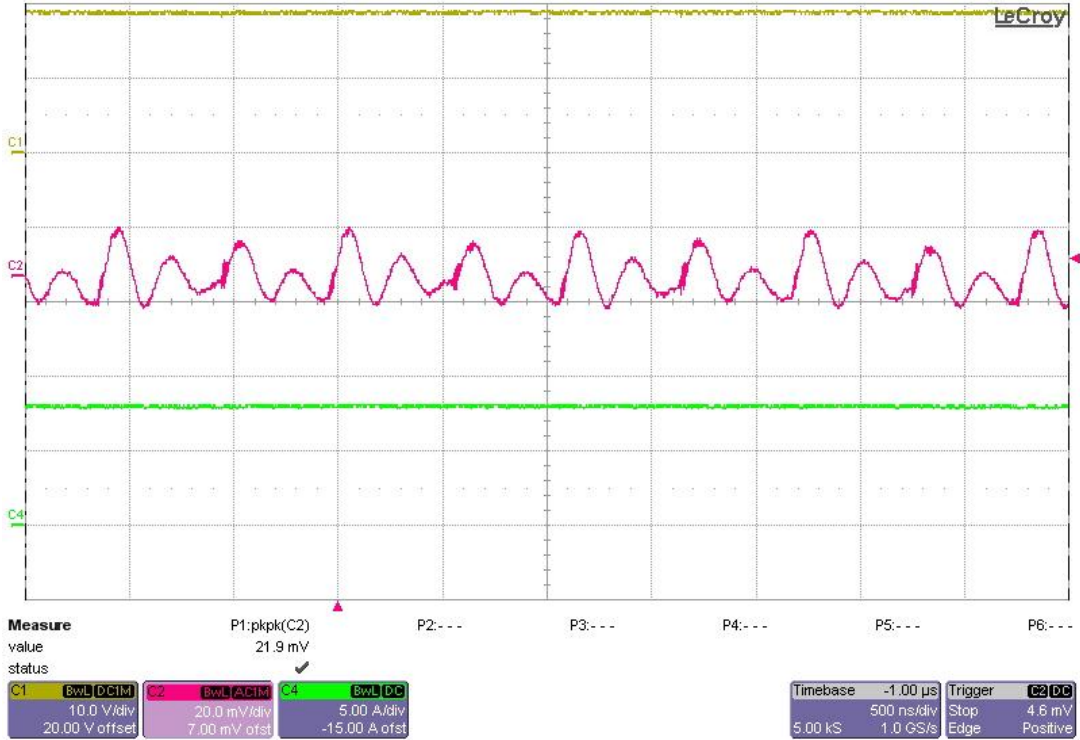
The output ripple voltage is shown below with  $V_{in} = 19V$ ,  $I_{out} = \text{No load}$ . (20mV/DIV)



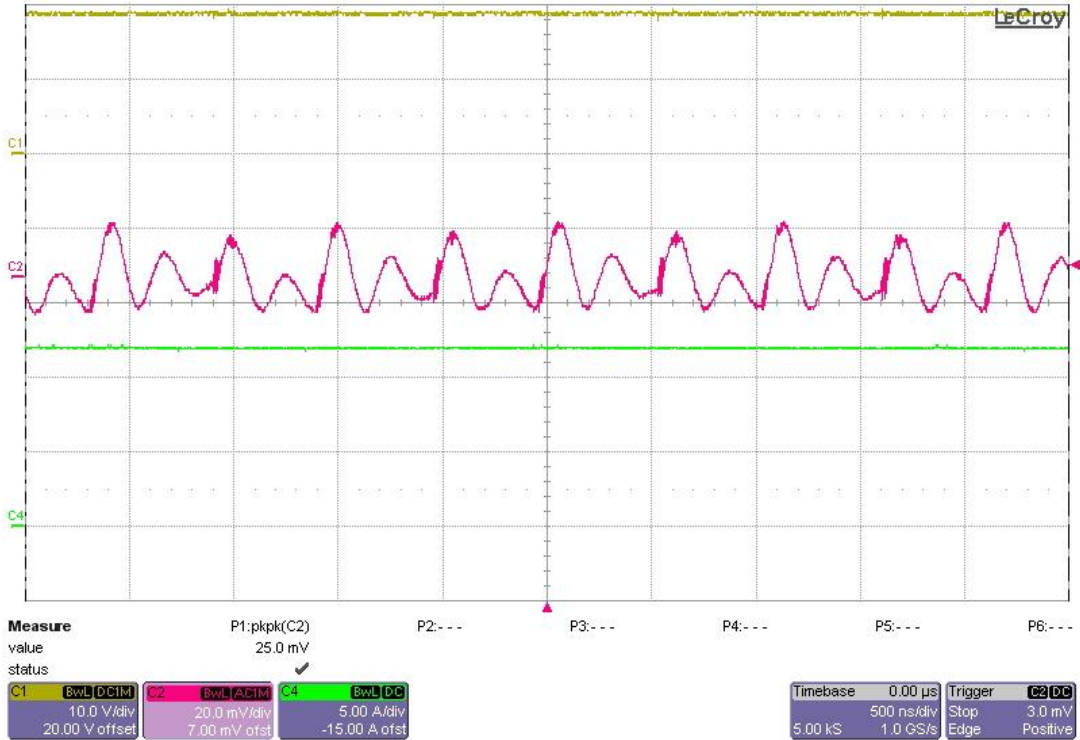
The output ripple voltage is shown in the figure below with  $V_{in} = 19V$ ,  $I_{out} = 100mA$ . (20mV/DIV)



The output ripple voltage is shown below with  $V_{in} = 19V$ ,  $I_{out} = 8A$ . (20mV/DIV)

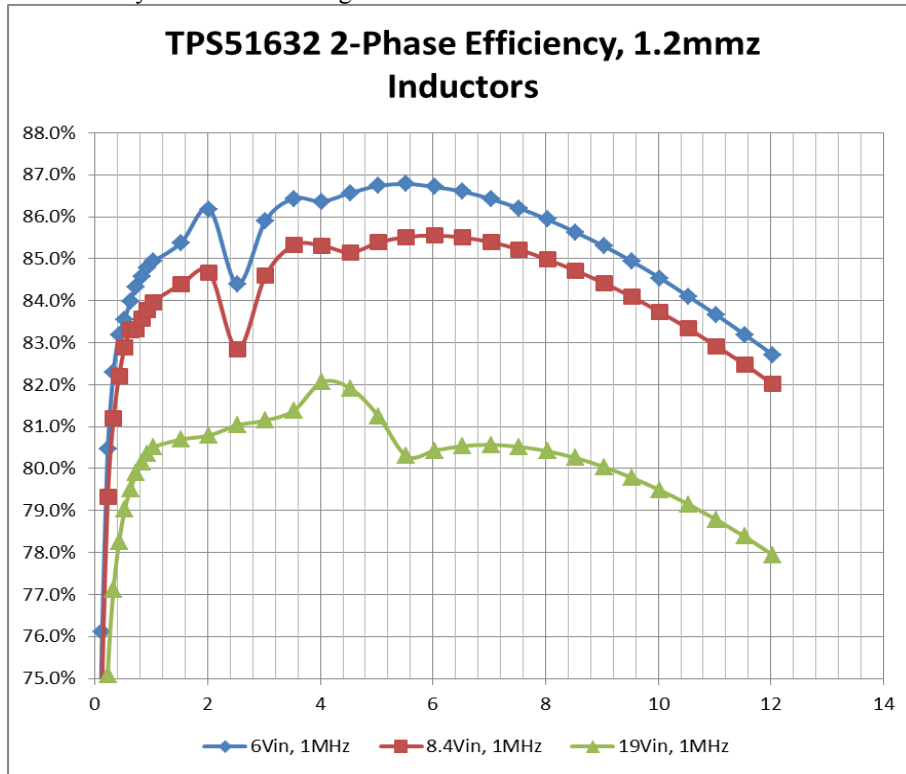


The output ripple voltage is shown in the figure below with  $V_{in} = 19V$ ,  $I_{out} = 12A$ . (20mV/DIV)



## 5 Efficiency

The converter efficiency is shown in the figure below.



## 6 Thermal

The converter temperature profile is shown below at  $V_{in} = 7.5V$ ,  $I_{out} = 8A$ .

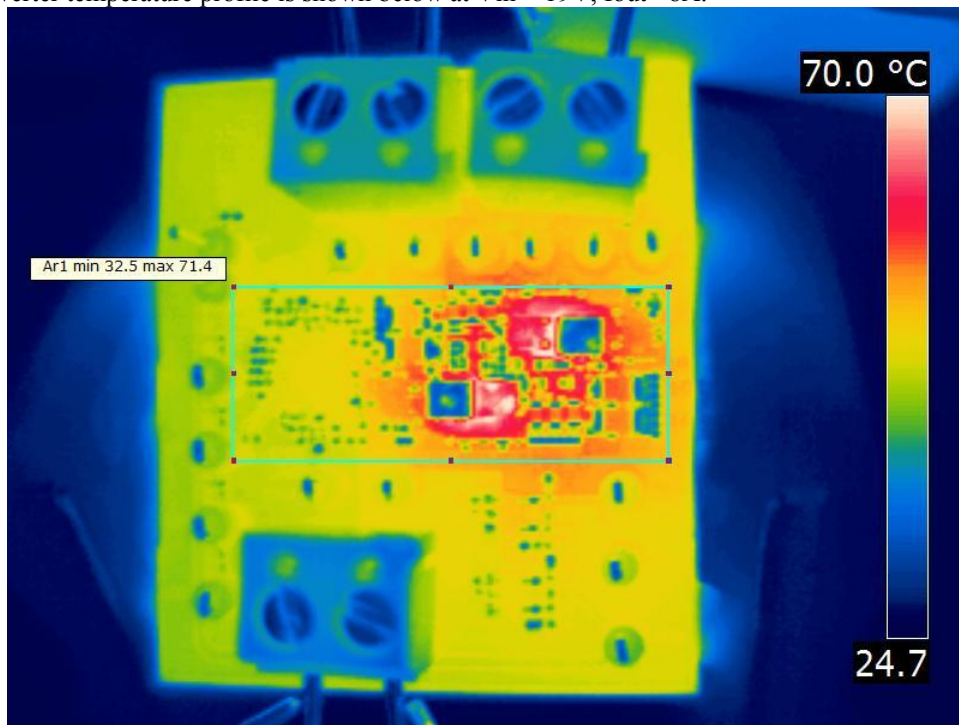




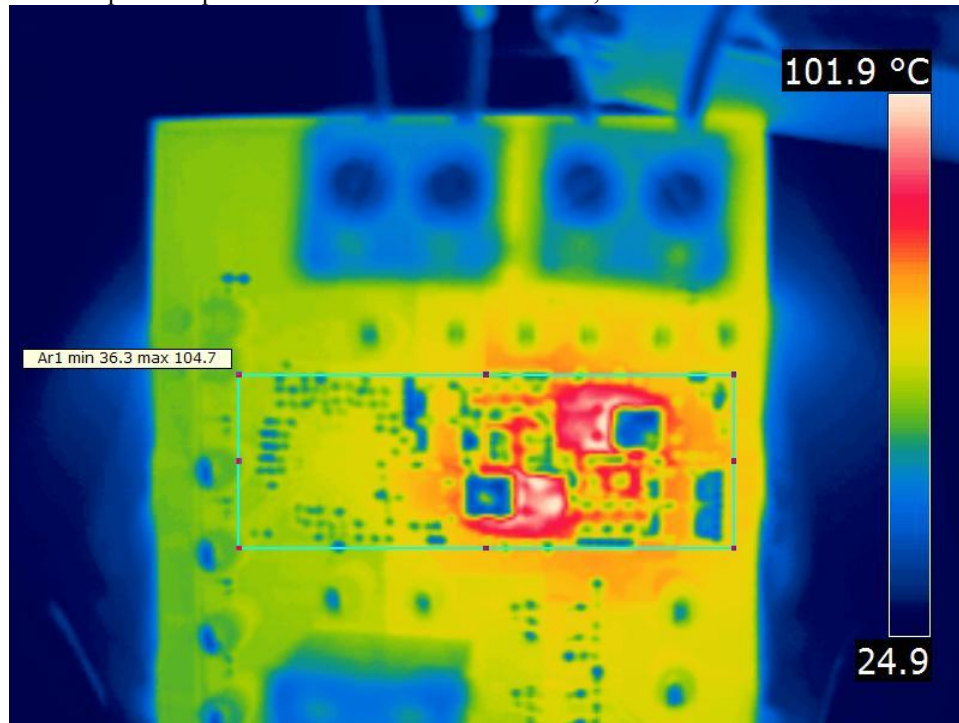
The converter temperature profile is shown below at  $V_{in} = 7.5V$ ,  $I_{out} = 12A$ .



The converter temperature profile is shown below at  $V_{in} = 19V$ ,  $I_{out} = 8A$ .

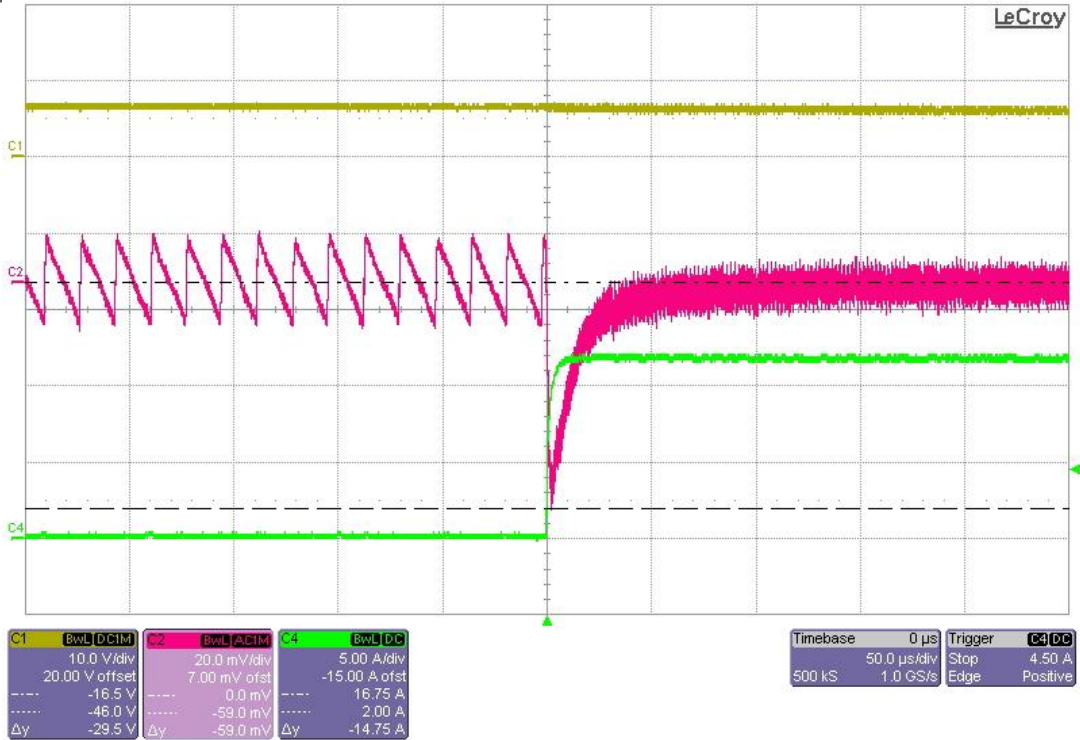


The converter temperature profile is shown below at  $V_{in} = 19V$ ,  $I_{out} = 12A$ .

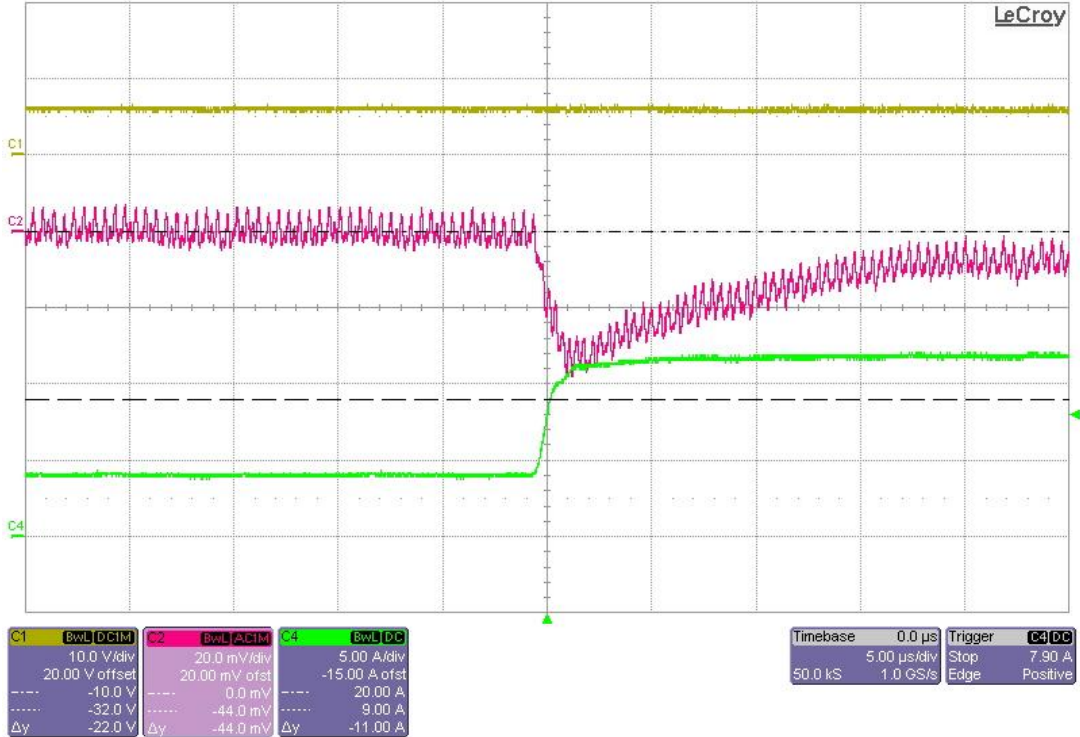


## 7 Load Transients, Rising – 6Vin

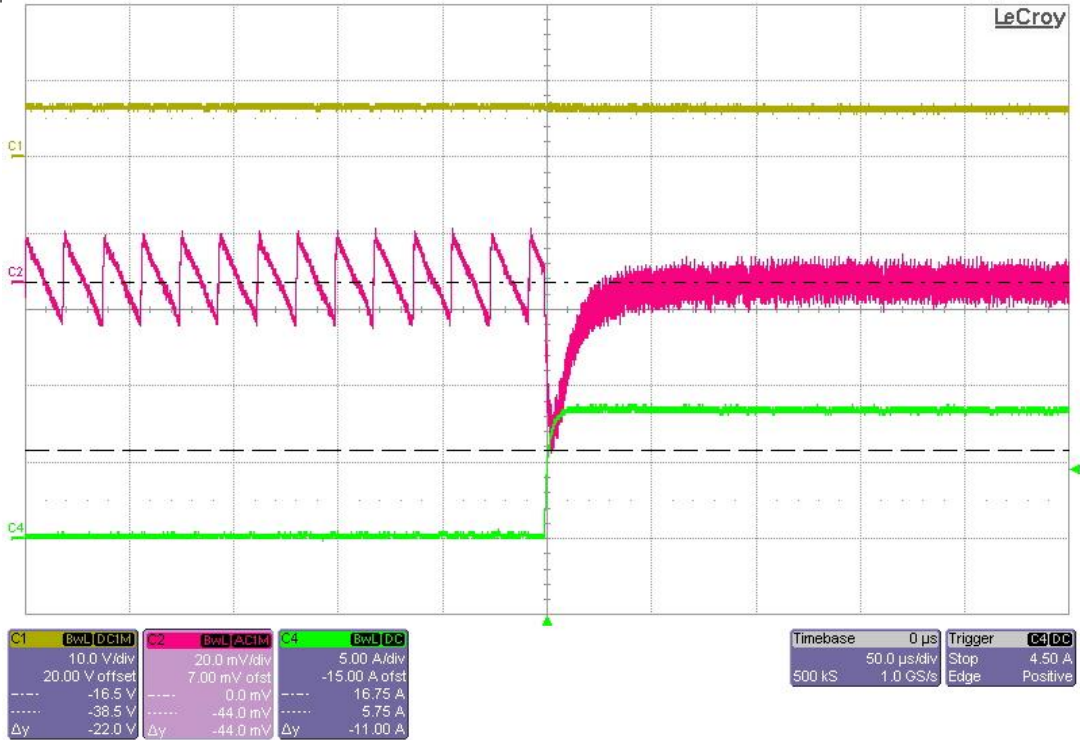
The photo below shows the output voltage when the load current is pulsed from 100mA to 12A.  
 $V_{in} = 6V_{dc}$ . (20mV/DIV, 5A/DIV)



The photo below shows the output voltage when the load current is pulsed from 4A to 12A.  $V_{in} = 6V_{dc}$ . (20mV/DIV, 5A/DIV)

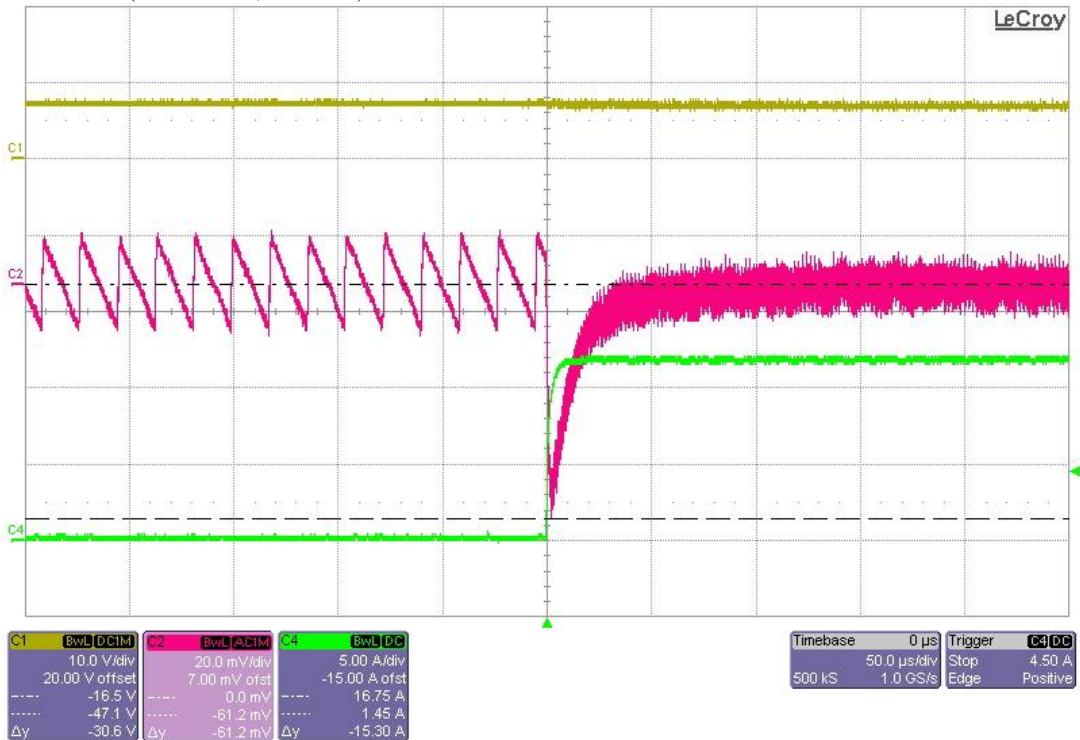


The photo below shows the output voltage deviation of 44mV (~4% $V_{out}$ ) when the load current is pulsed from 0A to 7.5A.  $V_{in} = 6V_{dc}$ . (20mV/DIV, 5A/DIV)



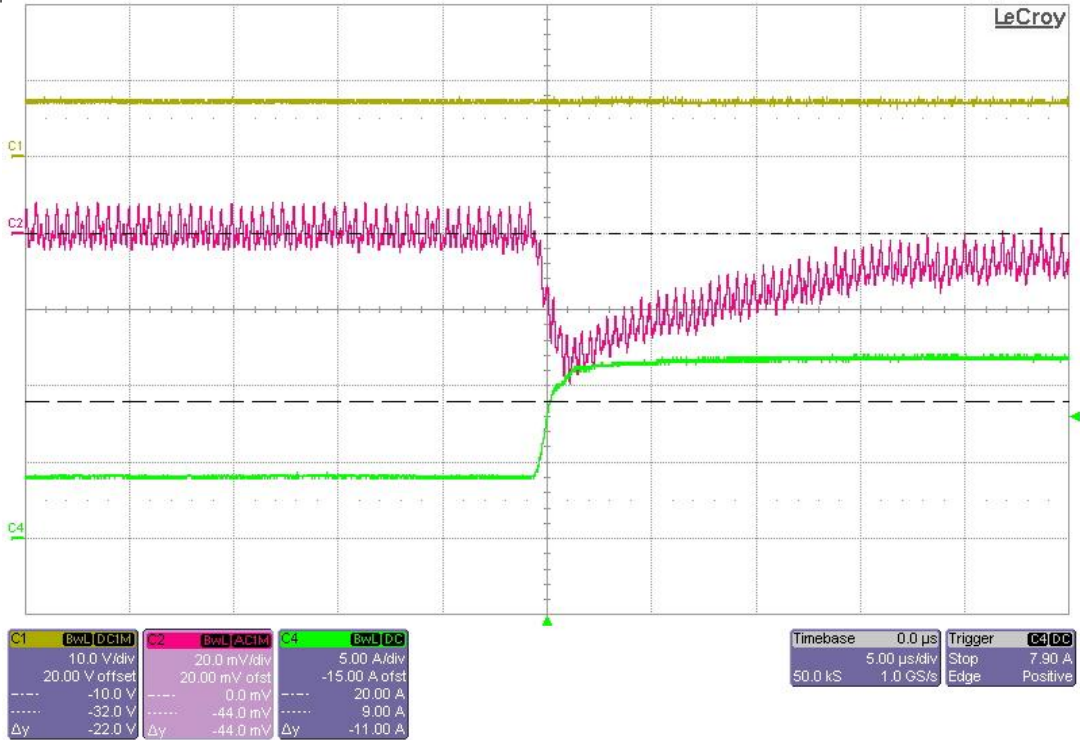
### 8 Load Transients, Rising – 7.2Vin

The photo below shows the output voltage when the load current is pulsed from 100mA to 12A.  
 $V_{in} = 7.2V_{dc}$ . (20mV/DIV, 5A/DIV)

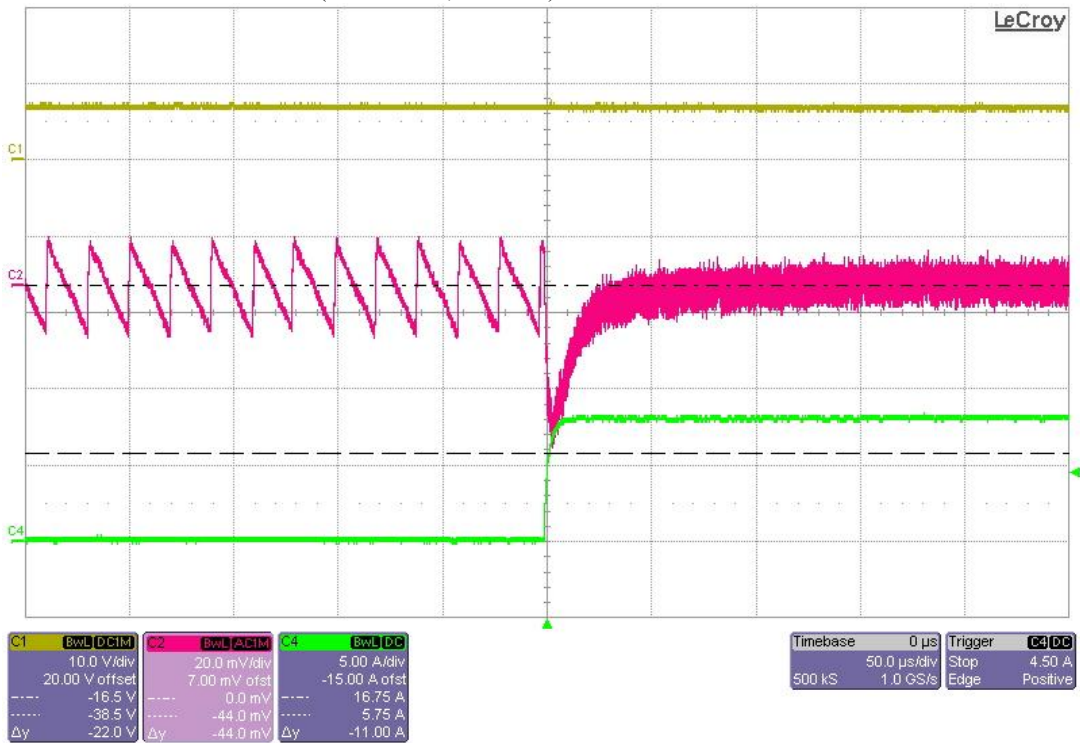


The photo below shows the output voltage when the load current is pulsed from 4A to 12A.  
 $V_{in} = 7.2V_{dc}$ . (20mV/DIV, 5A/DIV)



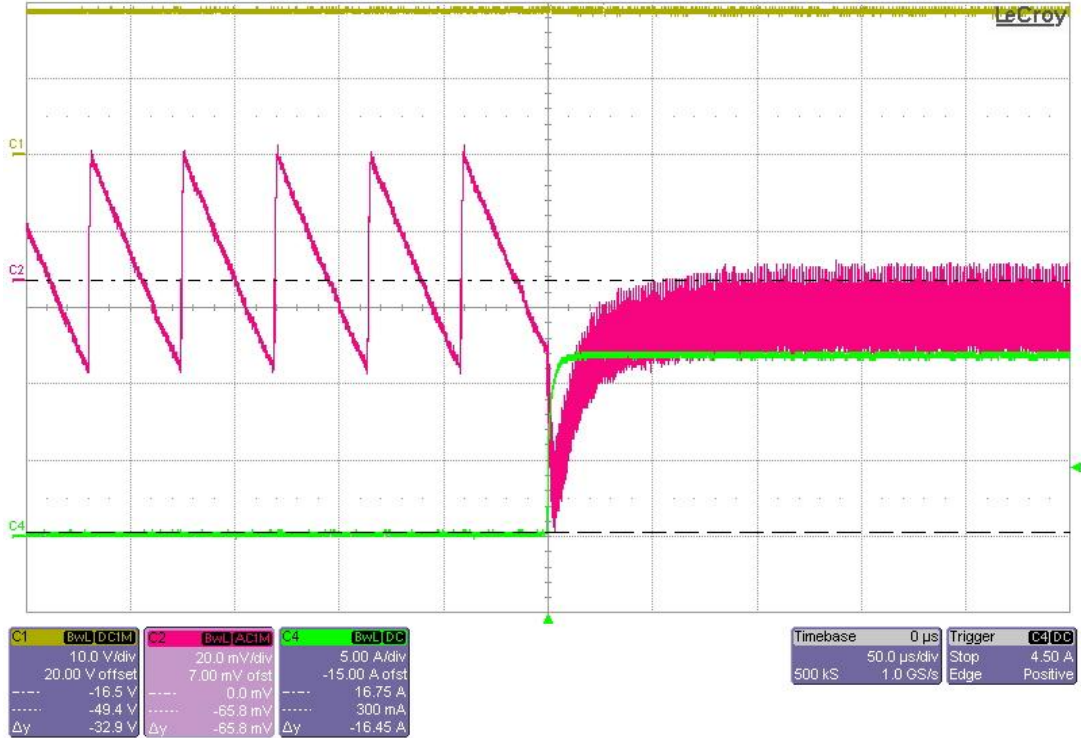


The photo below shows the output voltage deviation of 44mV (~4%Vout) when the load current is pulsed from 0A to 7.5A. Vin = 7.2Vdc. (20mV/DIV, 5A/DIV)

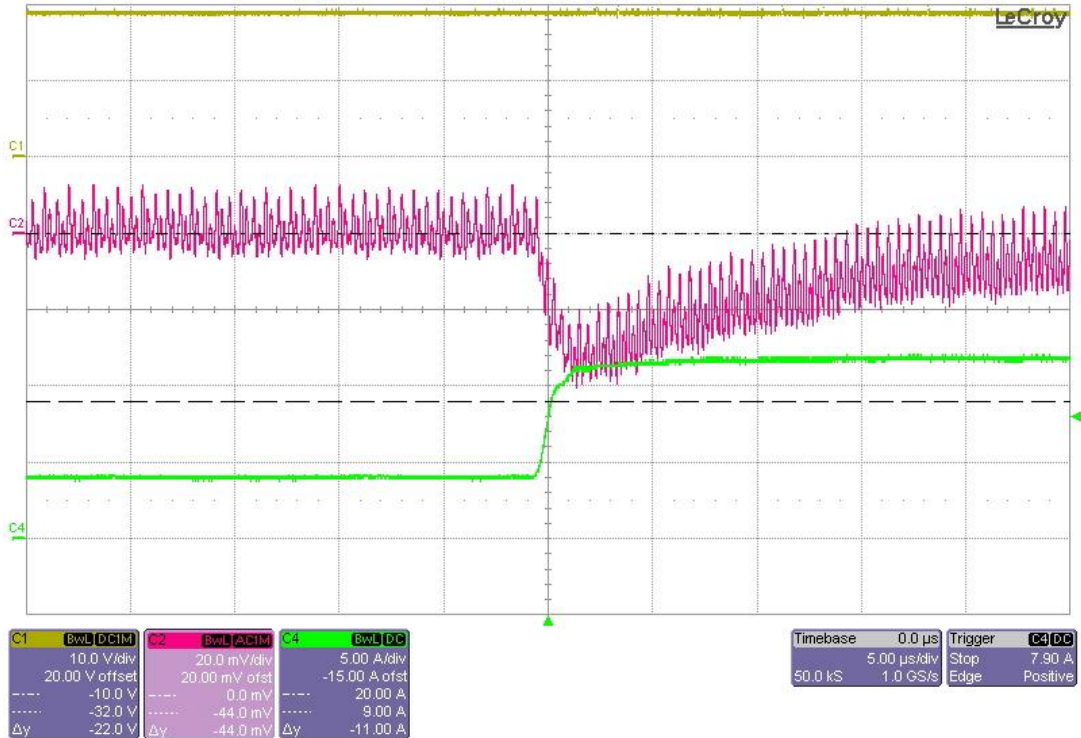


## 9 Load Transients, Rising – 19Vin

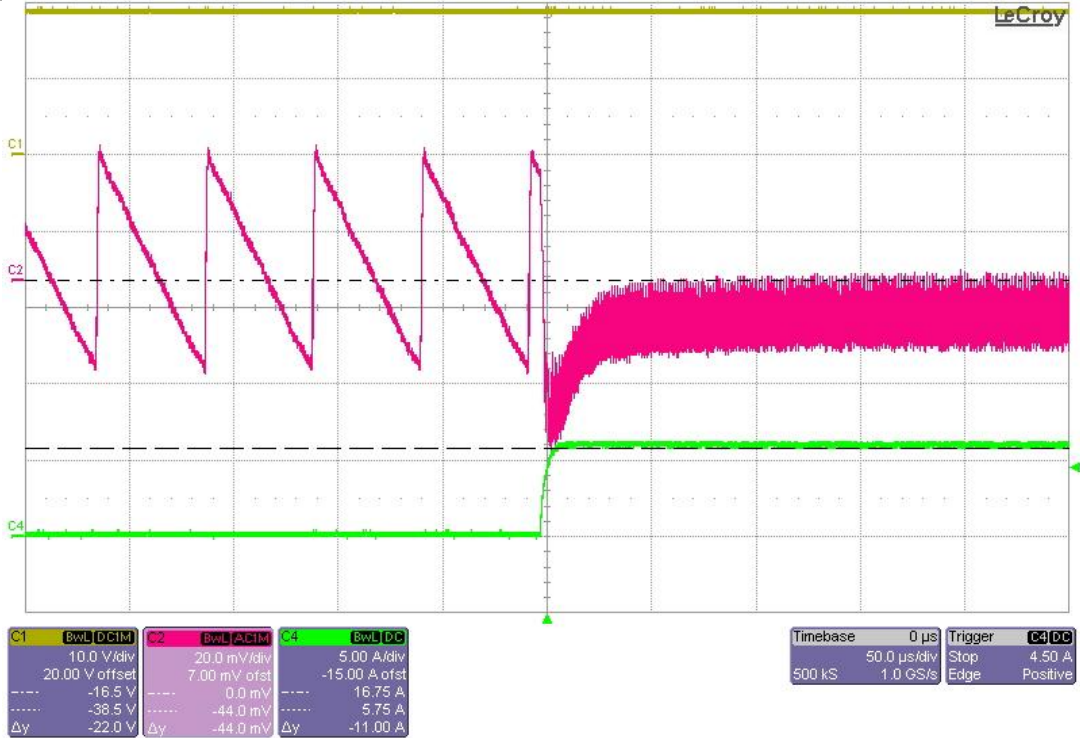
The photo below shows the output voltage when the load current is pulsed from 100mA to 12A.  
 $V_{in} = 19V_{dc}$ . (20mV/DIV, 5A/DIV)



The photo below shows the output voltage when the load current is pulsed from 4A to 12A.  
 $V_{in} = 19V_{dc}$ . (20mV/DIV, 5A/DIV)

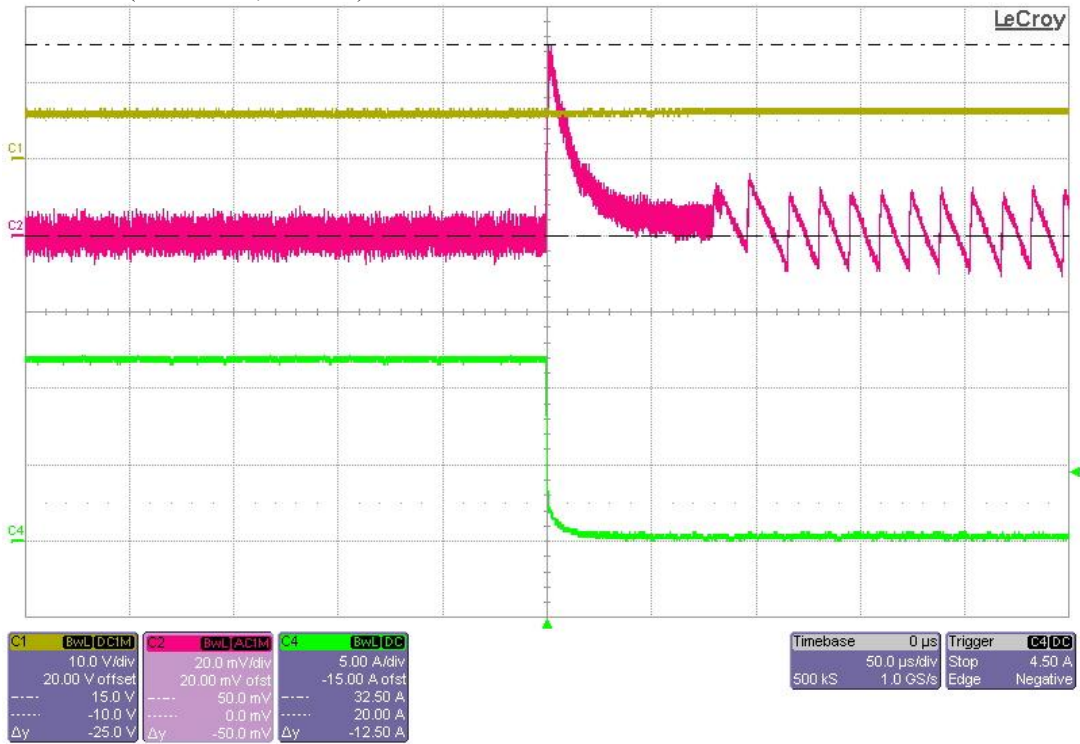


The photo below shows the output voltage deviation of 44mV (~4% $V_{out}$ ) when the load current is pulsed from 0A to 6A.  $V_{in} = 19V_{dc}$ . (20mV/DIV, 5A/DIV)



### 10 Load Transients, Falling – 6Vin

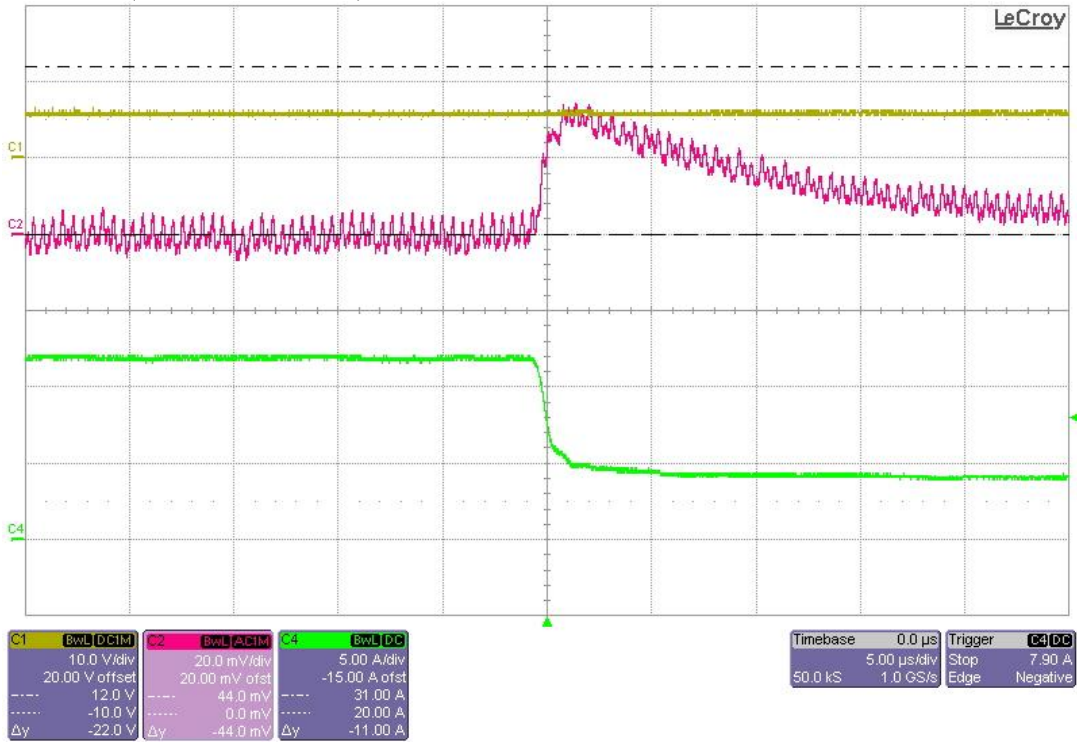
The photo below shows the output voltage when the load current is pulsed from 12A to 100mA.  $V_{in} = 7.2V_{dc}$ . (20mV/DIV, 5A/DIV)



The photo below shows the output voltage when the load current is pulsed from 12A to 4A.

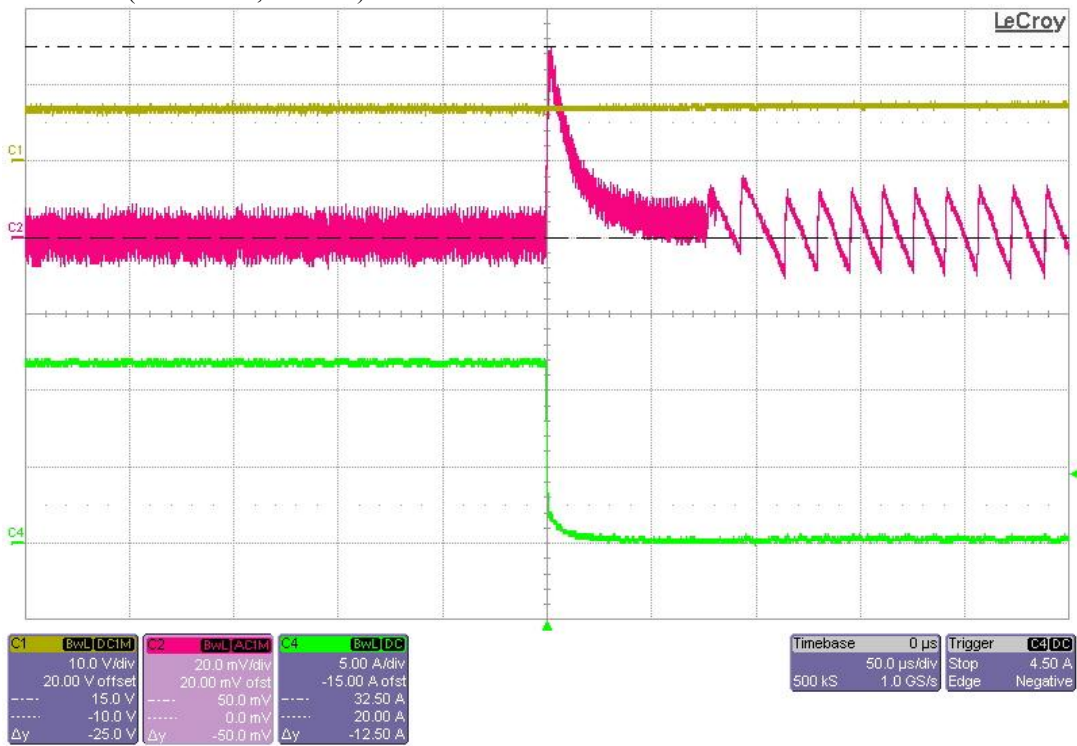


Vin = 7.2Vdc. (20mV/DIV, 5A/DIV)



### 11 Load Transients, Falling – 7.2Vin

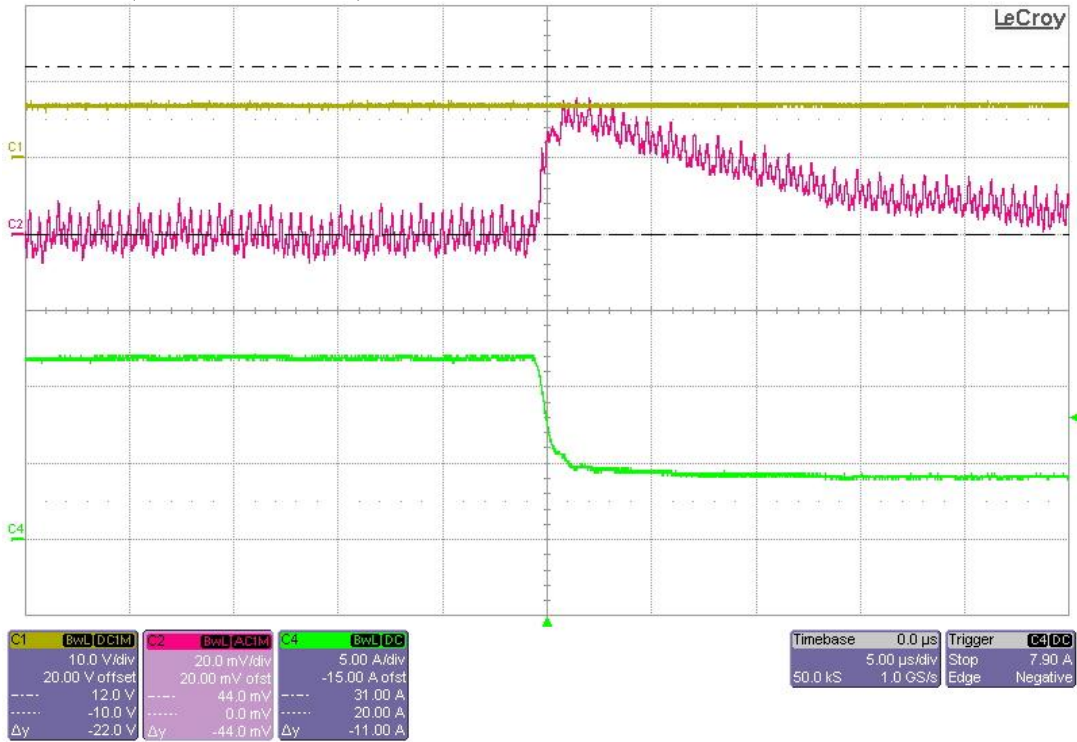
The photo below shows the output voltage when the load current is pulsed from 12A to 100mA.  
 Vin = 7.2Vdc. (20mV/DIV, 5A/DIV)



The photo below shows the output voltage when the load current is pulsed from 12A to 4A.

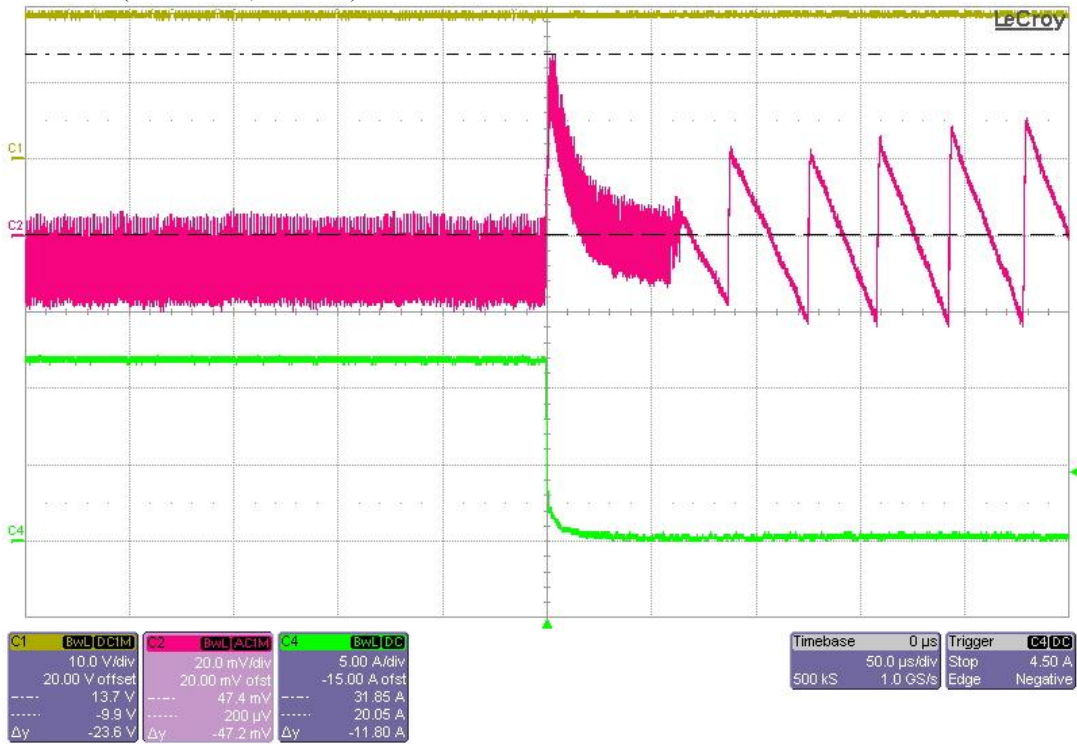


Vin = 7.2Vdc. (20mV/DIV, 5A/DIV)



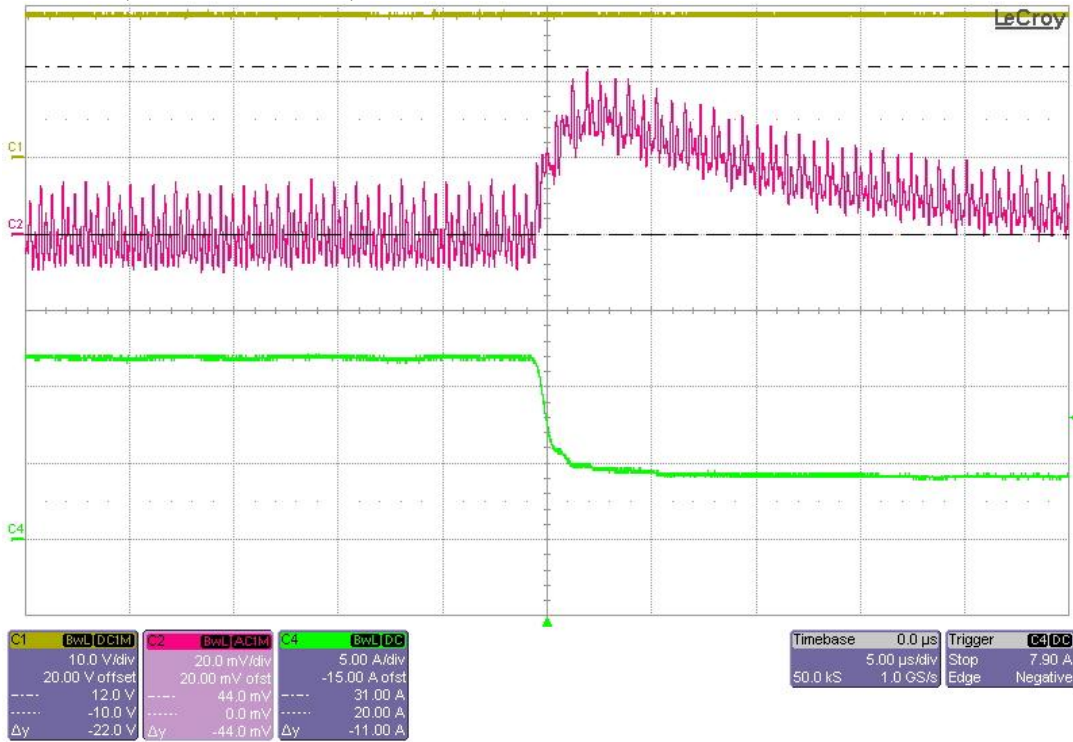
### 12 Load Transients, Falling – 19Vin

The photo below shows the output voltage when the load current is pulsed from 12A to 100mA.  
 Vin = 19Vdc. (20mV/DIV, 5A/DIV)



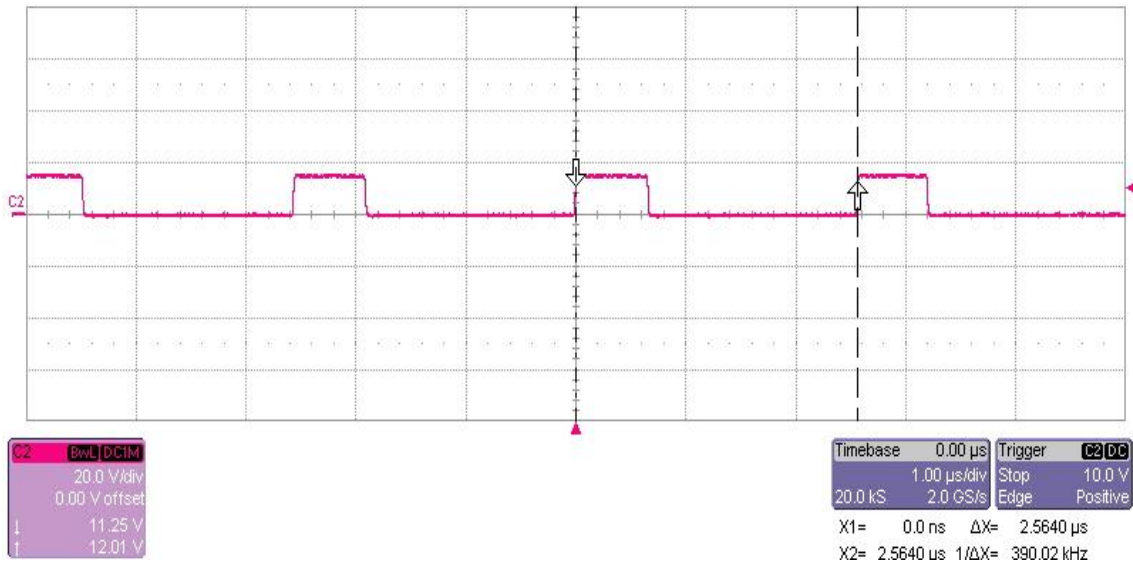
The photo below shows the output voltage when the load current is pulsed from 12A to 4A.

Vin = 19Vdc. (20mV/DIV, 5A/DIV)



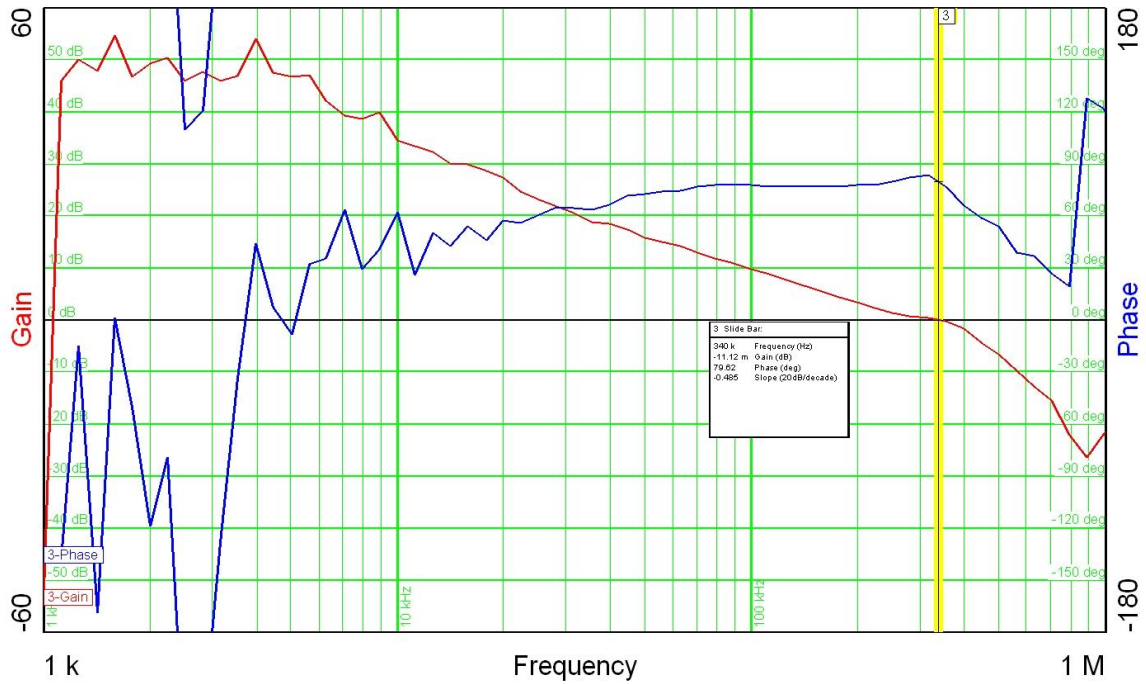
### 13 Switch Node Waveforms

The photo below shows the switching node waveform. The input voltage is 15V and the output is loaded to 2A. Curser measurement shows switching frequency at 390kHz.



## 14 Control Loop Gain / Stability

The plot below shows the loop gain and phase margin. Bandwidth = 340KHz, Phase Margin = 79 degrees.



## IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to TI's Terms of Sale (<https://www.ti.com/legal/termsofsale.html>) or other applicable terms available either on [ti.com](https://www.ti.com) or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265  
Copyright © 2021, Texas Instruments Incorporated