

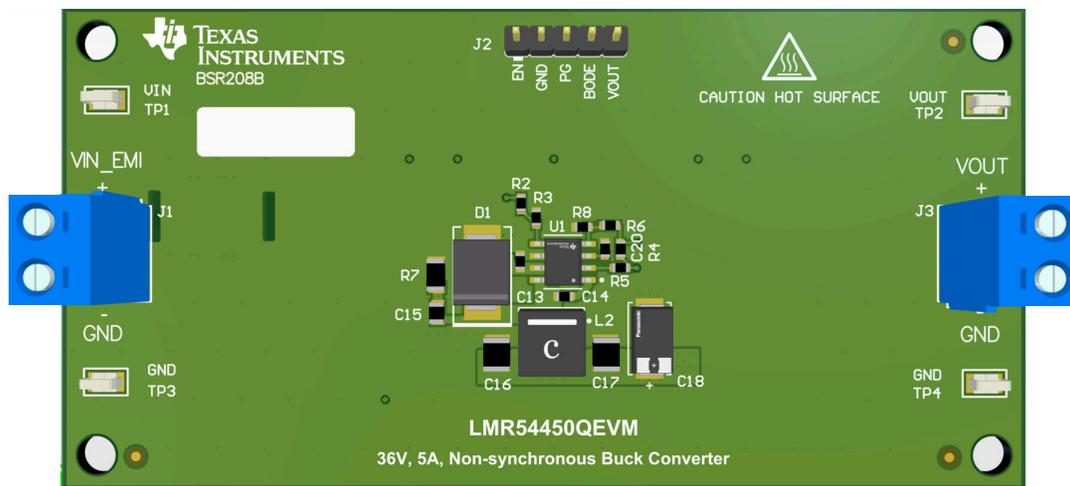
LMR54450-Q1 36-V, 5-A Buck Converter Evaluation Module



ABSTRACT

The LMR54450QEVM evaluation module (EVM) is designed to help customers evaluate the performance of the LMR54450-Q1 non-synchronous step-down converter. The EVM contains one LMR54450-Q1 device that is capable of delivering 5-V output voltage and up to 5-A load current at a nominal switching frequency internally set to 400 kHz. Configured to operate with input supply voltage ranging from 5.5-V to 36-V, the EVM provides multiple power connectors and test points for measuring efficiency, crossover frequency, phase margin, power good, and enable. The EVM also provides a layout example optimized for EMI and thermal performance. This user's guide includes the following:

- Background
- Schematic and Layout
- Test Setup and Waveforms
- PCB layer stackup
- Bill of materials (BOM)



LMR54450QEVM Board (Top View)



CAUTION

Hot surface. Contact can cause burns. Do not touch.

Table of Contents

1 Background	3
2 Quick Start	3
3 EVM Connections	4
4 Schematic	5
5 PCB Layout	6
6 Test Data and Performance Curves	9
6.1 Efficiency and Load Regulation Performance.....	9
6.2 Waveforms.....	9
6.3 Thermal Performance.....	10
6.4 EMI Performance.....	11
7 Bill of Materials	13
8 Custom Design With WEBENCH® Tools	14
9 Revision History	14

List of Figures

Figure 1-1. LMR54450-Q1 8-Pin SOIC DDA Package (Top View).....	3
Figure 3-1. EVM Test Setup.....	4
Figure 4-1. EVM Schematic.....	5
Figure 5-1. 3D Top View.....	6
Figure 5-2. 3D Bottom View.....	6
Figure 5-3. Top Layer Copper.....	7
Figure 5-4. Layer 2 Copper.....	7
Figure 5-5. Layer 3 Copper.....	7
Figure 5-6. Bottom Layer Copper (Top View).....	8
Figure 6-1. Conversion Efficiency.....	9
Figure 6-2. Load Regulation.....	9
Figure 6-3. Start-Up, V_{IN} Stepped to 24 V.....	9
Figure 6-4. Shutdown.....	9
Figure 6-5. Enable ON and OFF Toggle.....	9
Figure 6-6. Load Transient, 1.25 A to 3.75 A.....	9
Figure 6-7. Output Ripple Loaded to 5 A.....	10
Figure 6-8. Precision Enable Start-Up.....	10
Figure 6-9. Infrared Thermal Image: Test $V_{IN} = 13.5$ V.....	10
Figure 6-10. Infrared Thermal Image: Test $V_{IN} = 24$ V.....	10
Figure 6-11. CISPR 25 Class 5 Conducted Emissions.....	11
Figure 6-12. CISPR 25 Class 5 Radiated Emissions: Monopole.....	11
Figure 6-13. CISPR 25 Class 5 Radiated Emissions: Biconical Horizontal Polarization.....	11
Figure 6-14. CISPR 25 Class 5 Radiated Emissions: Biconical Vertical Polarization.....	12
Figure 6-15. CISPR 25 Class 5 Radiated Emissions: Log Periodic.....	12

List of Tables

Table 3-1. EVM Power Connections.....	4
Table 3-2. EVM Signal Connections.....	4
Table 5-1. Multi-Layer Stackup.....	8
Table 7-1. Component BOM.....	13

Trademarks

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1 Background

The LMR54450-Q1 is a high-output-current PWM converter that integrates a low-resistance, high-side N-channel MOSFET. Integrated features include a high-performance error amplifier that provides tight voltage regulation accuracy under transient conditions, an undervoltage-lockout circuit to prevent start-up until the input voltage reaches 5.5-V, and an internally set soft-start circuit to limit inrush currents. The LMR54450-Q1 implements voltage-mode control. Using the EN pin, shutdown supply current is reduced to 13 μ A typically. Other features include an active-high enable, overcurrent limiting, overvoltage protection, and thermal shutdown. The feedback loop is internally compensated to reduce design complexity and external component count. For more details, please visit the [LMR54450-Q1 data sheet](#).

The pin configuration of the LMR54450-Q1 is shown in [Figure 1-1](#).

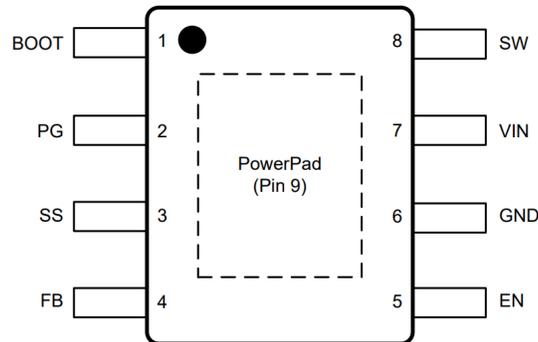


Figure 1-1. LMR54450-Q1 8-Pin SOIC DDA Package (Top View)

2 Quick Start

Refer to the connections depicted in [Figure 3-1](#) for a visual guide on powering up the EVM.

1. Connect the input voltage supply between VIN_EMI and GND (J1) terminals using short thick wires.
2. Connect the output load of the converter between VOUT and GND (J3) terminals using short thick wires.
3. Set the input voltage (V_{IN}) at the appropriate level between 5.5-V to 36-V.
4. Set the current limit of the input supply to an appropriate level depending on the connected output load.
5. Ensure the device is set to ENABLE ON by having the EN pin floating.
6. Turn on the power supply. The default configuration of the EVM must power up to $V_{OUT} = 5$ -V.
7. Monitor the output voltage. The maximum rated load current is 5-A.

3 EVM Connections

Reference the EVM connections described in [Table 3-1](#) to evaluate the LMR54450-Q1. While working at an ESD-protected workstation, ensure that any wrist straps, bootstraps, or mats are connected and referencing the user to earth ground before power is applied to the EVM.

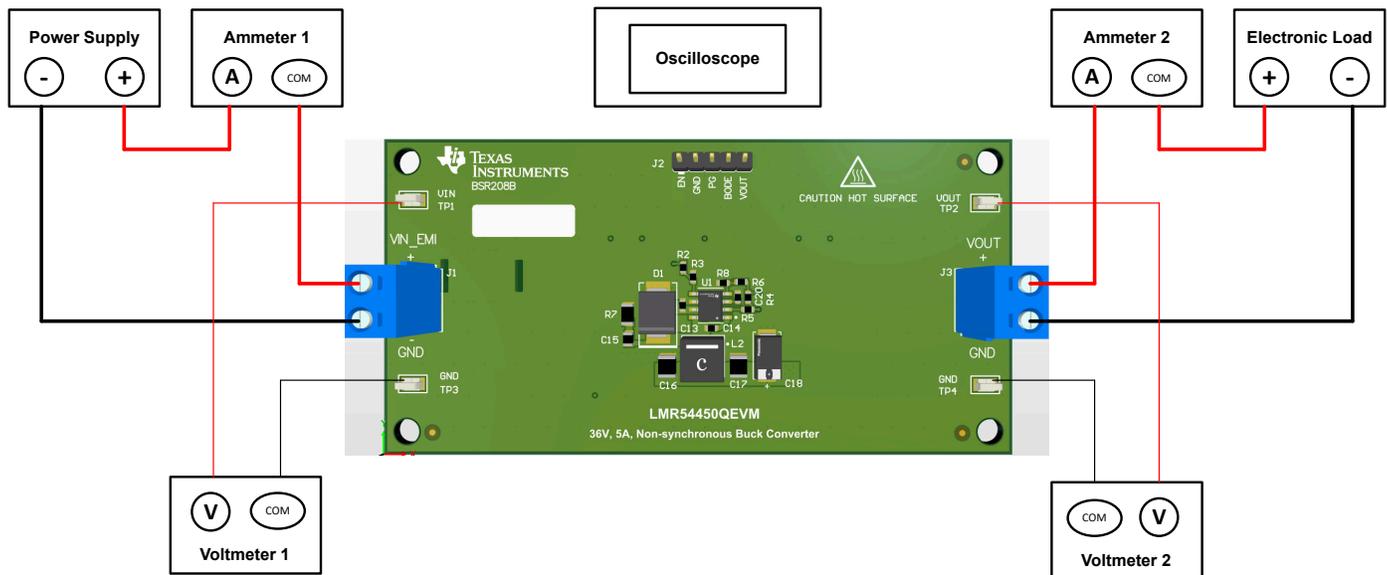


Figure 3-1. EVM Test Setup

Table 3-1. EVM Power Connections

Label	Description
VIN_EMI	Positive input power connection on terminal J1.
GND	Negative input power connection on terminal J1.
VOUT	Positive output power connection on terminal J3.
GND	Negative output power connection on terminal J3.

Table 3-2. EVM Signal Connections

Label	Description
VIN (TP1)	Positive input sense terminal. Connect the multimeter positive lead for measuring efficiency.
GND (TP3)	Negative input sense terminal. Connect the multimeter negative lead for measuring efficiency.
VOUT (TP2)	Positive output sense terminal. Connect a multimeter positive lead for measuring efficiency and line and load regulation.
GND (TP4)	Negative output sense terminal. Connect the multimeter negative lead for measuring efficiency and line and load regulation.
GND	Ground reference point
EN	Precision enable input and input voltage UVLO protection. Place a shunt from EN to GND to pull down the pin and turn OFF. Float EN select for default ON enable.
PG	Power-good monitor output. This is an open-drain flag with a 110-kΩ pullup resistor to VOUT.
BODE	Bode plot measurement and signal injection. A 20-Ω resistor from BODE to VOUT facilitates oscillator signal injection for bode plot measurement. Apply a swept-frequency signal between BODE and VOUT while measuring the respective response at each terminal for loop gain measurement.

4 Schematic

Figure 4-1 illustrates the EVM schematic.

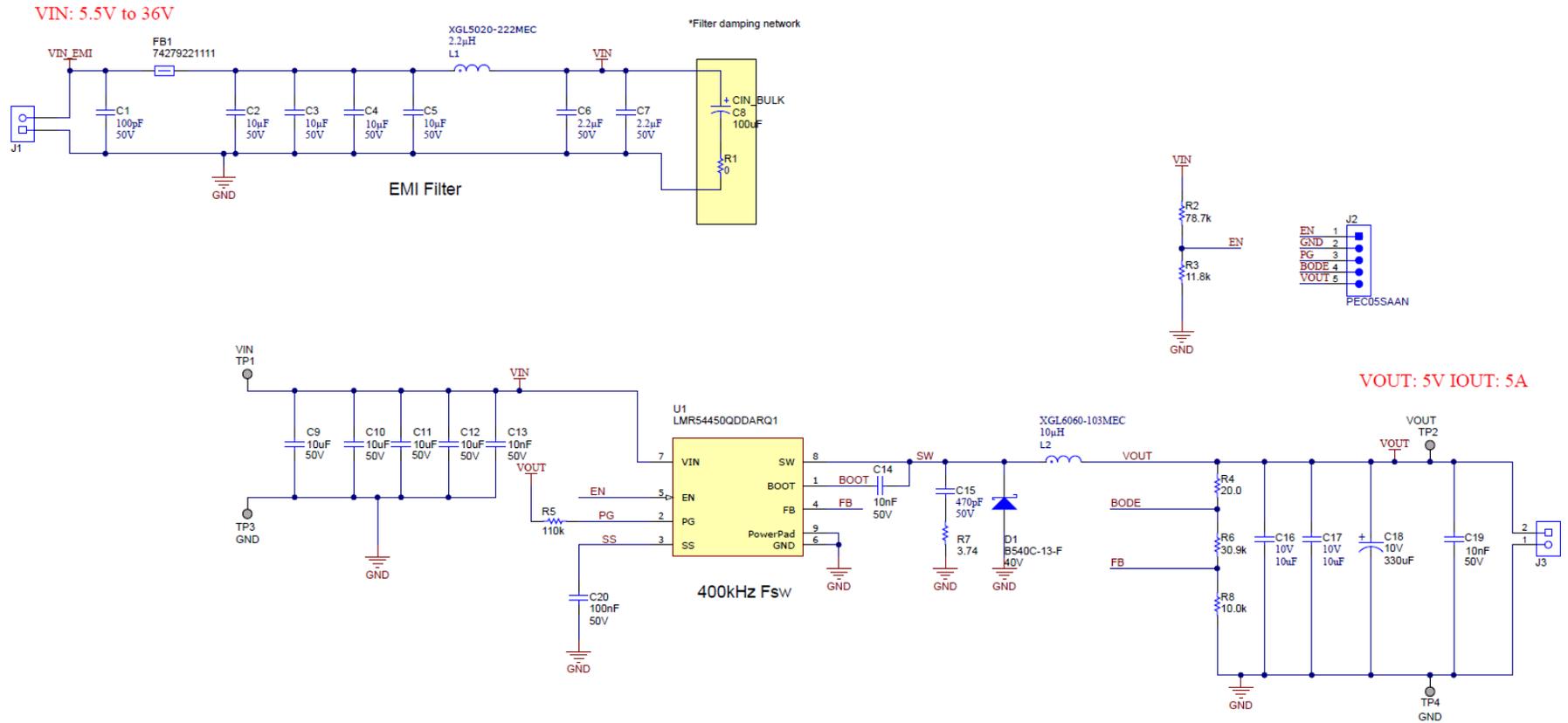


Figure 4-1. EVM Schematic

5 PCB Layout

Figure 5-1 through Figure 5-6 show the PCB layout images, including 3D views and copper layers. The PCB is 62 mils standard thickness with 2-oz copper on all four layers as shown in Table 5-1. The PCB is also optimized for EMI performance. The layout minimizes the area of high dv/dt nodes like SW and BOOT with the addition of a snubber circuit to attenuate SW node ringing. The small high-frequency ceramic input capacitors are placed very close to the IC to minimize the loop formed from VIN pins, through the capacitor, to the PGND pins. The board also features an EMI filter on the back-side of the board with options for an inductor, ferrite bead, and filter capacitors to tune the filter's EMI performance.

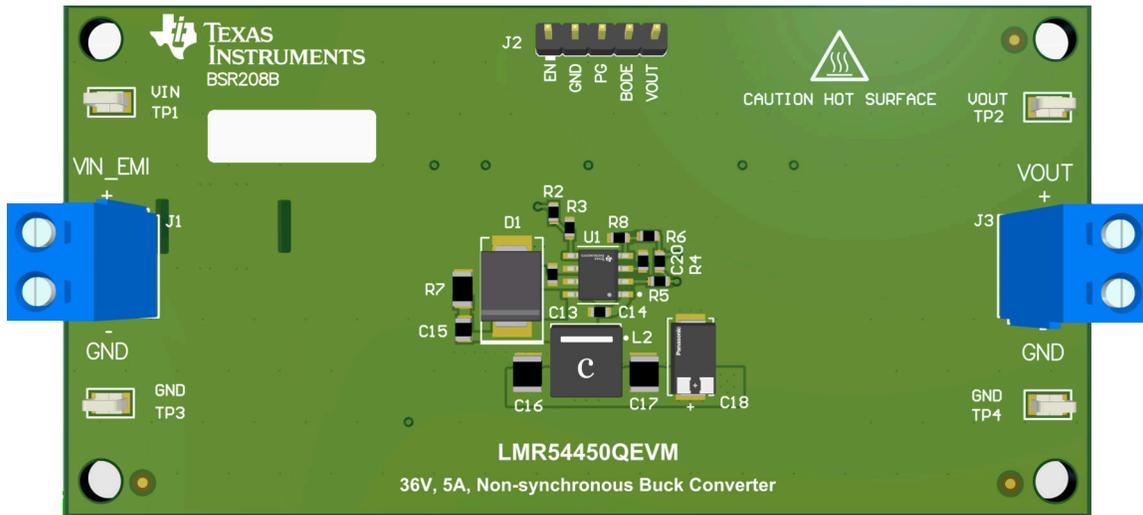


Figure 5-1. 3D Top View

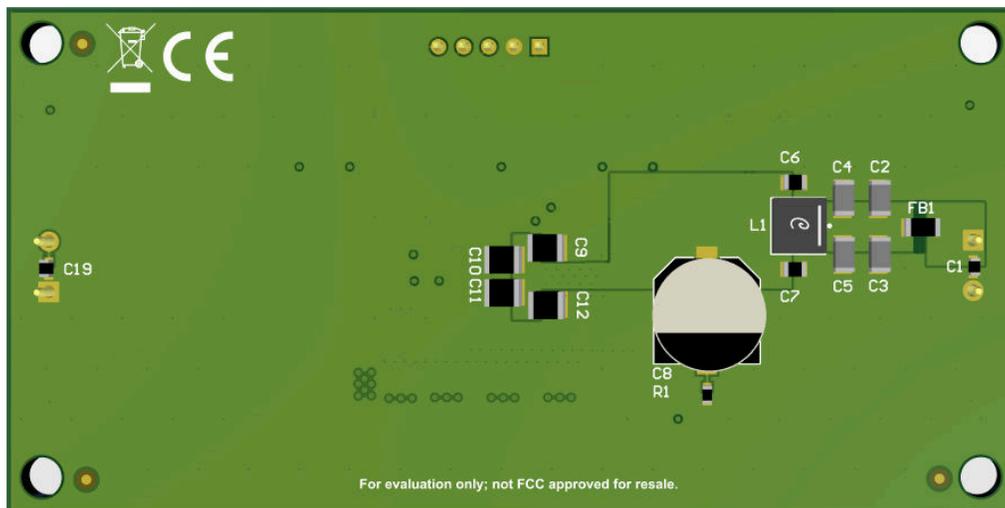


Figure 5-2. 3D Bottom View

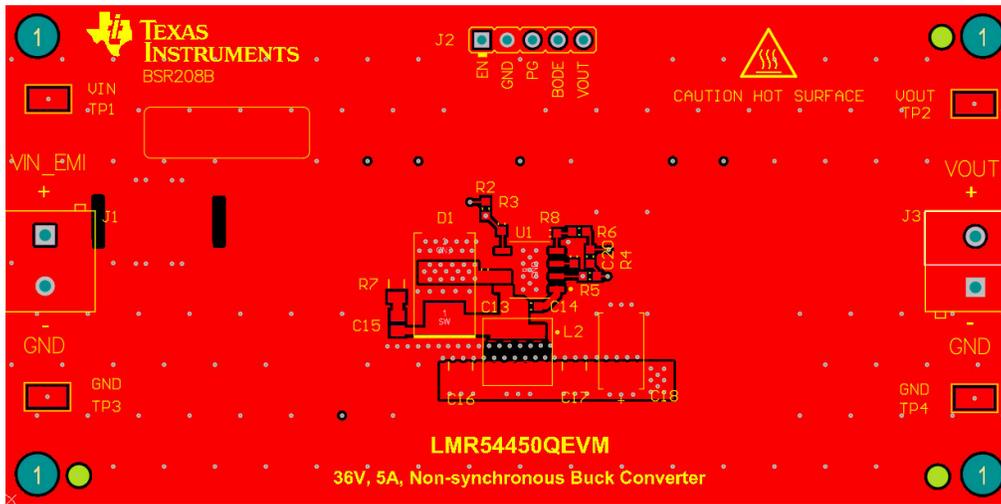


Figure 5-3. Top Layer Copper

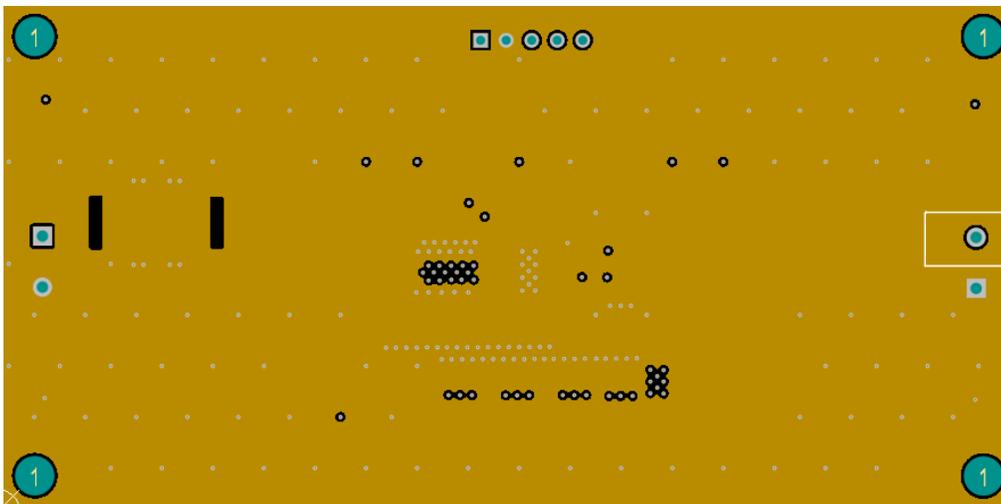


Figure 5-4. Layer 2 Copper

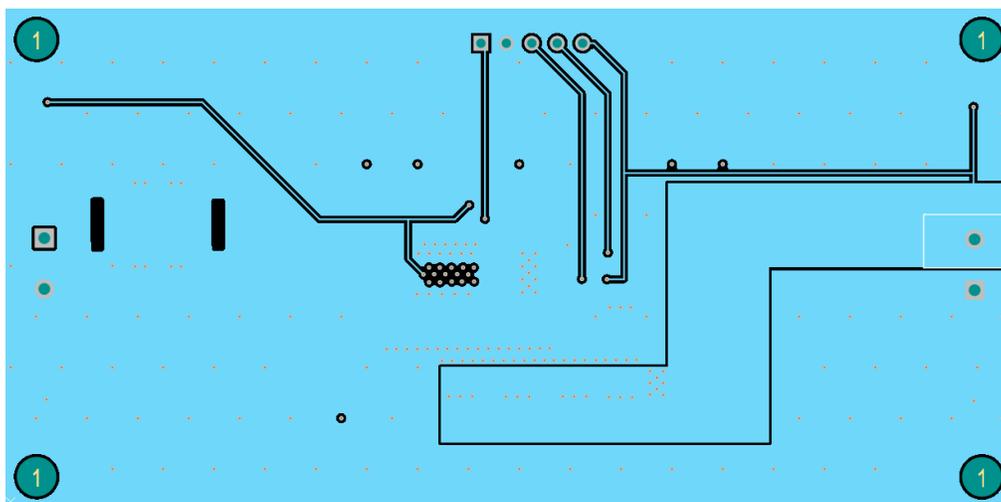


Figure 5-5. Layer 3 Copper

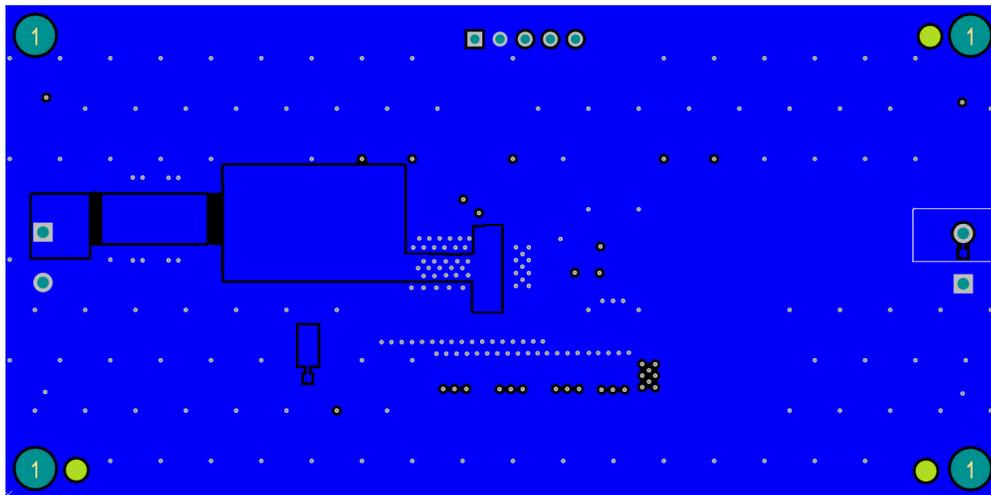


Figure 5-6. Bottom Layer Copper (Top View)

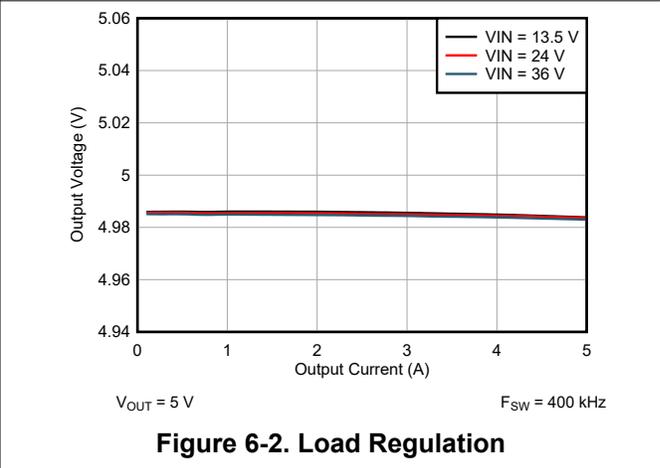
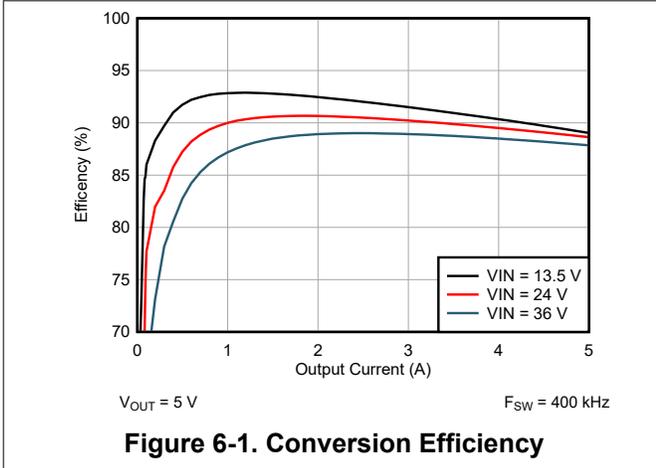
Table 5-1. Multi-Layer Stackup

No.	Name	Material	Type	Weight (oz)	Thickness (mil)	Dielectric Const.
	Top Overlay		Overlay			
	Top Solder	Solder Resist	Solder Mask		0.4	3.5
1	Top Layer	Copper	Signal	2	2.8	
	Dielectric 1	FR-4 High Tg	Dielectric Prepreg		6	4.2
2	Signal Layer 1	Copper	Signal	2	2.8	
	Dielectric 2	FR-4 High Tg	Dielectric Core		38	4.2
3	Signal Layer 2	Copper	Signal	2	2.8	
	Dielectric 3	FR-4 High Tg	Dielectric Prepreg		6	4.2
4	Bottom Layer	Copper	Signal	2	2.8	
	Bottom Solder	Solder Resist	Solder Mask		0.4	3.5
	Bottom Overlay		Overlay			

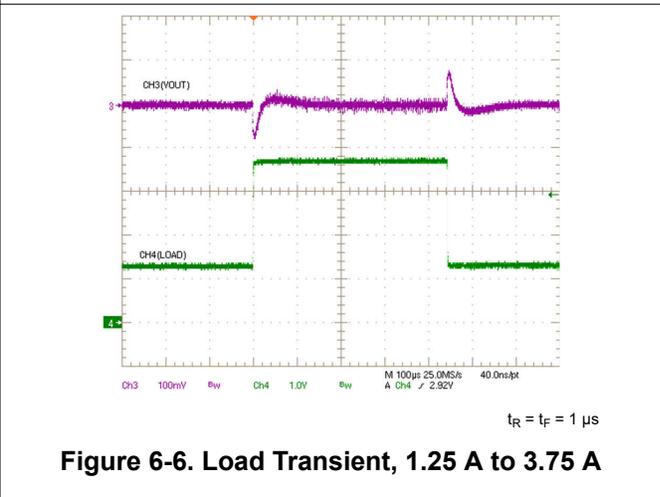
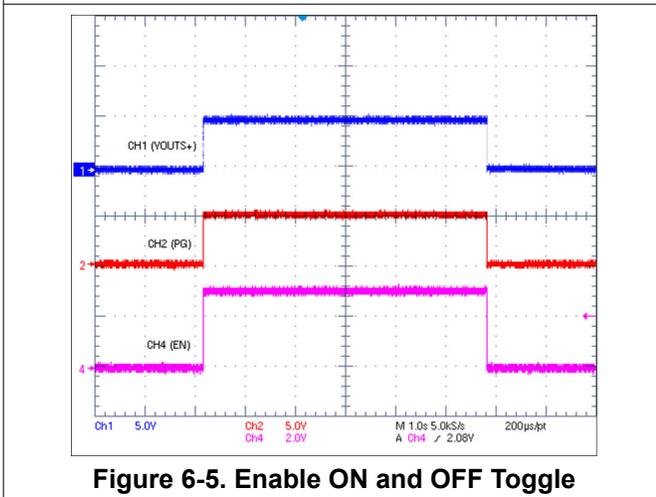
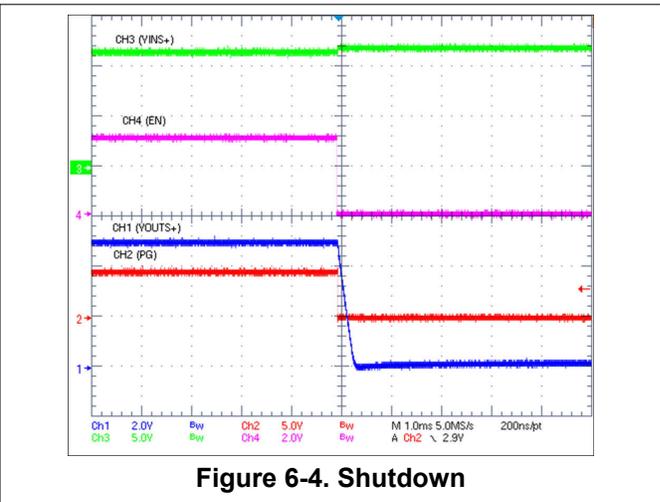
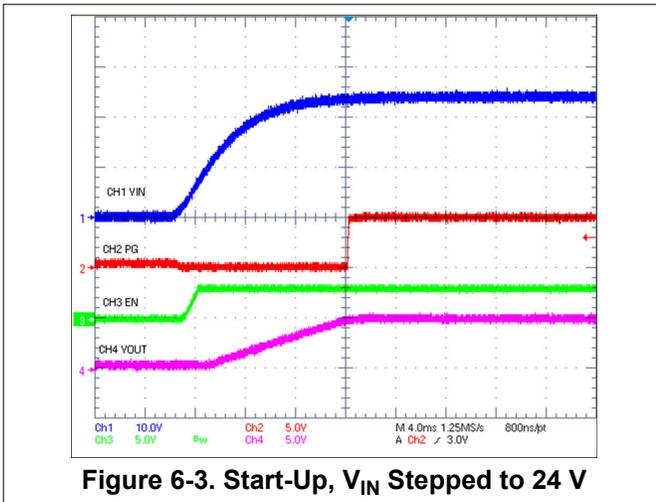
6 Test Data and Performance Curves

Because actual performance data can be affected by measurement techniques and environmental variables, these curves are presented for reference and can differ from actual field measurements. Unless otherwise indicated, $V_{in} = 13.5\text{ V}$, $V_{out} = 5\text{ V}$, $I_{out} = 5\text{ A}$, and $F_{sw} = 400\text{ kHz}$.

6.1 Efficiency and Load Regulation Performance



6.2 Waveforms



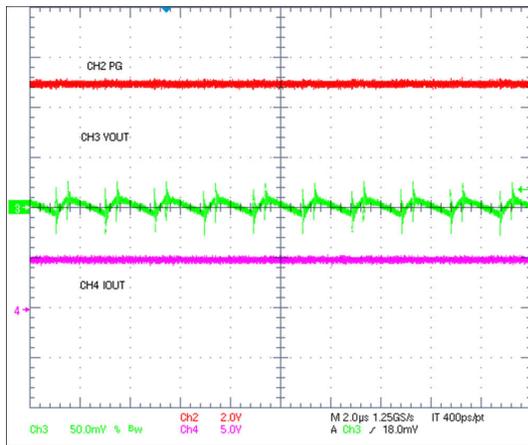
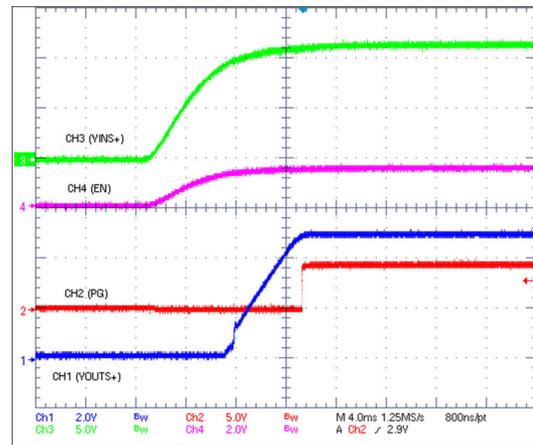


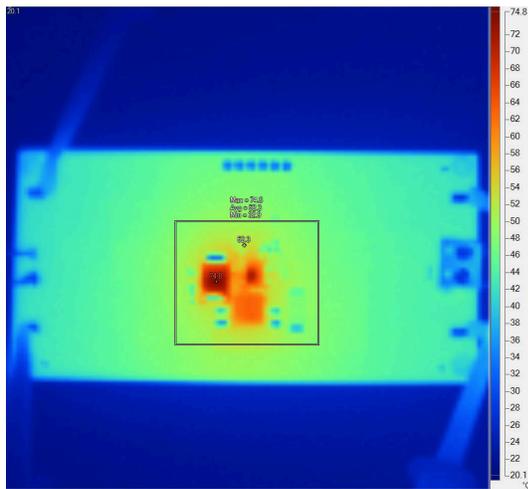
Figure 6-7. Output Ripple Loaded to 5 A



UVLO Set to an Input of
10 V

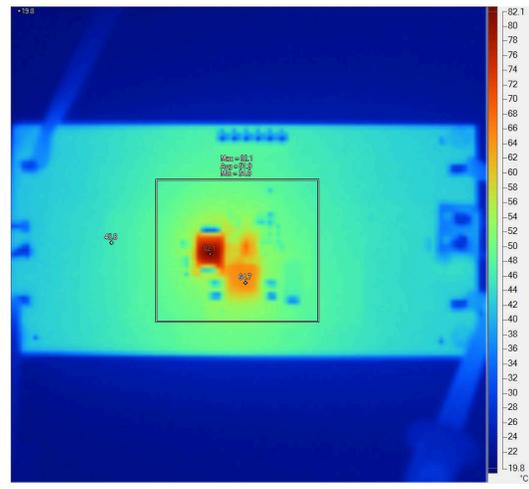
Figure 6-8. Precision Enable Start-Up

6.3 Thermal Performance



$f_{SW} = 400 \text{ kHz}$ $V_{OUT} = 5 \text{ V}$ $I_{out} = 5 \text{ A}$

Figure 6-9. Infrared Thermal Image: Test $V_{IN} = 13.5 \text{ V}$



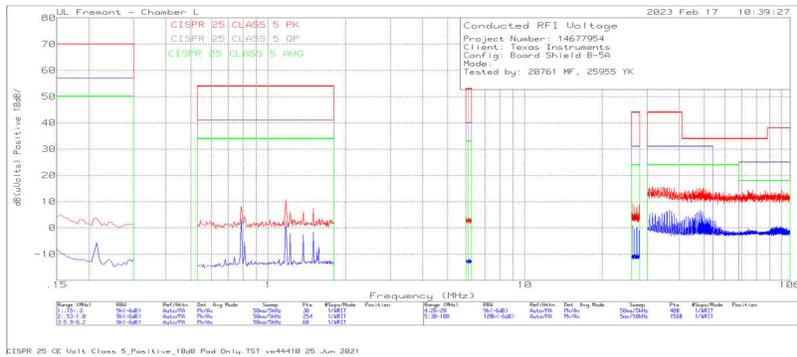
$f_{SW} = 400 \text{ kHz}$ $V_{OUT} = 5 \text{ V}$ $I_{out} = 5 \text{ A}$

Figure 6-10. Infrared Thermal Image: Test $V_{IN} = 24 \text{ V}$

6.4 EMI Performance

Figure 6-11 through Figure 6-15 provide peak and average EMI waveforms for passing CISPR 25 Class 5 EMI specifications. Both conducted and radiated emissions were evaluated on the LMR54450QEVm.

To pass CISPR 25 Class 5 Automotive radiated emissions, an EMI shield was added to the EVM. Unless otherwise indicated, $V_{in} = 13.5\text{ V}$, $V_{out} = 5\text{ V}$, $I_{out} = 5\text{ A}$, and $F_{sw} = 400\text{ kHz}$.

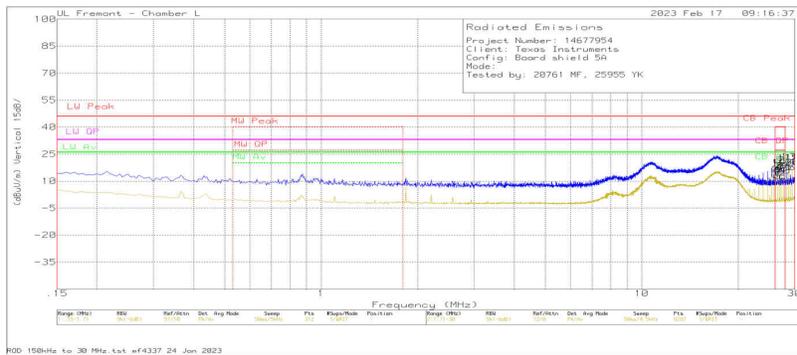


$V_{in} = 13.5$

$V_{out} = 5\text{ V}$

$I_{out} = 5\text{ A}$

Figure 6-11. CISPR 25 Class 5 Conducted Emissions

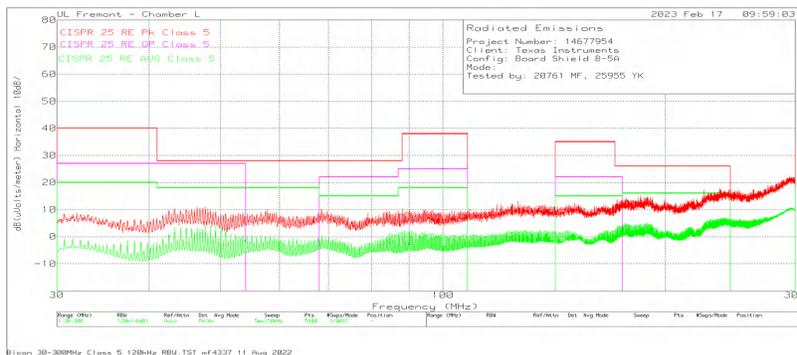


$V_{in} = 13.5$

$V_{out} = 5\text{ V}$

$I_{out} = 5\text{ A}$

Figure 6-12. CISPR 25 Class 5 Radiated Emissions: Monopole

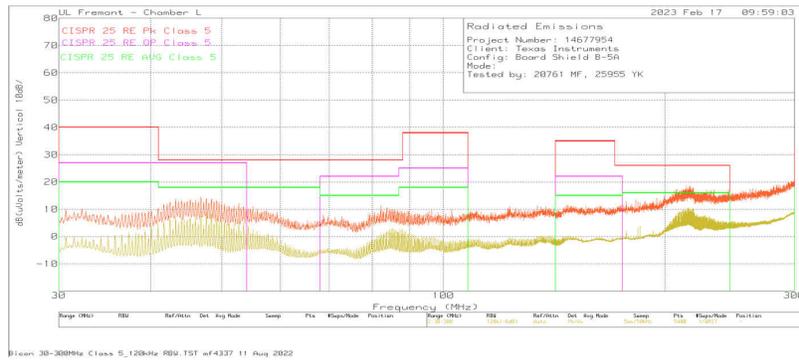


$V_{in} = 13.5$

$V_{out} = 5\text{ V}$

$I_{out} = 5\text{ A}$

Figure 6-13. CISPR 25 Class 5 Radiated Emissions: Biconical Horizontal Polarization

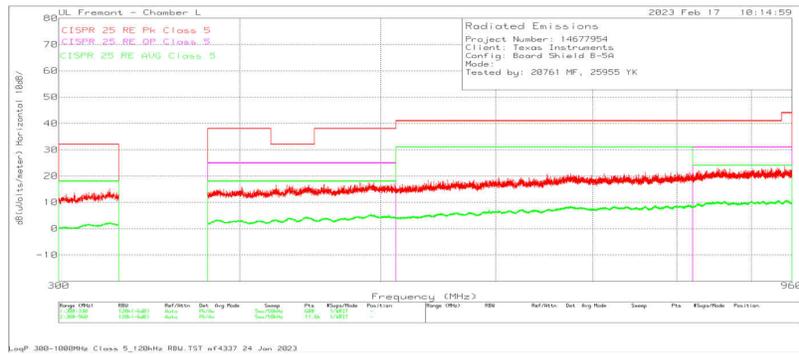


$V_{in} = 13.5$

$V_{out} = 5 V$

$I_{out} = 5 A$

Figure 6-14. CISPR 25 Class 5 Radiated Emissions: Biconical Vertical Polarization



$V_{in} = 13.5$

$V_{out} = 5 V$

$I_{out} = 5 A$

Figure 6-15. CISPR 25 Class 5 Radiated Emissions: Log Periodic

7 Bill of Materials

Table 7-1. Component BOM

RefDes	Description	Qty	Value	Package	Part Number	Manufacturer
C1	CAP, CERM, 100 pF, 50 V, +/- 10%, X7R, 0603	1	100 pF	0603	CL10B101KB8NNNC	Samsung Electro-Mechanics
C2, C3, C4, C5	CAP, CERM, 10 µF, 50 V, +/- 10%, X7R, AEC-Q200 Grade 1, 1206	4	10 µF	1206	CGA5L1X7R1H106K160AC	TDK
C6, C7	CAP, CERM, 2.2 µF, 50 V, +/- 10%, X7R, 0805	2	2.2 µF	0805	C2012X7R1H225K125AC	TDK
C8	CAP, AL, 100 µF, 63 V, +/- 20%, 0.35 ohm, AEC-Q200 Grade 2, SMD	1	100 µF	SMD	EEE-FK1J101P	Panasonic
C9, C10, C11, C12	CAP, CERM, 10 µF, 50 V, +/- 10%, X7R, 1210	4	10 µF	1210	GRM32ER71H106KA12L	MuRata
C13, C14, C19	CAP, CERM, 0.01 µF, 50 V, +/- 10%, X7R, 0603	3	0.01 µF	0603	C1608X7R1H103K080AA	TDK
C15	CAP, CERM, 470 pF, 50 V, +/- 10%, X7R, 0805	1	470 pF	0805	CC0805KRX7R9BB471	Yageo America
C16, C17	CAP, CERM, 10 µF, 10 V, +/- 10%, X7R, 1210	2	10 µF	1210	GRM32DR71A106KA01L	MuRata
C18	330µF Molded Tantalum Polymer Capacitor 10V 2917 (7343 Metric) 35mOhm @ 100kHz	1	330 µF	SMD2	10TPB330M	Panasonic
C20	CAP, CERM, 0.1 µF, 50 V, +/- 10%, X7R, 0603	1	0.1 µF	0603	885012206095	Würth Elektronik
D1	Diode, Schottky, 40 V, 5 A, SMC	1	40 V	SMC	B540C-13-F	Diodes Inc.
FB1	Ferrite Bead, 110 ohm @ 100 MHz, 5.4 A, 1206	1	110 Ω	1206	74279221111	Würth Elektronik
H1, H2, H3, H4	Standoff, Hex, 0.5"L #4-40 Nylon	4	-	-	1902C	Keystone
H5, H6, H7, H8	Screw, Pan Head , 4-40, 3/8", Nylon	4	-	-	NY PMS 440 0038 PH	B&F Fastener Supply
J1, J3	Terminal Block, 5.08 mm, 2x1, Brass, TH	2	-	-	ED120/2DS	On-Shore Technology
J2	Header, 2.54mm, 5x1, Tin, TH	1	-	-	PEC05SAAN	Sullins Connector Solutions
L1	Shielded Power Inductors 2.2µH 20% 18.8mOhm 10.7A	1	2.2 µH	-	XGL5020-222MEC	Coilcraft
L2	Power Inductor, shielded, composite, 20% tol, SMT, RoHS, halogen-free	1	10.0 µH	-	XGL6060-103MEC	Coilcraft
R1	RES, 0, 5%, 0.1 W, AEC-Q200 Grade 0, 0603	1	0 Ω	0603	CRCW06030000Z0EA	Vishay-Dale
R2	RES, 78.7 k, 1%, 0.1 W, AEC-Q200 Grade 0, 0603	1	78.7 kΩ	0603	CRCW060378K7FKEA	Vishay-Dale
R3	RES, 11.8 k, 1%, 0.1 W, AEC-Q200 Grade 0, 0603	1	11.8 kΩ	0603	CRCW060311K8FKEA	Vishay-Dale
R4	RES, 20.0, 1%, 0.1 W, AEC-Q200 Grade 0, 0603	1	20.0 Ω	0603	CRCW060320R0FKEA	Vishay-Dale
R5	RES, 110 k, 1%, 0.1 W, 0603	1	110 kΩ	0603	RC0603FR-07110KL	Yageo
R6	RES, 30.9 k, 0.1%, 0.1 W, 0603	1	30.9 kΩ	0603	RT0603BRD0730K9L	Yageo America
R7	RES, 3.74, 1%, 0.25 W, AEC-Q200 Grade 0, 1206	1	3.74 Ω	1206	CRCW12063R74FKEA	Vishay-Dale
R8	RES, 10.0 k, 1%, 0.1 W, AEC-Q200 Grade 0, 0603	1	10.0 kΩ	0603	CRCW060310K0FKEA	Vishay-Dale
TP1, TP2, TP3, TP4	Test Point, Miniature, SMT	4	-	-	5019	Keystone
U1	5-A, Wide Input Range, Step-Down Converter	1	-	HSOIC8	LMR54450QDDARQ1	Texas Instruments

8 Custom Design With WEBENCH® Tools

[Click here](#) to create a custom design using the LMR54450-Q1 device with WEBENCH® Power Designer.

1. Start by entering the input voltage (V_{IN}), output voltage (V_{OUT}), and output current (I_{OUT}) requirements.
2. Optimize the design for key parameters such as efficiency, footprint, and cost using the optimizer dial.
3. Compare the generated design with other possible devices from Texas Instruments.

The WEBENCH Power Designer provides a customized schematic along with a list of materials with real-time pricing and component availability.

In most cases, these actions are available:

- Run electrical simulations to see important waveforms and circuit performance.
- Run thermal simulations to understand board thermal performance.
- Export customized schematic and layout into popular CAD formats.
- Print PDF reports for the design, and share the design with colleagues.

Get more information about WEBENCH tools at www.ti.com/WEBENCH.

9 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Revision * (June 2022) to Revision A (May 2023)	Page
• Added EVM image.....	1
• Changed <i>Abstract</i> section.....	1
• Added <i>Background</i> section.....	3
• Changed <i>EVM Test Setup</i> figure in <i>EVM Connections</i> section.....	4
• Changed Layout images in <i>PCB Layout</i> section.....	6
• Added <i>Thermal Performance</i> section.....	10
• Added <i>EMI Performance</i> section.....	11

STANDARD TERMS FOR EVALUATION MODULES

1. *Delivery:* TI delivers TI evaluation boards, kits, or modules, including any accompanying demonstration software, components, and/or documentation which may be provided together or separately (collectively, an "EVM" or "EVMs") to the User ("User") in accordance with the terms set forth herein. User's acceptance of the EVM is expressly subject to the following terms.
 - 1.1 EVMs are intended solely for product or software developers for use in a research and development setting to facilitate feasibility evaluation, experimentation, or scientific analysis of TI semiconductors products. EVMs have no direct function and are not finished products. EVMs shall not be directly or indirectly assembled as a part or subassembly in any finished product. For clarification, any software or software tools provided with the EVM ("Software") shall not be subject to the terms and conditions set forth herein but rather shall be subject to the applicable terms that accompany such Software
 - 1.2 EVMs are not intended for consumer or household use. EVMs may not be sold, sublicensed, leased, rented, loaned, assigned, or otherwise distributed for commercial purposes by Users, in whole or in part, or used in any finished product or production system.
2. *Limited Warranty and Related Remedies/Disclaimers:*
 - 2.1 These terms do not apply to Software. The warranty, if any, for Software is covered in the applicable Software License Agreement.
 - 2.2 TI warrants that the TI EVM will conform to TI's published specifications for ninety (90) days after the date TI delivers such EVM to User. Notwithstanding the foregoing, TI shall not be liable for a nonconforming EVM if (a) the nonconformity was caused by neglect, misuse or mistreatment by an entity other than TI, including improper installation or testing, or for any EVMs that have been altered or modified in any way by an entity other than TI, (b) the nonconformity resulted from User's design, specifications or instructions for such EVMs or improper system design, or (c) User has not paid on time. Testing and other quality control techniques are used to the extent TI deems necessary. TI does not test all parameters of each EVM. User's claims against TI under this Section 2 are void if User fails to notify TI of any apparent defects in the EVMs within ten (10) business days after delivery, or of any hidden defects with ten (10) business days after the defect has been detected.
 - 2.3 TI's sole liability shall be at its option to repair or replace EVMs that fail to conform to the warranty set forth above, or credit User's account for such EVM. TI's liability under this warranty shall be limited to EVMs that are returned during the warranty period to the address designated by TI and that are determined by TI not to conform to such warranty. If TI elects to repair or replace such EVM, TI shall have a reasonable time to repair such EVM or provide replacements. Repaired EVMs shall be warranted for the remainder of the original warranty period. Replaced EVMs shall be warranted for a new full ninety (90) day warranty period.

WARNING

Evaluation Kits are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems.

User shall operate the Evaluation Kit within TI's recommended guidelines and any applicable legal or environmental requirements as well as reasonable and customary safeguards. Failure to set up and/or operate the Evaluation Kit within TI's recommended guidelines may result in personal injury or death or property damage. Proper set up entails following TI's instructions for electrical ratings of interface circuits such as input, output and electrical loads.

NOTE:

EXPOSURE TO ELECTROSTATIC DISCHARGE (ESD) MAY CAUSE DEGRADATION OR FAILURE OF THE EVALUATION KIT; TI RECOMMENDS STORAGE OF THE EVALUATION KIT IN A PROTECTIVE ESD BAG.

3 Regulatory Notices:

3.1 United States

3.1.1 Notice applicable to EVMs not FCC-Approved:

FCC NOTICE: This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

3.1.2 For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:

CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

3.2 Canada

3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247

Concerning EVMs Including Radio Transmitters:

This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:

(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concerning EVMs Including Detachable Antennas:

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur.

3.3 Japan

3.3.1 *Notice for EVMs delivered in Japan:* Please see http://www.tij.co.jp/lstds/ti_ja/general/eStore/notice_01.page 日本国内に輸入される評価用キット、ボードについては、次のところをご覧ください。

<https://www.ti.com/ja-jp/legal/notice-for-evaluation-kits-delivered-in-japan.html>

3.3.2 *Notice for Users of EVMs Considered "Radio Frequency Products" in Japan:* EVMs entering Japan may not be certified by TI as conforming to Technical Regulations of Radio Law of Japan.

If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required to follow the instructions set forth by Radio Law of Japan, which includes, but is not limited to, the instructions below with respect to EVMs (which for the avoidance of doubt are stated strictly for convenience and should be verified by User):

1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

【無線電波を送信する製品の開発キットをお使いになる際の注意事項】 開発キットの中には技術基準適合証明を受けていないものがあります。技術適合証明を受けていないものご使用に際しては、電波法遵守のため、以下のいずれかの措置を取っていただく必要がありますのでご注意ください。

1. 電波法施行規則第6条第1項第1号に基づく平成18年3月28日総務省告示第173号で定められた電波暗室等の試験設備でご使用いただく。
2. 実験局の免許を取得後ご使用いただく。
3. 技術基準適合証明を取得後ご使用いただく。

なお、本製品は、上記の「ご使用にあたっての注意」を譲渡先、移転先に通知しない限り、譲渡、移転できないものとします。

上記を遵守頂けない場合は、電波法の罰則が適用される可能性があることをご留意ください。日本テキサス・インスツルメンツ株式会社
東京都新宿区西新宿 6 丁目 2 4 番 1 号
西新宿三井ビル

3.3.3 *Notice for EVMs for Power Line Communication:* Please see http://www.tij.co.jp/lstds/ti_ja/general/eStore/notice_02.page

電力線搬送波通信についての開発キットをお使いになる際の注意事項については、次のところをご覧ください。 <https://www.ti.com/ja-jp/legal/notice-for-evaluation-kits-for-power-line-communication.html>

3.4 European Union

3.4.1 *For EVMs subject to EU Directive 2014/30/EU (Electromagnetic Compatibility Directive):*

This is a class A product intended for use in environments other than domestic environments that are connected to a low-voltage power-supply network that supplies buildings used for domestic purposes. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

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- 4 *EVM Use Restrictions and Warnings:*
 - 4.1 EVMS ARE NOT FOR USE IN FUNCTIONAL SAFETY AND/OR SAFETY CRITICAL EVALUATIONS, INCLUDING BUT NOT LIMITED TO EVALUATIONS OF LIFE SUPPORT APPLICATIONS.
 - 4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.
 - 4.3 *Safety-Related Warnings and Restrictions:*
 - 4.3.1 User shall operate the EVM within TI's recommended specifications and environmental considerations stated in the user guide, other available documentation provided by TI, and any other applicable requirements and employ reasonable and customary safeguards. Exceeding the specified performance ratings and specifications (including but not limited to input and output voltage, current, power, and environmental ranges) for the EVM may cause personal injury or death, or property damage. If there are questions concerning performance ratings and specifications, User should contact a TI field representative prior to connecting interface electronics including input power and intended loads. Any loads applied outside of the specified output range may also result in unintended and/or inaccurate operation and/or possible permanent damage to the EVM and/or interface electronics. Please consult the EVM user guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative. During normal operation, even with the inputs and outputs kept within the specified allowable ranges, some circuit components may have elevated case temperatures. These components include but are not limited to linear regulators, switching transistors, pass transistors, current sense resistors, and heat sinks, which can be identified using the information in the associated documentation. When working with the EVM, please be aware that the EVM may become very warm.
 - 4.3.2 EVMs are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems. User assumes all responsibility and liability for proper and safe handling and use of the EVM by User or its employees, affiliates, contractors or designees. User assumes all responsibility and liability to ensure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard. User assumes all responsibility and liability for any improper or unsafe handling or use of the EVM by User or its employees, affiliates, contractors or designees.
 - 4.4 User assumes all responsibility and liability to determine whether the EVM is subject to any applicable international, federal, state, or local laws and regulations related to User's handling and use of the EVM and, if applicable, User assumes all responsibility and liability for compliance in all respects with such laws and regulations. User assumes all responsibility and liability for proper disposal and recycling of the EVM consistent with all applicable international, federal, state, and local requirements.
 5. *Accuracy of Information:* To the extent TI provides information on the availability and function of EVMs, TI attempts to be as accurate as possible. However, TI does not warrant the accuracy of EVM descriptions, EVM availability or other information on its websites as accurate, complete, reliable, current, or error-free.
 6. *Disclaimers:*
 - 6.1 EXCEPT AS SET FORTH ABOVE, EVMS AND ANY MATERIALS PROVIDED WITH THE EVM (INCLUDING, BUT NOT LIMITED TO, REFERENCE DESIGNS AND THE DESIGN OF THE EVM ITSELF) ARE PROVIDED "AS IS" AND "WITH ALL FAULTS." TI DISCLAIMS ALL OTHER WARRANTIES, EXPRESS OR IMPLIED, REGARDING SUCH ITEMS, INCLUDING BUT NOT LIMITED TO ANY EPIDEMIC FAILURE WARRANTY OR IMPLIED WARRANTIES OF MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF ANY THIRD PARTY PATENTS, COPYRIGHTS, TRADE SECRETS OR OTHER INTELLECTUAL PROPERTY RIGHTS.
 - 6.2 EXCEPT FOR THE LIMITED RIGHT TO USE THE EVM SET FORTH HEREIN, NOTHING IN THESE TERMS SHALL BE CONSTRUED AS GRANTING OR CONFERRING ANY RIGHTS BY LICENSE, PATENT, OR ANY OTHER INDUSTRIAL OR INTELLECTUAL PROPERTY RIGHT OF TI, ITS SUPPLIERS/LICENSORS OR ANY OTHER THIRD PARTY, TO USE THE EVM IN ANY FINISHED END-USER OR READY-TO-USE FINAL PRODUCT, OR FOR ANY INVENTION, DISCOVERY OR IMPROVEMENT, REGARDLESS OF WHEN MADE, CONCEIVED OR ACQUIRED.
 7. *USER'S INDEMNITY OBLIGATIONS AND REPRESENTATIONS.* USER WILL DEFEND, INDEMNIFY AND HOLD TI, ITS LICENSORS AND THEIR REPRESENTATIVES HARMLESS FROM AND AGAINST ANY AND ALL CLAIMS, DAMAGES, LOSSES, EXPENSES, COSTS AND LIABILITIES (COLLECTIVELY, "CLAIMS") ARISING OUT OF OR IN CONNECTION WITH ANY HANDLING OR USE OF THE EVM THAT IS NOT IN ACCORDANCE WITH THESE TERMS. THIS OBLIGATION SHALL APPLY WHETHER CLAIMS ARISE UNDER STATUTE, REGULATION, OR THE LAW OF TORT, CONTRACT OR ANY OTHER LEGAL THEORY, AND EVEN IF THE EVM FAILS TO PERFORM AS DESCRIBED OR EXPECTED.

8. *Limitations on Damages and Liability:*

8.1 *General Limitations.* IN NO EVENT SHALL TI BE LIABLE FOR ANY SPECIAL, COLLATERAL, INDIRECT, PUNITIVE, INCIDENTAL, CONSEQUENTIAL, OR EXEMPLARY DAMAGES IN CONNECTION WITH OR ARISING OUT OF THESE TERMS OR THE USE OF THE EVMS , REGARDLESS OF WHETHER TI HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES. EXCLUDED DAMAGES INCLUDE, BUT ARE NOT LIMITED TO, COST OF REMOVAL OR REINSTALLATION, ANCILLARY COSTS TO THE PROCUREMENT OF SUBSTITUTE GOODS OR SERVICES, RETESTING, OUTSIDE COMPUTER TIME, LABOR COSTS, LOSS OF GOODWILL, LOSS OF PROFITS, LOSS OF SAVINGS, LOSS OF USE, LOSS OF DATA, OR BUSINESS INTERRUPTION. NO CLAIM, SUIT OR ACTION SHALL BE BROUGHT AGAINST TI MORE THAN TWELVE (12) MONTHS AFTER THE EVENT THAT GAVE RISE TO THE CAUSE OF ACTION HAS OCCURRED.

8.2 *Specific Limitations.* IN NO EVENT SHALL TI'S AGGREGATE LIABILITY FROM ANY USE OF AN EVM PROVIDED HEREUNDER, INCLUDING FROM ANY WARRANTY, INDEMNITY OR OTHER OBLIGATION ARISING OUT OF OR IN CONNECTION WITH THESE TERMS, , EXCEED THE TOTAL AMOUNT PAID TO TI BY USER FOR THE PARTICULAR EVM(S) AT ISSUE DURING THE PRIOR TWELVE (12) MONTHS WITH RESPECT TO WHICH LOSSES OR DAMAGES ARE CLAIMED. THE EXISTENCE OF MORE THAN ONE CLAIM SHALL NOT ENLARGE OR EXTEND THIS LIMIT.

9. *Return Policy.* Except as otherwise provided, TI does not offer any refunds, returns, or exchanges. Furthermore, no return of EVM(s) will be accepted if the package has been opened and no return of the EVM(s) will be accepted if they are damaged or otherwise not in a resalable condition. If User feels it has been incorrectly charged for the EVM(s) it ordered or that delivery violates the applicable order, User should contact TI. All refunds will be made in full within thirty (30) working days from the return of the components(s), excluding any postage or packaging costs.

10. *Governing Law:* These terms and conditions shall be governed by and interpreted in accordance with the laws of the State of Texas, without reference to conflict-of-laws principles. User agrees that non-exclusive jurisdiction for any dispute arising out of or relating to these terms and conditions lies within courts located in the State of Texas and consents to venue in Dallas County, Texas. Notwithstanding the foregoing, any judgment may be enforced in any United States or foreign court, and TI may seek injunctive relief in any United States or foreign court.

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