

LP5860 11 × 18 LED Matrix Driver Register Maps

Technical Reference Manual



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About This Manual

This Technical Reference Manual (TRM) details the register maps of LP5860.

The TRM should not be considered a substitute for the data sheet, rather a companion guide that should be used alongside the device-specific data sheet to understand the details to program the device. The primary purpose of the TRM is to abstract the programming registers of the device from the data manual. This allows the data sheet to outline the high-level features of the device without unnecessary information about register descriptions.

Notational Conventions

This document uses the following conventions.

- Hexadecimal numbers can be shown with the suffix h or the prefix 0x. For example, the following number is 40 hexadecimal (decimal 64): 40h or 0x40.
- Registers in this document are shown in figures and described in tables.
 - Each register figure shows a rectangle divided into fields that represent the fields of the register. Each field is labeled with its bit name, its beginning and ending bit numbers above, and its read/write properties with default reset value below. A legend explains the notation used for the properties.
 - Reserved bits in a register figure can have one of multiple meanings:
 - Not implemented on the device
 - Reserved for future device expansion
 - Reserved for TI testing
 - Reserved configurations of the device that are not supported
 - Writing nondefault values to the Reserved bits could cause unexpected behavior and should be avoided.

Glossary

[TI Glossary](#) This glossary lists and explains terms, acronyms, and definitions.

Related Documentation

For a complete listing of related documentation and development-support tools, visit the Texas Instruments website at <http://www.ti.com>.

[SNVSBU8](#) *LP5860 11 × 18 LED Matrix Driver with 8-bit Analog and 8-/16-bit PWM Dimming* describes the data sheet of the LP5860 device.

Support Resources

[TI E2E™ support forums](#) are an engineer's go-to source for fast, verified answers and design help — straight from the experts. Search existing answers or ask your own question to get the quick design help you need.

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1.1 Overview

The LP5860 is an 11×18 LED matrix driver. It integrates 11 switching FETs with 18 constant current sinks. One LP5860 device can drive up to 198 LED dots or 66 RGB pixels by using time-multiplexing matrix scheme.

1.2 Description

The LP5860 supports both analog dimming and PWM dimming methods. For analog dimming, the current gain of each individual LED dot can be adjusted with 256 steps through 8-bits dot correction. For PWM dimming, the integrated 8-bits or 16-bits configurable, > 20-KHz PWM generators for each LED dot enable smooth, vivid animation effects without audible noise. Each LED can also be mapped into a 8-bits group PWM to achieve the group control with minimum data traffic.

The LP5860 device implements full addressable SRAM. It supports entire SRAM data refresh and partial SRAM data update on demand to minimize the data traffic. The LP5860 implements the ghost cancellation circuit to eliminate both upside and downside ghosting. The LP5860 also utilizes low brightness compensation technology to support high density LED pixels. Both 1-MHz (max.) I²C and 12-MHz (max.) SPI interfaces are available in the LP5860.

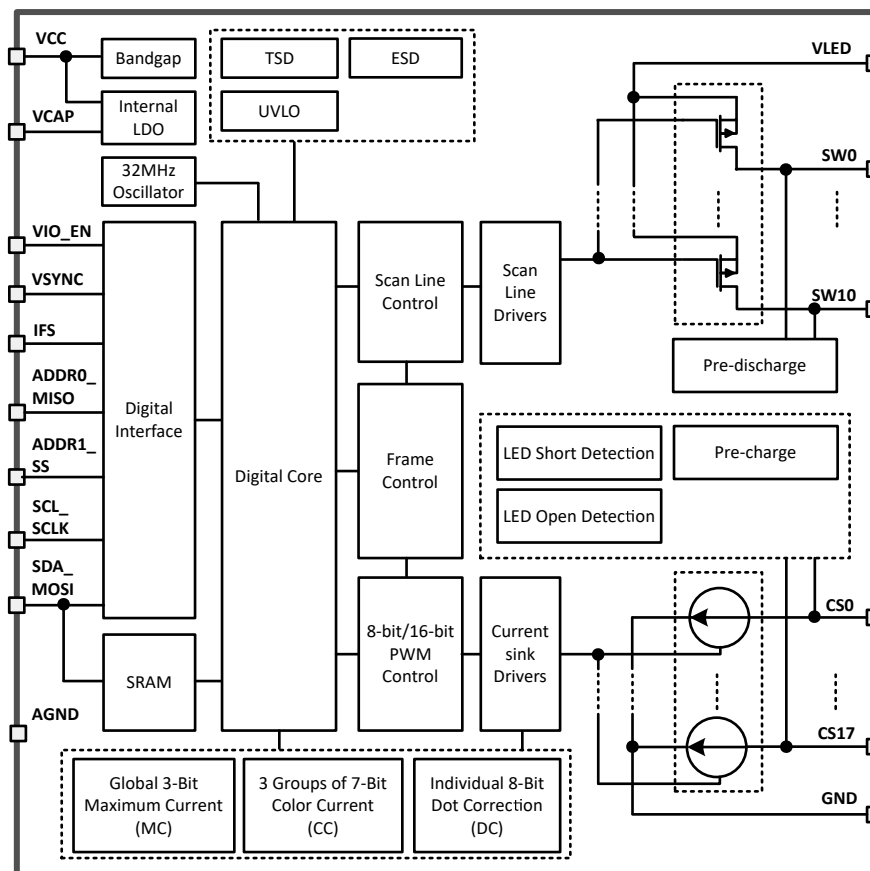


Figure 1-1. Device Block Diagram

This section shows the detailed register maps of LP5860.

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2.1 Register Map Table

This section provides a summary of the register maps.

Table 2-1. Register Section/Block Access Type Codes

Access Type	Code	Description
Read Type		
R	R	Read
RC	R C	Read to Clear
R-0	R -0	Read Returns 0s
Write Type		
W	W	Write
W0CP	W 0C P	W 0 to clear Requires privileged access
Reset or Default Value		
-n		Value after reset or the default value

Register Acronym	Address	Type	D7	D6	D5	D4	D3	D2	D1	D0	Default
Chip_en	000h	R/W	Reserved							Chip_EN	00h
Dev_initial	001h	R/W	Reserved	Max_Line_Num				Data_Ref_Mode		PWM_Fre	5Eh
Dev_config1	002h	R/W	Reserved	Reserved	Reserved	Reserved	SW_BLK	PWM_Sc ale_Mode	PWM_Ph ase_Shift	CS_ON_ Shift	00h
Dev_config2	003h	R/W	Comp_Group3		Comp_Group2		Comp_Group1		LOD_rem oval	LSD_rem oval	00h
Dev_config3	004h	R/W	Down_Deghost		Up_Deghost		Maximum_Current			Up_Degh ost_enabl e	47h
Global_bri	005h	R/W	PWM_Global								FFh
Group0_bri	006h	R/W	PWM_Group1								FFh
Group1_bri	007h	R/W	PWM_Group2								FFh
Group2_bri	008h	R/W	PWM_Group3								FFh
R_current_set	009h	R/W	Reserved	CC_Group1							40h
G_current_set	00Ah	R/W	Reserved	CC_Group2							40h
B_current_set	00Bh	R/W	Reserved	CC_Group3							40h
Dot_grp_sel0	00Ch	R/W	Dot L0-CS3 group		Dot L0-CS2 group		Dot L0-CS1 group		Dot L0-CS0 group		00h
Dot_grp_sel1	00Dh	R/W	Dot L0-CS7 group		Dot L0-CS6 group		Dot L0-CS5 group		Dot L0-CS4 group		00h
Dot_grp_sel2	00Eh	R/W	Dot L0-CS11 group		Dot L0-CS10 group		Dot L0-CS9 group		Dot L0-CS8 group		00h
Dot_grp_sel3	00Fh	R/W	Dot L0-CS15 group		Dot L0-CS14 group		Dot L0-CS13 group		Dot L0-CS12 group		00h
Dot_grp_sel4	010h	R/W	Reserved				Dot L0-CS17 group		Dot L0-CS16 group		00h
Dot_grp_sel5	011h	R/W	Dot L1-CS3 group		Dot L1-CS2 group		Dot L1-CS1 group		Dot L1-CS0 group		00h
Dot_grp_sel6	012h	R/W	Dot L1-CS7 group		Dot L1-CS6 group		Dot L1-CS5 group		Dot L1-CS4 group		00h
Dot_grp_sel7	013h	R/W	Dot L1-CS11 group		Dot L1-CS10 group		Dot L1-CS9 group		Dot L1-CS8 group		00h
Dot_grp_sel8	014h	R/W	Dot L1-CS15 group		Dot L1-CS14 group		Dot L1-CS13 group		Dot L1-CS12 group		00h
Dot_grp_sel9	015h	R/W	Reserved				Dot L1-CS17 group		Dot L1-CS16 group		00h
Dot_grp_sel10	016h	R/W	Dot L2-CS3 group		Dot L2-CS2 group		Dot L2-CS1 group		Dot L2-CS0 group		00h
Dot_grp_sel11	017h	R/W	Dot L2-CS7 group		Dot L2-CS6 group		Dot L2-CS5 group		Dot L2-CS4 group		00h

Dot_grp_sel12	018h	R/W	Dot L2-CS11 group				Dot L2-CS10 group		Dot L2-CS9 group		Dot L2-CS8 group		00h
Dot_grp_sel13	019h	R/W	Dot L2-CS15 group				Dot L2-CS14 group		Dot L2-CS13 group		Dot L2-CS12 group		00h
Dot_grp_sel14	01Ah	R/W	Reserved						Dot L2-CS17 group		Dot L2-CS16 group		00h
Dot_grp_sel15	01Bh	R/W	Dot L3-CS3 group		Dot L3-CS2 group		Dot L3-CS1 group		Dot L3-CS0 group			00h	
Dot_grp_sel16	01Ch	R/W	Dot L3-CS7 group		Dot L3-CS6 group		Dot L3-CS5 group		Dot L3-CS4 group			00h	
Dot_grp_sel17	01Dh	R/W	Dot L3-CS11 group		Dot L3-CS10 group		Dot L3-CS9 group		Dot L3-CS8 group			00h	
Dot_grp_sel18	01Eh	R/W	Dot L3-CS15 group		Dot L3-CS14 group		Dot L3-CS13 group		Dot L3-CS12 group			00h	
Dot_grp_sel19	01Fh	R/W	Reserved						Dot L3-CS17 group		Dot L3-CS16 group		00h
Dot_grp_sel20	020h	R/W	Dot L4-CS3 group		Dot L4-CS2 group		Dot L4-CS1 group		Dot L4-CS0 group			00h	
Dot_grp_sel21	021h	R/W	Dot L4-CS7 group		Dot L4-CS6 group		Dot L4-CS5 group		Dot L4-CS4 group			00h	
Dot_grp_sel22	022h	R/W	Dot L4-CS11 group		Dot L4-CS10 group		Dot L4-CS9 group		Dot L4-CS8 group			00h	
Dot_grp_sel23	023h	R/W	Dot L4-CS15 group		Dot L4-CS14 group		Dot L4-CS13 group		Dot L4-CS12 group			00h	
Dot_grp_sel24	024h	R/W	Reserved						Dot L4-CS17 group		Dot L4-CS16 group		00h
Dot_grp_sel25	025h	R/W	Dot L5-CS3 group		Dot L5-CS2 group		Dot L5-CS1 group		Dot L5-CS0 group			00h	
Dot_grp_sel26	026h	R/W	Dot L5-CS7 group		Dot L5-CS6 group		Dot L5-CS5 group		Dot L5-CS4 group			00h	
Dot_grp_sel27	027h	R/W	Dot L5-CS11 group		Dot L5-CS10 group		Dot L5-CS9 group		Dot L5-CS8 group			00h	
Dot_grp_sel28	028h	R/W	Dot L5-CS15 group		Dot L5-CS14 group		Dot L5-CS13 group		Dot L5-CS12 group			00h	
Dot_grp_sel29	029h	R/W	Reserved						Dot L5-CS17 group		Dot L5-CS16 group		00h
Dot_grp_sel30	02Ah	R/W	Dot L6-CS3 group		Dot L6-CS2 group		Dot L6-CS1 group		Dot L6-CS0 group			00h	
Dot_grp_sel31	02Bh	R/W	Dot L6-CS7 group		Dot L6-CS6 group		Dot L6-CS5 group		Dot L6-CS4 group			00h	
Dot_grp_sel32	02Ch	R/W	Dot L6-CS11 group		Dot L6-CS10 group		Dot L6-CS9 group		Dot L6-CS8 group			00h	
Dot_grp_sel33	02Dh	R/W	Dot L6-CS15 group		Dot L6-CS14 group		Dot L6-CS13 group		Dot L6-CS12 group			00h	
Dot_grp_sel34	02Eh	R/W	Reserved						Dot L6-CS17 group		Dot L6-CS16 group		00h
Dot_grp_sel35	02Fh	R/W	Dot L7-CS3 group		Dot L7-CS2 group		Dot L7-CS1 group		Dot L7-CS0 group			00h	
Dot_grp_sel36	030h	R/W	Dot L7-CS7 group		Dot L7-CS6 group		Dot L7-CS5 group		Dot L7-CS4 group			00h	
Dot_grp_sel37	031h	R/W	Dot L7-CS11 group		Dot L7-CS10 group		Dot L7-CS9 group		Dot L7-CS8 group			00h	
Dot_grp_sel38	032h	R/W	Dot L7-CS15 group		Dot L7-CS14 group		Dot L7-CS13 group		Dot L7-CS12 group			00h	
Dot_grp_sel39	033h	R/W	Reserved						Dot L7-CS17 group		Dot L7-CS16 group		00h
Dot_grp_sel40	034h	R/W	Dot L8-CS3 group		Dot L8-CS2 group		Dot L8-CS1 group		Dot L8-CS0 group			00h	
Dot_grp_sel41	035h	R/W	Dot L8-CS7 group		Dot L8-CS6 group		Dot L8-CS5 group		Dot L8-CS4 group			00h	
Dot_grp_sel42	036h	R/W	Dot L8-CS11 group		Dot L8-CS10 group		Dot L8-CS9 group		Dot L8-CS8 group			00h	
Dot_grp_sel43	037h	R/W	Dot L8-CS15 group		Dot L8-CS14 group		Dot L8-CS13 group		Dot L8-CS12 group			00h	
Dot_grp_sel44	038h	R/W	Reserved						Dot L8-CS17 group		Dot L8-CS16 group		00h
Dot_grp_sel45	039h	R/W	Dot L9-CS3 group		Dot L9-CS2 group		Dot L9-CS1 group		Dot L9-CS0 group			00h	
Dot_grp_sel46	03Ah	R/W	Dot L9-CS7 group		Dot L9-CS6 group		Dot L9-CS5 group		Dot L9-CS4 group			00h	
Dot_grp_sel47	03Bh	R/W	Dot L9-CS11 group		Dot L9-CS10 group		Dot L9-CS9 group		Dot L9-CS8 group			00h	
Dot_grp_sel48	03Ch	R/W	Dot L9-CS15 group		Dot L9-CS14 group		Dot L9-CS13 group		Dot L9-CS12 group			00h	
Dot_grp_sel49	03Dh	R/W	Reserved						Dot L9-CS17 group		Dot L9-CS16 group		00h
Dot_grp_sel50	03Eh	R/W	Dot L10-CS3 group		Dot L10-CS2 group		Dot L10-CS1 group		Dot L10-CS0 group			00h	
Dot_grp_sel51	03Fh	R/W	Dot L10-CS7 group		Dot L10-CS6 group		Dot L10-CS5 group		Dot L10-CS4 group			00h	
Dot_grp_sel52	040h	R/W	Dot L10-CS11 group		Dot L10-CS10 group		Dot L10-CS9 group		Dot L10-CS8 group			00h	
Dot_grp_sel53	041h	R/W	Dot L10-CS15 group		Dot L10-CS14 group		Dot L10-CS13 group		Dot L10-CS12 group			00h	
Dot_grp_sel54	042h	R/W	Reserved						Dot L10-CS17 group		Dot L10-CS16 group		00h
Dot_onoff0	043h	R/W	Dot L0-CS7 onoff	Dot L0-CS6 onoff	Dot L0-CS5 onoff	Dot L0-CS4 onoff	Dot L0-CS3 onoff	Dot L0-CS2 onoff	Dot L0-CS1 onoff	Dot L0-CS0 onoff	FFh		
Dot_onoff1	044h	R/W	Dot L0-CS15 onoff	Dot L0-CS14 onoff	Dot L0-CS13 onoff	Dot L0-CS12 onoff	Dot L0-CS11 onoff	Dot L0-CS10 onoff	Dot L0-CS9 onoff	Dot L0-CS8 onoff	FFh		

Dot_onoff2	045h	R/W	Reserved						Dot L0- CS17 onoff	Dot L0- CS16 onoff	03h
Dot_onoff3	046h	R/W	Dot L1- CS7 onoff	Dot L1- CS6 onoff	Dot L1- CS5 onoff	Dot L1- CS4 onoff	Dot L1- CS3 onoff	Dot L1- CS2 onoff	Dot L1- CS1 onoff	Dot L1- CS0 onoff	FFh
Dot_onoff4	047h	R/W	Dot L1- CS15 onoff	Dot L1- CS14 onoff	Dot L1- CS13 onoff	Dot L1- CS12 onoff	Dot L1- CS11 onoff	Dot L1- CS10 onoff	Dot L1- CS9 onoff	Dot L1- CS8 onoff	FFh
Dot_onoff5	048h	R/W	Reserved						Dot L1- CS17 onoff	Dot L1- CS16 onoff	03h
Dot_onoff6	049h	R/W	Dot L2- CS7 onoff	Dot L2- CS6 onoff	Dot L2- CS5 onoff	Dot L2- CS4 onoff	Dot L2- CS3 onoff	Dot L2- CS2 onoff	Dot L2- CS1 onoff	Dot L2- CS0 onoff	FFh
Dot_onoff7	04Ah	R/W	Dot L2- CS15 onoff	Dot L2- CS14 onoff	Dot L2- CS13 onoff	Dot L2- CS12 onoff	Dot L2- CS11 onoff	Dot L2- CS10 onoff	Dot L2- CS9 onoff	Dot L2- CS8 onoff	FFh
Dot_onoff8	04Bh	R/W	Reserved						Dot L2- CS17 onoff	Dot L2- CS16 onoff	03h
Dot_onoff9	04Ch	R/W	Dot L3- CS7 onoff	Dot L3- CS6 onoff	Dot L3- CS5 onoff	Dot L3- CS4 onoff	Dot L3- CS3 onoff	Dot L3- CS2 onoff	Dot L3- CS1 onoff	Dot L3- CS0 onoff	FFh
Dot_onoff10	04Dh	R/W	Dot L3- CS15 onoff	Dot L3- CS14 onoff	Dot L3- CS13 onoff	Dot L3- CS12 onoff	Dot L3- CS11 onoff	Dot L3- CS10 onoff	Dot L3- CS9 onoff	Dot L3- CS8 onoff	FFh
Dot_onoff11	04Eh	R/W	Reserved						Dot L3- CS17 onoff	Dot L3- CS16 onoff	03h
Dot_onoff12	04Fh	R/W	Dot L4- CS7 onoff	Dot L4- CS6 onoff	Dot L4- CS5 onoff	Dot L4- CS4 onoff	Dot L4- CS3 onoff	Dot L4- CS2 onoff	Dot L4- CS1 onoff	Dot L4- CS0 onoff	FFh
Dot_onoff13	050h	R/W	Dot L4- CS15 onoff	Dot L4- CS14 onoff	Dot L4- CS13 onoff	Dot L4- CS12 onoff	Dot L4- CS11 onoff	Dot L4- CS10 onoff	Dot L4- CS9 onoff	Dot L4- CS8 onoff	FFh
Dot_onoff14	051h	R/W	Reserved						Dot L4- CS17 onoff	Dot L4- CS16 onoff	03h
Dot_onoff15	052h	R/W	Dot L5- CS7 onoff	Dot L5- CS6 onoff	Dot L5- CS5 onoff	Dot L5- CS4 onoff	Dot L5- CS3 onoff	Dot L5- CS2 onoff	Dot L5- CS1 onoff	Dot L5- CS0 onoff	FFh
Dot_onoff16	053h	R/W	Dot L5- CS15 onoff	Dot L5- CS14 onoff	Dot L5- CS13 onoff	Dot L5- CS12 onoff	Dot L5- CS11 onoff	Dot L5- CS10 onoff	Dot L5- CS9 onoff	Dot L5- CS8 onoff	FFh
Dot_onoff17	054h	R/W	Reserved						Dot L5- CS17 onoff	Dot L5- CS16 onoff	03h
Dot_onoff18	055h	R/W	Dot L6- CS7 onoff	Dot L6- CS6 onoff	Dot L6- CS5 onoff	Dot L6- CS4 onoff	Dot L6- CS3 onoff	Dot L6- CS2 onoff	Dot L6- CS1 onoff	Dot L6- CS0 onoff	FFh
Dot_onoff19	056h	R/W	Dot L6- CS15 onoff	Dot L6- CS14 onoff	Dot L6- CS13 onoff	Dot L6- CS12 onoff	Dot L6- CS11 onoff	Dot L6- CS10 onoff	Dot L6- CS9 onoff	Dot L6- CS8 onoff	FFh
Dot_onoff20	057h	R/W	Reserved						Dot L6- CS17 onoff	Dot L6- CS16 onoff	03h
Dot_onoff21	058h	R/W	Dot L7- CS7 onoff	Dot L7- CS6 onoff	Dot L7- CS5 onoff	Dot L7- CS4 onoff	Dot L7- CS3 onoff	Dot L7- CS2 onoff	Dot L7- CS1 onoff	Dot L7- CS0 onoff	FFh
Dot_onoff22	059h	R/W	Dot L7- CS15 onoff	Dot L7- CS14 onoff	Dot L7- CS13 onoff	Dot L7- CS12 onoff	Dot L7- CS11 onoff	Dot L7- CS10 onoff	Dot L7- CS9 onoff	Dot L7- CS8 onoff	FFh
Dot_onoff23	05Ah	R/W	Reserved						Dot L7- CS17 onoff	Dot L7- CS16 onoff	03h

Dot_onoff24	05Bh	R/W	Dot L8- CS7 onoff	Dot L8- CS6 onoff	Dot L8- CS5 onoff	Dot L8- CS4 onoff	Dot L8- CS3 onoff	Dot L8- CS2 onoff	Dot L8- CS1 onoff	Dot L8- CS0 onoff	FFh
Dot_onoff25	05Ch	R/W	Dot L8- CS15 onoff	Dot L8- CS14 onoff	Dot L8- CS13 onoff	Dot L8- CS12 onoff	Dot L8- CS11 onoff	Dot L8- CS10 onoff	Dot L8- CS9 onoff	Dot L8- CS8 onoff	FFh
Dot_onoff26	05Dh	R/W	Reserved						Dot L8- CS17 onoff	Dot L8- CS16 onoff	03h
Dot_onoff27	05Eh	R/W	Dot L9- CS7 onoff	Dot L9- CS6 onoff	Dot L9- CS5 onoff	Dot L9- CS4 onoff	Dot L9- CS3 onoff	Dot L9- CS2 onoff	Dot L9- CS1 onoff	Dot L9- CS0 onoff	FFh
Dot_onoff28	05Fh	R/W	Dot L9- CS15 onoff	Dot L9- CS14 onoff	Dot L9- CS13 onoff	Dot L9- CS12 onoff	Dot L9- CS11 onoff	Dot L9- CS10 onoff	Dot L9- CS9 onoff	Dot L9- CS8 onoff	FFh
Dot_onoff29	060h	R/W	Reserved						Dot L9- CS17 onoff	Dot L9- CS16 onoff	03h
Dot_onoff30	061h	R/W	Dot L10- CS7 onoff	Dot L10- CS6 onoff	Dot L10- CS5 onoff	Dot L10- CS4 onoff	Dot L10- CS3 onoff	Dot L10- CS2 onoff	Dot L10- CS1 onoff	Dot L10- CS0 onoff	FFh
Dot_onoff31	062h	R/W	Dot L10- CS15 onoff	Dot L10- CS14 onoff	Dot L10- CS13 onoff	Dot L10- CS12 onoff	Dot L10- CS11 onoff	Dot L10- CS10 onoff	Dot L10- CS9 onoff	Dot L10- CS8 onoff	FFh
Dot_onoff32	063h	R/W	Reserved						Dot L10- CS17 onoff	Dot L10- CS16 onoff	03h
Fault_state	064h	R	Reserved						Global_L OD	Global_L SD	00h
Dot_lod0	065h	R	Dot L0- CS7 LOD	Dot L0- CS6 LOD	Dot L0- CS5 LOD	Dot L0- CS4 LOD	Dot L0- CS3 LOD	Dot L0- CS2 LOD	Dot L0- CS1 LOD	Dot L0- CS0 LOD	00h
Dot_lod1	066h	R	Dot L0- CS15 LOD	Dot L0- CS14 LOD	Dot L0- CS13 LOD	Dot L0- CS12 LOD	Dot L0- CS11 LOD	Dot L0- CS10 LOD	Dot L0- CS9 LOD	Dot L0- CS8 LOD	00h
Dot_lod2	067h	R	Reserved						Dot L0- CS17 LOD	Dot L0- CS16 LOD	00h
Dot_lod3	068h	R	Dot L1- CS7 LOD	Dot L1- CS6 LOD	Dot L1- CS5 LOD	Dot L1- CS4 LOD	Dot L1- CS3 LOD	Dot L1- CS2 LOD	Dot L1- CS1 LOD	Dot L1- CS0 LOD	00h
Dot_lod4	069h	R	Dot L1- CS15 LOD	Dot L1- CS14 LOD	Dot L1- CS13 LOD	Dot L1- CS12 LOD	Dot L1- CS11 LOD	Dot L1- CS10 LOD	Dot L1- CS9 LOD	Dot L1- CS8 LOD	00h
Dot_lod5	06Ah	R	Reserved						Dot L1- CS17 LOD	Dot L1- CS16 LOD	00h
Dot_lod6	06Bh	R	Dot L2- CS7 LOD	Dot L2- CS6 LOD	Dot L2- CS5 LOD	Dot L2- CS4 LOD	Dot L2- CS3 LOD	Dot L2- CS2 LOD	Dot L2- CS1 LOD	Dot L2- CS0 LOD	00h
Dot_lod7	06Ch	R	Dot L2- CS15 LOD	Dot L2- CS14 LOD	Dot L2- CS13 LOD	Dot L2- CS12 LOD	Dot L2- CS11 LOD	Dot L2- CS10 LOD	Dot L2- CS9 LOD	Dot L2- CS8 LOD	00h
Dot_lod8	06Dh	R	Reserved						Dot L2- CS17 LOD	Dot L2- CS16 LOD	00h
Dot_lod9	06Eh	R	Dot L3- CS7 LOD	Dot L3- CS6 LOD	Dot L3- CS5 LOD	Dot L3- CS4 LOD	Dot L3- CS3 LOD	Dot L3- CS2 LOD	Dot L3- CS1 LOD	Dot L3- CS0 LOD	00h
Dot_lod10	06Fh	R	Dot L3- CS15 LOD	Dot L3- CS14 LOD	Dot L3- CS13 LOD	Dot L3- CS12 LOD	Dot L3- CS11 LOD	Dot L3- CS10 LOD	Dot L3- CS9 LOD	Dot L3- CS8 LOD	00h
Dot_lod11	070h	R	Reserved						Dot L3- CS17 LOD	Dot L3- CS16 LOD	00h

Dot_lod12	071h	R	Dot L4- CS7 LOD	Dot L4- CS6 LOD	Dot L4- CS5 LOD	Dot L4- CS4 LOD	Dot L4- CS3 LOD	Dot L4- CS2 LOD	Dot L4- CS1 LOD	Dot L4- CS0 LOD	00h	
Dot_lod13	072h	R	Dot L4- CS15 LOD	Dot L4- CS14 LOD	Dot L4- CS13 LOD	Dot L4- CS12 LOD	Dot L4- CS11 LOD	Dot L4- CS10 LOD	Dot L4- CS9 LOD	Dot L4- CS8 LOD	00h	
Dot_lod14	073h	R	Reserved							Dot L4- CS17 LOD	Dot L4- CS16 LOD	00h
Dot_lod15	074h	R	Dot L5- CS7 LOD	Dot L5- CS6 LOD	Dot L5- CS5 LOD	Dot L5- CS4 LOD	Dot L5- CS3 LOD	Dot L5- CS2 LOD	Dot L5- CS1 LOD	Dot L5- CS0 LOD	00h	
Dot_lod16	075h	R	Dot L5- CS15 LOD	Dot L5- CS14 LOD	Dot L5- CS13 LOD	Dot L5- CS12 LOD	Dot L5- CS11 LOD	Dot L5- CS10 LOD	Dot L5- CS9 LOD	Dot L5- CS8 LOD	00h	
Dot_lod17	076h	R	Reserved							Dot L5- CS17 LOD	Dot L5- CS16 LOD	00h
Dot_lod18	077h	R	Dot L6- CS7 LOD	Dot L6- CS6 LOD	Dot L6- CS5 LOD	Dot L6- CS4 LOD	Dot L6- CS3 LOD	Dot L6- CS2 LOD	Dot L6- CS1 LOD	Dot L6- CS0 LOD	00h	
Dot_lod19	078h	R	Dot L6- CS15 LOD	Dot L6- CS14 LOD	Dot L6- CS13 LOD	Dot L6- CS12 LOD	Dot L6- CS11 LOD	Dot L6- CS10 LOD	Dot L6- CS9 LOD	Dot L6- CS8 LOD	00h	
Dot_lod20	079h	R	Reserved							Dot L6- CS17 LOD	Dot L6- CS16 LOD	00h
Dot_lod21	07Ah	R	Dot L7- CS7 LOD	Dot L7- CS6 LOD	Dot L7- CS5 LOD	Dot L7- CS4 LOD	Dot L7- CS3 LOD	Dot L7- CS2 LOD	Dot L7- CS1 LOD	Dot L7- CS0 LOD	00h	
Dot_lod22	07Bh	R	Dot L7- CS15 LOD	Dot L7- CS14 LOD	Dot L7- CS13 LOD	Dot L7- CS12 LOD	Dot L7- CS11 LOD	Dot L7- CS10 LOD	Dot L7- CS9 LOD	Dot L7- CS8 LOD	00h	
Dot_lod23	07Ch	R	Reserved							Dot L7- CS17 LOD	Dot L7- CS16 LOD	00h
Dot_lod24	07Dh	R	Dot L8- CS7 LOD	Dot L8- CS6 LOD	Dot L8- CS5 LOD	Dot L8- CS4 LOD	Dot L8- CS3 LOD	Dot L8- CS2 LOD	Dot L8- CS1 LOD	Dot L8- CS0 LOD	00h	
Dot_lod25	07Eh	R	Dot L8- CS15 LOD	Dot L8- CS14 LOD	Dot L8- CS13 LOD	Dot L8- CS12 LOD	Dot L8- CS11 LOD	Dot L8- CS10 LOD	Dot L8- CS9 LOD	Dot L8- CS8 LOD	00h	
Dot_lod26	07Fh	R	Reserved							Dot L8- CS17 LOD	Dot L8- CS16 LOD	
Dot_lod27	080h	R	Dot L9- CS7 LOD	Dot L9- CS6 LOD	Dot L9- CS5 LOD	Dot L9- CS4 LOD	Dot L9- CS3 LOD	Dot L9- CS2 LOD	Dot L9- CS1 LOD	Dot L9- CS0 LOD	00h	
Dot_lod28	081h	R	Dot L9- CS15 LOD	Dot L9- CS14 LOD	Dot L9- CS13 LOD	Dot L9- CS12 LOD	Dot L9- CS11 LOD	Dot L9- CS10 LOD	Dot L9- CS9 LOD	Dot L9- CS8 LOD	00h	
Dot_lod29	082h	R	Reserved							Dot L9- CS17 LOD	Dot L9- CS16 LOD	00h
Dot_lod30	083h	R	Dot L10- CS7 LOD	Dot L10- CS6 LOD	Dot L10- CS5 LOD	Dot L10- CS4 LOD	Dot L10- CS3 LOD	Dot L10- CS2 LOD	Dot L10- CS1 LOD	Dot L10- CS0 LOD	00h	
Dot_lod31	084h	R	Dot L10- CS15 LOD	Dot L10- CS14 LOD	Dot L10- CS13 LOD	Dot L10- CS12 LOD	Dot L10- CS11 LOD	Dot L10- CS10 LOD	Dot L10- CS9 LOD	Dot L10- CS8 LOD	00h	
Dot_lod32	085h	R	Reserved							Dot L10- CS17 LOD	Dot L10- CS16 LOD	00h
Dot_lsd0	086h	R	Dot L0- CS7 LSD	Dot L0- CS6 LSD	Dot L0- CS5 LSD	Dot L0- CS4 LSD	Dot L0- CS3 LSD	Dot L0- CS2 LSD	Dot L0- CS1 LSD	Dot L0- CS0 LSD	00h	

Dot_Isd1	087h	R	Dot L0- CS15 LSD	Dot L0- CS14 LSD	Dot L0- CS13 LSD	Dot L0- CS12 LSD	Dot L0- CS11 LSD	Dot L0- CS10 LSD	Dot L0- CS9 LSD	Dot L0- CS8 LSD	00h
Dot_Isd2	088h	R	Reserved						Dot L0- CS17 LSD	Dot L0- CS16 LSD	00h
Dot_Isd3	089h	R	Dot L1- CS7 LSD	Dot L1- CS6 LSD	Dot L1- CS5 LSD	Dot L1- CS4 LSD	Dot L1- CS3 LSD	Dot L1- CS2 LSD	Dot L1- CS1 LSD	Dot L1- CS0 LSD	00h
Dot_Isd4	08Ah	R	Dot L1- CS15 LSD	Dot L1- CS14 LSD	Dot L1- CS13 LSD	Dot L1- CS12 LSD	Dot L1- CS11 LSD	Dot L1- CS10 LSD	Dot L1- CS9 LSD	Dot L1- CS8 LSD	00h
Dot_Isd5	08Bh	R	Reserved						Dot L1- CS17 LSD	Dot L1- CS16 LSD	00h
Dot_Isd6	08Ch	R	Dot L2- CS7 LSD	Dot L2- CS6 LSD	Dot L2- CS5 LSD	Dot L2- CS4 LSD	Dot L2- CS3 LSD	Dot L2- CS2 LSD	Dot L2- CS1 LSD	Dot L2- CS0 LSD	00h
Dot_Isd7	08Dh	R	Dot L2- CS15 LSD	Dot L2- CS14 LSD	Dot L2- CS13 LSD	Dot L2- CS12 LSD	Dot L2- CS11 LSD	Dot L2- CS10 LSD	Dot L2- CS9 LSD	Dot L2- CS8 LSD	00h
Dot_Isd8	08Eh	R	Reserved						Dot L2- CS17 LSD	Dot L2- CS16 LSD	00h
Dot_Isd9	08Fh	R	Dot L3- CS7 LSD	Dot L3- CS6 LSD	Dot L3- CS5 LSD	Dot L3- CS4 LSD	Dot L3- CS3 LSD	Dot L3- CS2 LSD	Dot L3- CS1 LSD	Dot L3- CS0 LSD	00h
Dot_Isd10	090h	R	Dot L3- CS15 LSD	Dot L3- CS14 LSD	Dot L3- CS13 LSD	Dot L3- CS12 LSD	Dot L3- CS11 LSD	Dot L3- CS10 LSD	Dot L3- CS9 LSD	Dot L3- CS8 LSD	00h
Dot_Isd11	091h	R	Reserved						Dot L3- CS17 LSD	Dot L3- CS16 LSD	00h
Dot_Isd12	092h	R	Dot L4- CS7 LSD	Dot L4- CS6 LSD	Dot L4- CS5 LSD	Dot L4- CS4 LSD	Dot L4- CS3 LSD	Dot L4- CS2 LSD	Dot L4- CS1 LSD	Dot L4- CS0 LSD	00h
Dot_Isd13	093h	R	Dot L4- CS15 LSD	Dot L4- CS14 LSD	Dot L4- CS13 LSD	Dot L4- CS12 LSD	Dot L4- CS11 LSD	Dot L4- CS10 LSD	Dot L4- CS9 LSD	Dot L4- CS8 LSD	00h
Dot_Isd14	094h	R	Reserved						Dot L4- CS17 LSD	Dot L4- CS16 LSD	00h
Dot_Isd15	095h	R	Dot L5- CS7 LSD	Dot L5- CS6 LSD	Dot L5- CS5 LSD	Dot L5- CS4 LSD	Dot L5- CS3 LSD	Dot L5- CS2 LSD	Dot L5- CS1 LSD	Dot L5- CS0 LSD	00h
Dot_Isd16	096h	R	Dot L5- CS15 LSD	Dot L5- CS14 LSD	Dot L5- CS13 LSD	Dot L5- CS12 LSD	Dot L5- CS11 LSD	Dot L5- CS10 LSD	Dot L5- CS9 LSD	Dot L5- CS8 LSD	00h
Dot_Isd17	097h	R	Reserved						Dot L5- CS17 LSD	Dot L5- CS16 LSD	00h
Dot_Isd18	098h	R	Dot L6- CS7 LSD	Dot L6- CS6 LSD	Dot L6- CS5 LSD	Dot L6- CS4 LSD	Dot L6- CS3 LSD	Dot L6- CS2 LSD	Dot L6- CS1 LSD	Dot L6- CS0 LSD	00h
Dot_Isd19	099h	R	Dot L6- CS15 LSD	Dot L6- CS14 LSD	Dot L6- CS13 LSD	Dot L6- CS12 LSD	Dot L6- CS11 LSD	Dot L6- CS10 LSD	Dot L6- CS9 LSD	Dot L6- CS8 LSD	00h
Dot_Isd20	09Ah	R	Reserved						Dot L6- CS17 LSD	Dot L6- CS16 LSD	00h
Dot_Isd21	09Bh	R	Dot L7- CS7 LSD	Dot L7- CS6 LSD	Dot L7- CS5 LSD	Dot L7- CS4 LSD	Dot L7- CS3 LSD	Dot L7- CS2 LSD	Dot L7- CS1 LSD	Dot L7- CS0 LSD	00h
Dot_Isd22	09Ch	R	Dot L7- CS15 LSD	Dot L7- CS14 LSD	Dot L7- CS13 LSD	Dot L7- CS12 LSD	Dot L7- CS11 LSD	Dot L7- CS10 LSD	Dot L7- CS9 LSD	Dot L7- CS8 LSD	00h

Dot_Isd23	09Dh	R	Reserved						Dot L7- CS17 LSD	Dot L7- CS16 LSD	00h
Dot_Isd24	09Eh	R	Dot L8- CS7 LSD	Dot L8- CS6 LSD	Dot L8- CS5 LSD	Dot L8- CS4 LSD	Dot L8- CS3 LSD	Dot L8- CS2 LSD	Dot L8- CS1 LSD	Dot L8- CS0 LSD	00h
Dot_Isd25	09Fh	R	Dot L8- CS15 LSD	Dot L8- CS14 LSD	Dot L8- CS13 LSD	Dot L8- CS12 LSD	Dot L8- CS11 LSD	Dot L8- CS10 LSD	Dot L8- CS9 LSD	Dot L8- CS8 LSD	00h
Dot_Isd26	0A0h	R	Reserved						Dot L8- CS17 LSD	Dot L8- CS16 LSD	00h
Dot_Isd27	0A1h	R	Dot L9- CS7 LSD	Dot L9- CS6 LSD	Dot L9- CS5 LSD	Dot L9- CS4 LSD	Dot L9- CS3 LSD	Dot L9- CS2 LSD	Dot L9- CS1 LSD	Dot L9- CS0 LSD	00h
Dot_Isd28	0A2h	R	Dot L9- CS15 LSD	Dot L9- CS14 LSD	Dot L9- CS13 LSD	Dot L9- CS12 LSD	Dot L9- CS11 LSD	Dot L9- CS10 LSD	Dot L9- CS9 LSD	Dot L9- CS8 LSD	00h
Dot_Isd29	0A3h	R	Reserved						Dot L9- CS17 LSD	Dot L9- CS16 LSD	00h
Dot_Isd30	0A4h	R	Dot L10- CS7 LSD	Dot L10- CS6 LSD	Dot L10- CS5 LSD	Dot L10- CS4 LSD	Dot L10- CS3 LSD	Dot L10- CS2 LSD	Dot L10- CS1 LSD	Dot L10- CS0 LSD	00h
Dot_Isd31	0A5h	R	Dot L10- CS15 LSD	Dot L10- CS14 LSD	Dot L10- CS13 LSD	Dot L10- CS12 LSD	Dot L10- CS11 LSD	Dot L10- CS10 LSD	Dot L10- CS9 LSD	Dot L10- CS8 LSD	00h
Dot_Isd32	0A6h	R	Reserved						Dot L10- CS17 LSD	Dot L10- CS16 LSD	00h
LOD_clear	0A7h	W	Reserved				LOD_Clear				00h
LSD_clear	0A8h	W	Reserved				LSD_Clear				00h
Reset	0A9h	W	Reset								00h
DC0	100h	R/W	LED dot current setting for Dot L0-CS0								80h
DC1	101h	R/W	LED dot current setting for Dot L0-CS1								80h
DC2	102h	R/W	LED dot current setting for Dot L0-CS2								80h
DC3	103h	R/W	LED dot current setting for Dot L0-CS3								80h
DC4	104h	R/W	LED dot current setting for Dot L0-CS4								80h
DC5	105h	R/W	LED dot current setting for Dot L0-CS5								80h
DC6	106h	R/W	LED dot current setting for Dot L0-CS6								80h
DC7	107h	R/W	LED dot current setting for Dot L0-CS7								80h
DC8	108h	R/W	LED dot current setting for Dot L0-CS8								80h
DC9	109h	R/W	LED dot current setting for Dot L0-CS9								80h
DC10	10Ah	R/W	LED dot current setting for Dot L0-CS10								80h
DC11	10Bh	R/W	LED dot current setting for Dot L0-CS11								80h
DC12	10Ch	R/W	LED dot current setting for Dot L0-CS12								80h
DC13	10Dh	R/W	LED dot current setting for Dot L0-CS13								80h
DC14	10Eh	R/W	LED dot current setting for Dot L0-CS14								80h
DC15	10Fh	R/W	LED dot current setting for Dot L0-CS15								80h
DC16	110h	R/W	LED dot current setting for Dot L0-CS16								80h
DC17	111h	R/W	LED dot current setting for Dot L0-CS17								80h
DC18	112h	R/W	LED dot current setting for Dot L1-CS0								80h
DC19	113h	R/W	LED dot current setting for Dot L1-CS1								80h
DC20	114h	R/W	LED dot current setting for Dot L1-CS2								80h
DC21	115h	R/W	LED dot current setting for Dot L1-CS3								80h

DC22	116h	R/W	LED dot current setting for Dot L1-CS4	80h
DC23	117h	R/W	LED dot current setting for Dot L1-CS5	80h
DC24	118h	R/W	LED dot current setting for Dot L1-CS6	80h
DC25	119h	R/W	LED dot current setting for Dot L1-CS7	80h
DC26	11Ah	R/W	LED dot current setting for Dot L1-CS8	80h
DC27	11Bh	R/W	LED dot current setting for Dot L1-CS9	80h
DC28	11Ch	R/W	LED dot current setting for Dot L1-CS10	80h
DC29	11Dh	R/W	LED dot current setting for Dot L1-CS11	80h
DC30	11Eh	R/W	LED dot current setting for Dot L1-CS12	80h
DC31	11Fh	R/W	LED dot current setting for Dot L1-CS13	80h
DC32	120h	R/W	LED dot current setting for Dot L1-CS14	80h
DC33	121h	R/W	LED dot current setting for Dot L1-CS15	80h
DC34	122h	R/W	LED dot current setting for Dot L1-CS16	80h
DC35	123h	R/W	LED dot current setting for Dot L1-CS17	80h
DC36	124h	R/W	LED dot current setting for Dot L2-CS0	80h
DC37	125h	R/W	LED dot current setting for Dot L2-CS1	80h
DC38	126h	R/W	LED dot current setting for Dot L2-CS2	80h
DC39	127h	R/W	LED dot current setting for Dot L2-CS3	80h
DC40	128h	R/W	LED dot current setting for Dot L2-CS4	80h
DC41	129h	R/W	LED dot current setting for Dot L2-CS5	80h
DC42	12Ah	R/W	LED dot current setting for Dot L2-CS6	80h
DC43	12Bh	R/W	LED dot current setting for Dot L2-CS7	80h
DC44	12Ch	R/W	LED dot current setting for Dot L2-CS8	80h
DC45	12Dh	R/W	LED dot current setting for Dot L2-CS9	80h
DC46	12Eh	R/W	LED dot current setting for Dot L2-CS10	80h
DC47	12Fh	R/W	LED dot current setting for Dot L2-CS11	80h
DC48	130h	R/W	LED dot current setting for Dot L2-CS12	80h
DC49	131h	R/W	LED dot current setting for Dot L2-CS13	80h
DC50	132h	R/W	LED dot current setting for Dot L2-CS14	80h
DC51	133h	R/W	LED dot current setting for Dot L2-CS15	80h
DC52	134h	R/W	LED dot current setting for Dot L2-CS16	80h
DC53	135h	R/W	LED dot current setting for Dot L2-CS17	80h
DC54	136h	R/W	LED dot current setting for Dot L3-CS0	80h
DC55	137h	R/W	LED dot current setting for Dot L3-CS1	80h
DC56	138h	R/W	LED dot current setting for Dot L3-CS2	80h
DC57	139h	R/W	LED dot current setting for Dot L3-CS3	80h
DC58	13Ah	R/W	LED dot current setting for Dot L3-CS4	80h
DC59	13Bh	R/W	LED dot current setting for Dot L3-CS5	80h
DC60	13Ch	R/W	LED dot current setting for Dot L3-CS6	80h
DC61	13Dh	R/W	LED dot current setting for Dot L3-CS7	80h
DC62	13Eh	R/W	LED dot current setting for Dot L3-CS8	80h
DC63	13Fh	R/W	LED dot current setting for Dot L3-CS9	80h
DC64	140h	R/W	LED dot current setting for Dot L3-CS10	80h
DC65	141h	R/W	LED dot current setting for Dot L3-CS11	80h
DC66	142h	R/W	LED dot current setting for Dot L3-CS12	80h
DC67	143h	R/W	LED dot current setting for Dot L3-CS13	80h
DC68	144h	R/W	LED dot current setting for Dot L3-CS14	80h

DC69	145h	R/W	LED dot current setting for Dot L3-CS15	80h
DC70	146h	R/W	LED dot current setting for Dot L3-CS16	80h
DC71	147h	R/W	LED dot current setting for Dot L3-CS17	80h
DC72	148h	R/W	LED dot current setting for Dot L4-CS0	80h
DC73	149h	R/W	LED dot current setting for Dot L4-CS1	80h
DC74	14Ah	R/W	LED dot current setting for Dot L4-CS2	80h
DC75	14Bh	R/W	LED dot current setting for Dot L4-CS3	80h
DC76	14Ch	R/W	LED dot current setting for Dot L4-CS4	80h
DC77	14Dh	R/W	LED dot current setting for Dot L4-CS5	80h
DC78	14Eh	R/W	LED dot current setting for Dot L4-CS6	80h
DC79	14Fh	R/W	LED dot current setting for Dot L4-CS7	80h
DC80	150h	R/W	LED dot current setting for Dot L4-CS8	80h
DC81	151h	R/W	LED dot current setting for Dot L4-CS9	80h
DC82	152h	R/W	LED dot current setting for Dot L4-CS10	80h
DC83	153h	R/W	LED dot current setting for Dot L4-CS11	80h
DC84	154h	R/W	LED dot current setting for Dot L4-CS12	80h
DC85	155h	R/W	LED dot current setting for Dot L4-CS13	80h
DC86	156h	R/W	LED dot current setting for Dot L4-CS14	80h
DC87	157h	R/W	LED dot current setting for Dot L4-CS15	80h
DC88	158h	R/W	LED dot current setting for Dot L4-CS16	80h
DC89	159h	R/W	LED dot current setting for Dot L4-CS17	80h
DC90	15Ah	R/W	LED dot current setting for Dot L5-CS0	80h
DC91	15Bh	R/W	LED dot current setting for Dot L5-CS1	80h
DC92	15Ch	R/W	LED dot current setting for Dot L5-CS2	80h
DC93	15Dh	R/W	LED dot current setting for Dot L5-CS3	80h
DC94	15Eh	R/W	LED dot current setting for Dot L5-CS4	80h
DC95	15Fh	R/W	LED dot current setting for Dot L5-CS5	80h
DC96	160h	R/W	LED dot current setting for Dot L5-CS6	80h
DC97	161h	R/W	LED dot current setting for Dot L5-CS7	80h
DC98	162h	R/W	LED dot current setting for Dot L5-CS8	80h
DC99	163h	R/W	LED dot current setting for Dot L5-CS9	80h
DC100	164h	R/W	LED dot current setting for Dot L5-CS10	80h
DC101	165h	R/W	LED dot current setting for Dot L5-CS11	80h
DC102	166h	R/W	LED dot current setting for Dot L5-CS12	80h
DC103	167h	R/W	LED dot current setting for Dot L5-CS13	80h
DC104	168h	R/W	LED dot current setting for Dot L5-CS14	80h
DC105	169h	R/W	LED dot current setting for Dot L5-CS15	80h
DC106	16Ah	R/W	LED dot current setting for Dot L5-CS16	80h
DC107	16Bh	R/W	LED dot current setting for Dot L5-CS17	80h
DC108	16Ch	R/W	LED dot current setting for Dot L6-CS0	80h
DC109	16Dh	R/W	LED dot current setting for Dot L6-CS1	80h
DC110	16Eh	R/W	LED dot current setting for Dot L6-CS2	80h
DC111	16Fh	R/W	LED dot current setting for Dot L6-CS3	80h
DC112	170h	R/W	LED dot current setting for Dot L6-CS4	80h
DC113	171h	R/W	LED dot current setting for Dot L6-CS5	80h
DC114	172h	R/W	LED dot current setting for Dot L6-CS6	80h
DC115	173h	R/W	LED dot current setting for Dot L6-CS7	80h

DC116	174h	R/W	LED dot current setting for Dot L6-CS8	80h
DC117	175h	R/W	LED dot current setting for Dot L6-CS9	80h
DC118	176h	R/W	LED dot current setting for Dot L6-CS10	80h
DC119	177h	R/W	LED dot current setting for Dot L6-CS11	80h
DC120	178h	R/W	LED dot current setting for Dot L6-CS12	80h
DC121	179h	R/W	LED dot current setting for Dot L6-CS13	80h
DC122	17Ah	R/W	LED dot current setting for Dot L6-CS14	80h
DC123	17Bh	R/W	LED dot current setting for Dot L6-CS15	80h
DC124	17Ch	R/W	LED dot current setting for Dot L6-CS16	80h
DC125	17Dh	R/W	LED dot current setting for Dot L6-CS17	80h
DC126	17Eh	R/W	LED dot current setting for Dot L7-CS0	80h
DC127	17Fh	R/W	LED dot current setting for Dot L7-CS1	80h
DC128	180h	R/W	LED dot current setting for Dot L7-CS2	80h
DC129	181h	R/W	LED dot current setting for Dot L7-CS3	80h
DC130	182h	R/W	LED dot current setting for Dot L7-CS4	80h
DC131	183h	R/W	LED dot current setting for Dot L7-CS5	80h
DC132	184h	R/W	LED dot current setting for Dot L7-CS6	80h
DC133	185h	R/W	LED dot current setting for Dot L7-CS7	80h
DC134	186h	R/W	LED dot current setting for Dot L7-CS8	80h
DC135	187h	R/W	LED dot current setting for Dot L7-CS9	80h
DC136	188h	R/W	LED dot current setting for Dot L7-CS10	80h
DC137	189h	R/W	LED dot current setting for Dot L7-CS11	80h
DC138	18Ah	R/W	LED dot current setting for Dot L7-CS12	80h
DC139	18Bh	R/W	LED dot current setting for Dot L7-CS13	80h
DC140	18Ch	R/W	LED dot current setting for Dot L7-CS14	80h
DC141	18Dh	R/W	LED dot current setting for Dot L7-CS15	80h
DC142	18Eh	R/W	LED dot current setting for Dot L7-CS16	80h
DC143	18Fh	R/W	LED dot current setting for Dot L7-CS17	80h
DC144	190h	R/W	LED dot current setting for Dot L8-CS0	80h
DC145	191h	R/W	LED dot current setting for Dot L8-CS1	80h
DC146	192h	R/W	LED dot current setting for Dot L8-CS2	80h
DC147	193h	R/W	LED dot current setting for Dot L8-CS3	80h
DC148	194h	R/W	LED dot current setting for Dot L8-CS4	80h
DC149	195h	R/W	LED dot current setting for Dot L8-CS5	80h
DC150	196h	R/W	LED dot current setting for Dot L8-CS6	80h
DC151	197h	R/W	LED dot current setting for Dot L8-CS7	80h
DC152	198h	R/W	LED dot current setting for Dot L8-CS8	80h
DC153	199h	R/W	LED dot current setting for Dot L8-CS9	80h
DC154	19Ah	R/W	LED dot current setting for Dot L8-CS10	80h
DC155	19Bh	R/W	LED dot current setting for Dot L8-CS11	80h
DC156	19Ch	R/W	LED dot current setting for Dot L8-CS12	80h
DC157	19Dh	R/W	LED dot current setting for Dot L8-CS13	80h
DC158	19Eh	R/W	LED dot current setting for Dot L8-CS14	80h
DC159	19Fh	R/W	LED dot current setting for Dot L8-CS15	80h
DC160	1A0h	R/W	LED dot current setting for Dot L8-CS16	80h
DC161	1A1h	R/W	LED dot current setting for Dot L8-CS17	80h
DC162	1A2h	R/W	LED dot current setting for Dot L9-CS0	80h

DC163	1A3h	R/W	LED dot current setting for Dot L9-CS1	80h
DC164	1A4h	R/W	LED dot current setting for Dot L9-CS2	80h
DC165	1A5h	R/W	LED dot current setting for Dot L9-CS3	80h
DC166	1A6h	R/W	LED dot current setting for Dot L9-CS4	80h
DC167	1A7h	R/W	LED dot current setting for Dot L9-CS5	80h
DC168	1A8h	R/W	LED dot current setting for Dot L9-CS6	80h
DC169	1A9h	R/W	LED dot current setting for Dot L9-CS7	80h
DC170	1AAh	R/W	LED dot current setting for Dot L9-CS8	80h
DC171	1ABh	R/W	LED dot current setting for Dot L9-CS9	80h
DC172	1ACh	R/W	LED dot current setting for Dot L9-CS10	80h
DC173	1ADh	R/W	LED dot current setting for Dot L9-CS11	80h
DC174	1AEh	R/W	LED dot current setting for Dot L9-CS12	80h
DC175	1AFh	R/W	LED dot current setting for Dot L9-CS13	80h
DC176	1B0h	R/W	LED dot current setting for Dot L9-CS14	80h
DC177	1B1h	R/W	LED dot current setting for Dot L9-CS15	80h
DC178	1B2h	R/W	LED dot current setting for Dot L9-CS16	80h
DC179	1B3h	R/W	LED dot current setting for Dot L9-CS17	80h
DC180	1B4h	R/W	LED dot current setting for Dot L10-CS0	80h
DC181	1B5h	R/W	LED dot current setting for Dot L10-CS1	80h
DC182	1B6h	R/W	LED dot current setting for Dot L10-CS2	80h
DC183	1B7h	R/W	LED dot current setting for Dot L10-CS3	80h
DC184	1B8h	R/W	LED dot current setting for Dot L10-CS4	80h
DC185	1B9h	R/W	LED dot current setting for Dot L10-CS5	80h
DC186	1BAh	R/W	LED dot current setting for Dot L10-CS6	80h
DC187	1BBh	R/W	LED dot current setting for Dot L10-CS7	80h
DC188	1BCh	R/W	LED dot current setting for Dot L10-CS8	80h
DC189	1BDh	R/W	LED dot current setting for Dot L10-CS9	80h
DC190	1BEh	R/W	LED dot current setting for Dot L10-CS10	80h
DC191	1BFh	R/W	LED dot current setting for Dot L10-CS11	80h
DC192	1C0h	R/W	LED dot current setting for Dot L10-CS12	80h
DC193	1C1h	R/W	LED dot current setting for Dot L10-CS13	80h
DC194	1C2h	R/W	LED dot current setting for Dot L10-CS14	80h
DC195	1C3h	R/W	LED dot current setting for Dot L10-CS15	80h
DC196	1C4h	R/W	LED dot current setting for Dot L10-CS16	80h
DC197	1C5h	R/W	LED dot current setting for Dot L10-CS17	80h
pwm_bri0	200h	R/W	8-bits PWM for Dot L0-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS0	00h
pwm_bri1	201h	R/W	8-bits PWM for Dot L0-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS0	00h
pwm_bri2	202h	R/W	8-bits PWM for Dot L0-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS1	00h
pwm_bri3	203h	R/W	8-bits PWM for Dot L0-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS1	00h
pwm_bri4	204h	R/W	8-bits PWM for Dot L0-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS2	00h
pwm_bri5	205h	R/W	8-bits PWM for Dot L0-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS2	00h
pwm_bri6	206h	R/W	8-bits PWM for Dot L0-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS3	00h
pwm_bri7	207h	R/W	8-bits PWM for Dot L0-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS3	00h
pwm_bri8	208h	R/W	8-bits PWM for Dot L0-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS4	00h
pwm_bri9	209h	R/W	8-bits PWM for Dot L0-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS4	00h
pwm_bri10	20Ah	R/W	8-bits PWM for Dot L0-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS5	00h
pwm_bri11	20Bh	R/W	8-bits PWM for Dot L0-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS5	00h

pwm_bri12	20Ch	R/W	8-bits PWM for Dot L0-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS6	00h
pwm_bri13	20Dh	R/W	8-bits PWM for Dot L0-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS6	00h
pwm_bri14	20Eh	R/W	8-bits PWM for Dot L0-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS7	00h
pwm_bri15	20Fh	R/W	8-bits PWM for Dot L0-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS7	00h
pwm_bri16	210h	R/W	8-bits PWM for Dot L0-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS8	00h
pwm_bri17	211h	R/W	8-bits PWM for Dot L0-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS8	00h
pwm_bri18	212h	R/W	8-bits PWM for Dot L1-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS9	00h
pwm_bri19	213h	R/W	8-bits PWM for Dot L1-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS9	00h
pwm_bri20	214h	R/W	8-bits PWM for Dot L1-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS10	00h
pwm_bri21	215h	R/W	8-bits PWM for Dot L1-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS10	00h
pwm_bri22	216h	R/W	8-bits PWM for Dot L1-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS11	00h
pwm_bri23	217h	R/W	8-bits PWM for Dot L1-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS11	00h
pwm_bri24	218h	R/W	8-bits PWM for Dot L1-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS12	00h
pwm_bri25	219h	R/W	8-bits PWM for Dot L1-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS12	00h
pwm_bri26	21Ah	R/W	8-bits PWM for Dot L1-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS13	00h
pwm_bri27	21Bh	R/W	8-bits PWM for Dot L1-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS13	00h
pwm_bri28	21Ch	R/W	8-bits PWM for Dot L1-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS14	00h
pwm_bri29	21Dh	R/W	8-bits PWM for Dot L1-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS14	00h
pwm_bri30	21Eh	R/W	8-bits PWM for Dot L1-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS15	00h
pwm_bri31	21Fh	R/W	8-bits PWM for Dot L1-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS15	00h
pwm_bri32	220h	R/W	8-bits PWM for Dot L1-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS16	00h
pwm_bri33	221h	R/W	8-bits PWM for Dot L1-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS16	00h
pwm_bri34	222h	R/W	8-bits PWM for Dot L1-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS17	00h
pwm_bri35	223h	R/W	8-bits PWM for Dot L1-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS17	00h
pwm_bri36	224h	R/W	8-bits PWM for Dot L2-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS0	00h
pwm_bri37	225h	R/W	8-bits PWM for Dot L2-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS0	00h
pwm_bri38	226h	R/W	8-bits PWM for Dot L2-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS1	00h
pwm_bri39	227h	R/W	8-bits PWM for Dot L2-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS1	00h
pwm_bri40	228h	R/W	8-bits PWM for Dot L2-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS2	00h
pwm_bri41	229h	R/W	8-bits PWM for Dot L2-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS2	00h
pwm_bri42	22Ah	R/W	8-bits PWM for Dot L2-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS3	00h
pwm_bri43	22Bh	R/W	8-bits PWM for Dot L2-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS3	00h
pwm_bri44	22Ch	R/W	8-bits PWM for Dot L2-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS4	00h
pwm_bri45	22Dh	R/W	8-bits PWM for Dot L2-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS4	00h
pwm_bri46	22Eh	R/W	8-bits PWM for Dot L2-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS5	00h
pwm_bri47	22Fh	R/W	8-bits PWM for Dot L2-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS5	00h
pwm_bri48	230h	R/W	8-bits PWM for Dot L2-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS6	00h
pwm_bri49	231h	R/W	8-bits PWM for Dot L2-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS6	00h
pwm_bri50	232h	R/W	8-bits PWM for Dot L2-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS7	00h
pwm_bri51	233h	R/W	8-bits PWM for Dot L2-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS7	00h
pwm_bri52	234h	R/W	8-bits PWM for Dot L2-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS8	00h
pwm_bri53	235h	R/W	8-bits PWM for Dot L2-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS8	00h
pwm_bri54	236h	R/W	8-bits PWM for Dot L3-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS9	00h
pwm_bri55	237h	R/W	8-bits PWM for Dot L3-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS9	00h
pwm_bri56	238h	R/W	8-bits PWM for Dot L3-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS10	00h
pwm_bri57	239h	R/W	8-bits PWM for Dot L3-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS10	00h
pwm_bri58	23Ah	R/W	8-bits PWM for Dot L3-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS11	00h

pwm_bri59	23Bh	R/W	8-bits PWM for Dot L3-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS11	00h
pwm_bri60	23Ch	R/W	8-bits PWM for Dot L3-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS12	00h
pwm_bri61	23Dh	R/W	8-bits PWM for Dot L3-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS12	00h
pwm_bri62	23Eh	R/W	8-bits PWM for Dot L3-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS13	00h
pwm_bri63	23Fh	R/W	8-bits PWM for Dot L3-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS13	00h
pwm_bri64	240h	R/W	8-bits PWM for Dot L3-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS14	00h
pwm_bri65	241h	R/W	8-bits PWM for Dot L3-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS14	00h
pwm_bri66	242h	R/W	8-bits PWM for Dot L3-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS15	00h
pwm_bri67	243h	R/W	8-bits PWM for Dot L3-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS15	00h
pwm_bri68	244h	R/W	8-bits PWM for Dot L3-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS16	00h
pwm_bri69	245h	R/W	8-bits PWM for Dot L3-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS16	00h
pwm_bri70	246h	R/W	8-bits PWM for Dot L3-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS17	00h
pwm_bri71	247h	R/W	8-bits PWM for Dot L3-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS17	00h
pwm_bri72	248h	R/W	8-bits PWM for Dot L4-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS0	00h
pwm_bri73	249h	R/W	8-bits PWM for Dot L4-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS0	00h
pwm_bri74	24Ah	R/W	8-bits PWM for Dot L4-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS1	00h
pwm_bri75	24Bh	R/W	8-bits PWM for Dot L4-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS1	00h
pwm_bri76	24Ch	R/W	8-bits PWM for Dot L4-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS2	00h
pwm_bri77	24Dh	R/W	8-bits PWM for Dot L4-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS2	00h
pwm_bri78	24Eh	R/W	8-bits PWM for Dot L4-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS3	00h
pwm_bri79	24Fh	R/W	8-bits PWM for Dot L4-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS3	00h
pwm_bri80	250h	R/W	8-bits PWM for Dot L4-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS4	00h
pwm_bri81	251h	R/W	8-bits PWM for Dot L4-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS4	00h
pwm_bri82	252h	R/W	8-bits PWM for Dot L4-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS5	00h
pwm_bri83	253h	R/W	8-bits PWM for Dot L4-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS5	00h
pwm_bri84	254h	R/W	8-bits PWM for Dot L4-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS6	00h
pwm_bri85	255h	R/W	8-bits PWM for Dot L4-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS6	00h
pwm_bri86	256h	R/W	8-bits PWM for Dot L4-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS7	00h
pwm_bri87	257h	R/W	8-bits PWM for Dot L4-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS7	00h
pwm_bri88	258h	R/W	8-bits PWM for Dot L4-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS8	00h
pwm_bri89	259h	R/W	8-bits PWM for Dot L4-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS8	00h
pwm_bri90	25Ah	R/W	8-bits PWM for Dot L5-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS9	00h
pwm_bri91	25Bh	R/W	8-bits PWM for Dot L5-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS9	00h
pwm_bri92	25Ch	R/W	8-bits PWM for Dot L5-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS10	00h
pwm_bri93	25Dh	R/W	8-bits PWM for Dot L5-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS10	00h
pwm_bri94	25Eh	R/W	8-bits PWM for Dot L5-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS11	00h
pwm_bri95	25Fh	R/W	8-bits PWM for Dot L5-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS11	00h
pwm_bri96	260h	R/W	8-bits PWM for Dot L5-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS12	00h
pwm_bri97	261h	R/W	8-bits PWM for Dot L5-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS12	00h
pwm_bri98	262h	R/W	8-bits PWM for Dot L5-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS13	00h
pwm_bri99	263h	R/W	8-bits PWM for Dot L5-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS13	00h
pwm_bri100	264h	R/W	8-bits PWM for Dot L5-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS14	00h
pwm_bri101	265h	R/W	8-bits PWM for Dot L5-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS14	00h
pwm_bri102	266h	R/W	8-bits PWM for Dot L5-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS15	00h
pwm_bri103	267h	R/W	8-bits PWM for Dot L5-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS15	00h
pwm_bri104	268h	R/W	8-bits PWM for Dot L5-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS16	00h
pwm_bri105	269h	R/W	8-bits PWM for Dot L5-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS16	00h

pwm_bri106	26Ah	R/W	8-bits PWM for Dot L5-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS17	00h
pwm_bri107	26Bh	R/W	8-bits PWM for Dot L5-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS17	00h
pwm_bri108	26Ch	R/W	8-bits PWM for Dot L6-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS0	00h
pwm_bri109	26Dh	R/W	8-bits PWM for Dot L6-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS0	00h
pwm_bri110	26Eh	R/W	8-bits PWM for Dot L6-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS1	00h
pwm_bri111	26Fh	R/W	8-bits PWM for Dot L6-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS1	00h
pwm_bri112	270h	R/W	8-bits PWM for Dot L6-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS2	00h
pwm_bri113	271h	R/W	8-bits PWM for Dot L6-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS2	00h
pwm_bri114	272h	R/W	8-bits PWM for Dot L6-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS3	00h
pwm_bri115	273h	R/W	8-bits PWM for Dot L6-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS3	00h
pwm_bri116	274h	R/W	8-bits PWM for Dot L6-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS4	00h
pwm_bri117	275h	R/W	8-bits PWM for Dot L6-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS4	00h
pwm_bri118	276h	R/W	8-bits PWM for Dot L6-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS5	00h
pwm_bri119	277h	R/W	8-bits PWM for Dot L6-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS5	00h
pwm_bri120	278h	R/W	8-bits PWM for Dot L6-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS6	00h
pwm_bri121	279h	R/W	8-bits PWM for Dot L6-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS6	00h
pwm_bri122	27Ah	R/W	8-bits PWM for Dot L6-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS7	00h
pwm_bri123	27Bh	R/W	8-bits PWM for Dot L6-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS7	00h
pwm_bri124	27Ch	R/W	8-bits PWM for Dot L6-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS8	00h
pwm_bri125	27Dh	R/W	8-bits PWM for Dot L6-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS8	00h
pwm_bri126	27Eh	R/W	8-bits PWM for Dot L7-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS9	00h
pwm_bri127	27Fh	R/W	8-bits PWM for Dot L7-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS9	00h
pwm_bri128	280h	R/W	8-bits PWM for Dot L7-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS10	00h
pwm_bri129	281h	R/W	8-bits PWM for Dot L7-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS10	00h
pwm_bri130	282h	R/W	8-bits PWM for Dot L7-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS11	00h
pwm_bri131	283h	R/W	8-bits PWM for Dot L7-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS11	00h
pwm_bri132	284h	R/W	8-bits PWM for Dot L7-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS12	00h
pwm_bri133	285h	R/W	8-bits PWM for Dot L7-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS12	00h
pwm_bri134	286h	R/W	8-bits PWM for Dot L7-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS13	00h
pwm_bri135	287h	R/W	8-bits PWM for Dot L7-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS13	00h
pwm_bri136	288h	R/W	8-bits PWM for Dot L7-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS14	00h
pwm_bri137	289h	R/W	8-bits PWM for Dot L7-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS14	00h
pwm_bri138	28Ah	R/W	8-bits PWM for Dot L7-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS15	00h
pwm_bri139	28Bh	R/W	8-bits PWM for Dot L7-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS15	00h
pwm_bri140	28Ch	R/W	8-bits PWM for Dot L7-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS16	00h
pwm_bri141	28Dh	R/W	8-bits PWM for Dot L7-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS16	00h
pwm_bri142	28Eh	R/W	8-bits PWM for Dot L7-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS17	00h
pwm_bri143	28Fh	R/W	8-bits PWM for Dot L7-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS17	00h
pwm_bri144	290h	R/W	8-bits PWM for Dot L8-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS0	00h
pwm_bri145	291h	R/W	8-bits PWM for Dot L8-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS0	00h
pwm_bri146	292h	R/W	8-bits PWM for Dot L8-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS1	00h
pwm_bri147	293h	R/W	8-bits PWM for Dot L8-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS1	00h
pwm_bri148	294h	R/W	8-bits PWM for Dot L8-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS2	00h
pwm_bri149	295h	R/W	8-bits PWM for Dot L8-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS2	00h
pwm_bri150	296h	R/W	8-bits PWM for Dot L8-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS3	00h
pwm_bri151	297h	R/W	8-bits PWM for Dot L8-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS3	00h
pwm_bri152	298h	R/W	8-bits PWM for Dot L8-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS4	00h

pwm_bri153	299h	R/W	8-bits PWM for Dot L8-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS4	00h
pwm_bri154	29Ah	R/W	8-bits PWM for Dot L8-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS5	00h
pwm_bri155	29Bh	R/W	8-bits PWM for Dot L8-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS5	00h
pwm_bri156	29Ch	R/W	8-bits PWM for Dot L8-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS6	00h
pwm_bri157	29Dh	R/W	8-bits PWM for Dot L8-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS6	00h
pwm_bri158	29Eh	R/W	8-bits PWM for Dot L8-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS7	00h
pwm_bri159	29Fh	R/W	8-bits PWM for Dot L8-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS7	00h
pwm_bri160	2A0h	R/W	8-bits PWM for Dot L8-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS8	00h
pwm_bri161	2A1h	R/W	8-bits PWM for Dot L8-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS8	00h
pwm_bri162	2A2h	R/W	8-bits PWM for Dot L9-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS9	00h
pwm_bri163	2A3h	R/W	8-bits PWM for Dot L9-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS9	00h
pwm_bri164	2A4h	R/W	8-bits PWM for Dot L9-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS10	00h
pwm_bri165	2A5h	R/W	8-bits PWM for Dot L9-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS10	00h
pwm_bri166	2A6h	R/W	8-bits PWM for Dot L9-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS11	00h
pwm_bri167	2A7h	R/W	8-bits PWM for Dot L9-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS11	00h
pwm_bri168	2A8h	R/W	8-bits PWM for Dot L9-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS12	00h
pwm_bri169	2A9h	R/W	8-bits PWM for Dot L9-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS12	00h
pwm_bri170	2AAh	R/W	8-bits PWM for Dot L9-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS13	00h
pwm_bri171	2ABh	R/W	8-bits PWM for Dot L9-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS13	00h
pwm_bri172	2ACh	R/W	8-bits PWM for Dot L9-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS14	00h
pwm_bri173	2ADh	R/W	8-bits PWM for Dot L9-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS14	00h
pwm_bri174	2AEh	R/W	8-bits PWM for Dot L9-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS15	00h
pwm_bri175	2AFh	R/W	8-bits PWM for Dot L9-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS15	00h
pwm_bri176	2B0h	R/W	8-bits PWM for Dot L9-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS16	00h
pwm_bri177	2B1h	R/W	8-bits PWM for Dot L9-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS16	00h
pwm_bri178	2B2h	R/W	8-bits PWM for Dot L9-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS17	00h
pwm_bri179	2B3h	R/W	8-bits PWM for Dot L9-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS17	00h
pwm_bri180	2B4h	R/W	8-bits PWM for Dot L10-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L5-CS0	00h
pwm_bri181	2B5h	R/W	8-bits PWM for Dot L10-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L5-CS0	00h
pwm_bri182	2B6h	R/W	8-bits PWM for Dot L10-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L5-CS1	00h
pwm_bri183	2B7h	R/W	8-bits PWM for Dot L10-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L5-CS1	00h
pwm_bri184	2B8h	R/W	8-bits PWM for Dot L10-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L5-CS2	00h
pwm_bri185	2B9h	R/W	8-bits PWM for Dot L10-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L5-CS2	00h
pwm_bri186	2BAh	R/W	8-bits PWM for Dot L10-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L5-CS3	00h
pwm_bri187	2BBh	R/W	8-bits PWM for Dot L10-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L5-CS3	00h
pwm_bri188	2BCh	R/W	8-bits PWM for Dot L10-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L5-CS4	00h
pwm_bri189	2BDh	R/W	8-bits PWM for Dot L10-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L5-CS4	00h
pwm_bri190	2BEh	R/W	8-bits PWM for Dot L10-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L5-CS5	00h
pwm_bri191	2BFh	R/W	8-bits PWM for Dot L10-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L5-CS5	00h
pwm_bri192	2C0h	R/W	8-bits PWM for Dot L10-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L5-CS6	00h
pwm_bri193	2C1h	R/W	8-bits PWM for Dot L10-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L5-CS6	00h
pwm_bri194	2C2h	R/W	8-bits PWM for Dot L10-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L5-CS7	00h
pwm_bri195	2C3h	R/W	8-bits PWM for Dot L10-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L5-CS7	00h
pwm_bri196	2C4h	R/W	8-bits PWM for Dot L10-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L5-CS8	00h
pwm_bri197	2C5h	R/W	8-bits PWM for Dot L10-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L5-CS8	00h
pwm_bri198	2C6h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L5-CS9	00h
pwm_bri199	2C7h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L5-CS9	00h

pwm_bri200	2C8h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L5-CS10	00h
pwm_bri201	2C9h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L5-CS10	00h
pwm_bri202	2CAh	R/W	16-bits PWM lower 8 bits [7:0] for Dot L5-CS11	00h
pwm_bri203	2CBh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L5-CS11	00h
pwm_bri204	2CCh	R/W	16-bits PWM lower 8 bits [7:0] for Dot L5-CS12	00h
pwm_bri205	2CDh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L5-CS12	00h
pwm_bri206	2CEh	R/W	16-bits PWM lower 8 bits [7:0] for Dot L5-CS13	00h
pwm_bri207	2CFh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L5-CS13	00h
pwm_bri208	2D0h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L5-CS14	00h
pwm_bri209	2D1h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L5-CS14	00h
pwm_bri210	2D2h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L5-CS15	00h
pwm_bri211	2D3h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L5-CS15	00h
pwm_bri212	2D4h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L5-CS16	00h
pwm_bri213	2D5h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L5-CS16	00h
pwm_bri214	2D6h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L5-CS17	00h
pwm_bri215	2D7h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L5-CS17	00h
pwm_bri216	2D8h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L6-CS0	00h
pwm_bri217	2D9h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L6-CS0	00h
pwm_bri218	2DAh	R/W	16-bits PWM lower 8 bits [7:0] for Dot L6-CS1	00h
pwm_bri219	2DBh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L6-CS1	00h
pwm_bri220	2DCh	R/W	16-bits PWM lower 8 bits [7:0] for Dot L6-CS2	00h
pwm_bri221	2DDh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L6-CS2	00h
pwm_bri222	2DEh	R/W	16-bits PWM lower 8 bits [7:0] for Dot L6-CS3	00h
pwm_bri223	2DFh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L6-CS3	00h
pwm_bri224	2E0h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L6-CS4	00h
pwm_bri225	2E1h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L6-CS4	00h
pwm_bri226	2E2h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L6-CS5	00h
pwm_bri227	2E3h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L6-CS5	00h
pwm_bri228	2E4h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L6-CS6	00h
pwm_bri229	2E5h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L6-CS6	00h
pwm_bri230	2E6h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L6-CS7	00h
pwm_bri231	2E7h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L6-CS7	00h
pwm_bri232	2E8h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L6-CS8	00h
pwm_bri233	2E9h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L6-CS8	00h
pwm_bri234	2EAh	R/W	16-bits PWM lower 8 bits [7:0] for Dot L6-CS9	00h
pwm_bri235	2EBh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L6-CS9	00h
pwm_bri236	2ECh	R/W	16-bits PWM lower 8 bits [7:0] for Dot L6-CS10	00h
pwm_bri237	2EDh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L6-CS10	00h
pwm_bri238	2EEh	R/W	16-bits PWM lower 8 bits [7:0] for Dot L6-CS11	00h
pwm_bri239	2EFh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L6-CS11	00h
pwm_bri240	2F0h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L6-CS12	00h
pwm_bri241	2F1h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L6-CS12	00h
pwm_bri242	2F2h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L6-CS13	00h
pwm_bri243	2F3h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L6-CS13	00h
pwm_bri244	2F4h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L6-CS14	00h
pwm_bri245	2F5h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L6-CS14	00h
pwm_bri246	2F6h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L6-CS15	00h

pwm_bri247	2F7h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L6-CS15	00h
pwm_bri248	2F8h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L6-CS16	00h
pwm_bri249	2F9h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L6-CS16	00h
pwm_bri250	2FAh	R/W	16-bits PWM lower 8 bits [7:0] for Dot L6-CS17	00h
pwm_bri251	2FBh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L6-CS17	00h
pwm_bri252	2FCh	R/W	16-bits PWM lower 8 bits [7:0] for Dot L7-CS0	00h
pwm_bri253	2FDh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L7-CS0	00h
pwm_bri254	2FEh	R/W	16-bits PWM lower 8 bits [7:0] for Dot L7-CS1	00h
pwm_bri255	2FFh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L7-CS1	00h
pwm_bri256	300h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L7-CS2	00h
pwm_bri257	301h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L7-CS2	00h
pwm_bri258	302h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L7-CS3	00h
pwm_bri259	303h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L7-CS3	00h
pwm_bri260	304h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L7-CS4	00h
pwm_bri261	305h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L7-CS4	00h
pwm_bri262	306h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L7-CS5	00h
pwm_bri263	307h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L7-CS5	00h
pwm_bri264	308h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L7-CS6	00h
pwm_bri265	309h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L7-CS6	00h
pwm_bri266	30Ah	R/W	16-bits PWM lower 8 bits [7:0] for Dot L7-CS7	00h
pwm_bri267	30Bh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L7-CS7	00h
pwm_bri268	30Ch	R/W	16-bits PWM lower 8 bits [7:0] for Dot L7-CS8	00h
pwm_bri269	30Dh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L7-CS8	00h
pwm_bri270	30Eh	R/W	16-bits PWM lower 8 bits [7:0] for Dot L7-CS9	00h
pwm_bri271	30Fh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L7-CS9	00h
pwm_bri272	310h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L7-CS10	00h
pwm_bri273	311h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L7-CS10	00h
pwm_bri274	312h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L7-CS11	00h
pwm_bri275	313h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L7-CS11	00h
pwm_bri276	314h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L7-CS12	00h
pwm_bri277	315h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L7-CS12	00h
pwm_bri278	316h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L7-CS13	00h
pwm_bri279	317h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L7-CS13	00h
pwm_bri280	318h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L7-CS14	00h
pwm_bri281	319h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L7-CS14	00h
pwm_bri282	31Ah	R/W	16-bits PWM lower 8 bits [7:0] for Dot L7-CS15	00h
pwm_bri283	31Bh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L7-CS15	00h
pwm_bri284	31Ch	R/W	16-bits PWM lower 8 bits [7:0] for Dot L7-CS16	00h
pwm_bri285	31Dh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L7-CS16	00h
pwm_bri286	31Eh	R/W	16-bits PWM lower 8 bits [7:0] for Dot L7-CS17	00h
pwm_bri287	31Fh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L7-CS17	00h
pwm_bri288	320h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L8-CS0	00h
pwm_bri289	321h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L8-CS0	00h
pwm_bri290	322h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L8-CS1	00h
pwm_bri291	323h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L8-CS1	00h
pwm_bri292	324h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L8-CS2	00h
pwm_bri293	325h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L8-CS2	00h

pwm_bri294	326h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L8-CS3	00h
pwm_bri295	327h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L8-CS3	00h
pwm_bri296	328h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L8-CS4	00h
pwm_bri297	329h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L8-CS4	00h
pwm_bri298	32Ah	R/W	16-bits PWM lower 8 bits [7:0] for Dot L8-CS5	00h
pwm_bri299	32Bh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L8-CS5	00h
pwm_bri300	32Ch	R/W	16-bits PWM lower 8 bits [7:0] for Dot L8-CS6	00h
pwm_bri301	32Dh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L8-CS6	00h
pwm_bri302	32Eh	R/W	16-bits PWM lower 8 bits [7:0] for Dot L8-CS7	00h
pwm_bri303	32Fh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L8-CS7	00h
pwm_bri304	330h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L8-CS8	00h
pwm_bri305	331h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L8-CS8	00h
pwm_bri306	332h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L8-CS9	00h
pwm_bri307	333h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L8-CS9	00h
pwm_bri308	334h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L8-CS10	00h
pwm_bri309	335h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L8-CS10	00h
pwm_bri310	336h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L8-CS11	00h
pwm_bri311	337h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L8-CS11	00h
pwm_bri312	338h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L8-CS12	00h
pwm_bri313	339h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L8-CS12	00h
pwm_bri314	33Ah	R/W	16-bits PWM lower 8 bits [7:0] for Dot L8-CS13	00h
pwm_bri315	33Bh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L8-CS13	00h
pwm_bri316	33Ch	R/W	16-bits PWM lower 8 bits [7:0] for Dot L8-CS14	00h
pwm_bri317	33Dh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L8-CS14	00h
pwm_bri318	33Eh	R/W	16-bits PWM lower 8 bits [7:0] for Dot L8-CS15	00h
pwm_bri319	33Fh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L8-CS15	00h
pwm_bri320	340h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L8-CS16	00h
pwm_bri321	341h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L8-CS16	00h
pwm_bri322	342h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L8-CS17	00h
pwm_bri323	343h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L8-CS17	00h
pwm_bri324	344h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L9-CS0	00h
pwm_bri325	345h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L9-CS0	00h
pwm_bri326	346h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L9-CS1	00h
pwm_bri327	347h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L9-CS1	00h
pwm_bri328	348h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L9-CS2	00h
pwm_bri329	349h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L9-CS2	00h
pwm_bri330	34Ah	R/W	16-bits PWM lower 8 bits [7:0] for Dot L9-CS3	00h
pwm_bri331	34Bh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L9-CS3	00h
pwm_bri332	34Ch	R/W	16-bits PWM lower 8 bits [7:0] for Dot L9-CS4	00h
pwm_bri333	34Dh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L9-CS4	00h
pwm_bri334	34Eh	R/W	16-bits PWM lower 8 bits [7:0] for Dot L9-CS5	00h
pwm_bri335	34Fh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L9-CS5	00h
pwm_bri336	350h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L9-CS6	00h
pwm_bri337	351h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L9-CS6	00h
pwm_bri338	352h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L9-CS7	00h
pwm_bri339	353h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L9-CS7	00h
pwm_bri340	354h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L9-CS8	00h

pwm_bri341	355h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L9-CS8	00h
pwm_bri342	356h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L9-CS9	00h
pwm_bri343	357h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L9-CS9	00h
pwm_bri344	358h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L9-CS10	00h
pwm_bri345	359h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L9-CS10	00h
pwm_bri346	35Ah	R/W	16-bits PWM lower 8 bits [7:0] for Dot L9-CS11	00h
pwm_bri347	35Bh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L9-CS11	00h
pwm_bri348	35Ch	R/W	16-bits PWM lower 8 bits [7:0] for Dot L9-CS12	00h
pwm_bri349	35Dh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L9-CS12	00h
pwm_bri350	35Eh	R/W	16-bits PWM lower 8 bits [7:0] for Dot L9-CS13	00h
pwm_bri351	35Fh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L9-CS13	00h
pwm_bri352	360h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L9-CS14	00h
pwm_bri353	361h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L9-CS14	00h
pwm_bri354	362h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L9-CS15	00h
pwm_bri355	363h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L9-CS15	00h
pwm_bri356	364h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L9-CS16	00h
pwm_bri357	365h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L9-CS16	00h
pwm_bri358	366h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L9-CS17	00h
pwm_bri359	367h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L9-CS17	00h
pwm_bri360	368h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L10-CS0	00h
pwm_bri361	369h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L10-CS0	00h
pwm_bri362	36Ah	R/W	16-bits PWM lower 8 bits [7:0] for Dot L10-CS1	00h
pwm_bri363	36Bh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L10-CS1	00h
pwm_bri364	36Ch	R/W	16-bits PWM lower 8 bits [7:0] for Dot L10-CS2	00h
pwm_bri365	36Dh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L10-CS2	00h
pwm_bri366	36Eh	R/W	16-bits PWM lower 8 bits [7:0] for Dot L10-CS3	00h
pwm_bri367	36Fh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L10-CS3	00h
pwm_bri368	370h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L10-CS4	00h
pwm_bri369	371h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L10-CS4	00h
pwm_bri370	372h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L10-CS5	00h
pwm_bri371	373h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L10-CS5	00h
pwm_bri372	374h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L10-CS6	00h
pwm_bri373	375h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L10-CS6	00h
pwm_bri374	376h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L10-CS7	00h
pwm_bri375	377h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L10-CS7	00h
pwm_bri376	378h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L10-CS8	00h
pwm_bri377	379h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L10-CS8	00h
pwm_bri378	37Ah	R/W	16-bits PWM lower 8 bits [7:0] for Dot L10-CS9	00h
pwm_bri379	37Bh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L10-CS9	00h
pwm_bri380	37Ch	R/W	16-bits PWM lower 8 bits [7:0] for Dot L10-CS10	00h
pwm_bri381	37Dh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L10-CS10	00h
pwm_bri382	37Eh	R/W	16-bits PWM lower 8 bits [7:0] for Dot L10-CS11	00h
pwm_bri383	37Fh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L10-CS11	00h
pwm_bri384	380h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L10-CS12	00h
pwm_bri385	381h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L10-CS12	00h
pwm_bri386	382h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L10-CS13	00h
pwm_bri387	383h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L10-CS13	00h

pwm_bri388	384h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L10-CS14	00h
pwm_bri389	385h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L10-CS14	00h
pwm_bri390	386h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L10-CS15	00h
pwm_bri391	387h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L10-CS15	00h
pwm_bri392	388h	R/W	16-bits PWM lower 8 bits [7:0] for Dot L10-CS16	00h
pwm_bri393	389h	R/W	16-bits PWM higher 8 bits [15:8] for Dot L10-CS16	00h
pwm_bri394	38Ah	R/W	16-bits PWM lower 8 bits [7:0] for Dot L10-CS17	00h
pwm_bri395	38Bh	R/W	16-bits PWM higher 8 bits [15:8] for Dot L10-CS17	00h

2.2 CONFIG Registers

Table 2-2 lists the CONFIG registers. All register offset addresses not listed in Table 2-2 should be considered as reserved locations and the register contents should not be modified.

Device Configuration

Table 2-2. CONFIG Registers

Address	Acronym	Register Name	Section
0h	Chip_en	Chip enable	Go
1h	Dev_initial	Device initialization	Go
2h	Dev_config1	Device configuration register 1	Go
3h	Dev_config2	Device configuration register 2	Go
4h	Dev_config3	Device configuration register 3	Go

2.2.1 Chip_en Register (Address = 0h) [Default = 0h]

Chip_en is shown in Figure 2-1 and described in Table 2-3.

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Figure 2-1. Chip_en Register

7	6	5	4	3	2	1	0
RESERVED							Chip_EN
R-0h							R/W-0h

Table 2-3. Chip_en Register Field Descriptions

Bit	Field	Type	Default	Description
7-1	RESERVED	R	0h	Reserved
0	Chip_EN	R/W	0h	Chip enable 0h = Disabled 1h = Enabled

2.2.2 Dev_initial Register (Address = 1h) [Default = 5Eh]

Dev_initial is shown in Figure 2-2 and described in Table 2-4.

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Figure 2-2. Dev_initial Register

7	6	5	4	3	2	1	0
RESERVED	Max_Line_Num				Data_Ref_Mode	PWM_Fre	
R-0h	R/W-Bh				R/W-3h	R/W-0h	

Table 2-4. Dev_initial Register Field Descriptions

Bit	Field	Type	Default	Description
7	RESERVED	R	0h	Reserved

Table 2-4. Dev_initial Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
6-3	Max_Line_Num	R/W	Bh	Maximum scan line number selection 1h = 1 scan line (LED dot current must be set to lower than 20mA) 2h = 2 scan lines (LED dot current must be set to lower than 35mA) 3h = 3 scan lines 4h = 4 scan lines 5h = 5 scan lines 6h = 6 scan lines 7h = 7 scan lines 8h = 8 scan lines 9h = 9 scan lines Ah = 10 scan lines Bh = 11 scan lines
2-1	Data_Ref_Mode	R/W	3h	Data refresh mode selection 0h = Mode 1 1h = Mode 2 2h = Mode 3 3h = Mode 3
0	PWM_Fre	R/W	0h	Output PWM frequency setting 0h = 125kHz 1h = 62.5kHz

2.2.3 Dev_config1 Register (Address = 2h) [Default = 0h]

Dev_config1 is shown in [Figure 2-3](#) and described in [Table 2-5](#).

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Figure 2-3. Dev_config1 Register

7	6	5	4	3	2	1	0
RESERVED				SW_BLK	PWM_Scale_Mode	PWM_Phase_Shift	CS_ON_Shift
R-0h				R/W-0h	R/W-0h	R/W-0h	R/W-0h

Table 2-5. Dev_config1 Register Field Descriptions

Bit	Field	Type	Default	Description
7-4	RESERVED	R	0h	Reserved
3	SW_BLK	R/W	0h	Line switch blanking time setting 0h = 1us 1h = 0.5us
2	PWM_Scale_Mode	R/W	0h	Dimming scale setting of final PWM generator 0h = Linear scale dimming curve 1h = Exponential scale dimming curve
1	PWM_Phase_Shift	R/W	0h	PWM phase shift selection 0h = Phase shift off 1h = Phase shift on
0	CS_ON_Shift	R/W	0h	Current sink turn on delay setting 0h = Delay off 1h = Delay on

2.2.4 Dev_config2 Register (Address = 3h) [Default = 0h]

Dev_config2 is shown in [Figure 2-4](#) and described in [Table 2-6](#).

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Figure 2-4. Dev_config2 Register

7	6	5	4	3	2	1	0
Comp_Group3		Comp_Group2		Comp_Group1		LOD_removal	LSD_removal
R/W-0h		R/W-0h		R/W-0h		R/W-0h	R/W-0h

Table 2-6. Dev_config2 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Comp_Group3	R/W	0h	Low brightness compensation clock shift number setting for group1 0h = off 1h = 1 clock 2h = 2 clock 3h = 3 clock
5-4	Comp_Group2	R/W	0h	Low brightness compensation clock shift number setting for group2 0h = off 1h = 1 clock 2h = 2 clock 3h = 3 clock
3-2	Comp_Group1	R/W	0h	Low brightness compensation clock shift number setting for group3 0h = off 1h = 1 clock 2h = 2 clock 3h = 3 clock
1	LOD_removal	R/W	0h	LOD removal function enable 0h = Disabled 1h = Enabled
0	LSD_removal	R/W	0h	LSD removal function enable 0h = Disabled 1h = Enabled

2.2.5 Dev_config3 Register (Address = 4h) [Default = 57h]

Dev_config3 is shown in [Figure 2-5](#) and described in [Table 2-7](#).

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Figure 2-5. Dev_config3 Register

7	6	5	4	3	2	1	0
Down_Deghost		Up_Deghost		Maximum_Current			Up_Deghost_E nable
R/W-1h		R/W-1h		R/W-3h			R/W-1h

Table 2-7. Dev_config3 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Down_Deghost	R/W	1h	Downside deghosting level selection 0h = No deghosting 1h = Weak deghosting 2h = Medium deghosting 3h = Strong deghosting
5-4	Up_Deghost	R/W	1h	Scan line clamp voltage of upside deghosting 0h = VLED – 2V 1h = VLED – 2.5V 2h = VLED – 3V 3h = GND

Table 2-7. Dev_config3 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
3-1	Maximum_Current	R/W	3h	Maximum current setting (MC) 0h = 3mA 1h = 5mA 2h = 10mA 3h = 15mA (Default) 4h = 20mA 5h = 30mA 6h = 40mA 7h = 50mA
0	Up_Deghost_Enable	R/W	1h	Current sink turn on delay enable 0h = Disabled 1h = Enabled

2.3 GROUP Registers

Table 2-8 lists the GROUP registers. All register offset addresses not listed in Table 2-8 should be considered as reserved locations and the register contents should not be modified.

Group Configuration

Table 2-8. GROUP Registers

Address	Acronym	Register Name	Section
5h	Master_bri	Global PWM configuration	Go
6h	Group0_bri	Group1 PWM configuration	Go
7h	Group1_bri	Group2 PWM configuration	Go
8h	Group2_bri	Group3 PWM configuration	Go
9h	R_current_set	Group1 current configuration	Go
Ah	G_current_set	Group2 current configuration	Go
Bh	B_current_set	Group3 current configuration	Go

2.3.1 Master_bri Register (Address = 5h) [Default = FFh]

Master_bri is shown in Figure 2-6 and described in Table 2-9.

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Figure 2-6. Master_bri Register

7	6	5	4	3	2	1	0
PWM_Global							
R/W-FFh							

Table 2-9. Master_bri Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	PWM_Global	R/W	FFh	Global PWM setting

2.3.2 Group0_bri Register (Address = 6h) [Default = FFh]

Group0_bri is shown in Figure 2-7 and described in Table 2-10.

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Figure 2-7. Group0_bri Register

7	6	5	4	3	2	1	0
PWM_Group1							
R/W-FFh							

Table 2-10. Group0_bri Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	PWM_Group1	R/W	FFh	Group1 PWM setting

2.3.3 Group1_bri Register (Address = 7h) [Default = FFh]

Group1_bri is shown in Figure 2-8 and described in Table 2-11.

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Figure 2-8. Group1_bri Register

7	6	5	4	3	2	1	0
PWM_Group2							
R/W-FFh							

Table 2-11. Group1_bri Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	PWM_Group2	R/W	FFh	Group2 PWM setting

2.3.4 Group2_bri Register (Address = 8h) [Default = FFh]

Group2_bri is shown in [Figure 2-9](#) and described in [Table 2-12](#).

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Figure 2-9. Group2_bri Register

7	6	5	4	3	2	1	0
PWM_Group3							
R/W-FFh							

Table 2-12. Group2_bri Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	PWM_Group3	R/W	FFh	Group3 PWM setting

2.3.5 R_current_set Register (Address = 9h) [Default = 40h]

R_current_set is shown in [Figure 2-10](#) and described in [Table 2-13](#).

Return to the [Summary Table](#).

Figure 2-10. R_current_set Register

7	6	5	4	3	2	1	0
RESERVED							CC_Group1
R-0h							R/W-40h

Table 2-13. R_current_set Register Field Descriptions

Bit	Field	Type	Default	Description
7	RESERVED	R	0h	Reserved
6-0	CC_Group1	R/W	40h	Color-group current setting (CC) of group 1 (CS0, CS3, CS6, CS9, CS12, CS15)

2.3.6 G_current_set Register (Address = Ah) [Default = 40h]

G_current_set is shown in [Figure 2-11](#) and described in [Table 2-14](#).

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Figure 2-11. G_current_set Register

7	6	5	4	3	2	1	0
RESERVED							CC_Group2
R-0h							R/W-40h

Table 2-14. G_current_set Register Field Descriptions

Bit	Field	Type	Default	Description
7	RESERVED	R	0h	Reserved
6-0	CC_Group2	R/W	40h	Color-group current setting (CC) of group 2 (CS1, CS4, CS7, CS10, CS13, CS16)

2.3.7 B_current_set Register (Address = Bh) [Default = 40h]

B_current_set is shown in [Figure 2-12](#) and described in [Table 2-15](#).

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Figure 2-12. B_current_set Register

7	6	5	4	3	2	1	0
RESERVED	CC_Group3						
R-0h	R/W-40h						

Table 2-15. B_current_set Register Field Descriptions

Bit	Field	Type	Default	Description
7	RESERVED	R	0h	Reserved
6-0	CC_Group3	R/W	40h	Color-group current setting (CC) of group 3 (CS2, CS5, CS8, CS11, CS14, CS17)

2.4 LED_DOT_GROUP Registers

Table 2-16 lists the LED_DOT_GROUP registers. All register offset addresses not listed in Table 2-16 should be considered as reserved locations and the register contents should not be modified.

LED Dot Group Configuration

Table 2-16. LED_DOT_GROUP Registers

Address	Acronym	Register Name	Section
Ch	Dot_grp_sel0	LED dot group selection register 0	Go
Dh	Dot_grp_sel1	LED dot group selection register 1	Go
Eh	Dot_grp_sel2	LED dot group selection register 2	Go
Fh	Dot_grp_sel3	LED dot group selection register 3	Go
10h	Dot_grp_sel4	LED dot group selection register 4	Go
11h	Dot_grp_sel5	LED dot group selection register 5	Go
12h	Dot_grp_sel6	LED dot group selection register 6	Go
13h	Dot_grp_sel7	LED dot group selection register 7	Go
14h	Dot_grp_sel8	LED dot group selection register 8	Go
15h	Dot_grp_sel9	LED dot group selection register 9	Go
16h	Dot_grp_sel10	LED dot group selection register 10	Go
17h	Dot_grp_sel11	LED dot group selection register 11	Go
18h	Dot_grp_sel12	LED dot group selection register 12	Go
19h	Dot_grp_sel13	LED dot group selection register 13	Go
1Ah	Dot_grp_sel14	LED dot group selection register 14	Go
1Bh	Dot_grp_sel15	LED dot group selection register 15	Go
1Ch	Dot_grp_sel16	LED dot group selection register 16	Go
1Dh	Dot_grp_sel17	LED dot group selection register 17	Go
1Eh	Dot_grp_sel18	LED dot group selection register 18	Go
1Fh	Dot_grp_sel19	LED dot group selection register 19	Go
20h	Dot_grp_sel20	LED dot group selection register 20	Go
21h	Dot_grp_sel21	LED dot group selection register 21	Go
22h	Dot_grp_sel22	LED dot group selection register 22	Go
23h	Dot_grp_sel23	LED dot group selection register 23	Go
24h	Dot_grp_sel24	LED dot group selection register 24	Go
25h	Dot_grp_sel25	LED dot group selection register 25	Go
26h	Dot_grp_sel26	LED dot group selection register 26	Go
27h	Dot_grp_sel27	LED dot group selection register 27	Go
28h	Dot_grp_sel28	LED dot group selection register 28	Go
29h	Dot_grp_sel29	LED dot group selection register 29	Go
2Ah	Dot_grp_sel30	LED dot group selection register 30	Go
2Bh	Dot_grp_sel31	LED dot group selection register 31	Go
2Ch	Dot_grp_sel32	LED dot group selection register 32	Go
2Dh	Dot_grp_sel33	LED dot group selection register 33	Go
2Eh	Dot_grp_sel34	LED dot group selection register 34	Go
2Fh	Dot_grp_sel35	LED dot group selection register 35	Go
30h	Dot_grp_sel36	LED dot group selection register 36	Go
31h	Dot_grp_sel37	LED dot group selection register 37	Go
32h	Dot_grp_sel38	LED dot group selection register 38	Go
33h	Dot_grp_sel39	LED dot group selection register 39	Go

Table 2-16. LED_DOT_GROUP Registers (continued)

Address	Acronym	Register Name	Section
34h	Dot_grp_sel40	LED dot group selection register 40	Go
35h	Dot_grp_sel41	LED dot group selection register 41	Go
36h	Dot_grp_sel42	LED dot group selection register 42	Go
37h	Dot_grp_sel43	LED dot group selection register 43	Go
38h	Dot_grp_sel44	LED dot group selection register 44	Go
39h	Dot_grp_sel45	LED dot group selection register 45	Go
3Ah	Dot_grp_sel46	LED dot group selection register 46	Go
3Bh	Dot_grp_sel47	LED dot group selection register 47	Go
3Ch	Dot_grp_sel48	LED dot group selection register 48	Go
3Dh	Dot_grp_sel49	LED dot group selection register 49	Go
3Eh	Dot_grp_sel50	LED dot group selection register 50	Go
3Fh	Dot_grp_sel51	LED dot group selection register 51	Go
40h	Dot_grp_sel52	LED dot group selection register 52	Go
41h	Dot_grp_sel53	LED dot group selection register 53	Go
42h	Dot_grp_sel54	LED dot group selection register 54	Go

2.4.1 Dot_grp_sel0 Register (Address = Ch) [Default = 0h]

Dot_grp_sel0 is shown in [Figure 2-13](#) and described in [Table 2-17](#).

Return to the [Summary Table](#).

Figure 2-13. Dot_grp_sel0 Register

7	6	5	4	3	2	1	0
Dot_L0-CS3_group		Dot_L0-CS2_group		Dot_L0-CS1_group		Dot_L0-CS0_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-17. Dot_grp_sel0 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L0-CS3_group	R/W	0h	Dot L0-CS3 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L0-CS2_group	R/W	0h	Dot L0-CS2 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L0-CS1_group	R/W	0h	Dot L0-CS1 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L0-CS0_group	R/W	0h	Dot L0-CS0 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.2 Dot_grp_sel1 Register (Address = Dh) [Default = 0h]

Dot_grp_sel1 is shown in [Figure 2-14](#) and described in [Table 2-18](#).

Return to the [Summary Table](#).

Figure 2-14. Dot_grp_sel1 Register

7	6	5	4	3	2	1	0
Dot_L0-CS7_group		Dot_L0-CS6_group		Dot_L0-CS5_group		Dot_L0-CS4_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-18. Dot_grp_sel1 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L0-CS7_group	R/W	0h	Dot L0-CS7 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L0-CS6_group	R/W	0h	Dot L0-CS6 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L0-CS5_group	R/W	0h	Dot L0-CS5 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L0-CS4_group	R/W	0h	Dot L0-CS4 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.3 Dot_grp_sel2 Register (Address = Eh) [Default = 0h]

Dot_grp_sel2 is shown in [Figure 2-15](#) and described in [Table 2-19](#).

Return to the [Summary Table](#).

Figure 2-15. Dot_grp_sel2 Register

7	6	5	4	3	2	1	0
Dot_L0-CS11_group		Dot_L0-CS10_group		Dot_L0-CS9_group		Dot_L0-CS8_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-19. Dot_grp_sel2 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L0-CS11_group	R/W	0h	Dot L0-CS11 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L0-CS10_group	R/W	0h	Dot L0-CS10 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

Table 2-19. Dot_grp_sel2 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
3-2	Dot_L0-CS9_group	R/W	0h	Dot L0-CS9 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L0-CS8_group	R/W	0h	Dot L0-CS8 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.4 Dot_grp_sel3 Register (Address = Fh) [Default = 0h]

Dot_grp_sel3 is shown in [Figure 2-16](#) and described in [Table 2-20](#).

Return to the [Summary Table](#).

Figure 2-16. Dot_grp_sel3 Register

7	6	5	4	3	2	1	0
Dot_L0-CS15_group		Dot_L0-CS14_group		Dot_L0-CS13_group		Dot_L0-CS12_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-20. Dot_grp_sel3 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L0-CS15_group	R/W	0h	Dot L0-CS15 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L0-CS14_group	R/W	0h	Dot L0-CS14 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L0-CS13_group	R/W	0h	Dot L0-CS13 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L0-CS12_group	R/W	0h	Dot L0-CS12 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.5 Dot_grp_sel4 Register (Address = 10h) [Default = 0h]

Dot_grp_sel4 is shown in [Figure 2-17](#) and described in [Table 2-21](#).

Return to the [Summary Table](#).

Figure 2-17. Dot_grp_sel4 Register

7	6	5	4	3	2	1	0
RESERVED				Dot_L0-CS17_group		Dot_L0-CS16_group	
R-0h				R/W-0h		R/W-0h	

Table 2-21. Dot_grp_sel4 Register Field Descriptions

Bit	Field	Type	Default	Description
7-4	RESERVED	R	0h	Reserved
3-2	Dot_L0-CS17_group	R/W	0h	Dot L0-CS17 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L0-CS16_group	R/W	0h	Dot L0-CS16 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.6 Dot_grp_sel5 Register (Address = 11h) [Default = 0h]

Dot_grp_sel5 is shown in [Figure 2-18](#) and described in [Table 2-22](#).

Return to the [Summary Table](#).

Figure 2-18. Dot_grp_sel5 Register

7	6	5	4	3	2	1	0
Dot_L1-CS3_group		Dot_L1-CS2_group		Dot_L1-CS1_group		Dot_L1-CS0_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-22. Dot_grp_sel5 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L1-CS3_group	R/W	0h	Dot L1-CS3 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L1-CS2_group	R/W	0h	Dot L1-CS2 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L1-CS1_group	R/W	0h	Dot L1-CS1 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L1-CS0_group	R/W	0h	Dot L1-CS0 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.7 Dot_grp_sel6 Register (Address = 12h) [Default = 0h]

Dot_grp_sel6 is shown in [Figure 2-19](#) and described in [Table 2-23](#).

Return to the [Summary Table](#).

Figure 2-19. Dot_grp_sel6 Register

7	6	5	4	3	2	1	0
Dot_L1-CS7_group		Dot_L1-CS6_group		Dot_L1-CS5_group		Dot_L1-CS4_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-23. Dot_grp_sel6 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L1-CS7_group	R/W	0h	Dot L1-CS7 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L1-CS6_group	R/W	0h	Dot L1-CS6 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L1-CS5_group	R/W	0h	Dot L1-CS5 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L1-CS4_group	R/W	0h	Dot L1-CS4 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.8 Dot_grp_sel7 Register (Address = 13h) [Default = 0h]

Dot_grp_sel7 is shown in [Figure 2-20](#) and described in [Table 2-24](#).

Return to the [Summary Table](#).

Figure 2-20. Dot_grp_sel7 Register

7	6	5	4	3	2	1	0
Dot_L1-CS11_group		Dot_L1-CS10_group		Dot_L1-CS9_group		Dot_L1-CS8_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-24. Dot_grp_sel7 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L1-CS11_group	R/W	0h	Dot L1-CS11 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L1-CS10_group	R/W	0h	Dot L1-CS10 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

Table 2-24. Dot_grp_sel7 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
3-2	Dot_L1-CS9_group	R/W	0h	Dot L1-CS9 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L1-CS8_group	R/W	0h	Dot L1-CS8 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.9 Dot_grp_sel8 Register (Address = 14h) [Default = 0h]

Dot_grp_sel8 is shown in [Figure 2-21](#) and described in [Table 2-25](#).

Return to the [Summary Table](#).

Figure 2-21. Dot_grp_sel8 Register

7	6	5	4	3	2	1	0
Dot_L1-CS15_group		Dot_L1-CS14_group		Dot_L1-CS13_group		Dot_L1-CS12_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-25. Dot_grp_sel8 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L1-CS15_group	R/W	0h	Dot L1-CS15 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L1-CS14_group	R/W	0h	Dot L1-CS14 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L1-CS13_group	R/W	0h	Dot L1-CS13 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L1-CS12_group	R/W	0h	Dot L1-CS12 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.10 Dot_grp_sel9 Register (Address = 15h) [Default = 0h]

Dot_grp_sel9 is shown in [Figure 2-22](#) and described in [Table 2-26](#).

Return to the [Summary Table](#).

Figure 2-22. Dot_grp_sel9 Register

7	6	5	4	3	2	1	0
RESERVED				Dot_L1-CS17_group		Dot_L1-CS16_group	
R-0h				R/W-0h		R/W-0h	

Table 2-26. Dot_grp_sel9 Register Field Descriptions

Bit	Field	Type	Default	Description
7-4	RESERVED	R	0h	Reserved
3-2	Dot_L1-CS17_group	R/W	0h	Dot L1-CS17 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L1-CS16_group	R/W	0h	Dot L1-CS16 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.11 Dot_grp_sel10 Register (Address = 16h) [Default = 0h]

Dot_grp_sel10 is shown in [Figure 2-23](#) and described in [Table 2-27](#).

Return to the [Summary Table](#).

Figure 2-23. Dot_grp_sel10 Register

7	6	5	4	3	2	1	0
Dot_L2-CS3_group		Dot_L2-CS2_group		Dot_L2-CS1_group		Dot_L2-CS0_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-27. Dot_grp_sel10 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L2-CS3_group	R/W	0h	Dot L2-CS3 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L2-CS2_group	R/W	0h	Dot L2-CS2 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L2-CS1_group	R/W	0h	Dot L2-CS1 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L2-CS0_group	R/W	0h	Dot L2-CS0 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.12 Dot_grp_sel11 Register (Address = 17h) [Default = 0h]

Dot_grp_sel11 is shown in [Figure 2-24](#) and described in [Table 2-28](#).

Return to the [Summary Table](#).

Figure 2-24. Dot_grp_sel11 Register

7	6	5	4	3	2	1	0
Dot_L2-CS7_group		Dot_L2-CS6_group		Dot_L2-CS5_group		Dot_L2-CS4_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Figure 2-24. Dot_grp_sel11 Register (continued)
Table 2-28. Dot_grp_sel11 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L2-CS7_group	R/W	0h	Dot L2-CS7 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L2-CS6_group	R/W	0h	Dot L2-CS6 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L2-CS5_group	R/W	0h	Dot L2-CS5 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L2-CS4_group	R/W	0h	Dot L2-CS4 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.13 Dot_grp_sel12 Register (Address = 18h) [Default = 0h]

Dot_grp_sel12 is shown in [Figure 2-25](#) and described in [Table 2-29](#).

Return to the [Summary Table](#).

Figure 2-25. Dot_grp_sel12 Register

7	6	5	4	3	2	1	0
Dot_L2-CS11_group		Dot_L2-CS10_group		Dot_L2-CS9_group		Dot_L2-CS8_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-29. Dot_grp_sel12 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L2-CS11_group	R/W	0h	Dot L2-CS11 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L2-CS10_group	R/W	0h	Dot L2-CS10 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L2-CS9_group	R/W	0h	Dot L2-CS9 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L2-CS8_group	R/W	0h	Dot L2-CS8 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.14 Dot_grp_sel13 Register (Address = 19h) [Default = 0h]

Dot_grp_sel13 is shown in [Figure 2-26](#) and described in [Table 2-30](#).

Return to the [Summary Table](#).

Figure 2-26. Dot_grp_sel13 Register

7	6	5	4	3	2	1	0
Dot_L2-CS15_group		Dot_L2-CS14_group		Dot_L2-CS13_group		Dot_L2-CS12_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-30. Dot_grp_sel13 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L2-CS15_group	R/W	0h	Dot L2-CS15 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L2-CS14_group	R/W	0h	Dot L2-CS14 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L2-CS13_group	R/W	0h	Dot L2-CS13 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L2-CS12_group	R/W	0h	Dot L2-CS12 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.15 Dot_grp_sel14 Register (Address = 1Ah) [Default = 0h]

Dot_grp_sel14 is shown in [Figure 2-27](#) and described in [Table 2-31](#).

Return to the [Summary Table](#).

Figure 2-27. Dot_grp_sel14 Register

7	6	5	4	3	2	1	0
RESERVED				Dot_L2-CS17_group		Dot_L2-CS16_group	
R-0h				R/W-0h		R/W-0h	

Table 2-31. Dot_grp_sel14 Register Field Descriptions

Bit	Field	Type	Default	Description
7-4	RESERVED	R	0h	Reserved
3-2	Dot_L2-CS17_group	R/W	0h	Dot L2-CS17 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L2-CS16_group	R/W	0h	Dot L2-CS16 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.16 Dot_grp_sel15 Register (Address = 1Bh) [Default = 0h]

Dot_grp_sel15 is shown in [Figure 2-28](#) and described in [Table 2-32](#).

Return to the [Summary Table](#).

Figure 2-28. Dot_grp_sel15 Register

7	6	5	4	3	2	1	0
Dot_L3-CS3_group		Dot_L3-CS2_group		Dot_L3-CS1_group		Dot_L3-CS0_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-32. Dot_grp_sel15 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L3-CS3_group	R/W	0h	Dot L3-CS3 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L3-CS2_group	R/W	0h	Dot L3-CS2 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L3-CS1_group	R/W	0h	Dot L3-CS1 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L3-CS0_group	R/W	0h	Dot L3-CS0 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.17 Dot_grp_sel16 Register (Address = 1Ch) [Default = 0h]

Dot_grp_sel16 is shown in [Figure 2-29](#) and described in [Table 2-33](#).

Return to the [Summary Table](#).

Figure 2-29. Dot_grp_sel16 Register

7	6	5	4	3	2	1	0
Dot_L3-CS7_group		Dot_L3-CS6_group		Dot_L3-CS5_group		Dot_L3-CS4_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-33. Dot_grp_sel16 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L3-CS7_group	R/W	0h	Dot L3-CS7 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L3-CS6_group	R/W	0h	Dot L3-CS6 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

Table 2-33. Dot_grp_sel16 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
3-2	Dot_L3-CS5_group	R/W	0h	Dot L3-CS5 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L3-CS4_group	R/W	0h	Dot L3-CS4 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.18 Dot_grp_sel17 Register (Address = 1Dh) [Default = 0h]

Dot_grp_sel17 is shown in [Figure 2-30](#) and described in [Table 2-34](#).

Return to the [Summary Table](#).

Figure 2-30. Dot_grp_sel17 Register

7	6	5	4	3	2	1	0
Dot_L3-CS11_group		Dot_L3-CS10_group		Dot_L3-CS9_group		Dot_L3-CS8_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-34. Dot_grp_sel17 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L3-CS11_group	R/W	0h	Dot L3-CS11 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L3-CS10_group	R/W	0h	Dot L3-CS10 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L3-CS9_group	R/W	0h	Dot L3-CS9 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L3-CS8_group	R/W	0h	Dot L3-CS8 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.19 Dot_grp_sel18 Register (Address = 1Eh) [Default = 0h]

Dot_grp_sel18 is shown in [Figure 2-31](#) and described in [Table 2-35](#).

Return to the [Summary Table](#).

Figure 2-31. Dot_grp_sel18 Register

7	6	5	4	3	2	1	0
Dot_L3-CS15_group		Dot_L3-CS14_group		Dot_L3-CS13_group		Dot_L3-CS12_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-35. Dot_grp_sel18 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L3-CS15_group	R/W	0h	Dot L3-CS15 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L3-CS14_group	R/W	0h	Dot L3-CS14 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L3-CS13_group	R/W	0h	Dot L3-CS13 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L3-CS12_group	R/W	0h	Dot L3-CS12 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.20 Dot_grp_sel19 Register (Address = 1Fh) [Default = 0h]

Dot_grp_sel19 is shown in [Figure 2-32](#) and described in [Table 2-36](#).

Return to the [Summary Table](#).

Figure 2-32. Dot_grp_sel19 Register

7	6	5	4	3	2	1	0
RESERVED				Dot_L3-CS17_group		Dot_L3-CS16_group	
R-0h				R/W-0h		R/W-0h	

Table 2-36. Dot_grp_sel19 Register Field Descriptions

Bit	Field	Type	Default	Description
7-4	RESERVED	R	0h	Reserved
3-2	Dot_L3-CS17_group	R/W	0h	Dot L3-CS17 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L3-CS16_group	R/W	0h	Dot L3-CS16 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.21 Dot_grp_sel20 Register (Address = 20h) [Default = 0h]

Dot_grp_sel20 is shown in [Figure 2-33](#) and described in [Table 2-37](#).

Return to the [Summary Table](#).

Figure 2-33. Dot_grp_sel20 Register

7	6	5	4	3	2	1	0
Dot_L4-CS3_group		Dot_L4-CS2_group		Dot_L4-CS5_group		Dot_L4-CS0_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Figure 2-33. Dot_grp_sel20 Register (continued)
Table 2-37. Dot_grp_sel20 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L4-CS3_group	R/W	0h	Dot L4-CS3 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L4-CS2_group	R/W	0h	Dot L4-CS2 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L4-CS1_group	R/W	0h	Dot L4-CS1 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L4-CS5_group	R/W	0h	Dot L4-CS5 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L4-CS0_group	R/W	0h	Dot L4-CS0 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.22 Dot_grp_sel21 Register (Address = 21h) [Default = 0h]

Dot_grp_sel21 is shown in [Figure 2-34](#) and described in [Table 2-38](#).

Return to the [Summary Table](#).

Figure 2-34. Dot_grp_sel21 Register

7	6	5	4	3	2	1	0
Dot_L4-CS7_group		Dot_L4-CS6_group		RESERVED		Dot_L4-CS4_group	
R/W-0h		R/W-0h		R-0h		R/W-0h	

Table 2-38. Dot_grp_sel21 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L4-CS7_group	R/W	0h	Dot L4-CS7 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L4-CS6_group	R/W	0h	Dot L4-CS6 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	RESERVED	R	0h	

Table 2-38. Dot_grp_sel21 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
1-0	Dot_L4-CS4_group	R/W	0h	Dot L4-CS4 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.23 Dot_grp_sel22 Register (Address = 22h) [Default = 0h]

Dot_grp_sel22 is shown in [Figure 2-35](#) and described in [Table 2-39](#).

Return to the [Summary Table](#).

Figure 2-35. Dot_grp_sel22 Register

7	6	5	4	3	2	1	0
Dot_L4-CS11_group		Dot_L4-CS10_group		Dot_L4-CS9_group		Dot_L4-CS8_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-39. Dot_grp_sel22 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L4-CS11_group	R/W	0h	Dot L4-CS11 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L4-CS10_group	R/W	0h	Dot L4-CS10 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L4-CS9_group	R/W	0h	Dot L4-CS9 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L4-CS8_group	R/W	0h	Dot L4-CS8 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.24 Dot_grp_sel23 Register (Address = 23h) [Default = 0h]

Dot_grp_sel23 is shown in [Figure 2-36](#) and described in [Table 2-40](#).

Return to the [Summary Table](#).

Figure 2-36. Dot_grp_sel23 Register

7	6	5	4	3	2	1	0
Dot_L4-CS15_group		Dot_L4-CS14_group		Dot_L4-CS13_group		Dot_L4-CS12_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-40. Dot_grp_sel23 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L4-CS15_group	R/W	0h	Dot L4-CS15 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L4-CS14_group	R/W	0h	Dot L4-CS14 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L4-CS13_group	R/W	0h	Dot L4-CS13 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L4-CS12_group	R/W	0h	Dot L4-CS12 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.25 Dot_grp_sel24 Register (Address = 24h) [Default = 0h]

Dot_grp_sel24 is shown in [Figure 2-37](#) and described in [Table 2-41](#).

Return to the [Summary Table](#).

Figure 2-37. Dot_grp_sel24 Register

7	6	5	4	3	2	1	0
RESERVED				Dot_L4-CS17_group		Dot_L4-CS16_group	
R-0h				R/W-0h		R/W-0h	

Table 2-41. Dot_grp_sel24 Register Field Descriptions

Bit	Field	Type	Default	Description
7-4	RESERVED	R	0h	Reserved
3-2	Dot_L4-CS17_group	R/W	0h	Dot L4-CS17 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L4-CS16_group	R/W	0h	Dot L4-CS16 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.26 Dot_grp_sel25 Register (Address = 25h) [Default = 0h]

Dot_grp_sel25 is shown in [Figure 2-38](#) and described in [Table 2-42](#).

Return to the [Summary Table](#).

Figure 2-38. Dot_grp_sel25 Register

7	6	5	4	3	2	1	0
Dot_L5-CS3_group		Dot_L5-CS2_group		Dot_L5-CS1_group		Dot_L5-CS0_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Figure 2-38. Dot_grp_sel25 Register (continued)
Table 2-42. Dot_grp_sel25 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L5-CS3_group	R/W	0h	Dot L5-CS3 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L5-CS2_group	R/W	0h	Dot L5-CS2 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L5-CS1_group	R/W	0h	Dot L5-CS1 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L5-CS0_group	R/W	0h	Dot L5-CS0 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.27 Dot_grp_sel26 Register (Address = 26h) [Default = 0h]

Dot_grp_sel26 is shown in [Figure 2-39](#) and described in [Table 2-43](#).

Return to the [Summary Table](#).

Figure 2-39. Dot_grp_sel26 Register

7	6	5	4	3	2	1	0
Dot_L5-CS7_group		Dot_L5-CS6_group		Dot_L5-CS5_group		Dot_L5-CS4_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-43. Dot_grp_sel26 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L5-CS7_group	R/W	0h	Dot L5-CS7 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L5-CS6_group	R/W	0h	Dot L5-CS6 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L5-CS5_group	R/W	0h	Dot L5-CS5 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L5-CS4_group	R/W	0h	Dot L5-CS4 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.28 Dot_grp_sel27 Register (Address = 27h) [Default = 0h]

Dot_grp_sel27 is shown in [Figure 2-40](#) and described in [Table 2-44](#).

Return to the [Summary Table](#).

Figure 2-40. Dot_grp_sel27 Register

7	6	5	4	3	2	1	0
Dot_L5-CS11_group		Dot_L5-CS10_group		Dot_L5-CS9_group		Dot_L5-CS8_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-44. Dot_grp_sel27 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L5-CS11_group	R/W	0h	Dot L5-CS11 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L5-CS10_group	R/W	0h	Dot L5-CS10 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L5-CS9_group	R/W	0h	Dot L5-CS9 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L5-CS8_group	R/W	0h	Dot L5-CS8 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.29 Dot_grp_sel28 Register (Address = 28h) [Default = 0h]

Dot_grp_sel28 is shown in [Figure 2-41](#) and described in [Table 2-45](#).

Return to the [Summary Table](#).

Figure 2-41. Dot_grp_sel28 Register

7	6	5	4	3	2	1	0
Dot_L5-CS15_group		Dot_L5-CS14_group		Dot_L5-CS13_group		Dot_L5-CS12_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-45. Dot_grp_sel28 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L5-CS15_group	R/W	0h	Dot L5-CS15 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L5-CS14_group	R/W	0h	Dot L5-CS14 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

Table 2-45. Dot_grp_sel28 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
3-2	Dot_L5-CS13_group	R/W	0h	Dot L5-CS13 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L5-CS12_group	R/W	0h	Dot L5-CS12 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.30 Dot_grp_sel29 Register (Address = 29h) [Default = 0h]

Dot_grp_sel29 is shown in [Figure 2-42](#) and described in [Table 2-46](#).

Return to the [Summary Table](#).

Figure 2-42. Dot_grp_sel29 Register

7	6	5	4	3	2	1	0
RESERVED				Dot_L5-CS17_group		Dot_L5-CS16_group	
R-0h				R/W-0h		R/W-0h	

Table 2-46. Dot_grp_sel29 Register Field Descriptions

Bit	Field	Type	Default	Description
7-4	RESERVED	R	0h	Reserved
3-2	Dot_L5-CS17_group	R/W	0h	Dot L5-CS17 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L5-CS16_group	R/W	0h	Dot L5-CS16 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.31 Dot_grp_sel30 Register (Address = 2Ah) [Default = 0h]

Dot_grp_sel30 is shown in [Figure 2-43](#) and described in [Table 2-47](#).

Return to the [Summary Table](#).

Figure 2-43. Dot_grp_sel30 Register

7	6	5	4	3	2	1	0
Dot_L6-CS3_group		Dot_L6-CS2_group		Dot_L6-CS1_group		Dot_L6-CS0_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-47. Dot_grp_sel30 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L6-CS3_group	R/W	0h	Dot L6-CS3 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

Table 2-47. Dot_grp_sel30 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
5-4	Dot_L6-CS2_group	R/W	0h	Dot L6-CS2 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L6-CS1_group	R/W	0h	Dot L6-CS1 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L6-CS0_group	R/W	0h	Dot L6-CS0 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.32 Dot_grp_sel31 Register (Address = 2Bh) [Default = 0h]

Dot_grp_sel31 is shown in [Figure 2-44](#) and described in [Table 2-48](#).

Return to the [Summary Table](#).

Figure 2-44. Dot_grp_sel31 Register

7	6	5	4	3	2	1	0
Dot_L6-CS7_group		Dot_L6-CS6_group		Dot_L6-CS5_group		Dot_L6-CS4_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-48. Dot_grp_sel31 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L6-CS7_group	R/W	0h	Dot L6-CS7 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L6-CS6_group	R/W	0h	Dot L6-CS6 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L6-CS5_group	R/W	0h	Dot L6-CS5 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L6-CS4_group	R/W	0h	Dot L6-CS4 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.33 Dot_grp_sel32 Register (Address = 2Ch) [Default = 0h]

Dot_grp_sel32 is shown in [Figure 2-45](#) and described in [Table 2-49](#).

Return to the [Summary Table](#).

Figure 2-45. Dot_grp_sel32 Register

7	6	5	4	3	2	1	0
Dot_L6-CS11_group		Dot_L6-CS10_group		Dot_L6-CS9_group		Dot_L6-CS8_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-49. Dot_grp_sel32 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L6-CS11_group	R/W	0h	Dot L6-CS11 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L6-CS10_group	R/W	0h	Dot L6-CS10 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L6-CS9_group	R/W	0h	Dot L6-CS9 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L6-CS8_group	R/W	0h	Dot L6-CS8 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.34 Dot_grp_sel33 Register (Address = 2Dh) [Default = 0h]

Dot_grp_sel33 is shown in [Figure 2-46](#) and described in [Table 2-50](#).

Return to the [Summary Table](#).

Figure 2-46. Dot_grp_sel33 Register

7	6	5	4	3	2	1	0
Dot_L6-CS15_group		Dot_L6-CS14_group		Dot_L6-CS13_group		Dot_L6-CS12_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-50. Dot_grp_sel33 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L6-CS15_group	R/W	0h	Dot L6-CS15 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L6-CS14_group	R/W	0h	Dot L6-CS14 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L6-CS13_group	R/W	0h	Dot L6-CS13 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

Table 2-50. Dot_grp_sel33 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
1-0	Dot_L6-CS12_group	R/W	0h	Dot L6-CS12 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.35 Dot_grp_sel34 Register (Address = 2Eh) [Default = 0h]

Dot_grp_sel34 is shown in [Figure 2-47](#) and described in [Table 2-51](#).

Return to the [Summary Table](#).

Figure 2-47. Dot_grp_sel34 Register

7	6	5	4	3	2	1	0
RESERVED			Dot_L6-CS17_group		Dot_L6-CS16_group		
R-0h			R/W-0h		R/W-0h		

Table 2-51. Dot_grp_sel34 Register Field Descriptions

Bit	Field	Type	Default	Description
7-4	RESERVED	R	0h	Reserved
3-2	Dot_L6-CS17_group	R/W	0h	Dot L6-CS17 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L6-CS16_group	R/W	0h	Dot L6-CS16 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.36 Dot_grp_sel35 Register (Address = 2Fh) [Default = 0h]

Dot_grp_sel35 is shown in [Figure 2-48](#) and described in [Table 2-52](#).

Return to the [Summary Table](#).

Figure 2-48. Dot_grp_sel35 Register

7	6	5	4	3	2	1	0
Dot_L7-CS3_group		Dot_L7-CS2_group		Dot_L7-CS1_group		Dot_L7-CS0_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-52. Dot_grp_sel35 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L7-CS3_group	R/W	0h	Dot L7-CS3 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L7-CS2_group	R/W	0h	Dot L7-CS2 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

Table 2-52. Dot_grp_sel35 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
3-2	Dot_L7-CS1_group	R/W	0h	Dot L7-CS1 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L7-CS0_group	R/W	0h	Dot L7-CS0 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.37 Dot_grp_sel36 Register (Address = 30h) [Default = 0h]

Dot_grp_sel36 is shown in [Figure 2-49](#) and described in [Table 2-53](#).

Return to the [Summary Table](#).

Figure 2-49. Dot_grp_sel36 Register

7	6	5	4	3	2	1	0
Dot_L7-CS7_group		Dot_L7-CS6_group		Dot_L7-CS5_group		Dot_L7-CS4_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-53. Dot_grp_sel36 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L7-CS7_group	R/W	0h	Dot L7-CS7 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L7-CS6_group	R/W	0h	Dot L7-CS6 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L7-CS5_group	R/W	0h	Dot L7-CS5 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L7-CS4_group	R/W	0h	Dot L7-CS4 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.38 Dot_grp_sel37 Register (Address = 31h) [Default = 0h]

Dot_grp_sel37 is shown in [Figure 2-50](#) and described in [Table 2-54](#).

Return to the [Summary Table](#).

Figure 2-50. Dot_grp_sel37 Register

7	6	5	4	3	2	1	0
Dot_L7-CS11_group		Dot_L7-CS10_group		Dot_L7-CS9_group		Dot_L7-CS8_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-54. Dot_grp_sel37 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L7-CS11_group	R/W	0h	Dot L7-CS11 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L7-CS10_group	R/W	0h	Dot L7-CS10 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L7-CS9_group	R/W	0h	Dot L7-CS9 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L7-CS8_group	R/W	0h	Dot L7-CS8 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.39 Dot_grp_sel38 Register (Address = 32h) [Default = 0h]

Dot_grp_sel38 is shown in [Figure 2-51](#) and described in [Table 2-55](#).

Return to the [Summary Table](#).

Figure 2-51. Dot_grp_sel38 Register

7	6	5	4	3	2	1	0
Dot_L7-CS15_group		Dot_L7-CS14_group		Dot_L7-CS13_group		Dot_L7-CS12_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-55. Dot_grp_sel38 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L7-CS15_group	R/W	0h	Dot L7-CS15 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L7-CS14_group	R/W	0h	Dot L7-CS14 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L7-CS13_group	R/W	0h	Dot L7-CS13 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L7-CS12_group	R/W	0h	Dot L7-CS12 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.40 Dot_grp_sel39 Register (Address = 33h) [Default = 0h]

Dot_grp_sel39 is shown in [Figure 2-52](#) and described in [Table 2-56](#).

Return to the [Summary Table](#).

Figure 2-52. Dot_grp_sel39 Register

7	6	5	4	3	2	1	0
RESERVED				Dot_L7-CS17_group		Dot_L7-CS16_group	
R-0h				R/W-0h		R/W-0h	

Table 2-56. Dot_grp_sel39 Register Field Descriptions

Bit	Field	Type	Default	Description
7-4	RESERVED	R	0h	Reserved
3-2	Dot_L7-CS17_group	R/W	0h	Dot L7-CS17 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L7-CS16_group	R/W	0h	Dot L7-CS16 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.41 Dot_grp_sel40 Register (Address = 34h) [Default = 0h]

Dot_grp_sel40 is shown in [Figure 2-53](#) and described in [Table 2-57](#).

Return to the [Summary Table](#).

Figure 2-53. Dot_grp_sel40 Register

7	6	5	4	3	2	1	0
Dot_L8-CS3_group		Dot_L8-CS2_group		Dot_L8-CS1_group		Dot_L8-CS0_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-57. Dot_grp_sel40 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L8-CS3_group	R/W	0h	Dot L8-CS3 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L8-CS2_group	R/W	0h	Dot L8-CS2 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L8-CS1_group	R/W	0h	Dot L8-CS1 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L8-CS0_group	R/W	0h	Dot L8-CS0 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.42 Dot_grp_sel41 Register (Address = 35h) [Default = 0h]

Dot_grp_sel41 is shown in [Figure 2-54](#) and described in [Table 2-58](#).

Return to the [Summary Table](#).

Figure 2-54. Dot_grp_sel41 Register

7	6	5	4	3	2	1	0
Dot_L8-CS7_group		Dot_L8-CS6_group		Dot_L8-CS5_group		Dot_L8-CS4_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-58. Dot_grp_sel41 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L8-CS7_group	R/W	0h	Dot L8-CS7 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L8-CS6_group	R/W	0h	Dot L8-CS6 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L8-CS5_group	R/W	0h	Dot L8-CS5 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L8-CS4_group	R/W	0h	Dot L8-CS4 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.43 Dot_grp_sel42 Register (Address = 36h) [Default = 0h]

Dot_grp_sel42 is shown in [Figure 2-55](#) and described in [Table 2-59](#).

Return to the [Summary Table](#).

Figure 2-55. Dot_grp_sel42 Register

7	6	5	4	3	2	1	0
Dot_L8-CS11_group		Dot_L8-CS10_group		Dot_L8-CS9_group		Dot_L8-CS8_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-59. Dot_grp_sel42 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L8-CS11_group	R/W	0h	Dot L8-CS11 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L8-CS10_group	R/W	0h	Dot L8-CS10 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

Table 2-59. Dot_grp_sel42 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
3-2	Dot_L8-CS9_group	R/W	0h	Dot L8-CS9 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L8-CS8_group	R/W	0h	Dot L8-CS8 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.44 Dot_grp_sel43 Register (Address = 37h) [Default = 0h]

Dot_grp_sel43 is shown in [Figure 2-56](#) and described in [Table 2-60](#).

Return to the [Summary Table](#).

Figure 2-56. Dot_grp_sel43 Register

7	6	5	4	3	2	1	0
Dot_L8-CS15_group		Dot_L8-CS14_group		Dot_L8-CS13_group		Dot_L8-CS12_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-60. Dot_grp_sel43 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L8-CS15_group	R/W	0h	Dot L8-CS15 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L8-CS14_group	R/W	0h	Dot L8-CS14 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L8-CS13_group	R/W	0h	Dot L8-CS13 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L8-CS12_group	R/W	0h	Dot L8-CS12 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.45 Dot_grp_sel44 Register (Address = 38h) [Default = 0h]

Dot_grp_sel44 is shown in [Figure 2-57](#) and described in [Table 2-61](#).

Return to the [Summary Table](#).

Figure 2-57. Dot_grp_sel44 Register

7	6	5	4	3	2	1	0
RESERVED				Dot_L8-CS17_group		Dot_L8-CS16_group	
R-0h				R/W-0h		R/W-0h	

Table 2-61. Dot_grp_sel44 Register Field Descriptions

Bit	Field	Type	Default	Description
7-4	RESERVED	R	0h	Reserved
3-2	Dot_L8-CS17_group	R/W	0h	Dot L8-CS17 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L8-CS16_group	R/W	0h	Dot L8-CS16 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.46 Dot_grp_sel45 Register (Address = 39h) [Default = 0h]

Dot_grp_sel45 is shown in [Figure 2-58](#) and described in [Table 2-62](#).

Return to the [Summary Table](#).

Figure 2-58. Dot_grp_sel45 Register

7	6	5	4	3	2	1	0
Dot_L9-CS3_group		Dot_L9-CS2_group		Dot_L9-CS1_group		Dot_L9-CS0_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-62. Dot_grp_sel45 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L9-CS3_group	R/W	0h	Dot L9-CS3 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L9-CS2_group	R/W	0h	Dot L9-CS2 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L9-CS1_group	R/W	0h	Dot L9-CS1 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L9-CS0_group	R/W	0h	Dot L9-CS0 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.47 Dot_grp_sel46 Register (Address = 3Ah) [Default = 0h]

Dot_grp_sel46 is shown in [Figure 2-59](#) and described in [Table 2-63](#).

Return to the [Summary Table](#).

Figure 2-59. Dot_grp_sel46 Register

7	6	5	4	3	2	1	0
Dot_L9-CS7_group		Dot_L9-CS6_group		Dot_L9-CS5_group		Dot_L9-CS4_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Figure 2-59. Dot_grp_sel46 Register (continued)
Table 2-63. Dot_grp_sel46 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L9-CS7_group	R/W	0h	Dot L9-CS7 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L9-CS6_group	R/W	0h	Dot L9-CS6 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L9-CS5_group	R/W	0h	Dot L9-CS5 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L9-CS4_group	R/W	0h	Dot L9-CS4 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.48 Dot_grp_sel47 Register (Address = 3Bh) [Default = 0h]

Dot_grp_sel47 is shown in [Figure 2-60](#) and described in [Table 2-64](#).

Return to the [Summary Table](#).

Figure 2-60. Dot_grp_sel47 Register

7	6	5	4	3	2	1	0
Dot_L9-CS11_group		Dot_L9-CS10_group		Dot_L9-CS9_group		Dot_L9-CS8_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-64. Dot_grp_sel47 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L9-CS11_group	R/W	0h	Dot L9-CS11 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L9-CS10_group	R/W	0h	Dot L9-CS10 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L9-CS9_group	R/W	0h	Dot L9-CS9 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L9-CS8_group	R/W	0h	Dot L9-CS8 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.49 Dot_grp_sel48 Register (Address = 3Ch) [Default = 0h]

Dot_grp_sel48 is shown in [Figure 2-61](#) and described in [Table 2-65](#).

Return to the [Summary Table](#).

Figure 2-61. Dot_grp_sel48 Register

7	6	5	4	3	2	1	0
Dot_L9-CS15_group		Dot_L9-CS14_group		Dot_L9-CS13_group		Dot_L9-CS12_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-65. Dot_grp_sel48 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L9-CS15_group	R/W	0h	Dot L9-CS15 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L9-CS14_group	R/W	0h	Dot L9-CS14 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L9-CS13_group	R/W	0h	Dot L9-CS13 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L9-CS12_group	R/W	0h	Dot L9-CS12 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.50 Dot_grp_sel49 Register (Address = 3Dh) [Default = 0h]

Dot_grp_sel49 is shown in [Figure 2-62](#) and described in [Table 2-66](#).

Return to the [Summary Table](#).

Figure 2-62. Dot_grp_sel49 Register

7	6	5	4	3	2	1	0
RESERVED				Dot_L9-CS17_group		Dot_L9-CS16_group	
R-0h				R/W-0h		R/W-0h	

Table 2-66. Dot_grp_sel49 Register Field Descriptions

Bit	Field	Type	Default	Description
7-4	RESERVED	R	0h	Reserved
3-2	Dot_L9-CS17_group	R/W	0h	Dot L9-CS17 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L9-CS16_group	R/W	0h	Dot L9-CS16 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.51 Dot_grp_sel50 Register (Address = 3Eh) [Default = 0h]

Dot_grp_sel50 is shown in [Figure 2-63](#) and described in [Table 2-67](#).

Return to the [Summary Table](#).

Figure 2-63. Dot_grp_sel50 Register

7	6	5	4	3	2	1	0
Dot_L10-CS3_group		Dot_L10-CS2_group		Dot_L10-CS1_group		Dot_L10-CS0_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-67. Dot_grp_sel50 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L10-CS3_group	R/W	0h	Dot L10-CS3 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L10-CS2_group	R/W	0h	Dot L10-CS2 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L10-CS1_group	R/W	0h	Dot L10-CS1 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L10-CS0_group	R/W	0h	Dot L10-CS0 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.52 Dot_grp_sel51 Register (Address = 3Fh) [Default = 0h]

Dot_grp_sel51 is shown in [Figure 2-64](#) and described in [Table 2-68](#).

Return to the [Summary Table](#).

Figure 2-64. Dot_grp_sel51 Register

7	6	5	4	3	2	1	0
Dot_L10-CS7_group		Dot_L10-CS6_group		Dot_L10-CS5_group		Dot_L10-CS4_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-68. Dot_grp_sel51 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L10-CS7_group	R/W	0h	Dot L10-CS7 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L10-CS6_group	R/W	0h	Dot L10-CS6 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

Table 2-68. Dot_grp_sel51 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
3-2	Dot_L10-CS5_group	R/W	0h	Dot L10-CS5 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L10-CS4_group	R/W	0h	Dot L10-CS4 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.53 Dot_grp_sel52 Register (Address = 40h) [Default = 0h]

Dot_grp_sel52 is shown in [Figure 2-65](#) and described in [Table 2-69](#).

Return to the [Summary Table](#).

Figure 2-65. Dot_grp_sel52 Register

7	6	5	4	3	2	1	0
Dot_L10-CS11_group		Dot_L10-CS10_group		Dot_L10-CS9_group		Dot_L10-CS8_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-69. Dot_grp_sel52 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L10-CS11_group	R/W	0h	Dot L10-CS11 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L10-CS10_group	R/W	0h	Dot L10-CS10 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L10-CS9_group	R/W	0h	Dot L10-CS9 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L10-CS8_group	R/W	0h	Dot L10-CS8 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.54 Dot_grp_sel53 Register (Address = 41h) [Default = 0h]

Dot_grp_sel53 is shown in [Figure 2-66](#) and described in [Table 2-70](#).

Return to the [Summary Table](#).

Figure 2-66. Dot_grp_sel53 Register

7	6	5	4	3	2	1	0
Dot_L10-CS15_group		Dot_L10-CS14_group		Dot_L10-CS13_group		Dot_L10-CS12_group	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	

Table 2-70. Dot_grp_sel53 Register Field Descriptions

Bit	Field	Type	Default	Description
7-6	Dot_L10-CS15_group	R/W	0h	Dot L10-CS15 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
5-4	Dot_L10-CS14_group	R/W	0h	Dot L10-CS14 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
3-2	Dot_L10-CS13_group	R/W	0h	Dot L10-CS13 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L10-CS12_group	R/W	0h	Dot L10-CS12 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.4.55 Dot_grp_sel54 Register (Address = 42h) [Default = 0h]

Dot_grp_sel54 is shown in [Figure 2-67](#) and described in [Table 2-71](#).

Return to the [Summary Table](#).

Figure 2-67. Dot_grp_sel54 Register

7	6	5	4	3	2	1	0
RESERVED				Dot_L10-CS17_group		Dot_L10-CS16_group	
R-0h				R/W-0h		R/W-0h	

Table 2-71. Dot_grp_sel54 Register Field Descriptions

Bit	Field	Type	Default	Description
7-4	RESERVED	R	0h	Reserved
3-2	Dot_L10-CS17_group	R/W	0h	Dot L10-CS17 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3
1-0	Dot_L10-CS16_group	R/W	0h	Dot L10-CS16 group PWM control setting 0h = No group 1h = Group 1 2h = Group 2 3h = Group 3

2.5 LED_DOT_ONOFF Registers

Table 2-72 lists the LED_DOT_ONOFF registers. All register offset addresses not listed in Table 2-72 should be considered as reserved locations and the register contents should not be modified.

LED Dot ON/OFF Configuration

Table 2-72. LED_DOT_ONOFF Registers

Address	Acronym	Register Name	Section
43h	Dot_onoff0	LED dot ON/OFF selection register 0	Go
44h	Dot_onoff1	LED dot ON/OFF selection register 1	Go
45h	Dot_onoff2	LED dot ON/OFF selection register 2	Go
46h	Dot_onoff3	LED dot ON/OFF selection register 3	Go
47h	Dot_onoff4	LED dot ON/OFF selection register 4	Go
48h	Dot_onoff5	LED dot ON/OFF selection register 5	Go
49h	Dot_onoff6	LED dot ON/OFF selection register 6	Go
4Ah	Dot_onoff7	LED dot ON/OFF selection register 7	Go
4Bh	Dot_onoff8	LED dot ON/OFF selection register 8	Go
4Ch	Dot_onoff9	LED dot ON/OFF selection register 9	Go
4Dh	Dot_onoff10	LED dot ON/OFF selection register 10	Go
4Eh	Dot_onoff11	LED dot ON/OFF selection register 11	Go
4Fh	Dot_onoff12	LED dot ON/OFF selection register 12	Go
50h	Dot_onoff13	LED dot ON/OFF selection register 13	Go
51h	Dot_onoff14	LED dot ON/OFF selection register 14	Go
52h	Dot_onoff15	LED dot ON/OFF selection register 15	Go
53h	Dot_onoff16	LED dot ON/OFF selection register 16	Go
54h	Dot_onoff17	LED dot ON/OFF selection register 17	Go
55h	Dot_onoff18	LED dot ON/OFF selection register 18	Go
56h	Dot_onoff19	LED dot ON/OFF selection register 19	Go
57h	Dot_onoff20	LED dot ON/OFF selection register 20	Go
58h	Dot_onoff21	LED dot ON/OFF selection register 21	Go
59h	Dot_onoff22	LED dot ON/OFF selection register 22	Go
5Ah	Dot_onoff23	LED dot ON/OFF selection register 23	Go
5Bh	Dot_onoff24	LED dot ON/OFF selection register 24	Go
5Ch	Dot_onoff25	LED dot ON/OFF selection register 25	Go
5Dh	Dot_onoff26	LED dot ON/OFF selection register 26	Go
5Eh	Dot_onoff27	LED dot ON/OFF selection register 27	Go
5Fh	Dot_onoff28	LED dot ON/OFF selection register 28	Go
60h	Dot_onoff29	LED dot ON/OFF selection register 29	Go
61h	Dot_onoff30	LED dot ON/OFF selection register 30	Go
62h	Dot_onoff31	LED dot ON/OFF selection register 31	Go
63h	Dot_onoff32	LED dot ON/OFF selection register 32	Go

2.5.1 Dot_onoff0 Register (Address = 43h) [Default = FFh]

Dot_onoff0 is shown in Figure 2-68 and described in Table 2-73.

Return to the [Summary Table](#).

Figure 2-68. Dot_onoff0 Register

7	6	5	4	3	2	1	0
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Figure 2-68. Dot_onoff0 Register (continued)

Dot_L0- CS7_onoff	Dot_L0- CS6_onoff	Dot_L0- CS5_onoff	Dot_L0- CS4_onoff	Dot_L0- CS3_onoff	Dot_L0- CS2_onoff	Dot_L0- CS1_onoff	Dot_L0- CS0_onoff
R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h

Table 2-73. Dot_onoff0 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L0-CS7_onoff	R/W	1h	LED dot L0-CS7 on/off setting 0h = Off 1h = On
6	Dot_L0-CS6_onoff	R/W	1h	LED dot L0-CS6 on/off setting 0h = Off 1h = On
5	Dot_L0-CS5_onoff	R/W	1h	LED dot L0-CS5 on/off setting 0h = Off 1h = On
4	Dot_L0-CS4_onoff	R/W	1h	LED dot L0-CS4 on/off setting 0h = Off 1h = On
3	Dot_L0-CS3_onoff	R/W	1h	LED dot L0-CS3 on/off setting 0h = Off 1h = On
2	Dot_L0-CS2_onoff	R/W	1h	LED dot L0-CS2 on/off setting 0h = Off 1h = On
1	Dot_L0-CS1_onoff	R/W	1h	LED dot L0-CS1 on/off setting 0h = Off 1h = On
0	Dot_L0-CS0_onoff	R/W	1h	LED dot L0-CS0 on/off setting 0h = Off 1h = On

2.5.2 Dot_onoff1 Register (Address = 44h) [Default = FFh]

Dot_onoff1 is shown in [Figure 2-69](#) and described in [Table 2-74](#).

Return to the [Summary Table](#).

Figure 2-69. Dot_onoff1 Register

7	6	5	4	3	2	1	0
Dot_L0- CS15_onoff	Dot_L0- CS14_onoff	Dot_L0- CS13_onoff	Dot_L0- CS12_onoff	Dot_L0- CS11_onoff	Dot_L0- CS10_onoff	Dot_L0- CS1_onoff	Dot_L0- CS0_onoff
R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h

Table 2-74. Dot_onoff1 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L0-CS15_onoff	R/W	1h	LED dot L0-CS15 on/off setting 0h = Off 1h = On
6	Dot_L0-CS14_onoff	R/W	1h	LED dot L0-CS14 on/off setting 0h = Off 1h = On
5	Dot_L0-CS13_onoff	R/W	1h	LED dot L0-CS13 on/off setting 0h = Off 1h = On

Table 2-74. Dot_onoff1 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
4	Dot_L0-CS12_onoff	R/W	1h	LED dot L0-CS12 on/off setting 0h = Off 1h = On
3	Dot_L0-CS11_onoff	R/W	1h	LED dot L0-CS11 on/off setting 0h = Off 1h = On
2	Dot_L0-CS10_onoff	R/W	1h	LED dot L0-CS10 on/off setting 0h = Off 1h = On
1	Dot_L0-CS1_onoff	R/W	1h	LED dot L0-CS9 on/off setting 0h = Off 1h = On
0	Dot_L0-CS0_onoff	R/W	1h	LED dot L0-CS8 on/off setting 0h = Off 1h = On

2.5.3 Dot_onoff2 Register (Address = 45h) [Default = 3h]

Dot_onoff2 is shown in [Figure 2-70](#) and described in [Table 2-75](#).

Return to the [Summary Table](#).

Figure 2-70. Dot_onoff2 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L0- CS17_onoff	Dot_L0- CS16_onoff
R-0h						R/W-1h	R/W-1h

Table 2-75. Dot_onoff2 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L0-CS17_onoff	R/W	1h	LED dot L0-CS17 on/off setting 0h = Off 1h = On
0	Dot_L0-CS16_onoff	R/W	1h	LED dot L0-CS16 on/off setting 0h = Off 1h = On

2.5.4 Dot_onoff3 Register (Address = 46h) [Default = FFh]

Dot_onoff3 is shown in [Figure 2-71](#) and described in [Table 2-76](#).

Return to the [Summary Table](#).

Figure 2-71. Dot_onoff3 Register

7	6	5	4	3	2	1	0
Dot_L1- CS7_onoff	Dot_L1- CS6_onoff	Dot_L1- CS5_onoff	Dot_L1- CS4_onoff	Dot_L1- CS3_onoff	Dot_L1- CS2_onoff	Dot_L1- CS1_onoff	Dot_L1- CS0_onoff
R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h

Table 2-76. Dot_onoff3 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L1-CS7_onoff	R/W	1h	LED dot L1-CS7 on/off setting 0h = Off 1h = On
6	Dot_L1-CS6_onoff	R/W	1h	LED dot L1-CS6 on/off setting 0h = Off 1h = On
5	Dot_L1-CS5_onoff	R/W	1h	LED dot L1-CS5 on/off setting 0h = Off 1h = On
4	Dot_L1-CS4_onoff	R/W	1h	LED dot L1-CS4 on/off setting 0h = Off 1h = On
3	Dot_L1-CS3_onoff	R/W	1h	LED dot L1-CS3 on/off setting 0h = Off 1h = On
2	Dot_L1-CS2_onoff	R/W	1h	LED dot L1-CS2 on/off setting 0h = Off 1h = On
1	Dot_L1-CS1_onoff	R/W	1h	LED dot L1-CS1 on/off setting 0h = Off 1h = On
0	Dot_L1-CS0_onoff	R/W	1h	LED dot L1-CS0 on/off setting 0h = Off 1h = On

2.5.5 Dot_onoff4 Register (Address = 47h) [Default = FFh]

Dot_onoff4 is shown in [Figure 2-72](#) and described in [Table 2-77](#).

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Figure 2-72. Dot_onoff4 Register

7	6	5	4	3	2	1	0
Dot_L1-CS15_onoff	Dot_L1-CS14_onoff	Dot_L1-CS13_onoff	Dot_L1-CS12_onoff	Dot_L1-CS11_onoff	Dot_L1-CS10_onoff	Dot_L1-CS1_onoff	Dot_L1-CS0_onoff
R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h

Table 2-77. Dot_onoff4 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L1-CS15_onoff	R/W	1h	LED dot L1-CS15 on/off setting 0h = Off 1h = On
6	Dot_L1-CS14_onoff	R/W	1h	LED dot L1-CS14 on/off setting 0h = Off 1h = On
5	Dot_L1-CS13_onoff	R/W	1h	LED dot L1-CS13 on/off setting 0h = Off 1h = On
4	Dot_L1-CS12_onoff	R/W	1h	LED dot L1-CS12 on/off setting 0h = Off 1h = On
3	Dot_L1-CS11_onoff	R/W	1h	LED dot L1-CS11 on/off setting 0h = Off 1h = On

Table 2-77. Dot_onoff4 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
2	Dot_L1-CS10_onoff	R/W	1h	LED dot L1-CS10 on/off setting 0h = Off 1h = On
1	Dot_L1-CS9_onoff	R/W	1h	LED dot L1-CS9 on/off setting 0h = Off 1h = On
0	Dot_L1-CS8_onoff	R/W	1h	LED dot L1-CS8 on/off setting 0h = Off 1h = On

2.5.6 Dot_onoff5 Register (Address = 48h) [Default = 3h]

Dot_onoff5 is shown in [Figure 2-73](#) and described in [Table 2-78](#).

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Figure 2-73. Dot_onoff5 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L1-CS17_onoff	Dot_L1-CS16_onoff
R-0h						R/W-1h	R/W-1h

Table 2-78. Dot_onoff5 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L1-CS17_onoff	R/W	1h	LED dot L1-CS17 on/off setting 0h = Off 1h = On
0	Dot_L1-CS16_onoff	R/W	1h	LED dot L1-CS16 on/off setting 0h = Off 1h = On

2.5.7 Dot_onoff6 Register (Address = 49h) [Default = FFh]

Dot_onoff6 is shown in [Figure 2-74](#) and described in [Table 2-79](#).

Return to the [Summary Table](#).

Figure 2-74. Dot_onoff6 Register

7	6	5	4	3	2	1	0
Dot_L2-CS7_onoff	Dot_L2-CS6_onoff	Dot_L2-CS5_onoff	Dot_L2-CS4_onoff	Dot_L2-CS3_onoff	Dot_L2-CS2_onoff	Dot_L2-CS1_onoff	Dot_L2-CS0_onoff
R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h

Table 2-79. Dot_onoff6 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L2-CS7_onoff	R/W	1h	LED dot L2-CS7 on/off setting 0h = Off 1h = On
6	Dot_L2-CS6_onoff	R/W	1h	LED dot L2-CS6 on/off setting 0h = Off 1h = On

Table 2-79. Dot_onoff6 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
5	Dot_L2-CS5_onoff	R/W	1h	LED dot L2-CS5 on/off setting 0h = Off 1h = On
4	Dot_L2-CS4_onoff	R/W	1h	LED dot L2-CS4 on/off setting 0h = Off 1h = On
3	Dot_L2-CS3_onoff	R/W	1h	LED dot L2-CS3 on/off setting 0h = Off 1h = On
2	Dot_L2-CS2_onoff	R/W	1h	LED dot L2-CS2 on/off setting 0h = Off 1h = On
1	Dot_L2-CS1_onoff	R/W	1h	LED dot L2-CS1 on/off setting 0h = Off 1h = On
0	Dot_L2-CS0_onoff	R/W	1h	LED dot L2-CS0 on/off setting 0h = Off 1h = On

2.5.8 Dot_onoff7 Register (Address = 4Ah) [Default = FFh]

Dot_onoff7 is shown in [Figure 2-75](#) and described in [Table 2-80](#).

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Figure 2-75. Dot_onoff7 Register

7	6	5	4	3	2	1	0
Dot_L2-CS15_onoff	Dot_L2-CS14_onoff	Dot_L2-CS13_onoff	Dot_L2-CS12_onoff	Dot_L2-CS11_onoff	Dot_L2-CS10_onoff	Dot_L2-CS1_onoff	Dot_L2-CS0_onoff
R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h

Table 2-80. Dot_onoff7 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L2-CS15_onoff	R/W	1h	LED dot L2-CS15 on/off setting 0h = Off 1h = On
6	Dot_L2-CS14_onoff	R/W	1h	LED dot L2-CS14 on/off setting 0h = Off 1h = On
5	Dot_L2-CS13_onoff	R/W	1h	LED dot L2-CS13 on/off setting 0h = Off 1h = On
4	Dot_L2-CS12_onoff	R/W	1h	LED dot L2-CS12 on/off setting 0h = Off 1h = On
3	Dot_L2-CS11_onoff	R/W	1h	LED dot L2-CS11 on/off setting 0h = Off 1h = On
2	Dot_L2-CS10_onoff	R/W	1h	LED dot L2-CS10 on/off setting 0h = Off 1h = On
1	Dot_L2-CS1_onoff	R/W	1h	LED dot L2-CS9 on/off setting 0h = Off 1h = On

Table 2-80. Dot_onoff7 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
0	Dot_L2-CS0_onoff	R/W	1h	LED dot L2-CS8 on/off setting 0h = Off 1h = On

2.5.9 Dot_onoff8 Register (Address = 4Bh) [Default = 3h]

Dot_onoff8 is shown in [Figure 2-76](#) and described in [Table 2-81](#).

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Figure 2-76. Dot_onoff8 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L2-CS17_onoff	Dot_L2-CS16_onoff
R-0h						R/W-1h	R/W-1h

Table 2-81. Dot_onoff8 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L2-CS17_onoff	R/W	1h	LED dot L2-CS17 on/off setting 0h = Off 1h = On
0	Dot_L2-CS16_onoff	R/W	1h	LED dot L2-CS16 on/off setting 0h = Off 1h = On

2.5.10 Dot_onoff9 Register (Address = 4Ch) [Default = FFh]

Dot_onoff9 is shown in [Figure 2-77](#) and described in [Table 2-82](#).

Return to the [Summary Table](#).

Figure 2-77. Dot_onoff9 Register

7	6	5	4	3	2	1	0
Dot_L3-CS7_onoff	Dot_L3-CS6_onoff	Dot_L3-CS5_onoff	Dot_L3-CS4_onoff	Dot_L3-CS3_onoff	Dot_L3-CS2_onoff	Dot_L3-CS1_onoff	Dot_L3-CS0_onoff
R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h

Table 2-82. Dot_onoff9 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L3-CS7_onoff	R/W	1h	LED dot L3-CS7 on/off setting 0h = Off 1h = On
6	Dot_L3-CS6_onoff	R/W	1h	LED dot L3-CS6 on/off setting 0h = Off 1h = On
5	Dot_L3-CS5_onoff	R/W	1h	LED dot L3-CS5 on/off setting 0h = Off 1h = On
4	Dot_L3-CS4_onoff	R/W	1h	LED dot L3-CS4 on/off setting 0h = Off 1h = On

Table 2-82. Dot_onoff9 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
3	Dot_L3-CS3_onoff	R/W	1h	LED dot L3-CS3 on/off setting 0h = Off 1h = On
2	Dot_L3-CS2_onoff	R/W	1h	LED dot L3-CS2 on/off setting 0h = Off 1h = On
1	Dot_L3-CS1_onoff	R/W	1h	LED dot L3-CS1 on/off setting 0h = Off 1h = On
0	Dot_L3-CS0_onoff	R/W	1h	LED dot L3-CS0 on/off setting 0h = Off 1h = On

2.5.11 Dot_onoff10 Register (Address = 4Dh) [Default = FFh]

Dot_onoff10 is shown in [Figure 2-78](#) and described in [Table 2-83](#).

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Figure 2-78. Dot_onoff10 Register

7	6	5	4	3	2	1	0
Dot_L3-CS15_onoff	Dot_L3-CS14_onoff	Dot_L3-CS13_onoff	Dot_L3-CS12_onoff	Dot_L3-CS11_onoff	Dot_L3-CS10_onoff	Dot_L3-CS1_onoff	Dot_L3-CS0_onoff
R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h

Table 2-83. Dot_onoff10 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L3-CS15_onoff	R/W	1h	LED dot L3-CS15 on/off setting 0h = Off 1h = On
6	Dot_L3-CS14_onoff	R/W	1h	LED dot L3-CS14 on/off setting 0h = Off 1h = On
5	Dot_L3-CS13_onoff	R/W	1h	LED dot L3-CS13 on/off setting 0h = Off 1h = On
4	Dot_L3-CS12_onoff	R/W	1h	LED dot L3-CS12 on/off setting 0h = Off 1h = On
3	Dot_L3-CS11_onoff	R/W	1h	LED dot L3-CS11 on/off setting 0h = Off 1h = On
2	Dot_L3-CS10_onoff	R/W	1h	LED dot L3-CS10 on/off setting 0h = Off 1h = On
1	Dot_L3-CS1_onoff	R/W	1h	LED dot L3-CS9 on/off setting 0h = Off 1h = On
0	Dot_L3-CS0_onoff	R/W	1h	LED dot L3-CS8 on/off setting 0h = Off 1h = On

2.5.12 Dot_onoff11 Register (Address = 4Eh) [Default = 3h]

Dot_onoff11 is shown in [Figure 2-79](#) and described in [Table 2-84](#).

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Figure 2-79. Dot_onoff11 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L3- CS17_onoff	Dot_L3- CS16_onoff
R-0h						R/W-1h	R/W-1h

Table 2-84. Dot_onoff11 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L3-CS17_onoff	R/W	1h	LED dot L3-CS17 on/off setting 0h = Off 1h = On
0	Dot_L3-CS16_onoff	R/W	1h	LED dot L3-CS16 on/off setting 0h = Off 1h = On

2.5.13 Dot_onoff12 Register (Address = 4Fh) [Default = FFh]

Dot_onoff12 is shown in [Figure 2-80](#) and described in [Table 2-85](#).

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Figure 2-80. Dot_onoff12 Register

7	6	5	4	3	2	1	0
Dot_L4- CS7_onoff	Dot_L4- CS6_onoff	Dot_L4- CS5_onoff	Dot_L4- CS4_onoff	Dot_L4- CS3_onoff	Dot_L4- CS2_onoff	Dot_L4- CS1_onoff	Dot_L4- CS0_onoff
R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h

Table 2-85. Dot_onoff12 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L4-CS7_onoff	R/W	1h	LED dot L4-CS7 on/off setting 0h = Off 1h = On
6	Dot_L4-CS6_onoff	R/W	1h	LED dot L4-CS6 on/off setting 0h = Off 1h = On
5	Dot_L4-CS5_onoff	R/W	1h	LED dot L4-CS5 on/off setting 0h = Off 1h = On
4	Dot_L4-CS4_onoff	R/W	1h	LED dot L4-CS4 on/off setting 0h = Off 1h = On
3	Dot_L4-CS3_onoff	R/W	1h	LED dot L4-CS3 on/off setting 0h = Off 1h = On
2	Dot_L4-CS2_onoff	R/W	1h	LED dot L4-CS2 on/off setting 0h = Off 1h = On
1	Dot_L4-CS1_onoff	R/W	1h	LED dot L4-CS1 on/off setting 0h = Off 1h = On
0	Dot_L4-CS0_onoff	R/W	1h	LED dot L4-CS0 on/off setting 0h = Off 1h = On

2.5.14 Dot_onoff13 Register (Address = 50h) [Default = FFh]

Dot_onoff13 is shown in [Figure 2-81](#) and described in [Table 2-86](#).

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Figure 2-81. Dot_onoff13 Register

7	6	5	4	3	2	1	0
Dot_L4-CS15_onoff	Dot_L4-CS14_onoff	Dot_L4-CS13_onoff	Dot_L4-CS12_onoff	Dot_L4-CS11_onoff	Dot_L4-CS10_onoff	Dot_L4-CS1_onoff	Dot_L4-CS0_onoff
R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h

Table 2-86. Dot_onoff13 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L4-CS15_onoff	R/W	1h	LED dot L4-CS15 on/off setting 0h = Off 1h = On
6	Dot_L4-CS14_onoff	R/W	1h	LED dot L4-CS14 on/off setting 0h = Off 1h = On
5	Dot_L4-CS13_onoff	R/W	1h	LED dot L4-CS13 on/off setting 0h = Off 1h = On
4	Dot_L4-CS12_onoff	R/W	1h	LED dot L4-CS12 on/off setting 0h = Off 1h = On
3	Dot_L4-CS11_onoff	R/W	1h	LED dot L4-CS11 on/off setting 0h = Off 1h = On
2	Dot_L4-CS10_onoff	R/W	1h	LED dot L4-CS10 on/off setting 0h = Off 1h = On
1	Dot_L4-CS1_onoff	R/W	1h	LED dot L4-CS9 on/off setting 0h = Off 1h = On
0	Dot_L4-CS0_onoff	R/W	1h	LED dot L4-CS8 on/off setting 0h = Off 1h = On

2.5.15 Dot_onoff14 Register (Address = 51h) [Default = 3h]

Dot_onoff14 is shown in [Figure 2-82](#) and described in [Table 2-87](#).

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Figure 2-82. Dot_onoff14 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L4-CS17_onoff	Dot_L4-CS16_onoff
R-0h						R/W-1h	R/W-1h

Table 2-87. Dot_onoff14 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L4-CS17_onoff	R/W	1h	LED dot L4-CS17 on/off setting 0h = Off 1h = On

Table 2-87. Dot_onoff14 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
0	Dot_L4-CS16_onoff	R/W	1h	LED dot L4-CS16 on/off setting 0h = Off 1h = On

2.5.16 Dot_onoff15 Register (Address = 52h) [Default = FFh]

Dot_onoff15 is shown in [Figure 2-83](#) and described in [Table 2-88](#).

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Figure 2-83. Dot_onoff15 Register

7	6	5	4	3	2	1	0
Dot_L5-CS7_onoff	Dot_L5-CS6_onoff	Dot_L5-CS5_onoff	Dot_L5-CS4_onoff	Dot_L5-CS3_onoff	Dot_L5-CS2_onoff	Dot_L5-CS1_onoff	Dot_L5-CS0_onoff
R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h

Table 2-88. Dot_onoff15 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L5-CS7_onoff	R/W	1h	LED dot L5-CS7 on/off setting 0h = Off 1h = On
6	Dot_L5-CS6_onoff	R/W	1h	LED dot L5-CS6 on/off setting 0h = Off 1h = On
5	Dot_L5-CS5_onoff	R/W	1h	LED dot L5-CS5 on/off setting 0h = Off 1h = On
4	Dot_L5-CS4_onoff	R/W	1h	LED dot L5-CS4 on/off setting 0h = Off 1h = On
3	Dot_L5-CS3_onoff	R/W	1h	LED dot L5-CS3 on/off setting 0h = Off 1h = On
2	Dot_L5-CS2_onoff	R/W	1h	LED dot L5-CS2 on/off setting 0h = Off 1h = On
1	Dot_L5-CS1_onoff	R/W	1h	LED dot L5-CS1 on/off setting 0h = Off 1h = On
0	Dot_L5-CS0_onoff	R/W	1h	LED dot L5-CS0 on/off setting 0h = Off 1h = On

2.5.17 Dot_onoff16 Register (Address = 53h) [Default = FFh]

Dot_onoff16 is shown in [Figure 2-84](#) and described in [Table 2-89](#).

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Figure 2-84. Dot_onoff16 Register

7	6	5	4	3	2	1	0
Dot_L5-CS15_onoff	Dot_L5-CS14_onoff	Dot_L5-CS13_onoff	Dot_L5-CS12_onoff	Dot_L5-CS11_onoff	Dot_L5-CS10_onoff	Dot_L5-CS1_onoff	Dot_L5-CS0_onoff
R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h

Table 2-89. Dot_onoff16 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L5-CS15_onoff	R/W	1h	LED dot L5-CS15 on/off setting 0h = Off 1h = On
6	Dot_L5-CS14_onoff	R/W	1h	LED dot L5-CS14 on/off setting 0h = Off 1h = On
5	Dot_L5-CS13_onoff	R/W	1h	LED dot L5-CS13 on/off setting 0h = Off 1h = On
4	Dot_L5-CS12_onoff	R/W	1h	LED dot L5-CS12 on/off setting 0h = Off 1h = On
3	Dot_L5-CS11_onoff	R/W	1h	LED dot L5-CS11 on/off setting 0h = Off 1h = On
2	Dot_L5-CS10_onoff	R/W	1h	LED dot L5-CS10 on/off setting 0h = Off 1h = On
1	Dot_L5-CS1_onoff	R/W	1h	LED dot L5-CS9 on/off setting 0h = Off 1h = On
0	Dot_L5-CS0_onoff	R/W	1h	LED dot L5-CS8 on/off setting 0h = Off 1h = On

2.5.18 Dot_onoff17 Register (Address = 54h) [Default = 3h]

Dot_onoff17 is shown in [Figure 2-85](#) and described in [Table 2-90](#).

Return to the [Summary Table](#).

Figure 2-85. Dot_onoff17 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L5-CS17_onoff	Dot_L5-CS16_onoff
R-0h						R/W-1h	R/W-1h

Table 2-90. Dot_onoff17 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L5-CS17_onoff	R/W	1h	LED dot L5-CS17 on/off setting 0h = Off 1h = On
0	Dot_L5-CS16_onoff	R/W	1h	LED dot L5-CS16 on/off setting 0h = Off 1h = On

2.5.19 Dot_onoff18 Register (Address = 55h) [Default = FFh]

Dot_onoff18 is shown in [Figure 2-86](#) and described in [Table 2-91](#).

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Figure 2-86. Dot_onoff18 Register

7	6	5	4	3	2	1	0
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Figure 2-86. Dot_onoff18 Register (continued)

Dot_L6-CS7_onoff	Dot_L6-CS6_onoff	Dot_L6-CS5_onoff	Dot_L6-CS4_onoff	Dot_L6-CS3_onoff	Dot_L6-CS2_onoff	Dot_L6-CS1_onoff	Dot_L6-CS0_onoff
R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h

Table 2-91. Dot_onoff18 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L6-CS7_onoff	R/W	1h	LED dot L6-CS7 on/off setting 0h = Off 1h = On
6	Dot_L6-CS6_onoff	R/W	1h	LED dot L6-CS6 on/off setting 0h = Off 1h = On
5	Dot_L6-CS5_onoff	R/W	1h	LED dot L6-CS5 on/off setting 0h = Off 1h = On
4	Dot_L6-CS4_onoff	R/W	1h	LED dot L6-CS4 on/off setting 0h = Off 1h = On
3	Dot_L6-CS3_onoff	R/W	1h	LED dot L6-CS3 on/off setting 0h = Off 1h = On
2	Dot_L6-CS2_onoff	R/W	1h	LED dot L6-CS2 on/off setting 0h = Off 1h = On
1	Dot_L6-CS1_onoff	R/W	1h	LED dot L6-CS1 on/off setting 0h = Off 1h = On
0	Dot_L6-CS0_onoff	R/W	1h	LED dot L6-CS0 on/off setting 0h = Off 1h = On

2.5.20 Dot_onoff19 Register (Address = 56h) [Default = FFh]

Dot_onoff19 is shown in [Figure 2-87](#) and described in [Table 2-92](#).

Return to the [Summary Table](#).

Figure 2-87. Dot_onoff19 Register

7	6	5	4	3	2	1	0
Dot_L6-CS15_onoff	Dot_L6-CS14_onoff	Dot_L6-CS13_onoff	Dot_L6-CS12_onoff	Dot_L6-CS11_onoff	Dot_L6-CS10_onoff	Dot_L6-CS1_onoff	Dot_L6-CS0_onoff
R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h

Table 2-92. Dot_onoff19 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L6-CS15_onoff	R/W	1h	LED dot L6-CS15 on/off setting 0h = Off 1h = On
6	Dot_L6-CS14_onoff	R/W	1h	LED dot L6-CS14 on/off setting 0h = Off 1h = On
5	Dot_L6-CS13_onoff	R/W	1h	LED dot L6-CS13 on/off setting 0h = Off 1h = On

Table 2-92. Dot_onoff19 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
4	Dot_L6-CS12_onoff	R/W	1h	LED dot L6-CS12 on/off setting 0h = Off 1h = On
3	Dot_L6-CS11_onoff	R/W	1h	LED dot L6-CS11 on/off setting 0h = Off 1h = On
2	Dot_L6-CS10_onoff	R/W	1h	LED dot L6-CS10 on/off setting 0h = Off 1h = On
1	Dot_L6-CS1_onoff	R/W	1h	LED dot L6-CS9 on/off setting 0h = Off 1h = On
0	Dot_L6-CS0_onoff	R/W	1h	LED dot L6-CS8 on/off setting 0h = Off 1h = On

2.5.21 Dot_onoff20 Register (Address = 57h) [Default = 3h]

Dot_onoff20 is shown in [Figure 2-88](#) and described in [Table 2-93](#).

Return to the [Summary Table](#).

Figure 2-88. Dot_onoff20 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L6-CS17_onoff	Dot_L6-CS16_onoff
R-0h						R/W-1h	R/W-1h

Table 2-93. Dot_onoff20 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L6-CS17_onoff	R/W	1h	LED dot L6-CS17 on/off setting 0h = Off 1h = On
0	Dot_L6-CS16_onoff	R/W	1h	LED dot L6-CS16 on/off setting 0h = Off 1h = On

2.5.22 Dot_onoff21 Register (Address = 58h) [Default = FFh]

Dot_onoff21 is shown in [Figure 2-89](#) and described in [Table 2-94](#).

Return to the [Summary Table](#).

Figure 2-89. Dot_onoff21 Register

7	6	5	4	3	2	1	0
Dot_L7-CS7_onoff	Dot_L7-CS6_onoff	Dot_L7-CS5_onoff	Dot_L7-CS4_onoff	Dot_L7-CS3_onoff	Dot_L7-CS2_onoff	Dot_L7-CS1_onoff	Dot_L7-CS0_onoff
R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h

Table 2-94. Dot_onoff21 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L7-CS7_onoff	R/W	1h	LED dot L7-CS7 on/off setting 0h = Off 1h = On
6	Dot_L7-CS6_onoff	R/W	1h	LED dot L7-CS6 on/off setting 0h = Off 1h = On
5	Dot_L7-CS5_onoff	R/W	1h	LED dot L7-CS5 on/off setting 0h = Off 1h = On
4	Dot_L7-CS4_onoff	R/W	1h	LED dot L7-CS4 on/off setting 0h = Off 1h = On
3	Dot_L7-CS3_onoff	R/W	1h	LED dot L7-CS3 on/off setting 0h = Off 1h = On
2	Dot_L7-CS2_onoff	R/W	1h	LED dot L7-CS2 on/off setting 0h = Off 1h = On
1	Dot_L7-CS1_onoff	R/W	1h	LED dot L7-CS1 on/off setting 0h = Off 1h = On
0	Dot_L7-CS0_onoff	R/W	1h	LED dot L7-CS0 on/off setting 0h = Off 1h = On

2.5.23 Dot_onoff22 Register (Address = 59h) [Default = FFh]

Dot_onoff22 is shown in [Figure 2-90](#) and described in [Table 2-95](#).

Return to the [Summary Table](#).

Figure 2-90. Dot_onoff22 Register

7	6	5	4	3	2	1	0
Dot_L7-CS15_onoff	Dot_L7-CS14_onoff	Dot_L7-CS13_onoff	Dot_L7-CS12_onoff	Dot_L7-CS11_onoff	Dot_L7-CS10_onoff	Dot_L7-CS1_onoff	Dot_L7-CS0_onoff
R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h

Table 2-95. Dot_onoff22 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L7-CS15_onoff	R/W	1h	LED dot L7-CS15 on/off setting 0h = Off 1h = On
6	Dot_L7-CS14_onoff	R/W	1h	LED dot L7-CS14 on/off setting 0h = Off 1h = On
5	Dot_L7-CS13_onoff	R/W	1h	LED dot L7-CS13 on/off setting 0h = Off 1h = On
4	Dot_L7-CS12_onoff	R/W	1h	LED dot L7-CS12 on/off setting 0h = Off 1h = On
3	Dot_L7-CS11_onoff	R/W	1h	LED dot L7-CS11 on/off setting 0h = Off 1h = On

Table 2-95. Dot_onoff22 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
2	Dot_L7-CS10_onoff	R/W	1h	LED dot L7-CS10 on/off setting 0h = Off 1h = On
1	Dot_L7-CS9_onoff	R/W	1h	LED dot L7-CS9 on/off setting 0h = Off 1h = On
0	Dot_L7-CS8_onoff	R/W	1h	LED dot L7-CS8 on/off setting 0h = Off 1h = On

2.5.24 Dot_onoff23 Register (Address = 5Ah) [Default = 3h]

Dot_onoff23 is shown in [Figure 2-91](#) and described in [Table 2-96](#).

Return to the [Summary Table](#).

Figure 2-91. Dot_onoff23 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L7- CS17_onoff	Dot_L7- CS16_onoff
R-0h						R/W-1h	R/W-1h

Table 2-96. Dot_onoff23 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L7-CS17_onoff	R/W	1h	LED dot L7-CS17 on/off setting 0h = Off 1h = On
0	Dot_L7-CS16_onoff	R/W	1h	LED dot L7-CS16 on/off setting 0h = Off 1h = On

2.5.25 Dot_onoff24 Register (Address = 5Bh) [Default = FFh]

Dot_onoff24 is shown in [Figure 2-92](#) and described in [Table 2-97](#).

Return to the [Summary Table](#).

Figure 2-92. Dot_onoff24 Register

7	6	5	4	3	2	1	0
Dot_L8- CS7_onoff	Dot_L8- CS6_onoff	Dot_L8- CS5_onoff	Dot_L8- CS4_onoff	Dot_L8- CS3_onoff	Dot_L8- CS2_onoff	Dot_L8- CS1_onoff	Dot_L8- CS0_onoff
R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h

Table 2-97. Dot_onoff24 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L8-CS7_onoff	R/W	1h	LED dot L8-CS7 on/off setting 0h = Off 1h = On
6	Dot_L8-CS6_onoff	R/W	1h	LED dot L8-CS6 on/off setting 0h = Off 1h = On

Table 2-97. Dot_onoff24 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
5	Dot_L8-CS5_onoff	R/W	1h	LED dot L8-CS5 on/off setting 0h = Off 1h = On
4	Dot_L8-CS4_onoff	R/W	1h	LED dot L8-CS4 on/off setting 0h = Off 1h = On
3	Dot_L8-CS3_onoff	R/W	1h	LED dot L8-CS3 on/off setting 0h = Off 1h = On
2	Dot_L8-CS2_onoff	R/W	1h	LED dot L8-CS2 on/off setting 0h = Off 1h = On
1	Dot_L8-CS1_onoff	R/W	1h	LED dot L8-CS1 on/off setting 0h = Off 1h = On
0	Dot_L8-CS0_onoff	R/W	1h	LED dot L8-CS0 on/off setting 0h = Off 1h = On

2.5.26 Dot_onoff25 Register (Address = 5Ch) [Default = FFh]

Dot_onoff25 is shown in [Figure 2-93](#) and described in [Table 2-98](#).

Return to the [Summary Table](#).

Figure 2-93. Dot_onoff25 Register

7	6	5	4	3	2	1	0
Dot_L8-CS15_onoff	Dot_L8-CS14_onoff	Dot_L8-CS13_onoff	Dot_L8-CS12_onoff	Dot_L8-CS11_onoff	Dot_L8-CS10_onoff	Dot_L8-CS1_onoff	Dot_L8-CS0_onoff
R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h

Table 2-98. Dot_onoff25 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L8-CS15_onoff	R/W	1h	LED dot L8-CS15 on/off setting 0h = Off 1h = On
6	Dot_L8-CS14_onoff	R/W	1h	LED dot L8-CS14 on/off setting 0h = Off 1h = On
5	Dot_L8-CS13_onoff	R/W	1h	LED dot L8-CS13 on/off setting 0h = Off 1h = On
4	Dot_L8-CS12_onoff	R/W	1h	LED dot L8-CS12 on/off setting 0h = Off 1h = On
3	Dot_L8-CS11_onoff	R/W	1h	LED dot L8-CS11 on/off setting 0h = Off 1h = On
2	Dot_L8-CS10_onoff	R/W	1h	LED dot L8-CS10 on/off setting 0h = Off 1h = On
1	Dot_L8-CS1_onoff	R/W	1h	LED dot L8-CS9 on/off setting 0h = Off 1h = On

Table 2-98. Dot_onoff25 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
0	Dot_L8-CS0_onoff	R/W	1h	LED dot L8-CS8 on/off setting 0h = Off 1h = On

2.5.27 Dot_onoff26 Register (Address = 5Dh) [Default = 3h]

Dot_onoff26 is shown in [Figure 2-94](#) and described in [Table 2-99](#).

Return to the [Summary Table](#).

Figure 2-94. Dot_onoff26 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L8-CS17_onoff	Dot_L8-CS16_onoff
R-0h						R/W-1h	R/W-1h

Table 2-99. Dot_onoff26 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L8-CS17_onoff	R/W	1h	LED dot L8-CS17 on/off setting 0h = Off 1h = On
0	Dot_L8-CS16_onoff	R/W	1h	LED dot L8-CS16 on/off setting 0h = Off 1h = On

2.5.28 Dot_onoff27 Register (Address = 5Eh) [Default = FFh]

Dot_onoff27 is shown in [Figure 2-95](#) and described in [Table 2-100](#).

Return to the [Summary Table](#).

Figure 2-95. Dot_onoff27 Register

7	6	5	4	3	2	1	0
Dot_L9-CS7_onoff	Dot_L9-CS6_onoff	Dot_L9-CS5_onoff	Dot_L9-CS4_onoff	Dot_L9-CS3_onoff	Dot_L9-CS2_onoff	Dot_L9-CS1_onoff	Dot_L9-CS0_onoff
R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h

Table 2-100. Dot_onoff27 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L9-CS7_onoff	R/W	1h	LED dot L9-CS7 on/off setting 0h = Off 1h = On
6	Dot_L9-CS6_onoff	R/W	1h	LED dot L9-CS6 on/off setting 0h = Off 1h = On
5	Dot_L9-CS5_onoff	R/W	1h	LED dot L9-CS5 on/off setting 0h = Off 1h = On
4	Dot_L9-CS4_onoff	R/W	1h	LED dot L9-CS4 on/off setting 0h = Off 1h = On

Table 2-100. Dot_onoff27 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
3	Dot_L9-CS3_onoff	R/W	1h	LED dot L9-CS3 on/off setting 0h = Off 1h = On
2	Dot_L9-CS2_onoff	R/W	1h	LED dot L9-CS2 on/off setting 0h = Off 1h = On
1	Dot_L9-CS1_onoff	R/W	1h	LED dot L9-CS1 on/off setting 0h = Off 1h = On
0	Dot_L9-CS0_onoff	R/W	1h	LED dot L9-CS0 on/off setting 0h = Off 1h = On

2.5.29 Dot_onoff28 Register (Address = 5Fh) [Default = FFh]

Dot_onoff28 is shown in [Figure 2-96](#) and described in [Table 2-101](#).

Return to the [Summary Table](#).

Figure 2-96. Dot_onoff28 Register

7	6	5	4	3	2	1	0
Dot_L9-CS15_onoff	Dot_L9-CS14_onoff	Dot_L9-CS13_onoff	Dot_L9-CS12_onoff	Dot_L9-CS11_onoff	Dot_L9-CS10_onoff	Dot_L9-CS1_onoff	Dot_L9-CS0_onoff
R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h

Table 2-101. Dot_onoff28 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L9-CS15_onoff	R/W	1h	LED dot L9-CS15 on/off setting 0h = Off 1h = On
6	Dot_L9-CS14_onoff	R/W	1h	LED dot L9-CS14 on/off setting 0h = Off 1h = On
5	Dot_L9-CS13_onoff	R/W	1h	LED dot L9-CS13 on/off setting 0h = Off 1h = On
4	Dot_L9-CS12_onoff	R/W	1h	LED dot L9-CS12 on/off setting 0h = Off 1h = On
3	Dot_L9-CS11_onoff	R/W	1h	LED dot L9-CS11 on/off setting 0h = Off 1h = On
2	Dot_L9-CS10_onoff	R/W	1h	LED dot L9-CS10 on/off setting 0h = Off 1h = On
1	Dot_L9-CS1_onoff	R/W	1h	LED dot L9-CS9 on/off setting 0h = Off 1h = On
0	Dot_L9-CS0_onoff	R/W	1h	LED dot L9-CS8 on/off setting 0h = Off 1h = On

2.5.30 Dot_onoff29 Register (Address = 60h) [Default = 3h]

Dot_onoff29 is shown in [Figure 2-97](#) and described in [Table 2-102](#).

Return to the [Summary Table](#).

Figure 2-97. Dot_onoff29 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L9- CS17_onoff	Dot_L9- CS16_onoff
R-0h						R/W-1h	R/W-1h

Table 2-102. Dot_onoff29 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L9-CS17_onoff	R/W	1h	LED dot L9-CS17 on/off setting 0h = Off 1h = On
0	Dot_L9-CS16_onoff	R/W	1h	LED dot L9-CS16 on/off setting 0h = Off 1h = On

2.5.31 Dot_onoff30 Register (Address = 61h) [Default = FFh]

Dot_onoff30 is shown in [Figure 2-98](#) and described in [Table 2-103](#).

Return to the [Summary Table](#).

Figure 2-98. Dot_onoff30 Register

7	6	5	4	3	2	1	0
Dot_L10- CS7_onoff	Dot_L10- CS6_onoff	Dot_L10- CS5_onoff	Dot_L10- CS4_onoff	Dot_L10- CS3_onoff	Dot_L10- CS2_onoff	Dot_L10- CS1_onoff	Dot_L10- CS0_onoff
R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h

Table 2-103. Dot_onoff30 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L10-CS7_onoff	R/W	1h	LED dot L10-CS7 on/off setting 0h = Off 1h = On
6	Dot_L10-CS6_onoff	R/W	1h	LED dot L10-CS6 on/off setting 0h = Off 1h = On
5	Dot_L10-CS5_onoff	R/W	1h	LED dot L10-CS5 on/off setting 0h = Off 1h = On
4	Dot_L10-CS4_onoff	R/W	1h	LED dot L10-CS4 on/off setting 0h = Off 1h = On
3	Dot_L10-CS3_onoff	R/W	1h	LED dot L10-CS3 on/off setting 0h = Off 1h = On
2	Dot_L10-CS2_onoff	R/W	1h	LED dot L10-CS2 on/off setting 0h = Off 1h = On
1	Dot_L10-CS1_onoff	R/W	1h	LED dot L10-CS1 on/off setting 0h = Off 1h = On
0	Dot_L10-CS0_onoff	R/W	1h	LED dot L10-CS0 on/off setting 0h = Off 1h = On

2.5.32 Dot_onoff31 Register (Address = 62h) [Default = FFh]

Dot_onoff31 is shown in [Figure 2-99](#) and described in [Table 2-104](#).

Return to the [Summary Table](#).

Figure 2-99. Dot_onoff31 Register

7	6	5	4	3	2	1	0
Dot_L10-CS15_onoff	Dot_L10-CS14_onoff	Dot_L10-CS13_onoff	Dot_L10-CS12_onoff	Dot_L10-CS11_onoff	Dot_L10-CS10_onoff	Dot_L10-CS1_onoff	Dot_L10-CS0_onoff
R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h	R/W-1h

Table 2-104. Dot_onoff31 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L10-CS15_onoff	R/W	1h	LED dot L10-CS15 on/off setting 0h = Off 1h = On
6	Dot_L10-CS14_onoff	R/W	1h	LED dot L10-CS14 on/off setting 0h = Off 1h = On
5	Dot_L10-CS13_onoff	R/W	1h	LED dot L10-CS13 on/off setting 0h = Off 1h = On
4	Dot_L10-CS12_onoff	R/W	1h	LED dot L10-CS12 on/off setting 0h = Off 1h = On
3	Dot_L10-CS11_onoff	R/W	1h	LED dot L10-CS11 on/off setting 0h = Off 1h = On
2	Dot_L10-CS10_onoff	R/W	1h	LED dot L10-CS10 on/off setting 0h = Off 1h = On
1	Dot_L10-CS1_onoff	R/W	1h	LED dot L10-CS9 on/off setting 0h = Off 1h = On
0	Dot_L10-CS0_onoff	R/W	1h	LED dot L10-CS8 on/off setting 0h = Off 1h = On

2.5.33 Dot_onoff32 Register (Address = 63h) [Default = 3h]

Dot_onoff32 is shown in [Figure 2-100](#) and described in [Table 2-105](#).

Return to the [Summary Table](#).

Figure 2-100. Dot_onoff32 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L10-CS17_onoff	Dot_L10-CS16_onoff
R-0h						R/W-1h	R/W-1h

Table 2-105. Dot_onoff32 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L10-CS17_onoff	R/W	1h	LED dot L10-CS17 on/off setting 0h = Off 1h = On

Table 2-105. Dot_onoff32 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
0	Dot_L10-CS16_onoff	R/W	1h	LED dot L10-CS16 on/off setting 0h = Off 1h = On

2.6 FAULT_STATE Registers

Table 2-106 lists the FAULT_STATE registers. All register offset addresses not listed in Table 2-106 should be considered as reserved locations and the register contents should not be modified.

Global LOD/LSD Flag

Table 2-106. FAULT_STATE Registers

Address	Acronym	Register Name	Section
64h	Fault_state	Global LOD/LSD indication register	Go

2.6.1 Fault_state Register (Address = 64h) [Default = 0h]

Fault_state is shown in Figure 2-101 and described in Table 2-107.

Return to the [Summary Table](#).

Figure 2-101. Fault_state Register

7	6	5	4	3	2	1	0
RESERVED						Global_LOD	Global_LSD
R-0h						R-0h	R-0h

Table 2-107. Fault_state Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Global_LOD	R	0h	LOD indication bit if there is open fault detected at any LED dot 0h = Not open 1h = Open
0	Global_LSD	R	0h	LSD indication bit if there is short fault detected at any LED dot 0h = Not short 1h = Short

2.7 LOD Registers

[Table 2-108](#) lists the LOD registers. All register offset addresses not listed in [Table 2-108](#) should be considered as reserved locations and the register contents should not be modified.

Dot LOD Flag

Table 2-108. LOD Registers

Address	Acronym	Register Name	Section
65h	Dot_lod0	LED dot LOD indication register 0	Go
66h	Dot_lod1	LED dot LOD indication register 1	Go
67h	Dot_lod2	LED dot LOD indication register 2	Go
68h	Dot_lod3	LED dot LOD indication register 3	Go
69h	Dot_lod4	LED dot LOD indication register 4	Go
6Ah	Dot_lod5	LED dot LOD indication register 5	Go
6Bh	Dot_lod6	LED dot LOD indication register 6	Go
6Ch	Dot_lod7	LED dot LOD indication register 7	Go
6Dh	Dot_lod8	LED dot LOD indication register 8	Go
6Eh	Dot_lod9	LED dot LOD indication register 9	Go
6Fh	Dot_lod10	LED dot LOD indication register 10	Go
70h	Dot_lod11	LED dot LOD indication register 11	Go
71h	Dot_lod12	LED dot LOD indication register 12	Go
72h	Dot_lod13	LED dot LOD indication register 13	Go
73h	Dot_lod14	LED dot LOD indication register 14	Go
74h	Dot_lod15	LED dot LOD indication register 15	Go
75h	Dot_lod16	LED dot LOD indication register 16	Go
76h	Dot_lod17	LED dot LOD indication register 17	Go
77h	Dot_lod18	LED dot LOD indication register 18	Go
78h	Dot_lod19	LED dot LOD indication register 19	Go
79h	Dot_lod20	LED dot LOD indication register 20	Go
7Ah	Dot_lod21	LED dot LOD indication register 21	Go
7Bh	Dot_lod22	LED dot LOD indication register 22	Go
7Ch	Dot_lod23	LED dot LOD indication register 23	Go
7Dh	Dot_lod24	LED dot LOD indication register 24	Go
7Eh	Dot_lod25	LED dot LOD indication register 25	Go
7Fh	Dot_lod26	LED dot LOD indication register 26	Go
80h	Dot_lod27	LED dot LOD indication register 27	Go
81h	Dot_lod28	LED dot LOD indication register 28	Go
82h	Dot_lod29	LED dot LOD indication register 29	Go
83h	Dot_lod30	LED dot LOD indication register 30	Go
84h	Dot_lod31	LED dot LOD indication register 31	Go
85h	Dot_lod32	LED dot LOD indication register 32	Go

2.7.1 Dot_Iod0 Register (Address = 65h) [Default = 0h]

Dot_Iod0 is shown in [Figure 2-102](#) and described in [Table 2-109](#).

Return to the [Summary Table](#).

Figure 2-102. Dot_Iod0 Register

7	6	5	4	3	2	1	0
Dot_L0-CS7_LOD_state	Dot_L0-CS6_LOD_state	Dot_L0-CS5_LOD_state	Dot_L0-CS4_LOD_state	Dot_L0-CS3_LOD_state	Dot_L0-CS2_LOD_state	Dot_L0-CS1_LOD_state	Dot_L0-CS0_LOD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-109. Dot_Iod0 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L0-CS7_LOD_state	R	0h	LED dot L0-CS7 LOD state 0h = Not open 1h = Open
6	Dot_L0-CS6_LOD_state	R	0h	LED dot L0-CS6 LOD state 0h = Not open 1h = Open
5	Dot_L0-CS5_LOD_state	R	0h	LED dot L0-CS5 LOD state 0h = Not open 1h = Open
4	Dot_L0-CS4_LOD_state	R	0h	LED dot L0-CS4 LOD state 0h = Not open 1h = Open
3	Dot_L0-CS3_LOD_state	R	0h	LED dot L0-CS3 LOD state 0h = Not open 1h = Open
2	Dot_L0-CS2_LOD_state	R	0h	LED dot L0-CS2 LOD state 0h = Not open 1h = Open
1	Dot_L0-CS1_LOD_state	R	0h	LED dot L0-CS1 LOD state 0h = Not open 1h = Open
0	Dot_L0-CS0_LOD_state	R	0h	LED dot L0-CS0 LOD state 0h = Not open 1h = Open

2.7.2 Dot_Iod1 Register (Address = 66h) [Default = 0h]

Dot_Iod1 is shown in [Figure 2-103](#) and described in [Table 2-110](#).

Return to the [Summary Table](#).

Figure 2-103. Dot_Iod1 Register

7	6	5	4	3	2	1	0
Dot_L0-CS15_LOD_state	Dot_L0-CS14_LOD_state	Dot_L0-CS13_LOD_state	Dot_L0-CS12_LOD_state	Dot_L0-CS11_LOD_state	Dot_L0-CS10_LOD_state	Dot_L0-CS9_LOD_state	Dot_L0-CS8_LOD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-110. Dot_Iod1 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L0-CS15_LOD_state	R	0h	LED dot L0-CS15 LOD state 0h = Not open 1h = Open

Table 2-110. Dot_Iod1 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
6	Dot_L0-CS14_LOD_state	R	0h	LED dot L0-CS14 LOD state 0h = Not open 1h = Open
5	Dot_L0-CS13_LOD_state	R	0h	LED dot L0-CS13 LOD state 0h = Not open 1h = Open
4	Dot_L0-CS12_LOD_state	R	0h	LED dot L0-CS12 LOD state 0h = Not open 1h = Open
3	Dot_L0-CS11_LOD_state	R	0h	LED dot L0-CS11 LOD state 0h = Not open 1h = Open
2	Dot_L0-CS10_LOD_state	R	0h	LED dot L0-CS10 LOD state 0h = Not open 1h = Open
1	Dot_L0-CS9_LOD_state	R	0h	LED dot L0-CS9 LOD state 0h = Not open 1h = Open
0	Dot_L0-CS8_LOD_state	R	0h	LED dot L0-CS8 LOD state 0h = Not open 1h = Open

2.7.3 Dot_Iod2 Register (Address = 67h) [Default = 0h]

Dot_Iod2 is shown in [Figure 2-104](#) and described in [Table 2-111](#).

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Figure 2-104. Dot_Iod2 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L0- CS17_LOD_sta te	Dot_L0- CS16_LOD_sta te
R-0h						R-0h	R-0h

Table 2-111. Dot_Iod2 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L0-CS17_LOD_state	R	0h	LED dot L0-CS17 LOD state 0h = Not open 1h = Open
0	Dot_L0-CS16_LOD_state	R	0h	LED dot L0-CS16 LOD state 0h = Not open 1h = Open

2.7.4 Dot_Iod3 Register (Address = 68h) [Default = 0h]

Dot_Iod3 is shown in [Figure 2-105](#) and described in [Table 2-112](#).

Return to the [Summary Table](#).

Figure 2-105. Dot_Iod3 Register

7	6	5	4	3	2	1	0
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Figure 2-105. Dot_Iod3 Register (continued)

Dot_L1- CS7_LOD_stat e	Dot_L1- CS6_LOD_stat e	Dot_L1- CS5_LOD_stat e	Dot_L1- CS4_LOD_stat e	Dot_L1- CS3_LOD_stat e	Dot_L1- CS2_LOD_stat e	Dot_L1- CS1_LOD_stat e	Dot_L1- CS0_LOD_stat e
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-112. Dot_Iod3 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L1-CS7_LOD_state	R	0h	LED dot L1-CS7 LOD state 0h = Not open 1h = Open
6	Dot_L1-CS6_LOD_state	R	0h	LED dot L1-CS6 LOD state 0h = Not open 1h = Open
5	Dot_L1-CS5_LOD_state	R	0h	LED dot L1-CS5 LOD state 0h = Not open 1h = Open
4	Dot_L1-CS4_LOD_state	R	0h	LED dot L1-CS4 LOD state 0h = Not open 1h = Open
3	Dot_L1-CS3_LOD_state	R	0h	LED dot L1-CS3 LOD state 0h = Not open 1h = Open
2	Dot_L1-CS2_LOD_state	R	0h	LED dot L1-CS2 LOD state 0h = Not open 1h = Open
1	Dot_L1-CS1_LOD_state	R	0h	LED dot L1-CS1 LOD state 0h = Not open 1h = Open
0	Dot_L1-CS0_LOD_state	R	0h	LED dot L1-CS0 LOD state 0h = Not open 1h = Open

2.7.5 Dot_Iod4 Register (Address = 69h) [Default = 0h]

Dot_Iod4 is shown in [Figure 2-106](#) and described in [Table 2-113](#).

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Figure 2-106. Dot_Iod4 Register

7	6	5	4	3	2	1	0
Dot_L1- CS15_LOD_sta te	Dot_L1- CS14_LOD_sta te	Dot_L1- CS13_LOD_sta te	Dot_L1- CS12_LOD_sta te	Dot_L1- CS11_LOD_stat e	Dot_L1- CS10_LOD_sta te	Dot_L1- CS9_LOD_stat e	Dot_L1- CS8_LOD_stat e
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-113. Dot_Iod4 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L1-CS15_LOD_state	R	0h	LED dot L1-CS15 LOD state 0h = Not open 1h = Open
6	Dot_L1-CS14_LOD_state	R	0h	LED dot L1-CS14 LOD state 0h = Not open 1h = Open
5	Dot_L1-CS13_LOD_state	R	0h	LED dot L1-CS13 LOD state 0h = Not open 1h = Open

Table 2-113. Dot_Iod4 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
4	Dot_L1-CS12_LOD_state	R	0h	LED dot L1-CS12 LOD state 0h = Not open 1h = Open
3	Dot_L1-CS11_LOD_state	R	0h	LED dot L1-CS11 LOD state 0h = Not open 1h = Open
2	Dot_L1-CS10_LOD_state	R	0h	LED dot L1-CS10 LOD state 0h = Not open 1h = Open
1	Dot_L1-CS9_LOD_state	R	0h	LED dot L1-CS9 LOD state 0h = Not open 1h = Open
0	Dot_L1-CS8_LOD_state	R	0h	LED dot L1-CS8 LOD state 0h = Not open 1h = Open

2.7.6 Dot_Iod5 Register (Address = 6Ah) [Default = 0h]

Dot_Iod5 is shown in [Figure 2-107](#) and described in [Table 2-114](#).

Return to the [Summary Table](#).

Figure 2-107. Dot_Iod5 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L1- CS17_LOD_sta te	Dot_L1- CS16_LOD_sta te
R-0h						R-0h	R-0h

Table 2-114. Dot_Iod5 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L1-CS17_LOD_state	R	0h	LED dot L1-CS17 LOD state 0h = Not open 1h = Open
0	Dot_L1-CS16_LOD_state	R	0h	LED dot L1-CS16 LOD state 0h = Not open 1h = Open

2.7.7 Dot_Iod6 Register (Address = 6Bh) [Default = 0h]

Dot_Iod6 is shown in [Figure 2-108](#) and described in [Table 2-115](#).

Return to the [Summary Table](#).

Figure 2-108. Dot_Iod6 Register

7	6	5	4	3	2	1	0
Dot_L2- CS7_LOD_stat e	Dot_L2- CS6_LOD_stat e	Dot_L2- CS5_LOD_stat e	Dot_L2- CS4_LOD_stat e	Dot_L2- CS3_LOD_stat e	Dot_L2- CS2_LOD_stat e	Dot_L2- CS1_LOD_stat e	Dot_L2- CS0_LOD_stat e
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-115. Dot_lod6 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L2-CS7_LOD_state	R	0h	LED dot L2-CS7 LOD state 0h = Not open 1h = Open
6	Dot_L2-CS6_LOD_state	R	0h	LED dot L2-CS6 LOD state 0h = Not open 1h = Open
5	Dot_L2-CS5_LOD_state	R	0h	LED dot L2-CS5 LOD state 0h = Not open 1h = Open
4	Dot_L2-CS4_LOD_state	R	0h	LED dot L2-CS4 LOD state 0h = Not open 1h = Open
3	Dot_L2-CS3_LOD_state	R	0h	LED dot L2-CS3 LOD state 0h = Not open 1h = Open
2	Dot_L2-CS2_LOD_state	R	0h	LED dot L2-CS2 LOD state 0h = Not open 1h = Open
1	Dot_L2-CS1_LOD_state	R	0h	LED dot L2-CS1 LOD state 0h = Not open 1h = Open
0	Dot_L2-CS0_LOD_state	R	0h	LED dot L2-CS0 LOD state 0h = Not open 1h = Open

2.7.8 Dot_lod7 Register (Address = 6Ch) [Default = 0h]

Dot_lod7 is shown in [Figure 2-109](#) and described in [Table 2-116](#).

Return to the [Summary Table](#).

Figure 2-109. Dot_lod7 Register

7	6	5	4	3	2	1	0
Dot_L2-CS15_LOD_state	Dot_L2-CS14_LOD_state	Dot_L2-CS13_LOD_state	Dot_L2-CS12_LOD_state	Dot_L2-CS11_LOD_state	Dot_L2-CS10_LOD_state	Dot_L2-CS9_LOD_state	Dot_L2-CS8_LOD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-116. Dot_lod7 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L2-CS15_LOD_state	R	0h	LED dot L2-CS15 LOD state 0h = Not open 1h = Open
6	Dot_L2-CS14_LOD_state	R	0h	LED dot L2-CS14 LOD state 0h = Not open 1h = Open
5	Dot_L2-CS13_LOD_state	R	0h	LED dot L2-CS13 LOD state 0h = Not open 1h = Open
4	Dot_L2-CS12_LOD_state	R	0h	LED dot L2-CS12 LOD state 0h = Not open 1h = Open
3	Dot_L2-CS11_LOD_state	R	0h	LED dot L2-CS11 LOD state 0h = Not open 1h = Open

Table 2-116. Dot_Iod7 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
2	Dot_L2-CS10_LOD_state	R	0h	LED dot L2-CS10 LOD state 0h = Not open 1h = Open
1	Dot_L2-CS9_LOD_state	R	0h	LED dot L2-CS9 LOD state 0h = Not open 1h = Open
0	Dot_L2-CS8_LOD_state	R	0h	LED dot L2-CS8 LOD state 0h = Not open 1h = Open

2.7.9 Dot_Iod8 Register (Address = 6Dh) [Default = 0h]

Dot_Iod8 is shown in [Figure 2-110](#) and described in [Table 2-117](#).

Return to the [Summary Table](#).

Figure 2-110. Dot_Iod8 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L2- CS17_LOD_sta te	Dot_L2- CS16_LOD_sta te
R-0h						R-0h	R-0h

Table 2-117. Dot_Iod8 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L2-CS17_LOD_state	R	0h	LED dot L2-CS17 LOD state 0h = Not open 1h = Open
0	Dot_L2-CS16_LOD_state	R	0h	LED dot L2-CS16 LOD state 0h = Not open 1h = Open

2.7.10 Dot_Iod9 Register (Address = 6Eh) [Default = 0h]

Dot_Iod9 is shown in [Figure 2-111](#) and described in [Table 2-118](#).

Return to the [Summary Table](#).

Figure 2-111. Dot_Iod9 Register

7	6	5	4	3	2	1	0
Dot_L3- CS7_LOD_stat e	Dot_L3- CS6_LOD_stat e	Dot_L3- CS5_LOD_stat e	Dot_L3- CS4_LOD_stat e	Dot_L3- CS3_LOD_stat e	Dot_L3- CS2_LOD_stat e	Dot_L3- CS1_LOD_stat e	Dot_L3- CS0_LOD_stat e
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-118. Dot_Iod9 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L3-CS7_LOD_state	R	0h	LED dot L3-CS7 LOD state 0h = Not open 1h = Open
6	Dot_L3-CS6_LOD_state	R	0h	LED dot L3-CS6 LOD state 0h = Not open 1h = Open

Table 2-118. Dot_Iod9 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
5	Dot_L3-CS5_LOD_state	R	0h	LED dot L3-CS5 LOD state 0h = Not open 1h = Open
4	Dot_L3-CS4_LOD_state	R	0h	LED dot L3-CS4 LOD state 0h = Not open 1h = Open
3	Dot_L3-CS3_LOD_state	R	0h	LED dot L3-CS3 LOD state 0h = Not open 1h = Open
2	Dot_L3-CS2_LOD_state	R	0h	LED dot L3-CS2 LOD state 0h = Not open 1h = Open
1	Dot_L3-CS1_LOD_state	R	0h	LED dot L3-CS1 LOD state 0h = Not open 1h = Open
0	Dot_L3-CS0_LOD_state	R	0h	LED dot L3-CS0 LOD state 0h = Not open 1h = Open

2.7.11 Dot_Iod10 Register (Address = 6Fh) [Default = 0h]

Dot_Iod10 is shown in [Figure 2-112](#) and described in [Table 2-119](#).

Return to the [Summary Table](#).

Figure 2-112. Dot_Iod10 Register

7	6	5	4	3	2	1	0
Dot_L3-CS15_LOD_state	Dot_L3-CS14_LOD_state	Dot_L3-CS13_LOD_state	Dot_L3-CS12_LOD_state	Dot_L3-CS11_LOD_state	Dot_L3-CS10_LOD_state	Dot_L3-CS9_LOD_state	Dot_L3-CS8_LOD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-119. Dot_Iod10 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L3-CS15_LOD_state	R	0h	LED dot L3-CS15 LOD state 0h = Not open 1h = Open
6	Dot_L3-CS14_LOD_state	R	0h	LED dot L3-CS14 LOD state 0h = Not open 1h = Open
5	Dot_L3-CS13_LOD_state	R	0h	LED dot L3-CS13 LOD state 0h = Not open 1h = Open
4	Dot_L3-CS12_LOD_state	R	0h	LED dot L3-CS12 LOD state 0h = Not open 1h = Open
3	Dot_L3-CS11_LOD_state	R	0h	LED dot L3-CS11 LOD state 0h = Not open 1h = Open
2	Dot_L3-CS10_LOD_state	R	0h	LED dot L3-CS10 LOD state 0h = Not open 1h = Open
1	Dot_L3-CS9_LOD_state	R	0h	LED dot L3-CS9 LOD state 0h = Not open 1h = Open

Table 2-119. Dot_Iod10 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
0	Dot_L3-CS8_LOD_state	R	0h	LED dot L3-CS8 LOD state 0h = Not open 1h = Open

2.7.12 Dot_Iod11 Register (Address = 70h) [Default = 0h]

Dot_Iod11 is shown in [Figure 2-113](#) and described in [Table 2-120](#).

Return to the [Summary Table](#).

Figure 2-113. Dot_Iod11 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L3- CS17_LOD_sta te	Dot_L3- CS16_LOD_sta te
R-0h						R-0h	R-0h

Table 2-120. Dot_Iod11 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L3-CS17_LOD_state	R	0h	LED dot L3-CS17 LOD state 0h = Not open 1h = Open
0	Dot_L3-CS16_LOD_state	R	0h	LED dot L3-CS16 LOD state 0h = Not open 1h = Open

2.7.13 Dot_Iod12 Register (Address = 71h) [Default = 0h]

Dot_Iod12 is shown in [Figure 2-114](#) and described in [Table 2-121](#).

Return to the [Summary Table](#).

Figure 2-114. Dot_Iod12 Register

7	6	5	4	3	2	1	0
Dot_L4- CS7_LOD_stat e	Dot_L4- CS6_LOD_stat e	Dot_L4- CS5_LOD_stat e	Dot_L4- CS4_LOD_stat e	Dot_L4- CS3_LOD_stat e	Dot_L4- CS2_LOD_stat e	Dot_L4- CS1_LOD_stat e	Dot_L4- CS0_LOD_stat e
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-121. Dot_Iod12 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L4-CS7_LOD_state	R	0h	LED dot L4-CS7 LOD state 0h = Not open 1h = Open
6	Dot_L4-CS6_LOD_state	R	0h	LED dot L4-CS6 LOD state 0h = Not open 1h = Open
5	Dot_L4-CS5_LOD_state	R	0h	LED dot L4-CS5 LOD state 0h = Not open 1h = Open
4	Dot_L4-CS4_LOD_state	R	0h	LED dot L4-CS4 LOD state 0h = Not open 1h = Open

Table 2-121. Dot_Iod12 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
3	Dot_L4-CS3_LOD_state	R	0h	LED dot L4-CS3 LOD state 0h = Not open 1h = Open
2	Dot_L4-CS2_LOD_state	R	0h	LED dot L4-CS2 LOD state 0h = Not open 1h = Open
1	Dot_L4-CS1_LOD_state	R	0h	LED dot L4-CS1 LOD state 0h = Not open 1h = Open
0	Dot_L4-CS0_LOD_state	R	0h	LED dot L4-CS0 LOD state 0h = Not open 1h = Open

2.7.14 Dot_Iod13 Register (Address = 72h) [Default = 0h]

Dot_Iod13 is shown in [Figure 2-115](#) and described in [Table 2-122](#).

Return to the [Summary Table](#).

Figure 2-115. Dot_Iod13 Register

7	6	5	4	3	2	1	0
Dot_L4-CS15_LOD_state	Dot_L4-CS14_LOD_state	Dot_L4-CS13_LOD_state	Dot_L4-CS12_LOD_state	Dot_L4-CS11_LOD_state	Dot_L4-CS10_LOD_state	Dot_L4-CS9_LOD_state	Dot_L4-CS8_LOD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-122. Dot_Iod13 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L4-CS15_LOD_state	R	0h	LED dot L4-CS15 LOD state 0h = Not open 1h = Open
6	Dot_L4-CS14_LOD_state	R	0h	LED dot L4-CS14 LOD state 0h = Not open 1h = Open
5	Dot_L4-CS13_LOD_state	R	0h	LED dot L4-CS13 LOD state 0h = Not open 1h = Open
4	Dot_L4-CS12_LOD_state	R	0h	LED dot L4-CS12 LOD state 0h = Not open 1h = Open
3	Dot_L4-CS11_LOD_state	R	0h	LED dot L4-CS11 LOD state 0h = Not open 1h = Open
2	Dot_L4-CS10_LOD_state	R	0h	LED dot L4-CS10 LOD state 0h = Not open 1h = Open
1	Dot_L4-CS9_LOD_state	R	0h	LED dot L4-CS9 LOD state 0h = Not open 1h = Open
0	Dot_L4-CS8_LOD_state	R	0h	LED dot L4-CS8 LOD state 0h = Not open 1h = Open

2.7.15 Dot_Iod14 Register (Address = 73h) [Default = 0h]

Dot_Iod14 is shown in [Figure 2-116](#) and described in [Table 2-123](#).

Return to the [Summary Table](#).

Figure 2-116. Dot_Iod14 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L4- CS17_LOD_sta te	Dot_L4- CS16_LOD_sta te
R-0h						R-0h	R-0h

Table 2-123. Dot_Iod14 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L4-CS17_LOD_state	R	0h	LED dot L4-CS17 LOD state 0h = Not open 1h = Open
0	Dot_L4-CS16_LOD_state	R	0h	LED dot L4-CS16 LOD state 0h = Not open 1h = Open

2.7.16 Dot_Iod15 Register (Address = 74h) [Default = 0h]

Dot_Iod15 is shown in [Figure 2-117](#) and described in [Table 2-124](#).

Return to the [Summary Table](#).

Figure 2-117. Dot_Iod15 Register

7	6	5	4	3	2	1	0
Dot_L5- CS7_LOD_stat e	Dot_L5- CS6_LOD_stat e	Dot_L5- CS5_LOD_stat e	Dot_L5- CS4_LOD_stat e	Dot_L5- CS3_LOD_stat e	Dot_L5- CS2_LOD_stat e	Dot_L5- CS1_LOD_stat e	Dot_L5- CS0_LOD_stat e
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-124. Dot_Iod15 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L5-CS7_LOD_state	R	0h	LED dot L5-CS7 LOD state 0h = Not open 1h = Open
6	Dot_L5-CS6_LOD_state	R	0h	LED dot L5-CS6 LOD state 0h = Not open 1h = Open
5	Dot_L5-CS5_LOD_state	R	0h	LED dot L5-CS5 LOD state 0h = Not open 1h = Open
4	Dot_L5-CS4_LOD_state	R	0h	LED dot L5-CS4 LOD state 0h = Not open 1h = Open
3	Dot_L5-CS3_LOD_state	R	0h	LED dot L5-CS3 LOD state 0h = Not open 1h = Open
2	Dot_L5-CS2_LOD_state	R	0h	LED dot L5-CS2 LOD state 0h = Not open 1h = Open

Table 2-124. Dot_Iod15 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
1	Dot_L5-CS1_LOD_state	R	0h	LED dot L5-CS1 LOD state 0h = Not open 1h = Open
0	Dot_L5-CS0_LOD_state	R	0h	LED dot L5-CS0 LOD state 0h = Not open 1h = Open

2.7.17 Dot_Iod16 Register (Address = 75h) [Default = 0h]

Dot_Iod16 is shown in [Figure 2-118](#) and described in [Table 2-125](#).

Return to the [Summary Table](#).

Figure 2-118. Dot_Iod16 Register

7	6	5	4	3	2	1	0
Dot_L5-CS15_LOD_state	Dot_L5-CS14_LOD_state	Dot_L5-CS13_LOD_state	Dot_L5-CS12_LOD_state	Dot_L5-CS11_LOD_state	Dot_L5-CS10_LOD_state	Dot_L5-CS9_LOD_state	Dot_L5-CS8_LOD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-125. Dot_Iod16 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L5-CS15_LOD_state	R	0h	LED dot L5-CS15 LOD state 0h = Not open 1h = Open
6	Dot_L5-CS14_LOD_state	R	0h	LED dot L5-CS14 LOD state 0h = Not open 1h = Open
5	Dot_L5-CS13_LOD_state	R	0h	LED dot L5-CS13 LOD state 0h = Not open 1h = Open
4	Dot_L5-CS12_LOD_state	R	0h	LED dot L5-CS12 LOD state 0h = Not open 1h = Open
3	Dot_L5-CS11_LOD_state	R	0h	LED dot L5-CS11 LOD state 0h = Not open 1h = Open
2	Dot_L5-CS10_LOD_state	R	0h	LED dot L5-CS10 LOD state 0h = Not open 1h = Open
1	Dot_L5-CS9_LOD_state	R	0h	LED dot L5-CS9 LOD state 0h = Not open 1h = Open
0	Dot_L5-CS8_LOD_state	R	0h	LED dot L5-CS8 LOD state 0h = Not open 1h = Open

2.7.18 Dot_Iod17 Register (Address = 76h) [Default = 0h]

Dot_Iod17 is shown in [Figure 2-119](#) and described in [Table 2-126](#).

Return to the [Summary Table](#).

Figure 2-119. Dot_Iod17 Register

7	6	5	4	3	2	1	0
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Figure 2-119. Dot_Iod17 Register (continued)

RESERVED	Dot_L5- CS17_LOD_sta te	Dot_L5- CS16_LOD_sta te
R-0h	R-0h	R-0h

Table 2-126. Dot_Iod17 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L5-CS17_LOD_state	R	0h	LED dot L5-CS17 LOD state 0h = Not open 1h = Open
0	Dot_L5-CS16_LOD_state	R	0h	LED dot L5-CS16 LOD state 0h = Not open 1h = Open

2.7.19 Dot_Iod18 Register (Address = 77h) [Default = 0h]

Dot_Iod18 is shown in [Figure 2-120](#) and described in [Table 2-127](#).

Return to the [Summary Table](#).

Figure 2-120. Dot_Iod18 Register

7	6	5	4	3	2	1	0
Dot_L6- CS7_LOD_sta te	Dot_L6- CS6_LOD_sta te	Dot_L6- CS5_LOD_sta te	Dot_L6- CS4_LOD_sta te	Dot_L6- CS3_LOD_sta te	Dot_L6- CS2_LOD_sta te	Dot_L6- CS1_LOD_sta te	Dot_L6- CS0_LOD_sta te
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-127. Dot_Iod18 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L6-CS7_LOD_state	R	0h	LED dot L6-CS7 LOD state 0h = Not open 1h = Open
6	Dot_L6-CS6_LOD_state	R	0h	LED dot L6-CS6 LOD state 0h = Not open 1h = Open
5	Dot_L6-CS5_LOD_state	R	0h	LED dot L6-CS5 LOD state 0h = Not open 1h = Open
4	Dot_L6-CS4_LOD_state	R	0h	LED dot L6-CS4 LOD state 0h = Not open 1h = Open
3	Dot_L6-CS3_LOD_state	R	0h	LED dot L6-CS3 LOD state 0h = Not open 1h = Open
2	Dot_L6-CS2_LOD_state	R	0h	LED dot L6-CS2 LOD state 0h = Not open 1h = Open
1	Dot_L6-CS1_LOD_state	R	0h	LED dot L6-CS1 LOD state 0h = Not open 1h = Open
0	Dot_L6-CS0_LOD_state	R	0h	LED dot L6-CS0 LOD state 0h = Not open 1h = Open

2.7.20 Dot_Iod19 Register (Address = 78h) [Default = 0h]

Dot_Iod19 is shown in [Figure 2-121](#) and described in [Table 2-128](#).

Return to the [Summary Table](#).

Figure 2-121. Dot_Iod19 Register

7	6	5	4	3	2	1	0
Dot_L6-CS15_LOD_state	Dot_L6-CS14_LOD_state	Dot_L6-CS13_LOD_state	Dot_L6-CS12_LOD_state	Dot_L6-CS11_LOD_state	Dot_L6-CS10_LOD_state	Dot_L6-CS9_LOD_state	Dot_L6-CS8_LOD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-128. Dot_Iod19 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L6-CS15_LOD_state	R	0h	LED dot L6-CS15 LOD state 0h = Not open 1h = Open
6	Dot_L6-CS14_LOD_state	R	0h	LED dot L6-CS14 LOD state 0h = Not open 1h = Open
5	Dot_L6-CS13_LOD_state	R	0h	LED dot L6-CS13 LOD state 0h = Not open 1h = Open
4	Dot_L6-CS12_LOD_state	R	0h	LED dot L6-CS12 LOD state 0h = Not open 1h = Open
3	Dot_L6-CS11_LOD_state	R	0h	LED dot L6-CS11 LOD state 0h = Not open 1h = Open
2	Dot_L6-CS10_LOD_state	R	0h	LED dot L6-CS10 LOD state 0h = Not open 1h = Open
1	Dot_L6-CS9_LOD_state	R	0h	LED dot L6-CS9 LOD state 0h = Not open 1h = Open
0	Dot_L6-CS8_LOD_state	R	0h	LED dot L6-CS8 LOD state 0h = Not open 1h = Open

2.7.21 Dot_Iod20 Register (Address = 79h) [Default = 0h]

Dot_Iod20 is shown in [Figure 2-122](#) and described in [Table 2-129](#).

Return to the [Summary Table](#).

Figure 2-122. Dot_Iod20 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L6-CS17_LOD_state	Dot_L6-CS16_LOD_state
R-0h						R-0h	R-0h

Table 2-129. Dot_Iod20 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved

Table 2-129. Dot_Iod20 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
1	Dot_L6-CS17_LOD_state	R	0h	LED dot L6-CS17 LOD state 0h = Not open 1h = Open
0	Dot_L6-CS16_LOD_state	R	0h	LED dot L6-CS16 LOD state 0h = Not open 1h = Open

2.7.22 Dot_Iod21 Register (Address = 7Ah) [Default = 0h]

Dot_Iod21 is shown in [Figure 2-123](#) and described in [Table 2-130](#).

Return to the [Summary Table](#).

Figure 2-123. Dot_Iod21 Register

7	6	5	4	3	2	1	0
Dot_L7-CS7_LOD_state	Dot_L7-CS6_LOD_state	Dot_L7-CS5_LOD_state	Dot_L7-CS4_LOD_state	Dot_L7-CS3_LOD_state	Dot_L7-CS2_LOD_state	Dot_L7-CS1_LOD_state	Dot_L7-CS0_LOD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-130. Dot_Iod21 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L7-CS7_LOD_state	R	0h	LED dot L7-CS7 LOD state 0h = Not open 1h = Open
6	Dot_L7-CS6_LOD_state	R	0h	LED dot L7-CS6 LOD state 0h = Not open 1h = Open
5	Dot_L7-CS5_LOD_state	R	0h	LED dot L7-CS5 LOD state 0h = Not open 1h = Open
4	Dot_L7-CS4_LOD_state	R	0h	LED dot L7-CS4 LOD state 0h = Not open 1h = Open
3	Dot_L7-CS3_LOD_state	R	0h	LED dot L7-CS3 LOD state 0h = Not open 1h = Open
2	Dot_L7-CS2_LOD_state	R	0h	LED dot L7-CS2 LOD state 0h = Not open 1h = Open
1	Dot_L7-CS1_LOD_state	R	0h	LED dot L7-CS1 LOD state 0h = Not open 1h = Open
0	Dot_L7-CS0_LOD_state	R	0h	LED dot L7-CS0 LOD state 0h = Not open 1h = Open

2.7.23 Dot_Iod22 Register (Address = 7Bh) [Default = 0h]

Dot_Iod22 is shown in [Figure 2-124](#) and described in [Table 2-131](#).

Return to the [Summary Table](#).

Figure 2-124. Dot_Iod22 Register

7	6	5	4	3	2	1	0
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Figure 2-124. Dot_lod22 Register (continued)

Dot_L7- CS15_LOD_sta te	Dot_L7- CS14_LOD_sta te	Dot_L7- CS13_LOD_sta te	Dot_L7- CS12_LOD_sta te	Dot_L7- CS11_LOD_sta te	Dot_L7- CS10_LOD_sta te	Dot_L7- CS9_LOD_sta te	Dot_L7- CS8_LOD_sta te
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-131. Dot_lod22 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L7-CS15_LOD_state	R	0h	LED dot L7-CS15 LOD state 0h = Not open 1h = Open
6	Dot_L7-CS14_LOD_state	R	0h	LED dot L7-CS14 LOD state 0h = Not open 1h = Open
5	Dot_L7-CS13_LOD_state	R	0h	LED dot L7-CS13 LOD state 0h = Not open 1h = Open
4	Dot_L7-CS12_LOD_state	R	0h	LED dot L7-CS12 LOD state 0h = Not open 1h = Open
3	Dot_L7-CS11_LOD_state	R	0h	LED dot L7-CS11 LOD state 0h = Not open 1h = Open
2	Dot_L7-CS10_LOD_state	R	0h	LED dot L7-CS10 LOD state 0h = Not open 1h = Open
1	Dot_L7-CS9_LOD_state	R	0h	LED dot L7-CS9 LOD state 0h = Not open 1h = Open
0	Dot_L7-CS8_LOD_state	R	0h	LED dot L7-CS8 LOD state 0h = Not open 1h = Open

2.7.24 Dot_lod23 Register (Address = 7Ch) [Default = 0h]

Dot_lod23 is shown in [Figure 2-125](#) and described in [Table 2-132](#).

Return to the [Summary Table](#).

Figure 2-125. Dot_lod23 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L7- CS17_LOD_sta te	Dot_L7- CS16_LOD_sta te
R-0h						R-0h	R-0h

Table 2-132. Dot_lod23 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L7-CS17_LOD_state	R	0h	LED dot L7-CS17 LOD state 0h = Not open 1h = Open
0	Dot_L7-CS16_LOD_state	R	0h	LED dot L7-CS16 LOD state 0h = Not open 1h = Open

2.7.25 Dot_Iod24 Register (Address = 7Dh) [Default = 0h]

Dot_Iod24 is shown in [Figure 2-126](#) and described in [Table 2-133](#).

Return to the [Summary Table](#).

Figure 2-126. Dot_Iod24 Register

7	6	5	4	3	2	1	0
Dot_L8-CS7_LOD_state	Dot_L8-CS6_LOD_state	Dot_L8-CS5_LOD_state	Dot_L8-CS4_LOD_state	Dot_L8-CS3_LOD_state	Dot_L8-CS2_LOD_state	Dot_L8-CS1_LOD_state	Dot_L8-CS0_LOD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-133. Dot_Iod24 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L8-CS7_LOD_state	R	0h	LED dot L8-CS7 LOD state 0h = Not open 1h = Open
6	Dot_L8-CS6_LOD_state	R	0h	LED dot L8-CS6 LOD state 0h = Not open 1h = Open
5	Dot_L8-CS5_LOD_state	R	0h	LED dot L8-CS5 LOD state 0h = Not open 1h = Open
4	Dot_L8-CS4_LOD_state	R	0h	LED dot L8-CS4 LOD state 0h = Not open 1h = Open
3	Dot_L8-CS3_LOD_state	R	0h	LED dot L8-CS3 LOD state 0h = Not open 1h = Open
2	Dot_L8-CS2_LOD_state	R	0h	LED dot L8-CS2 LOD state 0h = Not open 1h = Open
1	Dot_L8-CS1_LOD_state	R	0h	LED dot L8-CS1 LOD state 0h = Not open 1h = Open
0	Dot_L8-CS0_LOD_state	R	0h	LED dot L8-CS0 LOD state 0h = Not open 1h = Open

2.7.26 Dot_Iod25 Register (Address = 7Eh) [Default = 0h]

Dot_Iod25 is shown in [Figure 2-127](#) and described in [Table 2-134](#).

Return to the [Summary Table](#).

Figure 2-127. Dot_Iod25 Register

7	6	5	4	3	2	1	0
Dot_L8-CS15_LOD_state	Dot_L8-CS14_LOD_state	Dot_L8-CS13_LOD_state	Dot_L8-CS12_LOD_state	Dot_L8-CS11_LOD_state	Dot_L8-CS10_LOD_state	Dot_L8-CS9_LOD_state	Dot_L8-CS8_LOD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-134. Dot_Iod25 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L8-CS15_LOD_state	R	0h	LED dot L8-CS15 LOD state 0h = Not open 1h = Open

Table 2-134. Dot_Iod25 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
6	Dot_L8-CS14_LOD_state	R	0h	LED dot L8-CS14 LOD state 0h = Not open 1h = Open
5	Dot_L8-CS13_LOD_state	R	0h	LED dot L8-CS13 LOD state 0h = Not open 1h = Open
4	Dot_L8-CS12_LOD_state	R	0h	LED dot L8-CS12 LOD state 0h = Not open 1h = Open
3	Dot_L8-CS11_LOD_state	R	0h	LED dot L8-CS11 LOD state 0h = Not open 1h = Open
2	Dot_L8-CS10_LOD_state	R	0h	LED dot L8-CS10 LOD state 0h = Not open 1h = Open
1	Dot_L8-CS9_LOD_state	R	0h	LED dot L8-CS9 LOD state 0h = Not open 1h = Open
0	Dot_L8-CS8_LOD_state	R	0h	LED dot L8-CS8 LOD state 0h = Not open 1h = Open

2.7.27 Dot_Iod26 Register (Address = 7Fh) [Default = 0h]

Dot_Iod26 is shown in [Figure 2-128](#) and described in [Table 2-135](#).

Return to the [Summary Table](#).

Figure 2-128. Dot_Iod26 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L8- CS17_LOD_sta te	Dot_L8- CS16_LOD_sta te
R-0h						R-0h	R-0h

Table 2-135. Dot_Iod26 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L8-CS17_LOD_state	R	0h	LED dot L8-CS17 LOD state 0h = Not open 1h = Open
0	Dot_L8-CS16_LOD_state	R	0h	LED dot L8-CS16 LOD state 0h = Not open 1h = Open

2.7.28 Dot_Iod27 Register (Address = 80h) [Default = 0h]

Dot_Iod27 is shown in [Figure 2-129](#) and described in [Table 2-136](#).

Return to the [Summary Table](#).

Figure 2-129. Dot_Iod27 Register

7	6	5	4	3	2	1	0
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Figure 2-129. Dot_lod27 Register (continued)

Dot_L9- CS7_LOD_stat e	Dot_L9- CS6_LOD_stat e	Dot_L9- CS5_LOD_stat e	Dot_L9- CS4_LOD_stat e	Dot_L9- CS3_LOD_stat e	Dot_L9- CS2_LOD_stat e	Dot_L9- CS1_LOD_stat e	Dot_L9- CS0_LOD_stat e
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-136. Dot_lod27 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L9-CS7_LOD_state	R	0h	LED dot L9-CS7 LOD state 0h = Not open 1h = Open
6	Dot_L9-CS6_LOD_state	R	0h	LED dot L9-CS6 LOD state 0h = Not open 1h = Open
5	Dot_L9-CS5_LOD_state	R	0h	LED dot L9-CS5 LOD state 0h = Not open 1h = Open
4	Dot_L9-CS4_LOD_state	R	0h	LED dot L9-CS4 LOD state 0h = Not open 1h = Open
3	Dot_L9-CS3_LOD_state	R	0h	LED dot L9-CS3 LOD state 0h = Not open 1h = Open
2	Dot_L9-CS2_LOD_state	R	0h	LED dot L9-CS2 LOD state 0h = Not open 1h = Open
1	Dot_L9-CS1_LOD_state	R	0h	LED dot L9-CS1 LOD state 0h = Not open 1h = Open
0	Dot_L9-CS0_LOD_state	R	0h	LED dot L9-CS0 LOD state 0h = Not open 1h = Open

2.7.29 Dot_lod28 Register (Address = 81h) [Default = 0h]

Dot_lod28 is shown in [Figure 2-130](#) and described in [Table 2-137](#).

Return to the [Summary Table](#).

Figure 2-130. Dot_lod28 Register

7	6	5	4	3	2	1	0
Dot_L9- CS15_LOD_sta te	Dot_L9- CS14_LOD_sta te	Dot_L9- CS13_LOD_sta te	Dot_L9- CS12_LOD_sta te	Dot_L9- CS11_LOD_stat e	Dot_L9- CS10_LOD_sta te	Dot_L9- CS9_LOD_stat e	Dot_L9- CS8_LOD_stat e
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-137. Dot_lod28 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L9-CS15_LOD_state	R	0h	LED dot L9-CS15 LOD state 0h = Not open 1h = Open
6	Dot_L9-CS14_LOD_state	R	0h	LED dot L9-CS14 LOD state 0h = Not open 1h = Open
5	Dot_L9-CS13_LOD_state	R	0h	LED dot L9-CS13 LOD state 0h = Not open 1h = Open

Table 2-137. Dot_Iod28 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
4	Dot_L9-CS12_LOD_state	R	0h	LED dot L9-CS12 LOD state 0h = Not open 1h = Open
3	Dot_L9-CS11_LOD_state	R	0h	LED dot L9-CS11 LOD state 0h = Not open 1h = Open
2	Dot_L9-CS10_LOD_state	R	0h	LED dot L9-CS10 LOD state 0h = Not open 1h = Open
1	Dot_L9-CS9_LOD_state	R	0h	LED dot L9-CS9 LOD state 0h = Not open 1h = Open
0	Dot_L9-CS8_LOD_state	R	0h	LED dot L9-CS8 LOD state 0h = Not open 1h = Open

2.7.30 Dot_Iod29 Register (Address = 82h) [Default = 0h]

Dot_Iod29 is shown in [Figure 2-131](#) and described in [Table 2-138](#).

Return to the [Summary Table](#).

Figure 2-131. Dot_Iod29 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L9- CS17_LOD_sta te	Dot_L9- CS16_LOD_sta te
R-0h						R-0h	R-0h

Table 2-138. Dot_Iod29 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L9-CS17_LOD_state	R	0h	LED dot L9-CS17 LOD state 0h = Not open 1h = Open
0	Dot_L9-CS16_LOD_state	R	0h	LED dot L9-CS16 LOD state 0h = Not open 1h = Open

2.7.31 Dot_Iod30 Register (Address = 83h) [Default = 0h]

Dot_Iod30 is shown in [Figure 2-132](#) and described in [Table 2-139](#).

Return to the [Summary Table](#).

Figure 2-132. Dot_Iod30 Register

7	6	5	4	3	2	1	0
Dot_L10- CS7_LOD_stat e	Dot_L10- CS6_LOD_stat e	Dot_L10- CS5_LOD_stat e	Dot_L10- CS4_LOD_stat e	Dot_L10- CS3_LOD_stat e	Dot_L10- CS2_LOD_stat e	Dot_L10- CS1_LOD_stat e	Dot_L10- CS0_LOD_stat e
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-139. Dot_Iod30 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L10-CS7_LOD_state	R	0h	LED dot L10-CS7 LOD state 0h = Not open 1h = Open
6	Dot_L10-CS6_LOD_state	R	0h	LED dot L10-CS6 LOD state 0h = Not open 1h = Open
5	Dot_L10-CS5_LOD_state	R	0h	LED dot L10-CS5 LOD state 0h = Not open 1h = Open
4	Dot_L10-CS4_LOD_state	R	0h	LED dot L10-CS4 LOD state 0h = Not open 1h = Open
3	Dot_L10-CS3_LOD_state	R	0h	LED dot L10-CS3 LOD state 0h = Not open 1h = Open
2	Dot_L10-CS2_LOD_state	R	0h	LED dot L10-CS2 LOD state 0h = Not open 1h = Open
1	Dot_L10-CS1_LOD_state	R	0h	LED dot L10-CS1 LOD state 0h = Not open 1h = Open
0	Dot_L10-CS0_LOD_state	R	0h	LED dot L10-CS0 LOD state 0h = Not open 1h = Open

2.7.32 Dot_Iod31 Register (Address = 84h) [Default = 0h]

Dot_Iod31 is shown in [Figure 2-133](#) and described in [Table 2-140](#).

Return to the [Summary Table](#).

Figure 2-133. Dot_Iod31 Register

7	6	5	4	3	2	1	0
Dot_L10-CS15_LOD_state	Dot_L10-CS14_LOD_state	Dot_L10-CS13_LOD_state	Dot_L10-CS12_LOD_state	Dot_L10-CS11_LOD_state	Dot_L10-CS10_LOD_state	Dot_L10-CS9_LOD_state	Dot_L10-CS8_LOD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-140. Dot_Iod31 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L10-CS15_LOD_state	R	0h	LED dot L10-CS15 LOD state 0h = Not open 1h = Open
6	Dot_L10-CS14_LOD_state	R	0h	LED dot L10-CS14 LOD state 0h = Not open 1h = Open
5	Dot_L10-CS13_LOD_state	R	0h	LED dot L10-CS13 LOD state 0h = Not open 1h = Open
4	Dot_L10-CS12_LOD_state	R	0h	LED dot L10-CS12 LOD state 0h = Not open 1h = Open
3	Dot_L10-CS11_LOD_state	R	0h	LED dot L10-CS11 LOD state 0h = Not open 1h = Open

Table 2-140. Dot_Iod31 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
2	Dot_L10-CS10_LOD_state	R	0h	LED dot L10-CS10 LOD state 0h = Not open 1h = Open
1	Dot_L10-CS9_LOD_state	R	0h	LED dot L10-CS9 LOD state 0h = Not open 1h = Open
0	Dot_L10-CS8_LOD_state	R	0h	LED dot L10-CS8 LOD state 0h = Not open 1h = Open

2.7.33 Dot_Iod32 Register (Address = 85h) [Default = 0h]

Dot_Iod32 is shown in [Figure 2-134](#) and described in [Table 2-141](#).

Return to the [Summary Table](#).

Figure 2-134. Dot_Iod32 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L10-CS17_LOD_state	Dot_L10-CS16_LOD_state
R-0h						R-0h	R-0h

Table 2-141. Dot_Iod32 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L10-CS17_LOD_state	R	0h	LED dot L10-CS17 LOD state 0h = Not open 1h = Open
0	Dot_L10-CS16_LOD_state	R	0h	LED dot L10-CS16 LOD state 0h = Not open 1h = Open

2.8 LSD Registers

Table 2-142 lists the LSD registers. All register offset addresses not listed in Table 2-142 should be considered as reserved locations and the register contents should not be modified.

Dot LSD Flag

Table 2-142. LSD Registers

Address	Acronym	Register Name	Section
86h	Dot_lsd0	LED dot LSD indication register 0	Go
87h	Dot_lsd1	LED dot LSD indication register 1	Go
88h	Dot_lsd2	LED dot LSD indication register 2	Go
89h	Dot_lsd3	LED dot LSD indication register 3	Go
8Ah	Dot_lsd4	LED dot LSD indication register 4	Go
8Bh	Dot_lsd5	LED dot LSD indication register 5	Go
8Ch	Dot_lsd6	LED dot LSD indication register 6	Go
8Dh	Dot_lsd7	LED dot LSD indication register 7	Go
8Eh	Dot_lsd8	LED dot LSD indication register 8	Go
8Fh	Dot_lsd9	LED dot LSD indication register 9	Go
90h	Dot_lsd10	LED dot LSD indication register 10	Go
91h	Dot_lsd11	LED dot LSD indication register 11	Go
92h	Dot_lsd12	LED dot LSD indication register 12	Go
93h	Dot_lsd13	LED dot LSD indication register 13	Go
94h	Dot_lsd14	LED dot LSD indication register 14	Go
95h	Dot_lsd15	LED dot LSD indication register 15	Go
96h	Dot_lsd16	LED dot LSD indication register 16	Go
97h	Dot_lsd17	LED dot LSD indication register 17	Go
98h	Dot_lsd18	LED dot LSD indication register 18	Go
99h	Dot_lsd19	LED dot LSD indication register 19	Go
9Ah	Dot_lsd20	LED dot LSD indication register 20	Go
9Bh	Dot_lsd21	LED dot LSD indication register 21	Go
9Ch	Dot_lsd22	LED dot LSD indication register 22	Go
9Dh	Dot_lsd23	LED dot LSD indication register 23	Go
9Eh	Dot_lsd24	LED dot LSD indication register 24	Go
9Fh	Dot_lsd25	LED dot LSD indication register 25	Go
A0h	Dot_lsd26	LED dot LSD indication register 26	Go
A1h	Dot_lsd27	LED dot LSD indication register 27	Go
A2h	Dot_lsd28	LED dot LSD indication register 28	Go
A3h	Dot_lsd29	LED dot LSD indication register 29	Go
A4h	Dot_lsd30	LED dot LSD indication register 30	Go
A5h	Dot_lsd31	LED dot LSD indication register 31	Go
A6h	Dot_lsd32	LED dot LSD indication register 32	Go

2.8.1 Dot_Isd0 Register (Address = 86h) [Default = 0h]

Dot_Isd0 is shown in [Figure 2-135](#) and described in [Table 2-143](#).

Return to the [Summary Table](#).

Figure 2-135. Dot_Isd0 Register

7	6	5	4	3	2	1	0
Dot_L0-CS7_LSD_state	Dot_L0-CS6_LSD_state	Dot_L0-CS5_LSD_state	Dot_L0-CS4_LSD_state	Dot_L0-CS3_LSD_state	Dot_L0-CS2_LSD_state	Dot_L0-CS1_LSD_state	Dot_L0-CS0_LSD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-143. Dot_Isd0 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L0-CS7_LSD_state	R	0h	LED dot L0-CS7 LSD state 0h = Not short 1h = Short
6	Dot_L0-CS6_LSD_state	R	0h	LED dot L0-CS6 LSD state 0h = Not short 1h = Short
5	Dot_L0-CS5_LSD_state	R	0h	LED dot L0-CS5 LSD state 0h = Not short 1h = Short
4	Dot_L0-CS4_LSD_state	R	0h	LED dot L0-CS4 LSD state 0h = Not short 1h = Short
3	Dot_L0-CS3_LSD_state	R	0h	LED dot L0-CS3 LSD state 0h = Not short 1h = Short
2	Dot_L0-CS2_LSD_state	R	0h	LED dot L0-CS2 LSD state 0h = Not short 1h = Short
1	Dot_L0-CS1_LSD_state	R	0h	LED dot L0-CS1 LSD state 0h = Not short 1h = Short
0	Dot_L0-CS0_LSD_state	R	0h	LED dot L0-CS0 LSD state 0h = Not short 1h = Short

2.8.2 Dot_Isd1 Register (Address = 87h) [Default = 0h]

Dot_Isd1 is shown in [Figure 2-136](#) and described in [Table 2-144](#).

Return to the [Summary Table](#).

Figure 2-136. Dot_Isd1 Register

7	6	5	4	3	2	1	0
Dot_L0-CS15_LSD_state	Dot_L0-CS14_LSD_state	Dot_L0-CS13_LSD_state	Dot_L0-CS12_LSD_state	Dot_L0-CS11_LSD_state	Dot_L0-CS10_LSD_state	Dot_L0-CS9_LSD_state	Dot_L0-CS8_LSD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-144. Dot_Isd1 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L0-CS15_LSD_state	R	0h	LED dot L0-CS15 LSD state 0h = Not short 1h = Short

Table 2-144. Dot_Isd1 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
6	Dot_L0-CS14_LSD_state	R	0h	LED dot L0-CS14 LSD state 0h = Not short 1h = Short
5	Dot_L0-CS13_LSD_state	R	0h	LED dot L0-CS13 LSD state 0h = Not short 1h = Short
4	Dot_L0-CS12_LSD_state	R	0h	LED dot L0-CS12 LSD state 0h = Not short 1h = Short
3	Dot_L0-CS11_LSD_state	R	0h	LED dot L0-CS11 LSD state 0h = Not short 1h = Short
2	Dot_L0-CS10_LSD_state	R	0h	LED dot L0-CS10 LSD state 0h = Not short 1h = Short
1	Dot_L0-CS9_LSD_state	R	0h	LED dot L0-CS9 LSD state 0h = Not short 1h = Short
0	Dot_L0-CS8_LSD_state	R	0h	LED dot L0-CS8 LSD state 0h = Not short 1h = Short

2.8.3 Dot_Isd2 Register (Address = 88h) [Default = 0h]

Dot_Isd2 is shown in [Figure 2-137](#) and described in [Table 2-145](#).

Return to the [Summary Table](#).

Figure 2-137. Dot_Isd2 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L0- CS17_LSD_stat e	Dot_L0- CS16_LSD_stat e
R-0h						R-0h	R-0h

Table 2-145. Dot_Isd2 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L0-CS17_LSD_state	R	0h	LED dot L0-CS17 LSD state 0h = Not short 1h = Short
0	Dot_L0-CS16_LSD_state	R	0h	LED dot L0-CS16 LSD state 0h = Not short 1h = Short

2.8.4 Dot_Isd3 Register (Address = 89h) [Default = 0h]

Dot_Isd3 is shown in [Figure 2-138](#) and described in [Table 2-146](#).

Return to the [Summary Table](#).

Figure 2-138. Dot_Isd3 Register

7	6	5	4	3	2	1	0
Dot_L1- CS7_LSD_state	Dot_L1- CS6_LSD_state	Dot_L1- CS5_LSD_state	Dot_L1- CS4_LSD_state	Dot_L1- CS3_LSD_state	Dot_L1- CS2_LSD_state	Dot_L1- CS1_LSD_state	Dot_L1- CS0_LSD_state

Figure 2-138. Dot_Isd3 Register (continued)

R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h
------	------	------	------	------	------	------	------

Table 2-146. Dot_Isd3 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L1-CS7_LSD_state	R	0h	LED dot L1-CS7 LSD state 0h = Not short 1h = Short
6	Dot_L1-CS6_LSD_state	R	0h	LED dot L1-CS6 LSD state 0h = Not short 1h = Short
5	Dot_L1-CS5_LSD_state	R	0h	LED dot L1-CS5 LSD state 0h = Not short 1h = Short
4	Dot_L1-CS4_LSD_state	R	0h	LED dot L1-CS4 LSD state 0h = Not short 1h = Short
3	Dot_L1-CS3_LSD_state	R	0h	LED dot L1-CS3 LSD state 0h = Not short 1h = Short
2	Dot_L1-CS2_LSD_state	R	0h	LED dot L1-CS2 LSD state 0h = Not short 1h = Short
1	Dot_L1-CS1_LSD_state	R	0h	LED dot L1-CS1 LSD state 0h = Not short 1h = Short
0	Dot_L1-CS0_LSD_state	R	0h	LED dot L1-CS0 LSD state 0h = Not short 1h = Short

2.8.5 Dot_Isd4 Register (Address = 8Ah) [Default = 0h]

Dot_Isd4 is shown in [Figure 2-139](#) and described in [Table 2-147](#).

Return to the [Summary Table](#).

Figure 2-139. Dot_Isd4 Register

7	6	5	4	3	2	1	0
Dot_L1-CS15_LSD_state	Dot_L1-CS14_LSD_state	Dot_L1-CS13_LSD_state	Dot_L1-CS12_LSD_state	Dot_L1-CS11_LSD_state	Dot_L1-CS10_LSD_state	Dot_L1-CS9_LSD_state	Dot_L1-CS8_LSD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-147. Dot_Isd4 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L1-CS15_LSD_state	R	0h	LED dot L1-CS15 LSD state 0h = Not short 1h = Short
6	Dot_L1-CS14_LSD_state	R	0h	LED dot L1-CS14 LSD state 0h = Not short 1h = Short
5	Dot_L1-CS13_LSD_state	R	0h	LED dot L1-CS13 LSD state 0h = Not short 1h = Short
4	Dot_L1-CS12_LSD_state	R	0h	LED dot L1-CS12 LSD state 0h = Not short 1h = Short

Table 2-147. Dot_Isd4 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
3	Dot_L1-CS11_LSD_state	R	0h	LED dot L1-CS11 LSD state 0h = Not short 1h = Short
2	Dot_L1-CS10_LSD_state	R	0h	LED dot L1-CS10 LSD state 0h = Not short 1h = Short
1	Dot_L1-CS9_LSD_state	R	0h	LED dot L1-CS9 LSD state 0h = Not short 1h = Short
0	Dot_L1-CS8_LSD_state	R	0h	LED dot L1-CS8 LSD state 0h = Not short 1h = Short

2.8.6 Dot_Isd5 Register (Address = 8Bh) [Default = 0h]

Dot_Isd5 is shown in [Figure 2-140](#) and described in [Table 2-148](#).

Return to the [Summary Table](#).

Figure 2-140. Dot_Isd5 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L1- CS17_LSD_stat e	Dot_L1- CS16_LSD_stat e
R-0h						R-0h	R-0h

Table 2-148. Dot_Isd5 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L1-CS17_LSD_state	R	0h	LED dot L1-CS17 LSD state 0h = Not short 1h = Short
0	Dot_L1-CS16_LSD_state	R	0h	LED dot L1-CS16 LSD state 0h = Not short 1h = Short

2.8.7 Dot_Isd6 Register (Address = 8Ch) [Default = 0h]

Dot_Isd6 is shown in [Figure 2-141](#) and described in [Table 2-149](#).

Return to the [Summary Table](#).

Figure 2-141. Dot_Isd6 Register

7	6	5	4	3	2	1	0
Dot_L2- CS7_LSD_state	Dot_L2- CS6_LSD_state	Dot_L2- CS5_LSD_state	Dot_L2- CS4_LSD_state	Dot_L2- CS3_LSD_state	Dot_L2- CS2_LSD_state	Dot_L2- CS1_LSD_state	Dot_L2- CS0_LSD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-149. Dot_Isd6 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L2-CS7_LSD_state	R	0h	LED dot L2-CS7 LSD state 0h = Not short 1h = Short

Table 2-149. Dot_Isd6 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
6	Dot_L2-CS6_LSD_state	R	0h	LED dot L2-CS6 LSD state 0h = Not short 1h = Short
5	Dot_L2-CS5_LSD_state	R	0h	LED dot L2-CS5 LSD state 0h = Not short 1h = Short
4	Dot_L2-CS4_LSD_state	R	0h	LED dot L2-CS4 LSD state 0h = Not short 1h = Short
3	Dot_L2-CS3_LSD_state	R	0h	LED dot L2-CS3 LSD state 0h = Not short 1h = Short
2	Dot_L2-CS2_LSD_state	R	0h	LED dot L2-CS2 LSD state 0h = Not short 1h = Short
1	Dot_L2-CS1_LSD_state	R	0h	LED dot L2-CS1 LSD state 0h = Not short 1h = Short
0	Dot_L2-CS0_LSD_state	R	0h	LED dot L2-CS0 LSD state 0h = Not short 1h = Short

2.8.8 Dot_Isd7 Register (Address = 8Dh) [Default = 0h]

Dot_Isd7 is shown in [Figure 2-142](#) and described in [Table 2-150](#).

Return to the [Summary Table](#).

Figure 2-142. Dot_Isd7 Register

7	6	5	4	3	2	1	0
Dot_L2-CS15_LSD_state	Dot_L2-CS14_LSD_state	Dot_L2-CS13_LSD_state	Dot_L2-CS12_LSD_state	Dot_L2-CS11_LSD_state	Dot_L2-CS10_LSD_state	Dot_L2-CS9_LSD_state	Dot_L2-CS8_LSD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-150. Dot_Isd7 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L2-CS15_LSD_state	R	0h	LED dot L2-CS15 LSD state 0h = Not short 1h = Short
6	Dot_L2-CS14_LSD_state	R	0h	LED dot L2-CS14 LSD state 0h = Not short 1h = Short
5	Dot_L2-CS13_LSD_state	R	0h	LED dot L2-CS13 LSD state 0h = Not short 1h = Short
4	Dot_L2-CS12_LSD_state	R	0h	LED dot L2-CS12 LSD state 0h = Not short 1h = Short
3	Dot_L2-CS11_LSD_state	R	0h	LED dot L2-CS11 LSD state 0h = Not short 1h = Short
2	Dot_L2-CS10_LSD_state	R	0h	LED dot L2-CS10 LSD state 0h = Not short 1h = Short

Table 2-150. Dot_Isd7 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
1	Dot_L2-CS9_LSD_state	R	0h	LED dot L2-CS9 LSD state 0h = Not short 1h = Short
0	Dot_L2-CS8_LSD_state	R	0h	LED dot L2-CS8 LSD state 0h = Not short 1h = Short

2.8.9 Dot_Isd8 Register (Address = 8Eh) [Default = 0h]

Dot_Isd8 is shown in [Figure 2-143](#) and described in [Table 2-151](#).

Return to the [Summary Table](#).

Figure 2-143. Dot_Isd8 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L2- CS17_LSD_stat e	Dot_L2- CS16_LSD_stat e
R-0h						R-0h	R-0h

Table 2-151. Dot_Isd8 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L2-CS17_LSD_state	R	0h	LED dot L2-CS17 LSD state 0h = Not short 1h = Short
0	Dot_L2-CS16_LSD_state	R	0h	LED dot L2-CS16 LSD state 0h = Not short 1h = Short

2.8.10 Dot_Isd9 Register (Address = 8Fh) [Default = 0h]

Dot_Isd9 is shown in [Figure 2-144](#) and described in [Table 2-152](#).

Return to the [Summary Table](#).

Figure 2-144. Dot_Isd9 Register

7	6	5	4	3	2	1	0
Dot_L3- CS7_LSD_state	Dot_L3- CS6_LSD_state	Dot_L3- CS5_LSD_state	Dot_L3- CS4_LSD_state	Dot_L3- CS3_LSD_state	Dot_L3- CS2_LSD_state	Dot_L3- CS1_LSD_state	Dot_L3- CS0_LSD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-152. Dot_Isd9 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L3-CS7_LSD_state	R	0h	LED dot L3-CS7 LSD state 0h = Not short 1h = Short
6	Dot_L3-CS6_LSD_state	R	0h	LED dot L3-CS6 LSD state 0h = Not short 1h = Short
5	Dot_L3-CS5_LSD_state	R	0h	LED dot L3-CS5 LSD state 0h = Not short 1h = Short

Table 2-152. Dot_Isd9 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
4	Dot_L3-CS4_LSD_state	R	0h	LED dot L3-CS4 LSD state 0h = Not short 1h = Short
3	Dot_L3-CS3_LSD_state	R	0h	LED dot L3-CS3 LSD state 0h = Not short 1h = Short
2	Dot_L3-CS2_LSD_state	R	0h	LED dot L3-CS2 LSD state 0h = Not short 1h = Short
1	Dot_L3-CS1_LSD_state	R	0h	LED dot L3-CS1 LSD state 0h = Not short 1h = Short
0	Dot_L3-CS0_LSD_state	R	0h	LED dot L3-CS0 LSD state 0h = Not short 1h = Short

2.8.11 Dot_Isd10 Register (Address = 90h) [Default = 0h]

Dot_Isd10 is shown in [Figure 2-145](#) and described in [Table 2-153](#).

Return to the [Summary Table](#).

Figure 2-145. Dot_Isd10 Register

7	6	5	4	3	2	1	0
Dot_L3-CS15_LSD_state	Dot_L3-CS14_LSD_state	Dot_L3-CS13_LSD_state	Dot_L3-CS12_LSD_state	Dot_L3-CS11_LSD_state	Dot_L3-CS10_LSD_state	Dot_L3-CS9_LSD_state	Dot_L3-CS8_LSD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-153. Dot_Isd10 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L3-CS15_LSD_state	R	0h	LED dot L3-CS15 LSD state 0h = Not short 1h = Short
6	Dot_L3-CS14_LSD_state	R	0h	LED dot L3-CS14 LSD state 0h = Not short 1h = Short
5	Dot_L3-CS13_LSD_state	R	0h	LED dot L3-CS13 LSD state 0h = Not short 1h = Short
4	Dot_L3-CS12_LSD_state	R	0h	LED dot L3-CS12 LSD state 0h = Not short 1h = Short
3	Dot_L3-CS11_LSD_state	R	0h	LED dot L3-CS11 LSD state 0h = Not short 1h = Short
2	Dot_L3-CS10_LSD_state	R	0h	LED dot L3-CS10 LSD state 0h = Not short 1h = Short
1	Dot_L3-CS9_LSD_state	R	0h	LED dot L3-CS9 LSD state 0h = Not short 1h = Short
0	Dot_L3-CS8_LSD_state	R	0h	LED dot L3-CS8 LSD state 0h = Not short 1h = Short

2.8.12 Dot_Isd11 Register (Address = 91h) [Default = 0h]

Dot_Isd11 is shown in [Figure 2-146](#) and described in [Table 2-154](#).

Return to the [Summary Table](#).

Figure 2-146. Dot_Isd11 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L3- CS17_LSD_stat e	Dot_L3- CS16_LSD_stat e
R-0h						R-0h	R-0h

Table 2-154. Dot_Isd11 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L3-CS17_LSD_state	R	0h	LED dot L3-CS17 LSD state 0h = Not short 1h = Short
0	Dot_L3-CS16_LSD_state	R	0h	LED dot L3-CS16 LSD state 0h = Not short 1h = Short

2.8.13 Dot_Isd12 Register (Address = 92h) [Default = 0h]

Dot_Isd12 is shown in [Figure 2-147](#) and described in [Table 2-155](#).

Return to the [Summary Table](#).

Figure 2-147. Dot_Isd12 Register

7	6	5	4	3	2	1	0
Dot_L4- CS7_LSD_state	Dot_L4- CS6_LSD_state	Dot_L4- CS5_LSD_state	Dot_L4- CS4_LSD_state	Dot_L4- CS3_LSD_state	Dot_L4- CS2_LSD_state	Dot_L4- CS1_LSD_state	Dot_L4- CS0_LSD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-155. Dot_Isd12 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L4-CS7_LSD_state	R	0h	LED dot L4-CS7 LSD state 0h = Not short 1h = Short
6	Dot_L4-CS6_LSD_state	R	0h	LED dot L4-CS6 LSD state 0h = Not short 1h = Short
5	Dot_L4-CS5_LSD_state	R	0h	LED dot L4-CS5 LSD state 0h = Not short 1h = Short
4	Dot_L4-CS4_LSD_state	R	0h	LED dot L4-CS4 LSD state 0h = Not short 1h = Short
3	Dot_L4-CS3_LSD_state	R	0h	LED dot L4-CS3 LSD state 0h = Not short 1h = Short
2	Dot_L4-CS2_LSD_state	R	0h	LED dot L4-CS2 LSD state 0h = Not short 1h = Short
1	Dot_L4-CS1_LSD_state	R	0h	LED dot L4-CS1 LSD state 0h = Not short 1h = Short

Table 2-155. Dot_Isd12 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
0	Dot_L4-CS0_LSD_state	R	0h	LED dot L4-CS0 LSD state 0h = Not short 1h = Short

2.8.14 Dot_Isd13 Register (Address = 93h) [Default = 0h]

Dot_Isd13 is shown in [Figure 2-148](#) and described in [Table 2-156](#).

Return to the [Summary Table](#).

Figure 2-148. Dot_Isd13 Register

7	6	5	4	3	2	1	0
Dot_L4-CS15_LSD_state	Dot_L4-CS14_LSD_state	Dot_L4-CS13_LSD_state	Dot_L4-CS12_LSD_state	Dot_L4-CS11_LSD_state	Dot_L4-CS10_LSD_state	Dot_L4-CS9_LSD_state	Dot_L4-CS8_LSD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-156. Dot_Isd13 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L4-CS15_LSD_state	R	0h	LED dot L4-CS15 LSD state 0h = Not short 1h = Short
6	Dot_L4-CS14_LSD_state	R	0h	LED dot L4-CS14 LSD state 0h = Not short 1h = Short
5	Dot_L4-CS13_LSD_state	R	0h	LED dot L4-CS13 LSD state 0h = Not short 1h = Short
4	Dot_L4-CS12_LSD_state	R	0h	LED dot L4-CS12 LSD state 0h = Not short 1h = Short
3	Dot_L4-CS11_LSD_state	R	0h	LED dot L4-CS11 LSD state 0h = Not short 1h = Short
2	Dot_L4-CS10_LSD_state	R	0h	LED dot L4-CS10 LSD state 0h = Not short 1h = Short
1	Dot_L4-CS9_LSD_state	R	0h	LED dot L4-CS9 LSD state 0h = Not short 1h = Short
0	Dot_L4-CS8_LSD_state	R	0h	LED dot L4-CS8 LSD state 0h = Not short 1h = Short

2.8.15 Dot_Isd14 Register (Address = 94h) [Default = 0h]

Dot_Isd14 is shown in [Figure 2-149](#) and described in [Table 2-157](#).

Return to the [Summary Table](#).

Figure 2-149. Dot_Isd14 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L4-CS17_LSD_state	Dot_L4-CS16_LSD_state

Figure 2-149. Dot_Isd14 Register (continued)

R-0h	R-0h	R-0h
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Table 2-157. Dot_Isd14 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L4-CS17_LSD_state	R	0h	LED dot L4-CS17 LSD state 0h = Not short 1h = Short
0	Dot_L4-CS16_LSD_state	R	0h	LED dot L4-CS16 LSD state 0h = Not short 1h = Short

2.8.16 Dot_Isd15 Register (Address = 95h) [Default = 0h]

Dot_Isd15 is shown in [Figure 2-150](#) and described in [Table 2-158](#).

Return to the [Summary Table](#).

Figure 2-150. Dot_Isd15 Register

7	6	5	4	3	2	1	0
Dot_L5-CS7_LSD_state	Dot_L5-CS6_LSD_state	Dot_L5-CS5_LSD_state	Dot_L5-CS4_LSD_state	Dot_L5-CS3_LSD_state	Dot_L5-CS2_LSD_state	Dot_L5-CS1_LSD_state	Dot_L5-CS0_LSD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-158. Dot_Isd15 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L5-CS7_LSD_state	R	0h	LED dot L5-CS7 LSD state 0h = Not short 1h = Short
6	Dot_L5-CS6_LSD_state	R	0h	LED dot L5-CS6 LSD state 0h = Not short 1h = Short
5	Dot_L5-CS5_LSD_state	R	0h	LED dot L5-CS5 LSD state 0h = Not short 1h = Short
4	Dot_L5-CS4_LSD_state	R	0h	LED dot L5-CS4 LSD state 0h = Not short 1h = Short
3	Dot_L5-CS3_LSD_state	R	0h	LED dot L5-CS3 LSD state 0h = Not short 1h = Short
2	Dot_L5-CS2_LSD_state	R	0h	LED dot L5-CS2 LSD state 0h = Not short 1h = Short
1	Dot_L5-CS1_LSD_state	R	0h	LED dot L5-CS1 LSD state 0h = Not short 1h = Short
0	Dot_L5-CS0_LSD_state	R	0h	LED dot L5-CS0 LSD state 0h = Not short 1h = Short

2.8.17 Dot_Isd16 Register (Address = 96h) [Default = 0h]

Dot_Isd16 is shown in [Figure 2-151](#) and described in [Table 2-159](#).

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Figure 2-151. Dot_Isd16 Register

7	6	5	4	3	2	1	0
Dot_L5-CS15_LSD_state	Dot_L5-CS14_LSD_state	Dot_L5-CS13_LSD_state	Dot_L5-CS12_LSD_state	Dot_L5-CS11_LSD_state	Dot_L5-CS10_LSD_state	Dot_L5-CS9_LSD_state	Dot_L5-CS8_LSD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-159. Dot_Isd16 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L5-CS15_LSD_state	R	0h	LED dot L5-CS15 LSD state 0h = Not short 1h = Short
6	Dot_L5-CS14_LSD_state	R	0h	LED dot L5-CS14 LSD state 0h = Not short 1h = Short
5	Dot_L5-CS13_LSD_state	R	0h	LED dot L5-CS13 LSD state 0h = Not short 1h = Short
4	Dot_L5-CS12_LSD_state	R	0h	LED dot L5-CS12 LSD state 0h = Not short 1h = Short
3	Dot_L5-CS11_LSD_state	R	0h	LED dot L5-CS11 LSD state 0h = Not short 1h = Short
2	Dot_L5-CS10_LSD_state	R	0h	LED dot L5-CS10 LSD state 0h = Not short 1h = Short
1	Dot_L5-CS9_LSD_state	R	0h	LED dot L5-CS9 LSD state 0h = Not short 1h = Short
0	Dot_L5-CS8_LSD_state	R	0h	LED dot L5-CS8 LSD state 0h = Not short 1h = Short

2.8.18 Dot_Isd17 Register (Address = 97h) [Default = 0h]

Dot_Isd17 is shown in [Figure 2-152](#) and described in [Table 2-160](#).

Return to the [Summary Table](#).

Figure 2-152. Dot_Isd17 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L5-CS17_LSD_state	Dot_L5-CS16_LSD_state
R-0h						R-0h	R-0h

Table 2-160. Dot_Isd17 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L5-CS17_LSD_state	R	0h	LED dot L5-CS17 LSD state 0h = Not short 1h = Short
0	Dot_L5-CS16_LSD_state	R	0h	LED dot L5-CS16 LSD state 0h = Not short 1h = Short

2.8.19 Dot_Isd18 Register (Address = 98h) [Default = 0h]

Dot_Isd18 is shown in [Figure 2-153](#) and described in [Table 2-161](#).

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Figure 2-153. Dot_Isd18 Register

7	6	5	4	3	2	1	0
Dot_L6-CS7_LSD_state	Dot_L6-CS6_LSD_state	Dot_L6-CS5_LSD_state	Dot_L6-CS4_LSD_state	Dot_L6-CS3_LSD_state	Dot_L6-CS2_LSD_state	Dot_L6-CS1_LSD_state	Dot_L6-CS0_LSD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-161. Dot_Isd18 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L6-CS7_LSD_state	R	0h	LED dot L6-CS7 LSD state 0h = Not short 1h = Short
6	Dot_L6-CS6_LSD_state	R	0h	LED dot L6-CS6 LSD state 0h = Not short 1h = Short
5	Dot_L6-CS5_LSD_state	R	0h	LED dot L6-CS5 LSD state 0h = Not short 1h = Short
4	Dot_L6-CS4_LSD_state	R	0h	LED dot L6-CS4 LSD state 0h = Not short 1h = Short
3	Dot_L6-CS3_LSD_state	R	0h	LED dot L6-CS3 LSD state 0h = Not short 1h = Short
2	Dot_L6-CS2_LSD_state	R	0h	LED dot L6-CS2 LSD state 0h = Not short 1h = Short
1	Dot_L6-CS1_LSD_state	R	0h	LED dot L6-CS1 LSD state 0h = Not short 1h = Short
0	Dot_L6-CS0_LSD_state	R	0h	LED dot L6-CS0 LSD state 0h = Not short 1h = Short

2.8.20 Dot_Isd19 Register (Address = 99h) [Default = 0h]

Dot_Isd19 is shown in [Figure 2-154](#) and described in [Table 2-162](#).

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Figure 2-154. Dot_Isd19 Register

7	6	5	4	3	2	1	0
Dot_L6-CS15_LSD_state	Dot_L6-CS14_LSD_state	Dot_L6-CS13_LSD_state	Dot_L6-CS12_LSD_state	Dot_L6-CS11_LSD_state	Dot_L6-CS10_LSD_state	Dot_L6-CS9_LSD_state	Dot_L6-CS8_LSD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-162. Dot_Isd19 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L6-CS15_LSD_state	R	0h	LED dot L6-CS15 LSD state 0h = Not short 1h = Short

Table 2-162. Dot_Isd19 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
6	Dot_L6-CS14_LSD_state	R	0h	LED dot L6-CS14 LSD state 0h = Not short 1h = Short
5	Dot_L6-CS13_LSD_state	R	0h	LED dot L6-CS13 LSD state 0h = Not short 1h = Short
4	Dot_L6-CS12_LSD_state	R	0h	LED dot L6-CS12 LSD state 0h = Not short 1h = Short
3	Dot_L6-CS11_LSD_state	R	0h	LED dot L6-CS11 LSD state 0h = Not short 1h = Short
2	Dot_L6-CS10_LSD_state	R	0h	LED dot L6-CS10 LSD state 0h = Not short 1h = Short
1	Dot_L6-CS9_LSD_state	R	0h	LED dot L6-CS9 LSD state 0h = Not short 1h = Short
0	Dot_L6-CS8_LSD_state	R	0h	LED dot L6-CS8 LSD state 0h = Not short 1h = Short

2.8.21 Dot_Isd20 Register (Address = 9Ah) [Default = 0h]

Dot_Isd20 is shown in [Figure 2-155](#) and described in [Table 2-163](#).

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Figure 2-155. Dot_Isd20 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L6- CS17_LSD_stat e	Dot_L6- CS16_LSD_stat e
R-0h						R-0h	R-0h

Table 2-163. Dot_Isd20 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L6-CS17_LSD_state	R	0h	LED dot L6-CS17 LSD state 0h = Not short 1h = Short
0	Dot_L6-CS16_LSD_state	R	0h	LED dot L6-CS16 LSD state 0h = Not short 1h = Short

2.8.22 Dot_Isd21 Register (Address = 9Bh) [Default = 0h]

Dot_Isd21 is shown in [Figure 2-156](#) and described in [Table 2-164](#).

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Figure 2-156. Dot_Isd21 Register

7	6	5	4	3	2	1	0
Dot_L7- CS7_LSD_state	Dot_L7- CS6_LSD_state	Dot_L7- CS5_LSD_state	Dot_L7- CS4_LSD_state	Dot_L7- CS3_LSD_state	Dot_L7- CS2_LSD_state	Dot_L7- CS1_LSD_state	Dot_L7- CS0_LSD_state

Figure 2-156. Dot_Isd21 Register (continued)

R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h
------	------	------	------	------	------	------	------

Table 2-164. Dot_Isd21 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L7-CS7_LSD_state	R	0h	LED dot L7-CS7 LSD state 0h = Not short 1h = Short
6	Dot_L7-CS6_LSD_state	R	0h	LED dot L7-CS6 LSD state 0h = Not short 1h = Short
5	Dot_L7-CS5_LSD_state	R	0h	LED dot L7-CS5 LSD state 0h = Not short 1h = Short
4	Dot_L7-CS4_LSD_state	R	0h	LED dot L7-CS4 LSD state 0h = Not short 1h = Short
3	Dot_L7-CS3_LSD_state	R	0h	LED dot L7-CS3 LSD state 0h = Not short 1h = Short
2	Dot_L7-CS2_LSD_state	R	0h	LED dot L7-CS2 LSD state 0h = Not short 1h = Short
1	Dot_L7-CS1_LSD_state	R	0h	LED dot L7-CS1 LSD state 0h = Not short 1h = Short
0	Dot_L7-CS0_LSD_state	R	0h	LED dot L7-CS0 LSD state 0h = Not short 1h = Short

2.8.23 Dot_Isd22 Register (Address = 9Ch) [Default = 0h]

Dot_Isd22 is shown in [Figure 2-157](#) and described in [Table 2-165](#).

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Figure 2-157. Dot_Isd22 Register

7	6	5	4	3	2	1	0
Dot_L7- CS15_LSD_stat e	Dot_L7- CS14_LSD_stat e	Dot_L7- CS13_LSD_stat e	Dot_L7- CS12_LSD_stat e	Dot_L7- CS11_LSD_stat e	Dot_L7- CS10_LSD_stat e	Dot_L7- CS9_LSD_state	Dot_L7- CS8_LSD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-165. Dot_Isd22 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L7-CS15_LSD_state	R	0h	LED dot L7-CS15 LSD state 0h = Not short 1h = Short
6	Dot_L7-CS14_LSD_state	R	0h	LED dot L7-CS14 LSD state 0h = Not short 1h = Short
5	Dot_L7-CS13_LSD_state	R	0h	LED dot L7-CS13 LSD state 0h = Not short 1h = Short
4	Dot_L7-CS12_LSD_state	R	0h	LED dot L7-CS12 LSD state 0h = Not short 1h = Short

Table 2-165. Dot_Isd22 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
3	Dot_L7-CS11_LSD_state	R	0h	LED dot L7-CS11 LSD state 0h = Not short 1h = Short
2	Dot_L7-CS10_LSD_state	R	0h	LED dot L7-CS10 LSD state 0h = Not short 1h = Short
1	Dot_L7-CS9_LSD_state	R	0h	LED dot L7-CS9 LSD state 0h = Not short 1h = Short
0	Dot_L7-CS8_LSD_state	R	0h	LED dot L7-CS8 LSD state 0h = Not short 1h = Short

2.8.24 Dot_Isd23 Register (Address = 9Dh) [Default = 0h]

Dot_Isd23 is shown in [Figure 2-158](#) and described in [Table 2-166](#).

Return to the [Summary Table](#).

Figure 2-158. Dot_Isd23 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L7- CS17_LSD_stat e	Dot_L7- CS16_LSD_stat e
R-0h						R-0h	R-0h

Table 2-166. Dot_Isd23 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L7-CS17_LSD_state	R	0h	LED dot L7-CS17 LSD state 0h = Not short 1h = Short
0	Dot_L7-CS16_LSD_state	R	0h	LED dot L7-CS16 LSD state 0h = Not short 1h = Short

2.8.25 Dot_Isd24 Register (Address = 9Eh) [Default = 0h]

Dot_Isd24 is shown in [Figure 2-159](#) and described in [Table 2-167](#).

Return to the [Summary Table](#).

Figure 2-159. Dot_Isd24 Register

7	6	5	4	3	2	1	0
Dot_L8- CS7_LSD_state	Dot_L8- CS6_LSD_state	Dot_L8- CS5_LSD_state	Dot_L8- CS4_LSD_state	Dot_L8- CS3_LSD_state	Dot_L8- CS2_LSD_state	Dot_L8- CS1_LSD_state	Dot_L8- CS0_LSD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-167. Dot_Isd24 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L8-CS7_LSD_state	R	0h	LED dot L8-CS7 LSD state 0h = Not short 1h = Short

Table 2-167. Dot_Isd24 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
6	Dot_L8-CS6_LSD_state	R	0h	LED dot L8-CS6 LSD state 0h = Not short 1h = Short
5	Dot_L8-CS5_LSD_state	R	0h	LED dot L8-CS5 LSD state 0h = Not short 1h = Short
4	Dot_L8-CS4_LSD_state	R	0h	LED dot L8-CS4 LSD state 0h = Not short 1h = Short
3	Dot_L8-CS3_LSD_state	R	0h	LED dot L8-CS3 LSD state 0h = Not short 1h = Short
2	Dot_L8-CS2_LSD_state	R	0h	LED dot L8-CS2 LSD state 0h = Not short 1h = Short
1	Dot_L8-CS1_LSD_state	R	0h	LED dot L8-CS1 LSD state 0h = Not short 1h = Short
0	Dot_L8-CS0_LSD_state	R	0h	LED dot L8-CS0 LSD state 0h = Not short 1h = Short

2.8.26 Dot_Isd25 Register (Address = 9Fh) [Default = 0h]

Dot_Isd25 is shown in [Figure 2-160](#) and described in [Table 2-168](#).

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Figure 2-160. Dot_Isd25 Register

7	6	5	4	3	2	1	0
Dot_L8-CS15_LSD_state	Dot_L8-CS14_LSD_state	Dot_L8-CS13_LSD_state	Dot_L8-CS12_LSD_state	Dot_L8-CS11_LSD_state	Dot_L8-CS10_LSD_state	Dot_L8-CS9_LSD_state	Dot_L8-CS8_LSD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-168. Dot_Isd25 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L8-CS15_LSD_state	R	0h	LED dot L8-CS15 LSD state 0h = Not short 1h = Short
6	Dot_L8-CS14_LSD_state	R	0h	LED dot L8-CS14 LSD state 0h = Not short 1h = Short
5	Dot_L8-CS13_LSD_state	R	0h	LED dot L8-CS13 LSD state 0h = Not short 1h = Short
4	Dot_L8-CS12_LSD_state	R	0h	LED dot L8-CS12 LSD state 0h = Not short 1h = Short
3	Dot_L8-CS11_LSD_state	R	0h	LED dot L8-CS11 LSD state 0h = Not short 1h = Short
2	Dot_L8-CS10_LSD_state	R	0h	LED dot L8-CS10 LSD state 0h = Not short 1h = Short

Table 2-168. Dot_Isd25 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
1	Dot_L8-CS9_LSD_state	R	0h	LED dot L8-CS9 LSD state 0h = Not short 1h = Short
0	Dot_L8-CS8_LSD_state	R	0h	LED dot L8-CS8 LSD state 0h = Not short 1h = Short

2.8.27 Dot_Isd26 Register (Address = A0h) [Default = 0h]

Dot_Isd26 is shown in [Figure 2-161](#) and described in [Table 2-169](#).

Return to the [Summary Table](#).

Figure 2-161. Dot_Isd26 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L8- CS17_LSD_stat e	Dot_L8- CS16_LSD_stat e
R-0h						R-0h	R-0h

Table 2-169. Dot_Isd26 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L8-CS17_LSD_state	R	0h	LED dot L8-CS17 LSD state 0h = Not short 1h = Short
0	Dot_L8-CS16_LSD_state	R	0h	LED dot L8-CS16 LSD state 0h = Not short 1h = Short

2.8.28 Dot_Isd27 Register (Address = A1h) [Default = 0h]

Dot_Isd27 is shown in [Figure 2-162](#) and described in [Table 2-170](#).

Return to the [Summary Table](#).

Figure 2-162. Dot_Isd27 Register

7	6	5	4	3	2	1	0
Dot_L9- CS7_LSD_state	Dot_L9- CS6_LSD_state	Dot_L9- CS5_LSD_state	Dot_L9- CS4_LSD_state	Dot_L9- CS3_LSD_state	Dot_L9- CS2_LSD_state	Dot_L9- CS1_LSD_state	Dot_L9- CS0_LSD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-170. Dot_Isd27 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L9-CS7_LSD_state	R	0h	LED dot L9-CS7 LSD state 0h = Not short 1h = Short
6	Dot_L9-CS6_LSD_state	R	0h	LED dot L9-CS6 LSD state 0h = Not short 1h = Short
5	Dot_L9-CS5_LSD_state	R	0h	LED dot L9-CS5 LSD state 0h = Not short 1h = Short

Table 2-170. Dot_Isd27 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
4	Dot_L9-CS4_LSD_state	R	0h	LED dot L9-CS4 LSD state 0h = Not short 1h = Short
3	Dot_L9-CS3_LSD_state	R	0h	LED dot L9-CS3 LSD state 0h = Not short 1h = Short
2	Dot_L9-CS2_LSD_state	R	0h	LED dot L9-CS2 LSD state 0h = Not short 1h = Short
1	Dot_L9-CS1_LSD_state	R	0h	LED dot L9-CS1 LSD state 0h = Not short 1h = Short
0	Dot_L9-CS0_LSD_state	R	0h	LED dot L9-CS0 LSD state 0h = Not short 1h = Short

2.8.29 Dot_Isd28 Register (Address = A2h) [Default = 0h]

Dot_Isd28 is shown in [Figure 2-163](#) and described in [Table 2-171](#).

Return to the [Summary Table](#).

Figure 2-163. Dot_Isd28 Register

7	6	5	4	3	2	1	0
Dot_L9-CS15_LSD_state	Dot_L9-CS14_LSD_state	Dot_L9-CS13_LSD_state	Dot_L9-CS12_LSD_state	Dot_L9-CS11_LSD_state	Dot_L9-CS10_LSD_state	Dot_L9-CS9_LSD_state	Dot_L9-CS8_LSD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-171. Dot_Isd28 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L9-CS15_LSD_state	R	0h	LED dot L9-CS15 LSD state 0h = Not short 1h = Short
6	Dot_L9-CS14_LSD_state	R	0h	LED dot L9-CS14 LSD state 0h = Not short 1h = Short
5	Dot_L9-CS13_LSD_state	R	0h	LED dot L9-CS13 LSD state 0h = Not short 1h = Short
4	Dot_L9-CS12_LSD_state	R	0h	LED dot L9-CS12 LSD state 0h = Not short 1h = Short
3	Dot_L9-CS11_LSD_state	R	0h	LED dot L9-CS11 LSD state 0h = Not short 1h = Short
2	Dot_L9-CS10_LSD_state	R	0h	LED dot L9-CS10 LSD state 0h = Not short 1h = Short
1	Dot_L9-CS9_LSD_state	R	0h	LED dot L9-CS9 LSD state 0h = Not short 1h = Short
0	Dot_L9-CS8_LSD_state	R	0h	LED dot L9-CS8 LSD state 0h = Not short 1h = Short

2.8.30 Dot_Isd29 Register (Address = A3h) [Default = 0h]

Dot_Isd29 is shown in [Figure 2-164](#) and described in [Table 2-172](#).

Return to the [Summary Table](#).

Figure 2-164. Dot_Isd29 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L9- CS17_LSD_stat e	Dot_L9- CS16_LSD_stat e
R-0h						R-0h	R-0h

Table 2-172. Dot_Isd29 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L9-CS17_LSD_state	R	0h	LED dot L9-CS17 LSD state 0h = Not short 1h = Short
0	Dot_L9-CS16_LSD_state	R	0h	LED dot L9-CS16 LSD state 0h = Not short 1h = Short

2.8.31 Dot_Isd30 Register (Address = A4h) [Default = 0h]

Dot_Isd30 is shown in [Figure 2-165](#) and described in [Table 2-173](#).

Return to the [Summary Table](#).

Figure 2-165. Dot_Isd30 Register

7	6	5	4	3	2	1	0
Dot_L10- CS7_LSD_state	Dot_L10- CS6_LSD_state	Dot_L10- CS5_LSD_state	Dot_L10- CS4_LSD_state	Dot_L10- CS3_LSD_state	Dot_L10- CS2_LSD_state	Dot_L10- CS1_LSD_state	Dot_L10- CS0_LSD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-173. Dot_Isd30 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L10-CS7_LSD_state	R	0h	LED dot L10-CS7 LSD state 0h = Not short 1h = Short
6	Dot_L10-CS6_LSD_state	R	0h	LED dot L10-CS6 LSD state 0h = Not short 1h = Short
5	Dot_L10-CS5_LSD_state	R	0h	LED dot L10-CS5 LSD state 0h = Not short 1h = Short
4	Dot_L10-CS4_LSD_state	R	0h	LED dot L10-CS4 LSD state 0h = Not short 1h = Short
3	Dot_L10-CS3_LSD_state	R	0h	LED dot L10-CS3 LSD state 0h = Not short 1h = Short
2	Dot_L10-CS2_LSD_state	R	0h	LED dot L10-CS2 LSD state 0h = Not short 1h = Short
1	Dot_L10-CS1_LSD_state	R	0h	LED dot L10-CS1 LSD state 0h = Not short 1h = Short

Table 2-173. Dot_Isd30 Register Field Descriptions (continued)

Bit	Field	Type	Default	Description
0	Dot_L10-CS0_LSD_state	R	0h	LED dot L10-CS0 LSD state 0h = Not short 1h = Short

2.8.32 Dot_Isd31 Register (Address = A5h) [Default = 0h]

Dot_Isd31 is shown in [Figure 2-166](#) and described in [Table 2-174](#).

Return to the [Summary Table](#).

Figure 2-166. Dot_Isd31 Register

7	6	5	4	3	2	1	0
Dot_L10-CS15_LSD_state	Dot_L10-CS14_LSD_state	Dot_L10-CS13_LSD_state	Dot_L10-CS12_LSD_state	Dot_L10-CS11_LSD_state	Dot_L10-CS10_LSD_state	Dot_L10-CS9_LSD_state	Dot_L10-CS8_LSD_state
R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h	R-0h

Table 2-174. Dot_Isd31 Register Field Descriptions

Bit	Field	Type	Default	Description
7	Dot_L10-CS15_LSD_state	R	0h	LED dot L10-CS15 LSD state 0h = Not short 1h = Short
6	Dot_L10-CS14_LSD_state	R	0h	LED dot L10-CS14 LSD state 0h = Not short 1h = Short
5	Dot_L10-CS13_LSD_state	R	0h	LED dot L10-CS13 LSD state 0h = Not short 1h = Short
4	Dot_L10-CS12_LSD_state	R	0h	LED dot L10-CS12 LSD state 0h = Not short 1h = Short
3	Dot_L10-CS11_LSD_state	R	0h	LED dot L10-CS11 LSD state 0h = Not short 1h = Short
2	Dot_L10-CS10_LSD_state	R	0h	LED dot L10-CS10 LSD state 0h = Not short 1h = Short
1	Dot_L10-CS9_LSD_state	R	0h	LED dot L10-CS9 LSD state 0h = Not short 1h = Short
0	Dot_L10-CS8_LSD_state	R	0h	LED dot L10-CS8 LSD state 0h = Not short 1h = Short

2.8.33 Dot_Isd32 Register (Address = A6h) [Default = 0h]

Dot_Isd32 is shown in [Figure 2-167](#) and described in [Table 2-175](#).

Return to the [Summary Table](#).

Figure 2-167. Dot_Isd32 Register

7	6	5	4	3	2	1	0
RESERVED						Dot_L10-CS17_LSD_state	Dot_L10-CS16_LSD_state

Figure 2-167. Dot_Isd32 Register (continued)

R-0h

R-0h

R-0h

Table 2-175. Dot_Isd32 Register Field Descriptions

Bit	Field	Type	Default	Description
7-2	RESERVED	R	0h	Reserved
1	Dot_L10-CS17_LSD_state	R	0h	LED dot L10-CS17 LSD state 0h = Not short 1h = Short
0	Dot_L10-CS16_LSD_state	R	0h	LED dot L10-CS16 LSD state 0h = Not short 1h = Short

2.9 LOD_CLR Registers

Table 2-176 lists the LOD_CLR registers. All register offset addresses not listed in Table 2-176 should be considered as reserved locations and the register contents should not be modified.

LOD Flag Clear

Table 2-176. LOD_CLR Registers

Address	Acronym	Register Name	Section
A7h	LOD_clear	LOD flag clear register	Go

2.9.1 LOD_clear Register (Address = A7h) [Default = 0h]

LOD_clear is shown in Figure 2-168 and described in Table 2-177.

Return to the [Summary Table](#).

Figure 2-168. LOD_clear Register

7	6	5	4	3	2	1	0
RESERVED				LOD_Clear			
R-0h				W-0h			

Table 2-177. LOD_clear Register Field Descriptions

Bit	Field	Type	Default	Description
7-4	RESERVED	R	0h	Reserved
3-0	LOD_Clear	W	0h	Write Fh to clear all LOD indication bits

2.10 LSD_CLR Registers

[Table 2-178](#) lists the LSD_CLR registers. All register offset addresses not listed in [Table 2-178](#) should be considered as reserved locations and the register contents should not be modified.

LSD Flag Clear

Table 2-178. LSD_CLR Registers

Address	Acronym	Register Name	Section
A8h	LSD_clear	LSD flag clear register	Go

2.10.1 LSD_clear Register (Address = A8h) [Default = 0h]

LSD_clear is shown in [Figure 2-169](#) and described in [Table 2-179](#).

Return to the [Summary Table](#).

Figure 2-169. LSD_clear Register

7	6	5	4	3	2	1	0
RESERVED				LSD_Clear			
R-0h				W-0h			

Table 2-179. LSD_clear Register Field Descriptions

Bit	Field	Type	Default	Description
7-4	RESERVED	R	0h	Reserved
3-0	LSD_Clear	W	0h	Write Fh to clear all LSD indication bits

2.11 RESET Registers

[Table 2-180](#) lists the RESET registers. All register offset addresses not listed in [Table 2-180](#) should be considered as reserved locations and the register contents should not be modified.

Reset

Table 2-180. RESET Registers

Address	Acronym	Register Name	Section
A9h	Reset	Software reset register	Go

2.11.1 Reset Register (Address = A9h) [Default = 0h]

Reset is shown in [Figure 2-170](#) and described in [Table 2-181](#).

Return to the [Summary Table](#).

Figure 2-170. Reset Register

7	6	5	4	3	2	1	0
Reset							
W-0h							

Table 2-181. Reset Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	Reset	W	0h	Write FFh to reset the device

2.12 DC Registers

Table 2-182 lists the DC registers. All register offset addresses not listed in Table 2-182 should be considered as reserved locations and the register contents should not be modified.

Dot Correction

Table 2-182. DC Registers

Address	Acronym	Register Name	Section
100h	DC0	LED dot current setting for Dot L0-CS0	Go
101h	DC1	LED dot current setting for Dot L0-CS1	Go
102h	DC2	LED dot current setting for Dot L0-CS2	Go
103h	DC3	LED dot current setting for Dot L0-CS3	Go
104h	DC4	LED dot current setting for Dot L0-CS4	Go
105h	DC5	LED dot current setting for Dot L0-CS5	Go
106h	DC6	LED dot current setting for Dot L0-CS6	Go
107h	DC7	LED dot current setting for Dot L0-CS7	Go
108h	DC8	LED dot current setting for Dot L0-CS8	Go
109h	DC9	LED dot current setting for Dot L0-CS9	Go
10Ah	DC10	LED dot current setting for Dot L0-CS10	Go
10Bh	DC11	LED dot current setting for Dot L0-CS11	Go
10Ch	DC12	LED dot current setting for Dot L0-CS12	Go
10Dh	DC13	LED dot current setting for Dot L0-CS13	Go
10Eh	DC14	LED dot current setting for Dot L0-CS14	Go
10Fh	DC15	LED dot current setting for Dot L0-CS15	Go
110h	DC16	LED dot current setting for Dot L0-CS16	Go
111h	DC17	LED dot current setting for Dot L0-CS17	Go
112h	DC18	LED dot current setting for Dot L1-CS0	Go
113h	DC19	LED dot current setting for Dot L1-CS1	Go
114h	DC20	LED dot current setting for Dot L1-CS2	Go
115h	DC21	LED dot current setting for Dot L1-CS3	Go
116h	DC22	LED dot current setting for Dot L1-CS4	Go
117h	DC23	LED dot current setting for Dot L1-CS5	Go
118h	DC24	LED dot current setting for Dot L1-CS6	Go
119h	DC25	LED dot current setting for Dot L1-CS7	Go
11Ah	DC26	LED dot current setting for Dot L1-CS8	Go
11Bh	DC27	LED dot current setting for Dot L1-CS9	Go
11Ch	DC28	LED dot current setting for Dot L1-CS10	Go
11Dh	DC29	LED dot current setting for Dot L1-CS11	Go
11Eh	DC30	LED dot current setting for Dot L1-CS12	Go
11Fh	DC31	LED dot current setting for Dot L1-CS13	Go
120h	DC32	LED dot current setting for Dot L1-CS14	Go
121h	DC33	LED dot current setting for Dot L1-CS15	Go
122h	DC34	LED dot current setting for Dot L1-CS16	Go
123h	DC35	LED dot current setting for Dot L1-CS17	Go
124h	DC36	LED dot current setting for Dot L2-CS0	Go
125h	DC37	LED dot current setting for Dot L2-CS1	Go
126h	DC38	LED dot current setting for Dot L2-CS2	Go
127h	DC39	LED dot current setting for Dot L2-CS3	Go

Table 2-182. DC Registers (continued)

Address	Acronym	Register Name	Section
128h	DC40	LED dot current setting for Dot L2-CS4	Go
129h	DC41	LED dot current setting for Dot L2-CS5	Go
12Ah	DC42	LED dot current setting for Dot L2-CS6	Go
12Bh	DC43	LED dot current setting for Dot L2-CS7	Go
12Ch	DC44	LED dot current setting for Dot L2-CS8	Go
12Dh	DC45	LED dot current setting for Dot L2-CS9	Go
12Eh	DC46	LED dot current setting for Dot L2-CS10	Go
12Fh	DC47	LED dot current setting for Dot L2-CS11	Go
130h	DC48	LED dot current setting for Dot L2-CS12	Go
131h	DC49	LED dot current setting for Dot L2-CS13	Go
132h	DC50	LED dot current setting for Dot L2-CS14	Go
133h	DC51	LED dot current setting for Dot L2-CS15	Go
134h	DC52	LED dot current setting for Dot L2-CS16	Go
135h	DC53	LED dot current setting for Dot L2-CS17	Go
136h	DC54	LED dot current setting for Dot L3-CS0	Go
137h	DC55	LED dot current setting for Dot L3-CS1	Go
138h	DC56	LED dot current setting for Dot L3-CS2	Go
139h	DC57	LED dot current setting for Dot L3-CS3	Go
13Ah	DC58	LED dot current setting for Dot L3-CS4	Go
13Bh	DC59	LED dot current setting for Dot L3-CS5	Go
13Ch	DC60	LED dot current setting for Dot L3-CS6	Go
13Dh	DC61	LED dot current setting for Dot L3-CS7	Go
13Eh	DC62	LED dot current setting for Dot L3-CS8	Go
13Fh	DC63	LED dot current setting for Dot L3-CS9	Go
140h	DC64	LED dot current setting for Dot L3-CS10	Go
141h	DC65	LED dot current setting for Dot L3-CS11	Go
142h	DC66	LED dot current setting for Dot L3-CS12	Go
143h	DC67	LED dot current setting for Dot L3-CS13	Go
144h	DC68	LED dot current setting for Dot L3-CS14	Go
145h	DC69	LED dot current setting for Dot L3-CS15	Go
146h	DC70	LED dot current setting for Dot L3-CS16	Go
147h	DC71	LED dot current setting for Dot L3-CS17	Go
148h	DC72	LED dot current setting for Dot L4-CS0	Go
149h	DC73	LED dot current setting for Dot L4-CS1	Go
14Ah	DC74	LED dot current setting for Dot L4-CS2	Go
14Bh	DC75	LED dot current setting for Dot L4-CS3	Go
14Ch	DC76	LED dot current setting for Dot L4-CS4	Go
14Dh	DC77	LED dot current setting for Dot L4-CS5	Go
14Eh	DC78	LED dot current setting for Dot L4-CS6	Go
14Fh	DC79	LED dot current setting for Dot L4-CS7	Go
150h	DC80	LED dot current setting for Dot L4-CS8	Go
151h	DC81	LED dot current setting for Dot L4-CS9	Go
152h	DC82	LED dot current setting for Dot L4-CS10	Go
153h	DC83	LED dot current setting for Dot L4-CS11	Go
154h	DC84	LED dot current setting for Dot L4-CS12	Go

Table 2-182. DC Registers (continued)

Address	Acronym	Register Name	Section
155h	DC85	LED dot current setting for Dot L4-CS13	Go
156h	DC86	LED dot current setting for Dot L4-CS14	Go
157h	DC87	LED dot current setting for Dot L4-CS15	Go
158h	DC88	LED dot current setting for Dot L4-CS16	Go
159h	DC89	LED dot current setting for Dot L4-CS17	Go
15Ah	DC90	LED dot current setting for Dot L5-CS0	Go
15Bh	DC91	LED dot current setting for Dot L5-CS1	Go
15Ch	DC92	LED dot current setting for Dot L5-CS2	Go
15Dh	DC93	LED dot current setting for Dot L5-CS3	Go
15Eh	DC94	LED dot current setting for Dot L5-CS4	Go
15Fh	DC95	LED dot current setting for Dot L5-CS5	Go
160h	DC96	LED dot current setting for Dot L5-CS6	Go
161h	DC97	LED dot current setting for Dot L5-CS7	Go
162h	DC98	LED dot current setting for Dot L5-CS8	Go
163h	DC99	LED dot current setting for Dot L5-CS9	Go
164h	DC100	LED dot current setting for Dot L5-CS10	Go
165h	DC101	LED dot current setting for Dot L5-CS11	Go
166h	DC102	LED dot current setting for Dot L5-CS12	Go
167h	DC103	LED dot current setting for Dot L5-CS13	Go
168h	DC104	LED dot current setting for Dot L5-CS14	Go
169h	DC105	LED dot current setting for Dot L5-CS15	Go
16Ah	DC106	LED dot current setting for Dot L5-CS16	Go
16Bh	DC107	LED dot current setting for Dot L5-CS17	Go
16Ch	DC108	LED dot current setting for Dot L6-CS0	Go
16Dh	DC109	LED dot current setting for Dot L6-CS1	Go
16Eh	DC110	LED dot current setting for Dot L6-CS2	Go
16Fh	DC111	LED dot current setting for Dot L6-CS3	Go
170h	DC112	LED dot current setting for Dot L6-CS4	Go
171h	DC113	LED dot current setting for Dot L6-CS5	Go
172h	DC114	LED dot current setting for Dot L6-CS6	Go
173h	DC115	LED dot current setting for Dot L6-CS7	Go
174h	DC116	LED dot current setting for Dot L6-CS8	Go
175h	DC117	LED dot current setting for Dot L6-CS9	Go
176h	DC118	LED dot current setting for Dot L6-CS10	Go
177h	DC119	LED dot current setting for Dot L6-CS11	Go
178h	DC120	LED dot current setting for Dot L6-CS12	Go
179h	DC121	LED dot current setting for Dot L6-CS13	Go
17Ah	DC122	LED dot current setting for Dot L6-CS14	Go
17Bh	DC123	LED dot current setting for Dot L6-CS15	Go
17Ch	DC124	LED dot current setting for Dot L6-CS16	Go
17Dh	DC125	LED dot current setting for Dot L6-CS17	Go
17Eh	DC126	LED dot current setting for Dot L7-CS0	Go
17Fh	DC127	LED dot current setting for Dot L7-CS1	Go
180h	DC128	LED dot current setting for Dot L7-CS2	Go
181h	DC129	LED dot current setting for Dot L7-CS3	Go

Table 2-182. DC Registers (continued)

Address	Acronym	Register Name	Section
182h	DC130	LED dot current setting for Dot L7-CS4	Go
183h	DC131	LED dot current setting for Dot L7-CS5	Go
184h	DC132	LED dot current setting for Dot L7-CS6	Go
185h	DC133	LED dot current setting for Dot L7-CS7	Go
186h	DC134	LED dot current setting for Dot L7-CS8	Go
187h	DC135	LED dot current setting for Dot L7-CS9	Go
188h	DC136	LED dot current setting for Dot L7-CS10	Go
189h	DC137	LED dot current setting for Dot L7-CS11	Go
18Ah	DC138	LED dot current setting for Dot L7-CS12	Go
18Bh	DC139	LED dot current setting for Dot L7-CS13	Go
18Ch	DC140	LED dot current setting for Dot L7-CS14	Go
18Dh	DC141	LED dot current setting for Dot L7-CS15	Go
18Eh	DC142	LED dot current setting for Dot L7-CS16	Go
18Fh	DC143	LED dot current setting for Dot L7-CS17	Go
190h	DC144	LED dot current setting for Dot L8-CS0	Go
191h	DC145	LED dot current setting for Dot L8-CS1	Go
192h	DC146	LED dot current setting for Dot L8-CS2	Go
193h	DC147	LED dot current setting for Dot L8-CS3	Go
194h	DC148	LED dot current setting for Dot L8-CS4	Go
195h	DC149	LED dot current setting for Dot L8-CS5	Go
196h	DC150	LED dot current setting for Dot L8-CS6	Go
197h	DC151	LED dot current setting for Dot L8-CS7	Go
198h	DC152	LED dot current setting for Dot L8-CS8	Go
199h	DC153	LED dot current setting for Dot L8-CS9	Go
19Ah	DC154	LED dot current setting for Dot L8-CS10	Go
19Bh	DC155	LED dot current setting for Dot L8-CS11	Go
19Ch	DC156	LED dot current setting for Dot L8-CS12	Go
19Dh	DC157	LED dot current setting for Dot L8-CS13	Go
19Eh	DC158	LED dot current setting for Dot L8-CS14	Go
19Fh	DC159	LED dot current setting for Dot L8-CS15	Go
1A0h	DC160	LED dot current setting for Dot L8-CS16	Go
1A1h	DC161	LED dot current setting for Dot L8-CS17	Go
1A2h	DC162	LED dot current setting for Dot L9-CS0	Go
1A3h	DC163	LED dot current setting for Dot L9-CS1	Go
1A4h	DC164	LED dot current setting for Dot L9-CS2	Go
1A5h	DC165	LED dot current setting for Dot L9-CS3	Go
1A6h	DC166	LED dot current setting for Dot L9-CS4	Go
1A7h	DC167	LED dot current setting for Dot L9-CS5	Go
1A8h	DC168	LED dot current setting for Dot L9-CS6	Go
1A9h	DC169	LED dot current setting for Dot L9-CS7	Go
1AAh	DC170	LED dot current setting for Dot L9-CS8	Go
1ABh	DC171	LED dot current setting for Dot L9-CS9	Go
1ACh	DC172	LED dot current setting for Dot L9-CS10	Go
1ADh	DC173	LED dot current setting for Dot L9-CS11	Go
1AEh	DC174	LED dot current setting for Dot L9-CS12	Go

Table 2-182. DC Registers (continued)

Address	Acronym	Register Name	Section
1AFh	DC175	LED dot current setting for Dot L9-CS13	Go
1B0h	DC176	LED dot current setting for Dot L9-CS14	Go
1B1h	DC177	LED dot current setting for Dot L9-CS15	Go
1B2h	DC178	LED dot current setting for Dot L9-CS16	Go
1B3h	DC179	LED dot current setting for Dot L9-CS17	Go
1B4h	DC180	LED dot current setting for Dot L10-CS0	Go
1B5h	DC181	LED dot current setting for Dot L10-CS1	Go
1B6h	DC182	LED dot current setting for Dot L10-CS2	Go
1B7h	DC183	LED dot current setting for Dot L10-CS3	Go
1B8h	DC184	LED dot current setting for Dot L10-CS4	Go
1B9h	DC185	LED dot current setting for Dot L10-CS5	Go
1BAh	DC186	LED dot current setting for Dot L10-CS6	Go
1BBh	DC187	LED dot current setting for Dot L10-CS7	Go
1BCh	DC188	LED dot current setting for Dot L10-CS8	Go
1BDh	DC189	LED dot current setting for Dot L10-CS9	Go
1BEh	DC190	LED dot current setting for Dot L10-CS10	Go
1BFh	DC191	LED dot current setting for Dot L10-CS11	Go
1C0h	DC192	LED dot current setting for Dot L10-CS12	Go
1C1h	DC193	LED dot current setting for Dot L10-CS13	Go
1C2h	DC194	LED dot current setting for Dot L10-CS14	Go
1C3h	DC195	LED dot current setting for Dot L10-CS15	Go
1C4h	DC196	LED dot current setting for Dot L10-CS16	Go
1C5h	DC197	LED dot current setting for Dot L10-CS17	Go

2.12.1 DC0 Register (Address = 100h) [Default = 80h]

DC0 is shown in [Figure 2-171](#) and described in [Table 2-183](#).

Return to the [Summary Table](#).

Figure 2-171. DC0 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L0-CS0							
R/W-80h							

Table 2-183. DC0 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L0-CS0	R/W	80h	8-bits constant current value for LED Dot L0-CS0

2.12.2 DC1 Register (Address = 101h) [Default = 80h]

DC1 is shown in [Figure 2-172](#) and described in [Table 2-184](#).

Return to the [Summary Table](#).

Figure 2-172. DC1 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L0-CS1							
R/W-80h							

Table 2-184. DC1 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L0-CS1	R/W	80h	8-bits constant current value for LED Dot L0-CS1

2.12.3 DC2 Register (Address = 102h) [Default = 80h]

DC2 is shown in [Figure 2-173](#) and described in [Table 2-185](#).

Return to the [Summary Table](#).

Figure 2-173. DC2 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L0-CS2							
R/W-80h							

Table 2-185. DC2 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L0-CS2	R/W	80h	8-bits constant current value for LED Dot L0-CS2

2.12.4 DC3 Register (Address = 103h) [Default = 80h]

DC3 is shown in [Figure 2-174](#) and described in [Table 2-186](#).

Return to the [Summary Table](#).

Figure 2-174. DC3 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L0-CS3							
R/W-80h							

Table 2-186. DC3 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L0-CS3	R/W	80h	8-bits constant current value for LED Dot L0-CS3

2.12.5 DC4 Register (Address = 104h) [Default = 80h]

DC4 is shown in [Figure 2-175](#) and described in [Table 2-187](#).

Return to the [Summary Table](#).

Figure 2-175. DC4 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L0-CS4							
R/W-80h							

Table 2-187. DC4 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L0-CS4	R/W	80h	8-bits constant current value for LED Dot L0-CS4

2.12.6 DC5 Register (Address = 105h) [Default = 80h]

DC5 is shown in [Figure 2-176](#) and described in [Table 2-188](#).

Return to the [Summary Table](#).

Figure 2-176. DC5 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L0-CS5							
R/W-80h							

Table 2-188. DC5 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L0-CS5	R/W	80h	8-bits constant current value for LED Dot L0-CS5

2.12.7 DC6 Register (Address = 106h) [Default = 80h]

DC6 is shown in [Figure 2-177](#) and described in [Table 2-189](#).

Return to the [Summary Table](#).

Figure 2-177. DC6 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L0-CS6							
R/W-80h							

Table 2-189. DC6 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L0-CS6	R/W	80h	8-bits constant current value for LED Dot L0-CS6

2.12.8 DC7 Register (Address = 107h) [Default = 80h]

DC7 is shown in [Figure 2-178](#) and described in [Table 2-190](#).

Return to the [Summary Table](#).

Figure 2-178. DC7 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L0-CS7							
R/W-80h							

Table 2-190. DC7 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L0-CS7	R/W	80h	8-bits constant current value for LED Dot L0-CS7

2.12.9 DC8 Register (Address = 108h) [Default = 80h]

DC8 is shown in [Figure 2-179](#) and described in [Table 2-191](#).

Return to the [Summary Table](#).

Figure 2-179. DC8 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L0-CS8							
R/W-80h							

Table 2-191. DC8 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L0-CS8	R/W	80h	8-bits constant current value for LED Dot L0-CS8

2.12.10 DC9 Register (Address = 109h) [Default = 80h]

DC9 is shown in [Figure 2-180](#) and described in [Table 2-192](#).

Return to the [Summary Table](#).

Figure 2-180. DC9 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L0-CS9							
R/W-80h							

Table 2-192. DC9 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L0-CS9	R/W	80h	8-bits constant current value for LED Dot L0-CS9

2.12.11 DC10 Register (Address = 10Ah) [Default = 80h]

DC10 is shown in [Figure 2-181](#) and described in [Table 2-193](#).

Return to the [Summary Table](#).

Figure 2-181. DC10 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L0-CS10							
R/W-80h							

Table 2-193. DC10 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L0-CS10	R/W	80h	8-bits constant current value for LED Dot L0-CS10

2.12.12 DC11 Register (Address = 10Bh) [Default = 80h]

DC11 is shown in [Figure 2-182](#) and described in [Table 2-194](#).

Return to the [Summary Table](#).

Figure 2-182. DC11 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L0-CS11							
R/W-80h							

Table 2-194. DC11 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L0-CS11	R/W	80h	8-bits constant current value for LED Dot L0-CS11

2.12.13 DC12 Register (Address = 10Ch) [Default = 80h]

DC12 is shown in [Figure 2-183](#) and described in [Table 2-195](#).

Return to the [Summary Table](#).

Figure 2-183. DC12 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L0-CS12							
R/W-80h							

Table 2-195. DC12 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L0-CS12	R/W	80h	8-bits constant current value for LED Dot L0-CS12

2.12.14 DC13 Register (Address = 10Dh) [Default = 80h]

DC13 is shown in [Figure 2-184](#) and described in [Table 2-196](#).

Return to the [Summary Table](#).

Figure 2-184. DC13 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L0-CS13							
R/W-80h							

Table 2-196. DC13 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L0-CS13	R/W	80h	8-bits constant current value for LED Dot L0-CS13

2.12.15 DC14 Register (Address = 10Eh) [Default = 80h]

DC14 is shown in [Figure 2-185](#) and described in [Table 2-197](#).

Return to the [Summary Table](#).

Figure 2-185. DC14 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L0-CS14							
R/W-80h							

Table 2-197. DC14 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L0-CS14	R/W	80h	8-bits constant current value for LED Dot L0-CS14

2.12.16 DC15 Register (Address = 10Fh) [Default = 80h]

DC15 is shown in [Figure 2-186](#) and described in [Table 2-198](#).

Return to the [Summary Table](#).

Figure 2-186. DC15 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L0-CS15							
R/W-80h							

Table 2-198. DC15 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L0-CS15	R/W	80h	8-bits constant current value for LED Dot L0-CS15

2.12.17 DC16 Register (Address = 110h) [Default = 80h]

DC16 is shown in [Figure 2-187](#) and described in [Table 2-199](#).

Return to the [Summary Table](#).

Figure 2-187. DC16 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L0-CS16							
R/W-80h							

Table 2-199. DC16 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L0-CS16	R/W	80h	8-bits constant current value for LED Dot L0-CS16

2.12.18 DC17 Register (Address = 111h) [Default = 80h]

DC17 is shown in [Figure 2-188](#) and described in [Table 2-200](#).

Return to the [Summary Table](#).

Figure 2-188. DC17 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L0-CS17							
R/W-80h							

Table 2-200. DC17 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L0-CS17	R/W	80h	8-bits constant current value for LED Dot L0-CS17

2.12.19 DC18 Register (Address = 112h) [Default = 80h]

DC18 is shown in [Figure 2-189](#) and described in [Table 2-201](#).

Return to the [Summary Table](#).

Figure 2-189. DC18 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L1-CS0							
R/W-80h							

Table 2-201. DC18 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L1-CS0	R/W	80h	8-bits constant current value for LED Dot L1-CS0

2.12.20 DC19 Register (Address = 113h) [Default = 80h]

DC19 is shown in [Figure 2-190](#) and described in [Table 2-202](#).

Return to the [Summary Table](#).

Figure 2-190. DC19 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L1-CS1							
R/W-80h							

Table 2-202. DC19 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L1-CS1	R/W	80h	8-bits constant current value for LED Dot L1-CS1

2.12.21 DC20 Register (Address = 114h) [Default = 80h]

DC20 is shown in [Figure 2-191](#) and described in [Table 2-203](#).

Return to the [Summary Table](#).

Figure 2-191. DC20 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L1-CS2							
R/W-80h							

Table 2-203. DC20 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L1-CS2	R/W	80h	8-bits constant current value for LED Dot L1-CS2

2.12.22 DC21 Register (Address = 115h) [Default = 80h]

DC21 is shown in [Figure 2-192](#) and described in [Table 2-204](#).

Return to the [Summary Table](#).

Figure 2-192. DC21 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L1-CS3							
R/W-80h							

Table 2-204. DC21 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L1-CS3	R/W	80h	8-bits constant current value for LED Dot L1-CS3

2.12.23 DC22 Register (Address = 116h) [Default = 80h]

DC22 is shown in [Figure 2-193](#) and described in [Table 2-205](#).

Return to the [Summary Table](#).

Figure 2-193. DC22 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L1-CS4							
R/W-80h							

Table 2-205. DC22 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L1-CS4	R/W	80h	8-bits constant current value for LED Dot L1-CS4

2.12.24 DC23 Register (Address = 117h) [Default = 80h]

DC23 is shown in [Figure 2-194](#) and described in [Table 2-206](#).

Return to the [Summary Table](#).

Figure 2-194. DC23 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L1-CS5							
R/W-80h							

Table 2-206. DC23 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L1-CS5	R/W	80h	8-bits constant current value for LED Dot L1-CS5

2.12.25 DC24 Register (Address = 118h) [Default = 80h]

DC24 is shown in [Figure 2-195](#) and described in [Table 2-207](#).

Return to the [Summary Table](#).

Figure 2-195. DC24 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L1-CS6							
R/W-80h							

Table 2-207. DC24 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L1-CS6	R/W	80h	8-bits constant current value for LED Dot L1-CS6

2.12.26 DC25 Register (Address = 119h) [Default = 80h]

DC25 is shown in [Figure 2-196](#) and described in [Table 2-208](#).

Return to the [Summary Table](#).

Figure 2-196. DC25 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L1-CS7							
R/W-80h							

Table 2-208. DC25 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L1-CS7	R/W	80h	8-bits constant current value for LED Dot L1-CS7

2.12.27 DC26 Register (Address = 11Ah) [Default = 80h]

DC26 is shown in [Figure 2-197](#) and described in [Table 2-209](#).

Return to the [Summary Table](#).

Figure 2-197. DC26 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L1-CS8							
R/W-80h							

Table 2-209. DC26 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L1-CS8	R/W	80h	8-bits constant current value for LED Dot L1-CS8

2.12.28 DC27 Register (Address = 11Bh) [Default = 80h]

DC27 is shown in [Figure 2-198](#) and described in [Table 2-210](#).

Return to the [Summary Table](#).

Figure 2-198. DC27 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L1-CS9							
R/W-80h							

Table 2-210. DC27 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L1-CS9	R/W	80h	8-bits constant current value for LED Dot L1-CS9

2.12.29 DC28 Register (Address = 11Ch) [Default = 80h]

DC28 is shown in [Figure 2-199](#) and described in [Table 2-211](#).

Return to the [Summary Table](#).

Figure 2-199. DC28 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L1-CS10							
R/W-80h							

Table 2-211. DC28 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L1-CS10	R/W	80h	8-bits constant current value for LED Dot L1-CS10

2.12.30 DC29 Register (Address = 11Dh) [Default = 80h]

DC29 is shown in [Figure 2-200](#) and described in [Table 2-212](#).

Return to the [Summary Table](#).

Figure 2-200. DC29 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L1-CS11							
R/W-80h							

Table 2-212. DC29 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L1-CS11	R/W	80h	8-bits constant current value for LED Dot L1-CS11

2.12.31 DC30 Register (Address = 11Eh) [Default = 80h]

DC30 is shown in [Figure 2-201](#) and described in [Table 2-213](#).

Return to the [Summary Table](#).

Figure 2-201. DC30 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L1-CS12							
R/W-80h							

Table 2-213. DC30 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L1-CS12	R/W	80h	8-bits constant current value for LED Dot L1-CS12

2.12.32 DC31 Register (Address = 11Fh) [Default = 80h]

DC31 is shown in [Figure 2-202](#) and described in [Table 2-214](#).

Return to the [Summary Table](#).

Figure 2-202. DC31 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L1-CS13							
R/W-80h							

Table 2-214. DC31 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L1-CS13	R/W	80h	8-bits constant current value for LED Dot L1-CS13

2.12.33 DC32 Register (Address = 120h) [Default = 80h]

DC32 is shown in [Figure 2-203](#) and described in [Table 2-215](#).

Return to the [Summary Table](#).

Figure 2-203. DC32 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L1-CS14							
R/W-80h							

Table 2-215. DC32 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L1-CS14	R/W	80h	8-bits constant current value for LED Dot L1-CS14

2.12.34 DC33 Register (Address = 121h) [Default = 80h]

DC33 is shown in [Figure 2-204](#) and described in [Table 2-216](#).

Return to the [Summary Table](#).

Figure 2-204. DC33 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L1-CS15							
R/W-80h							

Table 2-216. DC33 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L1-CS15	R/W	80h	8-bits constant current value for LED Dot L1-CS15

2.12.35 DC34 Register (Address = 122h) [Default = 80h]

DC34 is shown in [Figure 2-205](#) and described in [Table 2-217](#).

Return to the [Summary Table](#).

Figure 2-205. DC34 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L1-CS16							
R/W-80h							

Table 2-217. DC34 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L1-CS16	R/W	80h	8-bits constant current value for LED Dot L1-CS16

2.12.36 DC35 Register (Address = 123h) [Default = 80h]

DC35 is shown in [Figure 2-206](#) and described in [Table 2-218](#).

Return to the [Summary Table](#).

Figure 2-206. DC35 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L1-CS17							
R/W-80h							

Table 2-218. DC35 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L1-CS17	R/W	80h	8-bits constant current value for LED Dot L1-CS17

2.12.37 DC36 Register (Address = 124h) [Default = 80h]

DC36 is shown in [Figure 2-207](#) and described in [Table 2-219](#).

Return to the [Summary Table](#).

Figure 2-207. DC36 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L2-CS0							
R/W-80h							

Table 2-219. DC36 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L2-CS0	R/W	80h	8-bits constant current value for LED Dot L2-CS0

2.12.38 DC37 Register (Address = 125h) [Default = 80h]

DC37 is shown in [Figure 2-208](#) and described in [Table 2-220](#).

Return to the [Summary Table](#).

Figure 2-208. DC37 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L2-CS1							
R/W-80h							

Table 2-220. DC37 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L2-CS1	R/W	80h	8-bits constant current value for LED Dot L2-CS1

2.12.39 DC38 Register (Address = 126h) [Default = 80h]

DC38 is shown in [Figure 2-209](#) and described in [Table 2-221](#).

Return to the [Summary Table](#).

Figure 2-209. DC38 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L2-CS2							
R/W-80h							

Table 2-221. DC38 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L2-CS2	R/W	80h	8-bits constant current value for LED Dot L2-CS2

2.12.40 DC39 Register (Address = 127h) [Default = 80h]

DC39 is shown in [Figure 2-210](#) and described in [Table 2-222](#).

Return to the [Summary Table](#).

Figure 2-210. DC39 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L2-CS3							
R/W-80h							

Table 2-222. DC39 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L2-CS3	R/W	80h	8-bits constant current value for LED Dot L2-CS3

2.12.41 DC40 Register (Address = 128h) [Default = 80h]

DC40 is shown in [Figure 2-211](#) and described in [Table 2-223](#).

Return to the [Summary Table](#).

Figure 2-211. DC40 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L2-CS4							
R/W-80h							

Table 2-223. DC40 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L2-CS4	R/W	80h	8-bits constant current value for LED Dot L2-CS4

2.12.42 DC41 Register (Address = 129h) [Default = 80h]

DC41 is shown in [Figure 2-212](#) and described in [Table 2-224](#).

Return to the [Summary Table](#).

Figure 2-212. DC41 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L2-CS5							
R/W-80h							

Table 2-224. DC41 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L2-CS5	R/W	80h	8-bits constant current value for LED Dot L2-CS5

2.12.43 DC42 Register (Address = 12Ah) [Default = 80h]

DC42 is shown in [Figure 2-213](#) and described in [Table 2-225](#).

Return to the [Summary Table](#).

Figure 2-213. DC42 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L2-CS6							
R/W-80h							

Table 2-225. DC42 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L2-CS6	R/W	80h	8-bits constant current value for LED Dot L2-CS6

2.12.44 DC43 Register (Address = 12Bh) [Default = 80h]

DC43 is shown in [Figure 2-214](#) and described in [Table 2-226](#).

Return to the [Summary Table](#).

Figure 2-214. DC43 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L2-CS7							
R/W-80h							

Table 2-226. DC43 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L2-CS7	R/W	80h	8-bits constant current value for LED Dot L2-CS7

2.12.45 DC44 Register (Address = 12Ch) [Default = 80h]

DC44 is shown in [Figure 2-215](#) and described in [Table 2-227](#).

Return to the [Summary Table](#).

Figure 2-215. DC44 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L2-CS8							
R/W-80h							

Table 2-227. DC44 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L2-CS8	R/W	80h	8-bits constant current value for LED Dot L2-CS8

2.12.46 DC45 Register (Address = 12Dh) [Default = 80h]

DC45 is shown in [Figure 2-216](#) and described in [Table 2-228](#).

Return to the [Summary Table](#).

Figure 2-216. DC45 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L2-CS9							
R/W-80h							

Table 2-228. DC45 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L2-CS9	R/W	80h	8-bits constant current value for LED Dot L2-CS9

2.12.47 DC46 Register (Address = 12Eh) [Default = 80h]

DC46 is shown in [Figure 2-217](#) and described in [Table 2-229](#).

Return to the [Summary Table](#).

Figure 2-217. DC46 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L2-CS10							
R/W-80h							

Table 2-229. DC46 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L2-CS10	R/W	80h	8-bits constant current value for LED Dot L2-CS10

2.12.48 DC47 Register (Address = 12Fh) [Default = 80h]

DC47 is shown in [Figure 2-218](#) and described in [Table 2-230](#).

Return to the [Summary Table](#).

Figure 2-218. DC47 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L2-CS11							
R/W-80h							

Table 2-230. DC47 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L2-CS11	R/W	80h	8-bits constant current value for LED Dot L2-CS11

2.12.49 DC48 Register (Address = 130h) [Default = 80h]

DC48 is shown in [Figure 2-219](#) and described in [Table 2-231](#).

Return to the [Summary Table](#).

Figure 2-219. DC48 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L2-CS12							
R/W-80h							

Table 2-231. DC48 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L2-CS12	R/W	80h	8-bits constant current value for LED Dot L2-CS12

2.12.50 DC49 Register (Address = 131h) [Default = 80h]

DC49 is shown in [Figure 2-220](#) and described in [Table 2-232](#).

Return to the [Summary Table](#).

Figure 2-220. DC49 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L2-CS13							
R/W-80h							

Table 2-232. DC49 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L2-CS13	R/W	80h	8-bits constant current value for LED Dot L2-CS13

2.12.51 DC50 Register (Address = 132h) [Default = 80h]

DC50 is shown in [Figure 2-221](#) and described in [Table 2-233](#).

Return to the [Summary Table](#).

Figure 2-221. DC50 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L2-CS14							
R/W-80h							

Table 2-233. DC50 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L2-CS14	R/W	80h	8-bits constant current value for LED Dot L2-CS14

2.12.52 DC51 Register (Address = 133h) [Default = 80h]

DC51 is shown in [Figure 2-222](#) and described in [Table 2-234](#).

Return to the [Summary Table](#).

Figure 2-222. DC51 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L2-CS15							
R/W-80h							

Table 2-234. DC51 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L2-CS15	R/W	80h	8-bits constant current value for LED Dot L2-CS15

2.12.53 DC52 Register (Address = 134h) [Default = 80h]

DC52 is shown in [Figure 2-223](#) and described in [Table 2-235](#).

Return to the [Summary Table](#).

Figure 2-223. DC52 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L2-CS16							
R/W-80h							

Table 2-235. DC52 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L2-CS16	R/W	80h	8-bits constant current value for LED Dot L2-CS16

2.12.54 DC53 Register (Address = 135h) [Default = 80h]

DC53 is shown in [Figure 2-224](#) and described in [Table 2-236](#).

Return to the [Summary Table](#).

Figure 2-224. DC53 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L2-CS17							
R/W-80h							

Table 2-236. DC53 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L2-CS17	R/W	80h	8-bits constant current value for LED Dot L2-CS17

2.12.55 DC54 Register (Address = 136h) [Default = 80h]

DC54 is shown in [Figure 2-225](#) and described in [Table 2-237](#).

Return to the [Summary Table](#).

Figure 2-225. DC54 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L3-CS0							
R/W-80h							

Table 2-237. DC54 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L3-CS0	R/W	80h	8-bits constant current value for LED Dot L3-CS0

2.12.56 DC55 Register (Address = 137h) [Default = 80h]

DC55 is shown in [Figure 2-226](#) and described in [Table 2-238](#).

Return to the [Summary Table](#).

Figure 2-226. DC55 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L3-CS1							
R/W-80h							

Table 2-238. DC55 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L3-CS1	R/W	80h	8-bits constant current value for LED Dot L3-CS1

2.12.57 DC56 Register (Address = 138h) [Default = 80h]

DC56 is shown in [Figure 2-227](#) and described in [Table 2-239](#).

Return to the [Summary Table](#).

Figure 2-227. DC56 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L3-CS2							
R/W-80h							

Table 2-239. DC56 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L3-CS2	R/W	80h	8-bits constant current value for LED Dot L3-CS2

2.12.58 DC57 Register (Address = 139h) [Default = 80h]

DC57 is shown in [Figure 2-228](#) and described in [Table 2-240](#).

Return to the [Summary Table](#).

Figure 2-228. DC57 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L3-CS3							
R/W-80h							

Table 2-240. DC57 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L3-CS3	R/W	80h	8-bits constant current value for LED Dot L3-CS3

2.12.59 DC58 Register (Address = 13Ah) [Default = 80h]

DC58 is shown in [Figure 2-229](#) and described in [Table 2-241](#).

Return to the [Summary Table](#).

Figure 2-229. DC58 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L3-CS4							
R/W-80h							

Table 2-241. DC58 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L3-CS4	R/W	80h	8-bits constant current value for LED Dot L3-CS4

2.12.60 DC59 Register (Address = 13Bh) [Default = 80h]

DC59 is shown in [Figure 2-230](#) and described in [Table 2-242](#).

Return to the [Summary Table](#).

Figure 2-230. DC59 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L3-CS5							
R/W-80h							

Table 2-242. DC59 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L3-CS5	R/W	80h	8-bits constant current value for LED Dot L3-CS5

2.12.61 DC60 Register (Address = 13Ch) [Default = 80h]

DC60 is shown in [Figure 2-231](#) and described in [Table 2-243](#).

Return to the [Summary Table](#).

Figure 2-231. DC60 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L3-CS6							
R/W-80h							

Table 2-243. DC60 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L3-CS6	R/W	80h	8-bits constant current value for LED Dot L3-CS6

2.12.62 DC61 Register (Address = 13Dh) [Default = 80h]

DC61 is shown in [Figure 2-232](#) and described in [Table 2-244](#).

Return to the [Summary Table](#).

Figure 2-232. DC61 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L3-CS7							
R/W-80h							

Table 2-244. DC61 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L3-CS7	R/W	80h	8-bits constant current value for LED Dot L3-CS7

2.12.63 DC62 Register (Address = 13Eh) [Default = 80h]

DC62 is shown in [Figure 2-233](#) and described in [Table 2-245](#).

Return to the [Summary Table](#).

Figure 2-233. DC62 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L3-CS8							
R/W-80h							

Table 2-245. DC62 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L3-CS8	R/W	80h	8-bits constant current value for LED Dot L3-CS8

2.12.64 DC63 Register (Address = 13Fh) [Default = 80h]

DC63 is shown in [Figure 2-234](#) and described in [Table 2-246](#).

Return to the [Summary Table](#).

Figure 2-234. DC63 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L3-CS9							
R/W-80h							

Table 2-246. DC63 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L3-CS9	R/W	80h	8-bits constant current value for LED Dot L3-CS9

2.12.65 DC64 Register (Address = 140h) [Default = 80h]

DC64 is shown in [Figure 2-235](#) and described in [Table 2-247](#).

Return to the [Summary Table](#).

Figure 2-235. DC64 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L3-CS10							
R/W-80h							

Table 2-247. DC64 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L3-CS10	R/W	80h	8-bits constant current value for LED Dot L3-CS10

2.12.66 DC65 Register (Address = 141h) [Default = 80h]

DC65 is shown in [Figure 2-236](#) and described in [Table 2-248](#).

Return to the [Summary Table](#).

Figure 2-236. DC65 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L3-CS11							
R/W-80h							

Table 2-248. DC65 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L3-CS11	R/W	80h	8-bits constant current value for LED Dot L3-CS11

2.12.67 DC66 Register (Address = 142h) [Default = 80h]

DC66 is shown in [Figure 2-237](#) and described in [Table 2-249](#).

Return to the [Summary Table](#).

Figure 2-237. DC66 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L3-CS12							
R/W-80h							

Table 2-249. DC66 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L3-CS12	R/W	80h	8-bits constant current value for LED Dot L3-CS12

2.12.68 DC67 Register (Address = 143h) [Default = 80h]

DC67 is shown in [Figure 2-238](#) and described in [Table 2-250](#).

Return to the [Summary Table](#).

Figure 2-238. DC67 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L3-CS13							
R/W-80h							

Table 2-250. DC67 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L3-CS13	R/W	80h	8-bits constant current value for LED Dot L3-CS13

2.12.69 DC68 Register (Address = 144h) [Default = 80h]

DC68 is shown in [Figure 2-239](#) and described in [Table 2-251](#).

Return to the [Summary Table](#).

Figure 2-239. DC68 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L3-CS14							
R/W-80h							

Table 2-251. DC68 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L3-CS14	R/W	80h	8-bits constant current value for LED Dot L3-CS14

2.12.70 DC69 Register (Address = 145h) [Default = 80h]

DC69 is shown in [Figure 2-240](#) and described in [Table 2-252](#).

Return to the [Summary Table](#).

Figure 2-240. DC69 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L3-CS15							
R/W-80h							

Table 2-252. DC69 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L3-CS15	R/W	80h	8-bits constant current value for LED Dot L3-CS15

2.12.71 DC70 Register (Address = 146h) [Default = 80h]

DC70 is shown in [Figure 2-241](#) and described in [Table 2-253](#).

Return to the [Summary Table](#).

Figure 2-241. DC70 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L3-CS16							
R/W-80h							

Table 2-253. DC70 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L3-CS16	R/W	80h	8-bits constant current value for LED Dot L3-CS16

2.12.72 DC71 Register (Address = 147h) [Default = 80h]

DC71 is shown in [Figure 2-242](#) and described in [Table 2-254](#).

Return to the [Summary Table](#).

Figure 2-242. DC71 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L3-CS17							
R/W-80h							

Table 2-254. DC71 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L3-CS17	R/W	80h	8-bits constant current value for LED Dot L3-CS17

2.12.73 DC72 Register (Address = 148h) [Default = 80h]

DC72 is shown in [Figure 2-243](#) and described in [Table 2-255](#).

Return to the [Summary Table](#).

Figure 2-243. DC72 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L4-CS0							
R/W-80h							

Table 2-255. DC72 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L4-CS0	R/W	80h	8-bits constant current value for LED Dot L4-CS0

2.12.74 DC73 Register (Address = 149h) [Default = 80h]

DC73 is shown in [Figure 2-244](#) and described in [Table 2-256](#).

Return to the [Summary Table](#).

Figure 2-244. DC73 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L4-CS1							
R/W-80h							

Table 2-256. DC73 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L4-CS1	R/W	80h	8-bits constant current value for LED Dot L4-CS1

2.12.75 DC74 Register (Address = 14Ah) [Default = 80h]

DC74 is shown in [Figure 2-245](#) and described in [Table 2-257](#).

Return to the [Summary Table](#).

Figure 2-245. DC74 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L4-CS2							
R/W-80h							

Table 2-257. DC74 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L4-CS2	R/W	80h	8-bits constant current value for LED Dot L4-CS2

2.12.76 DC75 Register (Address = 14Bh) [Default = 80h]

DC75 is shown in [Figure 2-246](#) and described in [Table 2-258](#).

Return to the [Summary Table](#).

Figure 2-246. DC75 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L4-CS3							
R/W-80h							

Table 2-258. DC75 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L4-CS3	R/W	80h	8-bits constant current value for LED Dot L4-CS3

2.12.77 DC76 Register (Address = 14Ch) [Default = 80h]

DC76 is shown in [Figure 2-247](#) and described in [Table 2-259](#).

Return to the [Summary Table](#).

Figure 2-247. DC76 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L4-CS4							
R/W-80h							

Table 2-259. DC76 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L4-CS4	R/W	80h	8-bits constant current value for LED Dot L4-CS4

2.12.78 DC77 Register (Address = 14Dh) [Default = 80h]

DC77 is shown in [Figure 2-248](#) and described in [Table 2-260](#).

Return to the [Summary Table](#).

Figure 2-248. DC77 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L4-CS5							
R/W-80h							

Table 2-260. DC77 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L4-CS5	R/W	80h	8-bits constant current value for LED Dot L4-CS5

2.12.79 DC78 Register (Address = 14Eh) [Default = 80h]

DC78 is shown in [Figure 2-249](#) and described in [Table 2-261](#).

Return to the [Summary Table](#).

Figure 2-249. DC78 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L4-CS6							
R/W-80h							

Table 2-261. DC78 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L4-CS6	R/W	80h	8-bits constant current value for LED Dot L4-CS6

2.12.80 DC79 Register (Address = 14Fh) [Default = 80h]

DC79 is shown in [Figure 2-250](#) and described in [Table 2-262](#).

Return to the [Summary Table](#).

Figure 2-250. DC79 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L4-CS7							
R/W-80h							

Table 2-262. DC79 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L4-CS7	R/W	80h	8-bits constant current value for LED Dot L4-CS7

2.12.81 DC80 Register (Address = 150h) [Default = 80h]

DC80 is shown in [Figure 2-251](#) and described in [Table 2-263](#).

Return to the [Summary Table](#).

Figure 2-251. DC80 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L4-CS8							
R/W-80h							

Table 2-263. DC80 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L4-CS8	R/W	80h	8-bits constant current value for LED Dot L4-CS8

2.12.82 DC81 Register (Address = 151h) [Default = 80h]

DC81 is shown in [Figure 2-252](#) and described in [Table 2-264](#).

Return to the [Summary Table](#).

Figure 2-252. DC81 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L4-CS9							
R/W-80h							

Table 2-264. DC81 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L4-CS9	R/W	80h	8-bits constant current value for LED Dot L4-CS9

2.12.83 DC82 Register (Address = 152h) [Default = 80h]

DC82 is shown in [Figure 2-253](#) and described in [Table 2-265](#).

Return to the [Summary Table](#).

Figure 2-253. DC82 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L4-CS10							
R/W-80h							

Table 2-265. DC82 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L4-CS10	R/W	80h	8-bits constant current value for LED Dot L4-CS10

2.12.84 DC83 Register (Address = 153h) [Default = 80h]

DC83 is shown in [Figure 2-254](#) and described in [Table 2-266](#).

Return to the [Summary Table](#).

Figure 2-254. DC83 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L4-CS11							
R/W-80h							

Table 2-266. DC83 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L4-CS11	R/W	80h	8-bits constant current value for LED Dot L4-CS11

2.12.85 DC84 Register (Address = 154h) [Default = 80h]

DC84 is shown in [Figure 2-255](#) and described in [Table 2-267](#).

Return to the [Summary Table](#).

Figure 2-255. DC84 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L4-CS12							
R/W-80h							

Table 2-267. DC84 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L4-CS12	R/W	80h	8-bits constant current value for LED Dot L4-CS12

2.12.86 DC85 Register (Address = 155h) [Default = 80h]

DC85 is shown in [Figure 2-256](#) and described in [Table 2-268](#).

Return to the [Summary Table](#).

Figure 2-256. DC85 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L4-CS13							
R/W-80h							

Table 2-268. DC85 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L4-CS13	R/W	80h	8-bits constant current value for LED Dot L4-CS13

2.12.87 DC86 Register (Address = 156h) [Default = 80h]

DC86 is shown in [Figure 2-257](#) and described in [Table 2-269](#).

Return to the [Summary Table](#).

Figure 2-257. DC86 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L4-CS14							
R/W-80h							

Table 2-269. DC86 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L4-CS14	R/W	80h	8-bits constant current value for LED Dot L4-CS14

2.12.88 DC87 Register (Address = 157h) [Default = 80h]

DC87 is shown in [Figure 2-258](#) and described in [Table 2-270](#).

Return to the [Summary Table](#).

Figure 2-258. DC87 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L4-CS15							
R/W-80h							

Table 2-270. DC87 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L4-CS15	R/W	80h	8-bits constant current value for LED Dot L4-CS15

2.12.89 DC88 Register (Address = 158h) [Default = 80h]

DC88 is shown in [Figure 2-259](#) and described in [Table 2-271](#).

Return to the [Summary Table](#).

Figure 2-259. DC88 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L4-CS16							
R/W-80h							

Table 2-271. DC88 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L4-CS16	R/W	80h	8-bits constant current value for LED Dot L4-CS16

2.12.90 DC89 Register (Address = 159h) [Default = 80h]

DC89 is shown in [Figure 2-260](#) and described in [Table 2-272](#).

Return to the [Summary Table](#).

Figure 2-260. DC89 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L4-CS17							
R/W-80h							

Table 2-272. DC89 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L4-CS17	R/W	80h	8-bits constant current value for LED Dot L4-CS17

2.12.91 DC90 Register (Address = 15Ah) [Default = 80h]

DC90 is shown in [Figure 2-261](#) and described in [Table 2-273](#).

Return to the [Summary Table](#).

Figure 2-261. DC90 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L5-CS0							
R/W-80h							

Table 2-273. DC90 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L5-CS0	R/W	80h	8-bits constant current value for LED Dot L5-CS0

2.12.92 DC91 Register (Address = 15Bh) [Default = 80h]

DC91 is shown in [Figure 2-262](#) and described in [Table 2-274](#).

Return to the [Summary Table](#).

Figure 2-262. DC91 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L5-CS1							
R/W-80h							

Table 2-274. DC91 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L5-CS1	R/W	80h	8-bits constant current value for LED Dot L5-CS1

2.12.93 DC92 Register (Address = 15Ch) [Default = 80h]

DC92 is shown in [Figure 2-263](#) and described in [Table 2-275](#).

Return to the [Summary Table](#).

Figure 2-263. DC92 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L5-CS2							
R/W-80h							

Table 2-275. DC92 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L5-CS2	R/W	80h	8-bits constant current value for LED Dot L5-CS2

2.12.94 DC93 Register (Address = 15Dh) [Default = 80h]

DC93 is shown in [Figure 2-264](#) and described in [Table 2-276](#).

Return to the [Summary Table](#).

Figure 2-264. DC93 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L5-CS3							
R/W-80h							

Table 2-276. DC93 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L5-CS3	R/W	80h	8-bits constant current value for LED Dot L5-CS3

2.12.95 DC94 Register (Address = 15Eh) [Default = 80h]

DC94 is shown in [Figure 2-265](#) and described in [Table 2-277](#).

Return to the [Summary Table](#).

Figure 2-265. DC94 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L5-CS4							
R/W-80h							

Table 2-277. DC94 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L5-CS4	R/W	80h	8-bits constant current value for LED Dot L5-CS4

2.12.96 DC95 Register (Address = 15Fh) [Default = 80h]

DC95 is shown in [Figure 2-266](#) and described in [Table 2-278](#).

Return to the [Summary Table](#).

Figure 2-266. DC95 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L5-CS5							
R/W-80h							

Table 2-278. DC95 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L5-CS5	R/W	80h	8-bits constant current value for LED Dot L5-CS5

2.12.97 DC96 Register (Address = 160h) [Default = 80h]

DC96 is shown in [Figure 2-267](#) and described in [Table 2-279](#).

Return to the [Summary Table](#).

Figure 2-267. DC96 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L5-CS6							
R/W-80h							

Table 2-279. DC96 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L5-CS6	R/W	80h	8-bits constant current value for LED Dot L5-CS6

2.12.98 DC97 Register (Address = 161h) [Default = 80h]

DC97 is shown in [Figure 2-268](#) and described in [Table 2-280](#).

Return to the [Summary Table](#).

Figure 2-268. DC97 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L5-CS7							
R/W-80h							

Table 2-280. DC97 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L5-CS7	R/W	80h	8-bits constant current value for LED Dot L5-CS7

2.12.99 DC98 Register (Address = 162h) [Default = 80h]

DC98 is shown in [Figure 2-269](#) and described in [Table 2-281](#).

Return to the [Summary Table](#).

Figure 2-269. DC98 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L5-CS8							
R/W-80h							

Table 2-281. DC98 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L5-CS8	R/W	80h	8-bits constant current value for LED Dot L5-CS8

2.12.100 DC99 Register (Address = 163h) [Default = 80h]

DC99 is shown in [Figure 2-270](#) and described in [Table 2-282](#).

Return to the [Summary Table](#).

Figure 2-270. DC99 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L5-CS9							
R/W-80h							

Table 2-282. DC99 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L5-CS9	R/W	80h	8-bits constant current value for LED Dot L5-CS9

2.12.101 DC100 Register (Address = 164h) [Default = 80h]

DC100 is shown in [Figure 2-271](#) and described in [Table 2-283](#).

Return to the [Summary Table](#).

Figure 2-271. DC100 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L5-CS10							
R/W-80h							

Table 2-283. DC100 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L5-CS10	R/W	80h	8-bits constant current value for LED Dot L5-CS10

2.12.102 DC101 Register (Address = 165h) [Default = 80h]

DC101 is shown in [Figure 2-272](#) and described in [Table 2-284](#).

Return to the [Summary Table](#).

Figure 2-272. DC101 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L5-CS11							
R/W-80h							

Table 2-284. DC101 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L5-CS11	R/W	80h	8-bits constant current value for LED Dot L5-CS11

2.12.103 DC102 Register (Address = 166h) [Default = 80h]

DC102 is shown in [Figure 2-273](#) and described in [Table 2-285](#).

Return to the [Summary Table](#).

Figure 2-273. DC102 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L5-CS12							
R/W-80h							

Table 2-285. DC102 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L5-CS12	R/W	80h	8-bits constant current value for LED Dot L5-CS12

2.12.104 DC103 Register (Address = 167h) [Default = 80h]

DC103 is shown in [Figure 2-274](#) and described in [Table 2-286](#).

Return to the [Summary Table](#).

Figure 2-274. DC103 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L5-CS13							
R/W-80h							

Table 2-286. DC103 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L5-CS13	R/W	80h	8-bits constant current value for LED Dot L5-CS13

2.12.105 DC104 Register (Address = 168h) [Default = 80h]

DC104 is shown in [Figure 2-275](#) and described in [Table 2-287](#).

Return to the [Summary Table](#).

Figure 2-275. DC104 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L5-CS14							
R/W-80h							

Table 2-287. DC104 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L5-CS14	R/W	80h	8-bits constant current value for LED Dot L5-CS14

2.12.106 DC105 Register (Address = 169h) [Default = 80h]

DC105 is shown in [Figure 2-276](#) and described in [Table 2-288](#).

Return to the [Summary Table](#).

Figure 2-276. DC105 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L5-CS15							
R/W-80h							

Table 2-288. DC105 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L5-CS15	R/W	80h	8-bits constant current value for LED Dot L5-CS15

2.12.107 DC106 Register (Address = 16Ah) [Default = 80h]

DC106 is shown in [Figure 2-277](#) and described in [Table 2-289](#).

Return to the [Summary Table](#).

Figure 2-277. DC106 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L5-CS16							
R/W-80h							

Table 2-289. DC106 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L5-CS16	R/W	80h	8-bits constant current value for LED Dot L5-CS16

2.12.108 DC107 Register (Address = 16Bh) [Default = 80h]

DC107 is shown in [Figure 2-278](#) and described in [Table 2-290](#).

Return to the [Summary Table](#).

Figure 2-278. DC107 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L5-CS17							
R/W-80h							

Table 2-290. DC107 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L5-CS17	R/W	80h	8-bits constant current value for LED Dot L5-CS17

2.12.109 DC108 Register (Address = 16Ch) [Default = 80h]

DC108 is shown in [Figure 2-279](#) and described in [Table 2-291](#).

Return to the [Summary Table](#).

Figure 2-279. DC108 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L6-CS0							
R/W-80h							

Table 2-291. DC108 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L6-CS0	R/W	80h	8-bits constant current value for LED Dot L6-CS0

2.12.110 DC109 Register (Address = 16Dh) [Default = 80h]

DC109 is shown in [Figure 2-280](#) and described in [Table 2-292](#).

Return to the [Summary Table](#).

Figure 2-280. DC109 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L6-CS1							
R/W-80h							

Table 2-292. DC109 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L6-CS1	R/W	80h	8-bits constant current value for LED Dot L6-CS1

2.12.111 DC110 Register (Address = 16Eh) [Default = 80h]

DC110 is shown in [Figure 2-281](#) and described in [Table 2-293](#).

Return to the [Summary Table](#).

Figure 2-281. DC110 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L6-CS2							
R/W-80h							

Table 2-293. DC110 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L6-CS2	R/W	80h	8-bits constant current value for LED Dot L6-CS2

2.12.112 DC111 Register (Address = 16Fh) [Default = 80h]

DC111 is shown in [Figure 2-282](#) and described in [Table 2-294](#).

Return to the [Summary Table](#).

Figure 2-282. DC111 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L6-CS3							
R/W-80h							

Table 2-294. DC111 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L6-CS3	R/W	80h	8-bits constant current value for LED Dot L6-CS3

2.12.113 DC112 Register (Address = 170h) [Default = 80h]

DC112 is shown in [Figure 2-283](#) and described in [Table 2-295](#).

Return to the [Summary Table](#).

Figure 2-283. DC112 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L6-CS4							
R/W-80h							

Table 2-295. DC112 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L6-CS4	R/W	80h	8-bits constant current value for LED Dot L6-CS4

2.12.114 DC113 Register (Address = 171h) [Default = 80h]

DC113 is shown in [Figure 2-284](#) and described in [Table 2-296](#).

Return to the [Summary Table](#).

Figure 2-284. DC113 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L6-CS5							
R/W-80h							

Table 2-296. DC113 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L6-CS5	R/W	80h	8-bits constant current value for LED Dot L6-CS5

2.12.115 DC114 Register (Address = 172h) [Default = 80h]

DC114 is shown in [Figure 2-285](#) and described in [Table 2-297](#).

Return to the [Summary Table](#).

Figure 2-285. DC114 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L6-CS6							
R/W-80h							

Table 2-297. DC114 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L6-CS6	R/W	80h	8-bits constant current value for LED Dot L6-CS6

2.12.116 DC115 Register (Address = 173h) [Default = 80h]

DC115 is shown in [Figure 2-286](#) and described in [Table 2-298](#).

Return to the [Summary Table](#).

Figure 2-286. DC115 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L6-CS7							
R/W-80h							

Table 2-298. DC115 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L6-CS7	R/W	80h	8-bits constant current value for LED Dot L6-CS7

2.12.117 DC116 Register (Address = 174h) [Default = 80h]

DC116 is shown in [Figure 2-287](#) and described in [Table 2-299](#).

Return to the [Summary Table](#).

Figure 2-287. DC116 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L6-CS8							
R/W-80h							

Table 2-299. DC116 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L6-CS8	R/W	80h	8-bits constant current value for LED Dot L6-CS8

2.12.118 DC117 Register (Address = 175h) [Default = 80h]

DC117 is shown in [Figure 2-288](#) and described in [Table 2-300](#).

Return to the [Summary Table](#).

Figure 2-288. DC117 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L6-CS9							
R/W-80h							

Table 2-300. DC117 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L6-CS9	R/W	80h	8-bits constant current value for LED Dot L6-CS9

2.12.119 DC118 Register (Address = 176h) [Default = 80h]

DC118 is shown in [Figure 2-289](#) and described in [Table 2-301](#).

Return to the [Summary Table](#).

Figure 2-289. DC118 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L6-CS10							
R/W-80h							

Table 2-301. DC118 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L6-CS10	R/W	80h	8-bits constant current value for LED Dot L6-CS10

2.12.120 DC119 Register (Address = 177h) [Default = 80h]

DC119 is shown in [Figure 2-290](#) and described in [Table 2-302](#).

Return to the [Summary Table](#).

Figure 2-290. DC119 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L6-CS11							
R/W-80h							

Table 2-302. DC119 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L6-CS11	R/W	80h	8-bits constant current value for LED Dot L6-CS11

2.12.121 DC120 Register (Address = 178h) [Default = 80h]

DC120 is shown in [Figure 2-291](#) and described in [Table 2-303](#).

Return to the [Summary Table](#).

Figure 2-291. DC120 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L6-CS12							
R/W-80h							

Table 2-303. DC120 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L6-CS12	R/W	80h	8-bits constant current value for LED Dot L6-CS12

2.12.122 DC121 Register (Address = 179h) [Default = 80h]

DC121 is shown in [Figure 2-292](#) and described in [Table 2-304](#).

Return to the [Summary Table](#).

Figure 2-292. DC121 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L6-CS13							
R/W-80h							

Table 2-304. DC121 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L6-CS13	R/W	80h	8-bits constant current value for LED Dot L6-CS13

2.12.123 DC122 Register (Address = 17Ah) [Default = 80h]

DC122 is shown in [Figure 2-293](#) and described in [Table 2-305](#).

Return to the [Summary Table](#).

Figure 2-293. DC122 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L6-CS14							
R/W-80h							

Table 2-305. DC122 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L6-CS14	R/W	80h	8-bits constant current value for LED Dot L6-CS14

2.12.124 DC123 Register (Address = 17Bh) [Default = 80h]

DC123 is shown in [Figure 2-294](#) and described in [Table 2-306](#).

Return to the [Summary Table](#).

Figure 2-294. DC123 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L6-CS15							
R/W-80h							

Table 2-306. DC123 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L6-CS15	R/W	80h	8-bits constant current value for LED Dot L6-CS15

2.12.125 DC124 Register (Address = 17Ch) [Default = 80h]

DC124 is shown in [Figure 2-295](#) and described in [Table 2-307](#).

Return to the [Summary Table](#).

Figure 2-295. DC124 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L6-CS16							
R/W-80h							

Table 2-307. DC124 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L6-CS16	R/W	80h	8-bits constant current value for LED Dot L6-CS16

2.12.126 DC125 Register (Address = 17Dh) [Default = 80h]

DC125 is shown in [Figure 2-296](#) and described in [Table 2-308](#).

Return to the [Summary Table](#).

Figure 2-296. DC125 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L6-CS17							
R/W-80h							

Table 2-308. DC125 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L6-CS17	R/W	80h	8-bits constant current value for LED Dot L6-CS17

2.12.127 DC126 Register (Address = 17Eh) [Default = 80h]

DC126 is shown in [Figure 2-297](#) and described in [Table 2-309](#).

Return to the [Summary Table](#).

Figure 2-297. DC126 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L7-CS0							
R/W-80h							

Table 2-309. DC126 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L7-CS0	R/W	80h	8-bits constant current value for LED Dot L7-CS0

2.12.128 DC127 Register (Address = 17Fh) [Default = 80h]

DC127 is shown in [Figure 2-298](#) and described in [Table 2-310](#).

Return to the [Summary Table](#).

Figure 2-298. DC127 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L7-CS1							
R/W-80h							

Table 2-310. DC127 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L7-CS1	R/W	80h	8-bits constant current value for LED Dot L7-CS1

2.12.129 DC128 Register (Address = 180h) [Default = 80h]

DC128 is shown in [Figure 2-299](#) and described in [Table 2-311](#).

Return to the [Summary Table](#).

Figure 2-299. DC128 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L7-CS2							
R/W-80h							

Table 2-311. DC128 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L7-CS2	R/W	80h	8-bits constant current value for LED Dot L7-CS2

2.12.130 DC129 Register (Address = 181h) [Default = 80h]

DC129 is shown in [Figure 2-300](#) and described in [Table 2-312](#).

Return to the [Summary Table](#).

Figure 2-300. DC129 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L7-CS3							
R/W-80h							

Table 2-312. DC129 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L7-CS3	R/W	80h	8-bits constant current value for LED Dot L7-CS3

2.12.131 DC130 Register (Address = 182h) [Default = 80h]

DC130 is shown in [Figure 2-301](#) and described in [Table 2-313](#).

Return to the [Summary Table](#).

Figure 2-301. DC130 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L7-CS4							
R/W-80h							

Table 2-313. DC130 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L7-CS4	R/W	80h	8-bits constant current value for LED Dot L7-CS4

2.12.132 DC131 Register (Address = 183h) [Default = 80h]

DC131 is shown in [Figure 2-302](#) and described in [Table 2-314](#).

Return to the [Summary Table](#).

Figure 2-302. DC131 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L7-CS5							
R/W-80h							

Table 2-314. DC131 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L7-CS5	R/W	80h	8-bits constant current value for LED Dot L7-CS5

2.12.133 DC132 Register (Address = 184h) [Default = 80h]

DC132 is shown in [Figure 2-303](#) and described in [Table 2-315](#).

Return to the [Summary Table](#).

Figure 2-303. DC132 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L7-CS6							
R/W-80h							

Table 2-315. DC132 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L7-CS6	R/W	80h	8-bits constant current value for LED Dot L7-CS6

2.12.134 DC133 Register (Address = 185h) [Default = 80h]

DC133 is shown in [Figure 2-304](#) and described in [Table 2-316](#).

Return to the [Summary Table](#).

Figure 2-304. DC133 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L7-CS7							
R/W-80h							

Table 2-316. DC133 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L7-CS7	R/W	80h	8-bits constant current value for LED Dot L7-CS7

2.12.135 DC134 Register (Address = 186h) [Default = 80h]

DC134 is shown in [Figure 2-305](#) and described in [Table 2-317](#).

Return to the [Summary Table](#).

Figure 2-305. DC134 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L7-CS8							
R/W-80h							

Table 2-317. DC134 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L7-CS8	R/W	80h	8-bits constant current value for LED Dot L7-CS8

2.12.136 DC135 Register (Address = 187h) [Default = 80h]

DC135 is shown in [Figure 2-306](#) and described in [Table 2-318](#).

Return to the [Summary Table](#).

Figure 2-306. DC135 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L7-CS9							
R/W-80h							

Table 2-318. DC135 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L7-CS9	R/W	80h	8-bits constant current value for LED Dot L7-CS9

2.12.137 DC136 Register (Address = 188h) [Default = 80h]

DC136 is shown in [Figure 2-307](#) and described in [Table 2-319](#).

Return to the [Summary Table](#).

Figure 2-307. DC136 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L7-CS10							
R/W-80h							

Table 2-319. DC136 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L7-CS10	R/W	80h	8-bits constant current value for LED Dot L7-CS10

2.12.138 DC137 Register (Address = 189h) [Default = 80h]

DC137 is shown in [Figure 2-308](#) and described in [Table 2-320](#).

Return to the [Summary Table](#).

Figure 2-308. DC137 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L7-CS11							
R/W-80h							

Table 2-320. DC137 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L7-CS11	R/W	80h	8-bits constant current value for LED Dot L7-CS11

2.12.139 DC138 Register (Address = 18Ah) [Default = 80h]

DC138 is shown in [Figure 2-309](#) and described in [Table 2-321](#).

Return to the [Summary Table](#).

Figure 2-309. DC138 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L7-CS12							
R/W-80h							

Table 2-321. DC138 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L7-CS12	R/W	80h	8-bits constant current value for LED Dot L7-CS12

2.12.140 DC139 Register (Address = 18Bh) [Default = 80h]

DC139 is shown in [Figure 2-310](#) and described in [Table 2-322](#).

Return to the [Summary Table](#).

Figure 2-310. DC139 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L7-CS13							
R/W-80h							

Table 2-322. DC139 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L7-CS13	R/W	80h	8-bits constant current value for LED Dot L7-CS13

2.12.141 DC140 Register (Address = 18Ch) [Default = 80h]

DC140 is shown in [Figure 2-311](#) and described in [Table 2-323](#).

Return to the [Summary Table](#).

Figure 2-311. DC140 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L7-CS14							
R/W-80h							

Table 2-323. DC140 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L7-CS14	R/W	80h	8-bits constant current value for LED Dot L7-CS14

2.12.142 DC141 Register (Address = 18Dh) [Default = 80h]

DC141 is shown in [Figure 2-312](#) and described in [Table 2-324](#).

Return to the [Summary Table](#).

Figure 2-312. DC141 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L7-CS15							
R/W-80h							

Table 2-324. DC141 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L7-CS15	R/W	80h	8-bits constant current value for LED Dot L7-CS15

2.12.143 DC142 Register (Address = 18Eh) [Default = 80h]

DC142 is shown in [Figure 2-313](#) and described in [Table 2-325](#).

Return to the [Summary Table](#).

Figure 2-313. DC142 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L7-CS16							
R/W-80h							

Table 2-325. DC142 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L7-CS16	R/W	80h	8-bits constant current value for LED Dot L7-CS16

2.12.144 DC143 Register (Address = 18Fh) [Default = 80h]

DC143 is shown in [Figure 2-314](#) and described in [Table 2-326](#).

Return to the [Summary Table](#).

Figure 2-314. DC143 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L7-CS17							
R/W-80h							

Table 2-326. DC143 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L7-CS17	R/W	80h	8-bits constant current value for LED Dot L7-CS17

2.12.145 DC144 Register (Address = 190h) [Default = 80h]

DC144 is shown in [Figure 2-315](#) and described in [Table 2-327](#).

Return to the [Summary Table](#).

Figure 2-315. DC144 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L8-CS0							
R/W-80h							

Table 2-327. DC144 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L8-CS0	R/W	80h	8-bits constant current value for LED Dot L8-CS0

2.12.146 DC145 Register (Address = 191h) [Default = 80h]

DC145 is shown in [Figure 2-316](#) and described in [Table 2-328](#).

Return to the [Summary Table](#).

Figure 2-316. DC145 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L8-CS1							
R/W-80h							

Table 2-328. DC145 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L8-CS1	R/W	80h	8-bits constant current value for LED Dot L8-CS1

2.12.147 DC146 Register (Address = 192h) [Default = 80h]

DC146 is shown in [Figure 2-317](#) and described in [Table 2-329](#).

Return to the [Summary Table](#).

Figure 2-317. DC146 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L8-CS2							
R/W-80h							

Table 2-329. DC146 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L8-CS2	R/W	80h	8-bits constant current value for LED Dot L8-CS2

2.12.148 DC147 Register (Address = 193h) [Default = 80h]

DC147 is shown in [Figure 2-318](#) and described in [Table 2-330](#).

Return to the [Summary Table](#).

Figure 2-318. DC147 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L8-CS3							
R/W-80h							

Table 2-330. DC147 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L8-CS3	R/W	80h	8-bits constant current value for LED Dot L8-CS3

2.12.149 DC148 Register (Address = 194h) [Default = 80h]

DC148 is shown in [Figure 2-319](#) and described in [Table 2-331](#).

Return to the [Summary Table](#).

Figure 2-319. DC148 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L8-CS4							
R/W-80h							

Table 2-331. DC148 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L8-CS4	R/W	80h	8-bits constant current value for LED Dot L8-CS4

2.12.150 DC149 Register (Address = 195h) [Default = 80h]

DC149 is shown in [Figure 2-320](#) and described in [Table 2-332](#).

Return to the [Summary Table](#).

Figure 2-320. DC149 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L8-CS5							
R/W-80h							

Table 2-332. DC149 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L8-CS5	R/W	80h	8-bits constant current value for LED Dot L8-CS5

2.12.151 DC150 Register (Address = 196h) [Default = 80h]

DC150 is shown in [Figure 2-321](#) and described in [Table 2-333](#).

Return to the [Summary Table](#).

Figure 2-321. DC150 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L8-CS6							
R/W-80h							

Table 2-333. DC150 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L8-CS6	R/W	80h	8-bits constant current value for LED Dot L8-CS6

2.12.152 DC151 Register (Address = 197h) [Default = 80h]

DC151 is shown in [Figure 2-322](#) and described in [Table 2-334](#).

Return to the [Summary Table](#).

Figure 2-322. DC151 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L8-CS7							
R/W-80h							

Table 2-334. DC151 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L8-CS7	R/W	80h	8-bits constant current value for LED Dot L8-CS7

2.12.153 DC152 Register (Address = 198h) [Default = 80h]

DC152 is shown in [Figure 2-323](#) and described in [Table 2-335](#).

Return to the [Summary Table](#).

Figure 2-323. DC152 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L8-CS8							
R/W-80h							

Table 2-335. DC152 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L8-CS8	R/W	80h	8-bits constant current value for LED Dot L8-CS8

2.12.154 DC153 Register (Address = 199h) [Default = 80h]

DC153 is shown in [Figure 2-324](#) and described in [Table 2-336](#).

Return to the [Summary Table](#).

Figure 2-324. DC153 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L8-CS9							
R/W-80h							

Table 2-336. DC153 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L8-CS9	R/W	80h	8-bits constant current value for LED Dot L8-CS9

2.12.155 DC154 Register (Address = 19Ah) [Default = 80h]

DC154 is shown in [Figure 2-325](#) and described in [Table 2-337](#).

Return to the [Summary Table](#).

Figure 2-325. DC154 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L8-CS10							
R/W-80h							

Table 2-337. DC154 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L8-CS10	R/W	80h	8-bits constant current value for LED Dot L8-CS10

2.12.156 DC155 Register (Address = 19Bh) [Default = 80h]

DC155 is shown in [Figure 2-326](#) and described in [Table 2-338](#).

Return to the [Summary Table](#).

Figure 2-326. DC155 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L8-CS11							
R/W-80h							

Table 2-338. DC155 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L8-CS11	R/W	80h	8-bits constant current value for LED Dot L8-CS11

2.12.157 DC156 Register (Address = 19Ch) [Default = 80h]

DC156 is shown in [Figure 2-327](#) and described in [Table 2-339](#).

Return to the [Summary Table](#).

Figure 2-327. DC156 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L8-CS12							
R/W-80h							

Table 2-339. DC156 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L8-CS12	R/W	80h	8-bits constant current value for LED Dot L8-CS12

2.12.158 DC157 Register (Address = 19Dh) [Default = 80h]

DC157 is shown in [Figure 2-328](#) and described in [Table 2-340](#).

Return to the [Summary Table](#).

Figure 2-328. DC157 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L8-CS13							
R/W-80h							

Table 2-340. DC157 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L8-CS13	R/W	80h	8-bits constant current value for LED Dot L8-CS13

2.12.159 DC158 Register (Address = 19Eh) [Default = 80h]

DC158 is shown in [Figure 2-329](#) and described in [Table 2-341](#).

Return to the [Summary Table](#).

Figure 2-329. DC158 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L8-CS14							
R/W-80h							

Table 2-341. DC158 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L8-CS14	R/W	80h	8-bits constant current value for LED Dot L8-CS14

2.12.160 DC159 Register (Address = 19Fh) [Default = 80h]

DC159 is shown in [Figure 2-330](#) and described in [Table 2-342](#).

Return to the [Summary Table](#).

Figure 2-330. DC159 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L8-CS15							
R/W-80h							

Table 2-342. DC159 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L8-CS15	R/W	80h	8-bits constant current value for LED Dot L8-CS15

2.12.161 DC160 Register (Address = 1A0h) [Default = 80h]

DC160 is shown in [Figure 2-331](#) and described in [Table 2-343](#).

Return to the [Summary Table](#).

Figure 2-331. DC160 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L8-CS16							
R/W-80h							

Table 2-343. DC160 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L8-CS16	R/W	80h	8-bits constant current value for LED Dot L8-CS16

2.12.162 DC161 Register (Address = 1A1h) [Default = 80h]

DC161 is shown in [Figure 2-332](#) and described in [Table 2-344](#).

Return to the [Summary Table](#).

Figure 2-332. DC161 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L8-CS17							
R/W-80h							

Table 2-344. DC161 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L8-CS17	R/W	80h	8-bits constant current value for LED Dot L8-CS17

2.12.163 DC162 Register (Address = 1A2h) [Default = 80h]

DC162 is shown in [Figure 2-333](#) and described in [Table 2-345](#).

Return to the [Summary Table](#).

Figure 2-333. DC162 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L9-CS0							
R/W-80h							

Table 2-345. DC162 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L9-CS0	R/W	80h	8-bits constant current value for LED Dot L9-CS0

2.12.164 DC163 Register (Address = 1A3h) [Default = 80h]

DC163 is shown in [Figure 2-334](#) and described in [Table 2-346](#).

Return to the [Summary Table](#).

Figure 2-334. DC163 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L9-CS1							
R/W-80h							

Table 2-346. DC163 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L9-CS1	R/W	80h	8-bits constant current value for LED Dot L9-CS1

2.12.165 DC164 Register (Address = 1A4h) [Default = 80h]

DC164 is shown in [Figure 2-335](#) and described in [Table 2-347](#).

Return to the [Summary Table](#).

Figure 2-335. DC164 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L9-CS2							
R/W-80h							

Table 2-347. DC164 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L9-CS2	R/W	80h	8-bits constant current value for LED Dot L9-CS2

2.12.166 DC165 Register (Address = 1A5h) [Default = 80h]

DC165 is shown in [Figure 2-336](#) and described in [Table 2-348](#).

Return to the [Summary Table](#).

Figure 2-336. DC165 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L9-CS3							
R/W-80h							

Table 2-348. DC165 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L9-CS3	R/W	80h	8-bits constant current value for LED Dot L9-CS3

2.12.167 DC166 Register (Address = 1A6h) [Default = 80h]

DC166 is shown in [Figure 2-337](#) and described in [Table 2-349](#).

Return to the [Summary Table](#).

Figure 2-337. DC166 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L9-CS4							
R/W-80h							

Table 2-349. DC166 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L9-CS4	R/W	80h	8-bits constant current value for LED Dot L9-CS4

2.12.168 DC167 Register (Address = 1A7h) [Default = 80h]

DC167 is shown in [Figure 2-338](#) and described in [Table 2-350](#).

Return to the [Summary Table](#).

Figure 2-338. DC167 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L9-CS5							
R/W-80h							

Table 2-350. DC167 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L9-CS5	R/W	80h	8-bits constant current value for LED Dot L9-CS5

2.12.169 DC168 Register (Address = 1A8h) [Default = 80h]

DC168 is shown in [Figure 2-339](#) and described in [Table 2-351](#).

Return to the [Summary Table](#).

Figure 2-339. DC168 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L9-CS6							
R/W-80h							

Table 2-351. DC168 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L9-CS6	R/W	80h	8-bits constant current value for LED Dot L9-CS6

2.12.170 DC169 Register (Address = 1A9h) [Default = 80h]

DC169 is shown in [Figure 2-340](#) and described in [Table 2-352](#).

Return to the [Summary Table](#).

Figure 2-340. DC169 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L9-CS7							
R/W-80h							

Table 2-352. DC169 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L9-CS7	R/W	80h	8-bits constant current value for LED Dot L9-CS7

2.12.171 DC170 Register (Address = 1AAh) [Default = 80h]

DC170 is shown in [Figure 2-341](#) and described in [Table 2-353](#).

Return to the [Summary Table](#).

Figure 2-341. DC170 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L9-CS8							
R/W-80h							

Table 2-353. DC170 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L9-CS8	R/W	80h	8-bits constant current value for LED Dot L9-CS8

2.12.172 DC171 Register (Address = 1ABh) [Default = 80h]

DC171 is shown in [Figure 2-342](#) and described in [Table 2-354](#).

Return to the [Summary Table](#).

Figure 2-342. DC171 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L9-CS9							
R/W-80h							

Table 2-354. DC171 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L9-CS9	R/W	80h	8-bits constant current value for LED Dot L9-CS9

2.12.173 DC172 Register (Address = 1ACh) [Default = 80h]

DC172 is shown in [Figure 2-343](#) and described in [Table 2-355](#).

Return to the [Summary Table](#).

Figure 2-343. DC172 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L9-CS10							
R/W-80h							

Table 2-355. DC172 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L9-CS10	R/W	80h	8-bits constant current value for LED Dot L9-CS10

2.12.174 DC173 Register (Address = 1ADh) [Default = 80h]

DC173 is shown in [Figure 2-344](#) and described in [Table 2-356](#).

Return to the [Summary Table](#).

Figure 2-344. DC173 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L9-CS11							
R/W-80h							

Table 2-356. DC173 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L9-CS11	R/W	80h	8-bits constant current value for LED Dot L9-CS11

2.12.175 DC174 Register (Address = 1AEh) [Default = 80h]

DC174 is shown in [Figure 2-345](#) and described in [Table 2-357](#).

Return to the [Summary Table](#).

Figure 2-345. DC174 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L9-CS12							
R/W-80h							

Table 2-357. DC174 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L9-CS12	R/W	80h	8-bits constant current value for LED Dot L9-CS12

2.12.176 DC175 Register (Address = 1AFh) [Default = 80h]

DC175 is shown in [Figure 2-346](#) and described in [Table 2-358](#).

Return to the [Summary Table](#).

Figure 2-346. DC175 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L9-CS13							
R/W-80h							

Table 2-358. DC175 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L9-CS13	R/W	80h	8-bits constant current value for LED Dot L9-CS13

2.12.177 DC176 Register (Address = 1B0h) [Default = 80h]

DC176 is shown in [Figure 2-347](#) and described in [Table 2-359](#).

Return to the [Summary Table](#).

Figure 2-347. DC176 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L9-CS14							
R/W-80h							

Table 2-359. DC176 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L9-CS14	R/W	80h	8-bits constant current value for LED Dot L9-CS14

2.12.178 DC177 Register (Address = 1B1h) [Default = 80h]

DC177 is shown in [Figure 2-348](#) and described in [Table 2-360](#).

Return to the [Summary Table](#).

Figure 2-348. DC177 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L9-CS15							
R/W-80h							

Table 2-360. DC177 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L9-CS15	R/W	80h	8-bits constant current value for LED Dot L9-CS15

2.12.179 DC178 Register (Address = 1B2h) [Default = 80h]

DC178 is shown in [Figure 2-349](#) and described in [Table 2-361](#).

Return to the [Summary Table](#).

Figure 2-349. DC178 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L9-CS16							
R/W-80h							

Table 2-361. DC178 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L9-CS16	R/W	80h	8-bits constant current value for LED Dot L9-CS16

2.12.180 DC179 Register (Address = 1B3h) [Default = 80h]

DC179 is shown in [Figure 2-350](#) and described in [Table 2-362](#).

Return to the [Summary Table](#).

Figure 2-350. DC179 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L9-CS17							
R/W-80h							

Table 2-362. DC179 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L9-CS17	R/W	80h	8-bits constant current value for LED Dot L9-CS17

2.12.181 DC180 Register (Address = 1B4h) [Default = 80h]

DC180 is shown in [Figure 2-351](#) and described in [Table 2-363](#).

Return to the [Summary Table](#).

Figure 2-351. DC180 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L10-CS0							
R/W-80h							

Table 2-363. DC180 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L10-CS0	R/W	80h	8-bits constant current value for LED Dot L10-CS0

2.12.182 DC181 Register (Address = 1B5h) [Default = 80h]

DC181 is shown in [Figure 2-352](#) and described in [Table 2-364](#).

Return to the [Summary Table](#).

Figure 2-352. DC181 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L10-CS1							
R/W-80h							

Table 2-364. DC181 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L10-CS1	R/W	80h	8-bits constant current value for LED Dot L10-CS1

2.12.183 DC182 Register (Address = 1B6h) [Default = 80h]

DC182 is shown in [Figure 2-353](#) and described in [Table 2-365](#).

Return to the [Summary Table](#).

Figure 2-353. DC182 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L10-CS2							
R/W-80h							

Table 2-365. DC182 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L10-CS2	R/W	80h	8-bits constant current value for LED Dot L10-CS2

2.12.184 DC183 Register (Address = 1B7h) [Default = 80h]

DC183 is shown in [Figure 2-354](#) and described in [Table 2-366](#).

Return to the [Summary Table](#).

Figure 2-354. DC183 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L10-CS3							
R/W-80h							

Table 2-366. DC183 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L10-CS3	R/W	80h	8-bits constant current value for LED Dot L10-CS3

2.12.185 DC184 Register (Address = 1B8h) [Default = 80h]

DC184 is shown in [Figure 2-355](#) and described in [Table 2-367](#).

Return to the [Summary Table](#).

Figure 2-355. DC184 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L10-CS4							
R/W-80h							

Table 2-367. DC184 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L10-CS4	R/W	80h	8-bits constant current value for LED Dot L10-CS4

2.12.186 DC185 Register (Address = 1B9h) [Default = 80h]

DC185 is shown in [Figure 2-356](#) and described in [Table 2-368](#).

Return to the [Summary Table](#).

Figure 2-356. DC185 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L10-CS5							
R/W-80h							

Table 2-368. DC185 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L10-CS5	R/W	80h	8-bits constant current value for LED Dot L10-CS5

2.12.187 DC186 Register (Address = 1BAh) [Default = 80h]

DC186 is shown in [Figure 2-357](#) and described in [Table 2-369](#).

Return to the [Summary Table](#).

Figure 2-357. DC186 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L10-CS6							
R/W-80h							

Table 2-369. DC186 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L10-CS6	R/W	80h	8-bits constant current value for LED Dot L10-CS6

2.12.188 DC187 Register (Address = 1BBh) [Default = 80h]

DC187 is shown in [Figure 2-358](#) and described in [Table 2-370](#).

Return to the [Summary Table](#).

Figure 2-358. DC187 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L10-CS7							
R/W-80h							

Table 2-370. DC187 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L10-CS7	R/W	80h	8-bits constant current value for LED Dot L10-CS7

2.12.189 DC188 Register (Address = 1BCh) [Default = 80h]

DC188 is shown in [Figure 2-359](#) and described in [Table 2-371](#).

Return to the [Summary Table](#).

Figure 2-359. DC188 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L10-CS8							
R/W-80h							

Table 2-371. DC188 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L10-CS8	R/W	80h	8-bits constant current value for LED Dot L10-CS8

2.12.190 DC189 Register (Address = 1BDh) [Default = 80h]

DC189 is shown in [Figure 2-360](#) and described in [Table 2-372](#).

Return to the [Summary Table](#).

Figure 2-360. DC189 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L10-CS9							
R/W-80h							

Table 2-372. DC189 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L10-CS9	R/W	80h	8-bits constant current value for LED Dot L10-CS9

2.12.191 DC190 Register (Address = 1BEh) [Default = 80h]

DC190 is shown in [Figure 2-361](#) and described in [Table 2-373](#).

Return to the [Summary Table](#).

Figure 2-361. DC190 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L10-CS10							
R/W-80h							

Table 2-373. DC190 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L10-CS10	R/W	80h	8-bits constant current value for LED Dot L10-CS10

2.12.192 DC191 Register (Address = 1BFh) [Default = 80h]

DC191 is shown in [Figure 2-362](#) and described in [Table 2-374](#).

Return to the [Summary Table](#).

Figure 2-362. DC191 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L10-CS11							
R/W-80h							

Table 2-374. DC191 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L10-CS11	R/W	80h	8-bits constant current value for LED Dot L10-CS11

2.12.193 DC192 Register (Address = 1C0h) [Default = 80h]

DC192 is shown in [Figure 2-363](#) and described in [Table 2-375](#).

Return to the [Summary Table](#).

Figure 2-363. DC192 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L10-CS12							
R/W-80h							

Table 2-375. DC192 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L10-CS12	R/W	80h	8-bits constant current value for LED Dot L10-CS12

2.12.194 DC193 Register (Address = 1C1h) [Default = 80h]

DC193 is shown in [Figure 2-364](#) and described in [Table 2-376](#).

Return to the [Summary Table](#).

Figure 2-364. DC193 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L10-CS13							
R/W-80h							

Table 2-376. DC193 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L10-CS13	R/W	80h	8-bits constant current value for LED Dot L10-CS13

2.12.195 DC194 Register (Address = 1C2h) [Default = 80h]

DC194 is shown in [Figure 2-365](#) and described in [Table 2-377](#).

Return to the [Summary Table](#).

Figure 2-365. DC194 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L10-CS14							
R/W-80h							

Table 2-377. DC194 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L10-CS14	R/W	80h	8-bits constant current value for LED Dot L10-CS14

2.12.196 DC195 Register (Address = 1C3h) [Default = 80h]

DC195 is shown in [Figure 2-366](#) and described in [Table 2-378](#).

Return to the [Summary Table](#).

Figure 2-366. DC195 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L10-CS15							
R/W-80h							

Table 2-378. DC195 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_for_Dot_L10-CS15	R/W	80h	8-bits constant current value for LED Dot L10-CS15

2.12.197 DC196 Register (Address = 1C4h) [Default = 80h]

DC196 is shown in [Figure 2-367](#) and described in [Table 2-379](#).

Return to the [Summary Table](#).

Figure 2-367. DC196 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L10-CS16							
R/W-80h							

Table 2-379. DC196 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L10-CS16	R/W	80h	8-bits constant current value for LED Dot L10-CS16

2.12.198 DC197 Register (Address = 1C5h) [Default = 80h]

DC197 is shown in [Figure 2-368](#) and described in [Table 2-380](#).

Return to the [Summary Table](#).

Figure 2-368. DC197 Register

7	6	5	4	3	2	1	0
LED_dot_current_setting_for_Dot_L10-CS17							
R/W-80h							

Table 2-380. DC197 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	LED_dot_current_setting_f or_Dot_L10-CS17	R/W	80h	8-bits constant current value for LED Dot L10-CS17

2.13 PWM Registers

Table 2-381 lists the PWM registers. All register offset addresses not listed in Table 2-381 should be considered as reserved locations and the register contents should not be modified.

PWM dimming

Table 2-381. PWM Registers

Address	Acronym	Register Name	Section
200h	pwm_bri0	8-bits PWM for Dot L0-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS0	Go
201h	pwm_bri1	8-bits PWM for Dot L0-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS0	Go
202h	pwm_bri2	8-bits PWM for Dot L0-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS1	Go
203h	pwm_bri3	8-bits PWM for Dot L0-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS1	Go
204h	pwm_bri4	8-bits PWM for Dot L0-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS2	Go
205h	pwm_bri5	8-bits PWM for Dot L0-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS2	Go
206h	pwm_bri6	8-bits PWM for Dot L0-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS3	Go
207h	pwm_bri7	8-bits PWM for Dot L0-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS3	Go
208h	pwm_bri8	8-bits PWM for Dot L0-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS4	Go
209h	pwm_bri9	8-bits PWM for Dot L0-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS4	Go
20Ah	pwm_bri10	8-bits PWM for Dot L0-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS5	Go
20Bh	pwm_bri11	8-bits PWM for Dot L0-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS5	Go
20Ch	pwm_bri12	8-bits PWM for Dot L0-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS6	Go
20Dh	pwm_bri13	8-bits PWM for Dot L0-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS6	Go
20Eh	pwm_bri14	8-bits PWM for Dot L0-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS7	Go
20Fh	pwm_bri15	8-bits PWM for Dot L0-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS7	Go
210h	pwm_bri16	8-bits PWM for Dot L0-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS8	Go
211h	pwm_bri17	8-bits PWM for Dot L0-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS8	Go
212h	pwm_bri18	8-bits PWM for Dot L1-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS9	Go
213h	pwm_bri19	8-bits PWM for Dot L1-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS9	Go
214h	pwm_bri20	8-bits PWM for Dot L1-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS10	Go
215h	pwm_bri21	8-bits PWM for Dot L1-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS10	Go
216h	pwm_bri22	8-bits PWM for Dot L1-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS11	Go
217h	pwm_bri23	8-bits PWM for Dot L1-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS11	Go

Table 2-381. PWM Registers (continued)

Address	Acronym	Register Name	Section
218h	pwm_bri24	8-bits PWM for Dot L1-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS12	Go
219h	pwm_bri25	8-bits PWM for Dot L1-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS12	Go
21Ah	pwm_bri26	8-bits PWM for Dot L1-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS13	Go
21Bh	pwm_bri27	8-bits PWM for Dot L1-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS13	Go
21Ch	pwm_bri28	8-bits PWM for Dot L1-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS14	Go
21Dh	pwm_bri29	8-bits PWM for Dot L1-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS14	Go
21Eh	pwm_bri30	8-bits PWM for Dot L1-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS15	Go
21Fh	pwm_bri31	8-bits PWM for Dot L1-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS15	Go
220h	pwm_bri32	8-bits PWM for Dot L1-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS16	Go
221h	pwm_bri33	8-bits PWM for Dot L1-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS16	Go
222h	pwm_bri34	8-bits PWM for Dot L1-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS17	Go
223h	pwm_bri35	8-bits PWM for Dot L1-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS17	Go
224h	pwm_bri36	8-bits PWM for Dot L2-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS0	Go
225h	pwm_bri37	8-bits PWM for Dot L2-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS0	Go
226h	pwm_bri38	8-bits PWM for Dot L2-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS1	Go
227h	pwm_bri39	8-bits PWM for Dot L2-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS1	Go
228h	pwm_bri40	8-bits PWM for Dot L2-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS2	Go
229h	pwm_bri41	8-bits PWM for Dot L2-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS2	Go
22Ah	pwm_bri42	8-bits PWM for Dot L2-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS3	Go
22Bh	pwm_bri43	8-bits PWM for Dot L2-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS3	Go
22Ch	pwm_bri44	8-bits PWM for Dot L2-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS4	Go
22Dh	pwm_bri45	8-bits PWM for Dot L2-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS4	Go
22Eh	pwm_bri46	8-bits PWM for Dot L2-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS5	Go
22Fh	pwm_bri47	8-bits PWM for Dot L2-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS5	Go
230h	pwm_bri48	8-bits PWM for Dot L2-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS6	Go
231h	pwm_bri49	8-bits PWM for Dot L2-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS6	Go
232h	pwm_bri50	8-bits PWM for Dot L2-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS7	Go

Table 2-381. PWM Registers (continued)

Address	Acronym	Register Name	Section
233h	pwm_bri51	8-bits PWM for Dot L2-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS7	Go
234h	pwm_bri52	8-bits PWM for Dot L2-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS8	Go
235h	pwm_bri53	8-bits PWM for Dot L2-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS8	Go
236h	pwm_bri54	8-bits PWM for Dot L3-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS9	Go
237h	pwm_bri55	8-bits PWM for Dot L3-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS9	Go
238h	pwm_bri56	8-bits PWM for Dot L3-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS10	Go
239h	pwm_bri57	8-bits PWM for Dot L3-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS10	Go
23Ah	pwm_bri58	8-bits PWM for Dot L3-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS11	Go
23Bh	pwm_bri59	8-bits PWM for Dot L3-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS11	Go
23Ch	pwm_bri60	8-bits PWM for Dot L3-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS12	Go
23Dh	pwm_bri61	8-bits PWM for Dot L3-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS12	Go
23Eh	pwm_bri62	8-bits PWM for Dot L3-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS13	Go
23Fh	pwm_bri63	8-bits PWM for Dot L3-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS13	Go
240h	pwm_bri64	8-bits PWM for Dot L3-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS14	Go
241h	pwm_bri65	8-bits PWM for Dot L3-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS14	Go
242h	pwm_bri66	8-bits PWM for Dot L3-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS15	Go
243h	pwm_bri67	8-bits PWM for Dot L3-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS15	Go
244h	pwm_bri68	8-bits PWM for Dot L3-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS16	Go
245h	pwm_bri69	8-bits PWM for Dot L3-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS16	Go
246h	pwm_bri70	8-bits PWM for Dot L3-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS17	Go
247h	pwm_bri71	8-bits PWM for Dot L3-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS17	Go
248h	pwm_bri72	8-bits PWM for Dot L4-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS0	Go
249h	pwm_bri73	8-bits PWM for Dot L4-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS0	Go
24Ah	pwm_bri74	8-bits PWM for Dot L4-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS1	Go
24Bh	pwm_bri75	8-bits PWM for Dot L4-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS1	Go
24Ch	pwm_bri76	8-bits PWM for Dot L4-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS2	Go
24Dh	pwm_bri77	8-bits PWM for Dot L4-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS2	Go

Table 2-381. PWM Registers (continued)

Address	Acronym	Register Name	Section
24Eh	pwm_bri78	8-bits PWM for Dot L4-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS3	Go
24Fh	pwm_bri79	8-bits PWM for Dot L4-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS3	Go
250h	pwm_bri80	8-bits PWM for Dot L4-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS4	Go
251h	pwm_bri81	8-bits PWM for Dot L4-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS4	Go
252h	pwm_bri82	8-bits PWM for Dot L4-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS5	Go
253h	pwm_bri83	8-bits PWM for Dot L4-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS5	Go
254h	pwm_bri84	8-bits PWM for Dot L4-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS6	Go
255h	pwm_bri85	8-bits PWM for Dot L4-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS6	Go
256h	pwm_bri86	8-bits PWM for Dot L4-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS7	Go
257h	pwm_bri87	8-bits PWM for Dot L4-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS7	Go
258h	pwm_bri88	8-bits PWM for Dot L4-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS8	Go
259h	pwm_bri89	8-bits PWM for Dot L4-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS8	Go
25Ah	pwm_bri90	8-bits PWM for Dot L5-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS9	Go
25Bh	pwm_bri91	8-bits PWM for Dot L5-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS9	Go
25Ch	pwm_bri92	8-bits PWM for Dot L5-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS10	Go
25Dh	pwm_bri93	8-bits PWM for Dot L5-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS10	Go
25Eh	pwm_bri94	8-bits PWM for Dot L5-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS11	Go
25Fh	pwm_bri95	8-bits PWM for Dot L5-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS11	Go
260h	pwm_bri96	8-bits PWM for Dot L5-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS12	Go
261h	pwm_bri97	8-bits PWM for Dot L5-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS12	Go
262h	pwm_bri98	8-bits PWM for Dot L5-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS13	Go
263h	pwm_bri99	8-bits PWM for Dot L5-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS13	Go
264h	pwm_bri100	8-bits PWM for Dot L5-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS14	Go
265h	pwm_bri101	8-bits PWM for Dot L5-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS14	Go
266h	pwm_bri102	8-bits PWM for Dot L5-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS15	Go
267h	pwm_bri103	8-bits PWM for Dot L5-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS15	Go
268h	pwm_bri104	8-bits PWM for Dot L5-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS16	Go

Table 2-381. PWM Registers (continued)

Address	Acronym	Register Name	Section
269h	pwm_bri105	8-bits PWM for Dot L5-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS16	Go
26Ah	pwm_bri106	8-bits PWM for Dot L5-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS17	Go
26Bh	pwm_bri107	8-bits PWM for Dot L5-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS17	Go
26Ch	pwm_bri108	8-bits PWM for Dot L6-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS0	Go
26Dh	pwm_bri109	8-bits PWM for Dot L6-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS0	Go
26Eh	pwm_bri110	8-bits PWM for Dot L6-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS1	Go
26Fh	pwm_bri111	8-bits PWM for Dot L6-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS1	Go
270h	pwm_bri112	8-bits PWM for Dot L6-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS2	Go
271h	pwm_bri113	8-bits PWM for Dot L6-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS2	Go
272h	pwm_bri114	8-bits PWM for Dot L6-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS3	Go
273h	pwm_bri115	8-bits PWM for Dot L6-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS3	Go
274h	pwm_bri116	8-bits PWM for Dot L6-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS4	Go
275h	pwm_bri117	8-bits PWM for Dot L6-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS4	Go
276h	pwm_bri118	8-bits PWM for Dot L6-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS5	Go
277h	pwm_bri119	8-bits PWM for Dot L6-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS5	Go
278h	pwm_bri120	8-bits PWM for Dot L6-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS6	Go
279h	pwm_bri121	8-bits PWM for Dot L6-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS6	Go
27Ah	pwm_bri122	8-bits PWM for Dot L6-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS7	Go
27Bh	pwm_bri123	8-bits PWM for Dot L6-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS7	Go
27Ch	pwm_bri124	8-bits PWM for Dot L6-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS8	Go
27Dh	pwm_bri125	8-bits PWM for Dot L6-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS8	Go
27Eh	pwm_bri126	8-bits PWM for Dot L7-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS9	Go
27Fh	pwm_bri127	8-bits PWM for Dot L7-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS9	Go
280h	pwm_bri128	8-bits PWM for Dot L7-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS10	Go
281h	pwm_bri129	8-bits PWM for Dot L7-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS10	Go
282h	pwm_bri130	8-bits PWM for Dot L7-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS11	Go
283h	pwm_bri131	8-bits PWM for Dot L7-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS11	Go

Table 2-381. PWM Registers (continued)

Address	Acronym	Register Name	Section
284h	pwm_bri132	8-bits PWM for Dot L7-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS12	Go
285h	pwm_bri133	8-bits PWM for Dot L7-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS12	Go
286h	pwm_bri134	8-bits PWM for Dot L7-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS13	Go
287h	pwm_bri135	8-bits PWM for Dot L7-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS13	Go
288h	pwm_bri136	8-bits PWM for Dot L7-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS14	Go
289h	pwm_bri137	8-bits PWM for Dot L7-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS14	Go
28Ah	pwm_bri138	8-bits PWM for Dot L7-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS15	Go
28Bh	pwm_bri139	8-bits PWM for Dot L7-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS15	Go
28Ch	pwm_bri140	8-bits PWM for Dot L7-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS16	Go
28Dh	pwm_bri141	8-bits PWM for Dot L7-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS16	Go
28Eh	pwm_bri142	8-bits PWM for Dot L7-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS17	Go
28Fh	pwm_bri143	8-bits PWM for Dot L7-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS17	Go
290h	pwm_bri144	8-bits PWM for Dot L8-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS0	Go
291h	pwm_bri145	8-bits PWM for Dot L8-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS0	Go
292h	pwm_bri146	8-bits PWM for Dot L8-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS1	Go
293h	pwm_bri147	8-bits PWM for Dot L8-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS1	Go
294h	pwm_bri148	8-bits PWM for Dot L8-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS2	Go
295h	pwm_bri149	8-bits PWM for Dot L8-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS2	Go
296h	pwm_bri150	8-bits PWM for Dot L8-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS3	Go
297h	pwm_bri151	8-bits PWM for Dot L8-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS3	Go
298h	pwm_bri152	8-bits PWM for Dot L8-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS4	Go
299h	pwm_bri153	8-bits PWM for Dot L8-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS4	Go
29Ah	pwm_bri154	8-bits PWM for Dot L8-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS5	Go
29Bh	pwm_bri155	8-bits PWM for Dot L8-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS5	Go
29Ch	pwm_bri156	8-bits PWM for Dot L8-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS6	Go
29Dh	pwm_bri157	8-bits PWM for Dot L8-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS6	Go
29Eh	pwm_bri158	8-bits PWM for Dot L8-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS7	Go

Table 2-381. PWM Registers (continued)

Address	Acronym	Register Name	Section
29Fh	pwm_bri159	8-bits PWM for Dot L8-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS7	Go
2A0h	pwm_bri160	8-bits PWM for Dot L8-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS8	Go
2A1h	pwm_bri161	8-bits PWM for Dot L8-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS8	Go
2A2h	pwm_bri162	8-bits PWM for Dot L9-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS9	Go
2A3h	pwm_bri163	8-bits PWM for Dot L9-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS9	Go
2A4h	pwm_bri164	8-bits PWM for Dot L9-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS10	Go
2A5h	pwm_bri165	8-bits PWM for Dot L9-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS10	Go
2A6h	pwm_bri166	8-bits PWM for Dot L9-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS11	Go
2A7h	pwm_bri167	8-bits PWM for Dot L9-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS11	Go
2A8h	pwm_bri168	8-bits PWM for Dot L9-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS12	Go
2A9h	pwm_bri169	8-bits PWM for Dot L9-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS12	Go
2AAh	pwm_bri170	8-bits PWM for Dot L9-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS13	Go
2ABh	pwm_bri171	8-bits PWM for Dot L9-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS13	Go
2ACh	pwm_bri172	8-bits PWM for Dot L9-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS14	Go
2ADh	pwm_bri173	8-bits PWM for Dot L9-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS14	Go
2AEh	pwm_bri174	8-bits PWM for Dot L9-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS15	Go
2AFh	pwm_bri175	8-bits PWM for Dot L9-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS15	Go
2B0h	pwm_bri176	8-bits PWM for Dot L9-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS16	Go
2B1h	pwm_bri177	8-bits PWM for Dot L9-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS16	Go
2B2h	pwm_bri178	8-bits PWM for Dot L9-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS17	Go
2B3h	pwm_bri179	8-bits PWM for Dot L9-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS17	Go
2B4h	pwm_bri180	8-bits PWM for Dot L10-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L5-CS0	Go
2B5h	pwm_bri181	8-bits PWM for Dot L10-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L5-CS0	Go
2B6h	pwm_bri182	8-bits PWM for Dot L10-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L5-CS1	Go
2B7h	pwm_bri183	8-bits PWM for Dot L10-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L5-CS1	Go
2B8h	pwm_bri184	8-bits PWM for Dot L10-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L5-CS2	Go
2B9h	pwm_bri185	8-bits PWM for Dot L10-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L5-CS2	Go

Table 2-381. PWM Registers (continued)

Address	Acronym	Register Name	Section
2BAh	pwm_bri186	8-bits PWM for Dot L10-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L5-CS3	Go
2BBh	pwm_bri187	8-bits PWM for Dot L10-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L5-CS3	Go
2BCh	pwm_bri188	8-bits PWM for Dot L10-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L5-CS4	Go
2BDh	pwm_bri189	8-bits PWM for Dot L10-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L5-CS4	Go
2BEh	pwm_bri190	8-bits PWM for Dot L10-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L5-CS5	Go
2BFh	pwm_bri191	8-bits PWM for Dot L10-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L5-CS5	Go
2C0h	pwm_bri192	8-bits PWM for Dot L10-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L5-CS6	Go
2C1h	pwm_bri193	8-bits PWM for Dot L10-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L5-CS6	Go
2C2h	pwm_bri194	8-bits PWM for Dot L10-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L5-CS7	Go
2C3h	pwm_bri195	8-bits PWM for Dot L10-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L5-CS7	Go
2C4h	pwm_bri196	8-bits PWM for Dot L10-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L5-CS8	Go
2C5h	pwm_bri197	8-bits PWM for Dot L10-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L5-CS8	Go
2C6h	pwm_bri198	16-bits PWM lower 8 bits [7:0] for Dot L5-CS9	Go
2C7h	pwm_bri199	16-bits PWM higher 8 bits [15:8] for Dot L5-CS9	Go
2C8h	pwm_bri200	16-bits PWM lower 8 bits [7:0] for Dot L5-CS10	Go
2C9h	pwm_bri201	16-bits PWM higher 8 bits [15:8] for Dot L5-CS10	Go
2CAh	pwm_bri202	16-bits PWM lower 8 bits [7:0] for Dot L5-CS11	Go
2CBh	pwm_bri203	16-bits PWM higher 8 bits [15:8] for Dot L5-CS11	Go
2CCh	pwm_bri204	16-bits PWM lower 8 bits [7:0] for Dot L5-CS12	Go
2CDh	pwm_bri205	16-bits PWM higher 8 bits [15:8] for Dot L5-CS12	Go
2CEh	pwm_bri206	16-bits PWM lower 8 bits [7:0] for Dot L5-CS13	Go
2CFh	pwm_bri207	16-bits PWM higher 8 bits [15:8] for Dot L5-CS13	Go
2D0h	pwm_bri208	16-bits PWM lower 8 bits [7:0] for Dot L5-CS14	Go
2D1h	pwm_bri209	16-bits PWM higher 8 bits [15:8] for Dot L5-CS14	Go
2D2h	pwm_bri210	16-bits PWM lower 8 bits [7:0] for Dot L5-CS15	Go
2D3h	pwm_bri211	16-bits PWM higher 8 bits [15:8] for Dot L5-CS15	Go
2D4h	pwm_bri212	16-bits PWM lower 8 bits [7:0] for Dot L5-CS16	Go
2D5h	pwm_bri213	16-bits PWM higher 8 bits [15:8] for Dot L5-CS16	Go
2D6h	pwm_bri214	16-bits PWM lower 8 bits [7:0] for Dot L5-CS17	Go
2D7h	pwm_bri215	16-bits PWM higher 8 bits [15:8] for Dot L5-CS17	Go
2D8h	pwm_bri216	16-bits PWM lower 8 bits [7:0] for Dot L6-CS0	Go
2D9h	pwm_bri217	16-bits PWM higher 8 bits [15:8] for Dot L6-CS0	Go
2DAh	pwm_bri218	16-bits PWM lower 8 bits [7:0] for Dot L6-CS1	Go
2DBh	pwm_bri219	16-bits PWM higher 8 bits [15:8] for Dot L6-CS1	Go
2DCh	pwm_bri220	16-bits PWM lower 8 bits [7:0] for Dot L6-CS2	Go
2DDh	pwm_bri221	16-bits PWM higher 8 bits [15:8] for Dot L6-CS2	Go
2DEh	pwm_bri222	16-bits PWM lower 8 bits [7:0] for Dot L6-CS3	Go

Table 2-381. PWM Registers (continued)

Address	Acronym	Register Name	Section
2DFh	pwm_bri223	16-bits PWM higher 8 bits [15:8] for Dot L6-CS3	Go
2E0h	pwm_bri224	16-bits PWM lower 8 bits [7:0] for Dot L6-CS4	Go
2E1h	pwm_bri225	16-bits PWM higher 8 bits [15:8] for Dot L6-CS4	Go
2E2h	pwm_bri226	16-bits PWM lower 8 bits [7:0] for Dot L6-CS5	Go
2E3h	pwm_bri227	16-bits PWM higher 8 bits [15:8] for Dot L6-CS5	Go
2E4h	pwm_bri228	16-bits PWM lower 8 bits [7:0] for Dot L6-CS6	Go
2E5h	pwm_bri229	16-bits PWM higher 8 bits [15:8] for Dot L6-CS6	Go
2E6h	pwm_bri230	16-bits PWM lower 8 bits [7:0] for Dot L6-CS7	Go
2E7h	pwm_bri231	16-bits PWM higher 8 bits [15:8] for Dot L6-CS7	Go
2E8h	pwm_bri232	16-bits PWM lower 8 bits [7:0] for Dot L6-CS8	Go
2E9h	pwm_bri233	16-bits PWM higher 8 bits [15:8] for Dot L6-CS8	Go
2EAh	pwm_bri234	16-bits PWM lower 8 bits [7:0] for Dot L6-CS9	Go
2EBh	pwm_bri235	16-bits PWM higher 8 bits [15:8] for Dot L6-CS9	Go
2ECh	pwm_bri236	16-bits PWM lower 8 bits [7:0] for Dot L6-CS10	Go
2EDh	pwm_bri237	16-bits PWM higher 8 bits [15:8] for Dot L6-CS10	Go
2EEh	pwm_bri238	16-bits PWM lower 8 bits [7:0] for Dot L6-CS11	Go
2EFh	pwm_bri239	16-bits PWM higher 8 bits [15:8] for Dot L6-CS11	Go
2F0h	pwm_bri240	16-bits PWM lower 8 bits [7:0] for Dot L6-CS12	Go
2F1h	pwm_bri241	16-bits PWM higher 8 bits [15:8] for Dot L6-CS12	Go
2F2h	pwm_bri242	16-bits PWM lower 8 bits [7:0] for Dot L6-CS13	Go
2F3h	pwm_bri243	16-bits PWM higher 8 bits [15:8] for Dot L6-CS13	Go
2F4h	pwm_bri244	16-bits PWM lower 8 bits [7:0] for Dot L6-CS14	Go
2F5h	pwm_bri245	16-bits PWM higher 8 bits [15:8] for Dot L6-CS14	Go
2F6h	pwm_bri246	16-bits PWM lower 8 bits [7:0] for Dot L6-CS15	Go
2F7h	pwm_bri247	16-bits PWM higher 8 bits [15:8] for Dot L6-CS15	Go
2F8h	pwm_bri248	16-bits PWM lower 8 bits [7:0] for Dot L6-CS16	Go
2F9h	pwm_bri249	16-bits PWM higher 8 bits [15:8] for Dot L6-CS16	Go
2FAh	pwm_bri250	16-bits PWM lower 8 bits [7:0] for Dot L6-CS17	Go
2FBh	pwm_bri251	16-bits PWM higher 8 bits [15:8] for Dot L6-CS17	Go
2FCh	pwm_bri252	16-bits PWM lower 8 bits [7:0] for Dot L7-CS0	Go
2FDh	pwm_bri253	16-bits PWM higher 8 bits [15:8] for Dot L7-CS0	Go
2FEh	pwm_bri254	16-bits PWM lower 8 bits [7:0] for Dot L7-CS1	Go
2FFh	pwm_bri255	16-bits PWM higher 8 bits [15:8] for Dot L7-CS1	Go
300h	pwm_bri256	16-bits PWM lower 8 bits [7:0] for Dot L7-CS2	Go
301h	pwm_bri257	16-bits PWM higher 8 bits [15:8] for Dot L7-CS2	Go
302h	pwm_bri258	16-bits PWM lower 8 bits [7:0] for Dot L7-CS3	Go
303h	pwm_bri259	16-bits PWM higher 8 bits [15:8] for Dot L7-CS3	Go
304h	pwm_bri260	16-bits PWM lower 8 bits [7:0] for Dot L7-CS4	Go
305h	pwm_bri261	16-bits PWM higher 8 bits [15:8] for Dot L7-CS4	Go
306h	pwm_bri262	16-bits PWM lower 8 bits [7:0] for Dot L7-CS5	Go
307h	pwm_bri263	16-bits PWM higher 8 bits [15:8] for Dot L7-CS5	Go
308h	pwm_bri264	16-bits PWM lower 8 bits [7:0] for Dot L7-CS6	Go
309h	pwm_bri265	16-bits PWM higher 8 bits [15:8] for Dot L7-CS6	Go
30Ah	pwm_bri266	16-bits PWM lower 8 bits [7:0] for Dot L7-CS7	Go
30Bh	pwm_bri267	16-bits PWM higher 8 bits [15:8] for Dot L7-CS7	Go

Table 2-381. PWM Registers (continued)

Address	Acronym	Register Name	Section
30Ch	pwm_bri268	16-bits PWM lower 8 bits [7:0] for Dot L7-CS8	Go
30Dh	pwm_bri269	16-bits PWM higher 8 bits [15:8] for Dot L7-CS8	Go
30Eh	pwm_bri270	16-bits PWM lower 8 bits [7:0] for Dot L7-CS9	Go
30Fh	pwm_bri271	16-bits PWM higher 8 bits [15:8] for Dot L7-CS9	Go
310h	pwm_bri272	16-bits PWM lower 8 bits [7:0] for Dot L7-CS10	Go
311h	pwm_bri273	16-bits PWM higher 8 bits [15:8] for Dot L7-CS10	Go
312h	pwm_bri274	16-bits PWM lower 8 bits [7:0] for Dot L7-CS11	Go
313h	pwm_bri275	16-bits PWM higher 8 bits [15:8] for Dot L7-CS11	Go
314h	pwm_bri276	16-bits PWM lower 8 bits [7:0] for Dot L7-CS12	Go
315h	pwm_bri277	16-bits PWM higher 8 bits [15:8] for Dot L7-CS12	Go
316h	pwm_bri278	16-bits PWM lower 8 bits [7:0] for Dot L7-CS13	Go
317h	pwm_bri279	16-bits PWM higher 8 bits [15:8] for Dot L7-CS13	Go
318h	pwm_bri280	16-bits PWM lower 8 bits [7:0] for Dot L7-CS14	Go
319h	pwm_bri281	16-bits PWM higher 8 bits [15:8] for Dot L7-CS14	Go
31Ah	pwm_bri282	16-bits PWM lower 8 bits [7:0] for Dot L7-CS15	Go
31Bh	pwm_bri283	16-bits PWM higher 8 bits [15:8] for Dot L7-CS15	Go
31Ch	pwm_bri284	16-bits PWM lower 8 bits [7:0] for Dot L7-CS16	Go
31Dh	pwm_bri285	16-bits PWM higher 8 bits [15:8] for Dot L7-CS16	Go
31Eh	pwm_bri286	16-bits PWM lower 8 bits [7:0] for Dot L7-CS17	Go
31Fh	pwm_bri287	16-bits PWM higher 8 bits [15:8] for Dot L7-CS17	Go
320h	pwm_bri288	16-bits PWM lower 8 bits [7:0] for Dot L8-CS0	Go
321h	pwm_bri289	16-bits PWM higher 8 bits [15:8] for Dot L8-CS0	Go
322h	pwm_bri290	16-bits PWM lower 8 bits [7:0] for Dot L8-CS1	Go
323h	pwm_bri291	16-bits PWM higher 8 bits [15:8] for Dot L8-CS1	Go
324h	pwm_bri292	16-bits PWM lower 8 bits [7:0] for Dot L8-CS2	Go
325h	pwm_bri293	16-bits PWM higher 8 bits [15:8] for Dot L8-CS2	Go
326h	pwm_bri294	16-bits PWM lower 8 bits [7:0] for Dot L8-CS3	Go
327h	pwm_bri295	16-bits PWM higher 8 bits [15:8] for Dot L8-CS3	Go
328h	pwm_bri296	16-bits PWM lower 8 bits [7:0] for Dot L8-CS4	Go
329h	pwm_bri297	16-bits PWM higher 8 bits [15:8] for Dot L8-CS4	Go
32Ah	pwm_bri298	16-bits PWM lower 8 bits [7:0] for Dot L8-CS5	Go
32Bh	pwm_bri299	16-bits PWM higher 8 bits [15:8] for Dot L8-CS5	Go
32Ch	pwm_bri300	16-bits PWM lower 8 bits [7:0] for Dot L8-CS6	Go
32Dh	pwm_bri301	16-bits PWM higher 8 bits [15:8] for Dot L8-CS6	Go
32Eh	pwm_bri302	16-bits PWM lower 8 bits [7:0] for Dot L8-CS7	Go
32Fh	pwm_bri303	16-bits PWM higher 8 bits [15:8] for Dot L8-CS7	Go
330h	pwm_bri304	16-bits PWM lower 8 bits [7:0] for Dot L8-CS8	Go
331h	pwm_bri305	16-bits PWM higher 8 bits [15:8] for Dot L8-CS8	Go
332h	pwm_bri306	16-bits PWM lower 8 bits [7:0] for Dot L8-CS9	Go
333h	pwm_bri307	16-bits PWM higher 8 bits [15:8] for Dot L8-CS9	Go
334h	pwm_bri308	16-bits PWM lower 8 bits [7:0] for Dot L8-CS10	Go
335h	pwm_bri309	16-bits PWM higher 8 bits [15:8] for Dot L8-CS10	Go
336h	pwm_bri310	16-bits PWM lower 8 bits [7:0] for Dot L8-CS11	Go
337h	pwm_bri311	16-bits PWM higher 8 bits [15:8] for Dot L8-CS11	Go
338h	pwm_bri312	16-bits PWM lower 8 bits [7:0] for Dot L8-CS12	Go

Table 2-381. PWM Registers (continued)

Address	Acronym	Register Name	Section
339h	pwm_bri313	16-bits PWM higher 8 bits [15:8] for Dot L8-CS12	Go
33Ah	pwm_bri314	16-bits PWM lower 8 bits [7:0] for Dot L8-CS13	Go
33Bh	pwm_bri315	16-bits PWM higher 8 bits [15:8] for Dot L8-CS13	Go
33Ch	pwm_bri316	16-bits PWM lower 8 bits [7:0] for Dot L8-CS14	Go
33Dh	pwm_bri317	16-bits PWM higher 8 bits [15:8] for Dot L8-CS14	Go
33Eh	pwm_bri318	16-bits PWM lower 8 bits [7:0] for Dot L8-CS15	Go
33Fh	pwm_bri319	16-bits PWM higher 8 bits [15:8] for Dot L8-CS15	Go
340h	pwm_bri320	16-bits PWM lower 8 bits [7:0] for Dot L8-CS16	Go
341h	pwm_bri321	16-bits PWM higher 8 bits [15:8] for Dot L8-CS16	Go
342h	pwm_bri322	16-bits PWM lower 8 bits [7:0] for Dot L8-CS17	Go
343h	pwm_bri323	16-bits PWM higher 8 bits [15:8] for Dot L8-CS17	Go
344h	pwm_bri324	16-bits PWM lower 8 bits [7:0] for Dot L9-CS0	Go
345h	pwm_bri325	16-bits PWM higher 8 bits [15:8] for Dot L9-CS0	Go
346h	pwm_bri326	16-bits PWM lower 8 bits [7:0] for Dot L9-CS1	Go
347h	pwm_bri327	16-bits PWM higher 8 bits [15:8] for Dot L9-CS1	Go
348h	pwm_bri328	16-bits PWM lower 8 bits [7:0] for Dot L9-CS2	Go
349h	pwm_bri329	16-bits PWM higher 8 bits [15:8] for Dot L9-CS2	Go
34Ah	pwm_bri330	16-bits PWM lower 8 bits [7:0] for Dot L9-CS3	Go
34Bh	pwm_bri331	16-bits PWM higher 8 bits [15:8] for Dot L9-CS3	Go
34Ch	pwm_bri332	16-bits PWM lower 8 bits [7:0] for Dot L9-CS4	Go
34Dh	pwm_bri333	16-bits PWM higher 8 bits [15:8] for Dot L9-CS4	Go
34Eh	pwm_bri334	16-bits PWM lower 8 bits [7:0] for Dot L9-CS5	Go
34Fh	pwm_bri335	16-bits PWM higher 8 bits [15:8] for Dot L9-CS5	Go
350h	pwm_bri336	16-bits PWM lower 8 bits [7:0] for Dot L9-CS6	Go
351h	pwm_bri337	16-bits PWM higher 8 bits [15:8] for Dot L9-CS6	Go
352h	pwm_bri338	16-bits PWM lower 8 bits [7:0] for Dot L9-CS7	Go
353h	pwm_bri339	16-bits PWM higher 8 bits [15:8] for Dot L9-CS7	Go
354h	pwm_bri340	16-bits PWM lower 8 bits [7:0] for Dot L9-CS8	Go
355h	pwm_bri341	16-bits PWM higher 8 bits [15:8] for Dot L9-CS8	Go
356h	pwm_bri342	16-bits PWM lower 8 bits [7:0] for Dot L9-CS9	Go
357h	pwm_bri343	16-bits PWM higher 8 bits [15:8] for Dot L9-CS9	Go
358h	pwm_bri344	16-bits PWM lower 8 bits [7:0] for Dot L9-CS10	Go
359h	pwm_bri345	16-bits PWM higher 8 bits [15:8] for Dot L9-CS10	Go
35Ah	pwm_bri346	16-bits PWM lower 8 bits [7:0] for Dot L9-CS11	Go
35Bh	pwm_bri347	16-bits PWM higher 8 bits [15:8] for Dot L9-CS11	Go
35Ch	pwm_bri348	16-bits PWM lower 8 bits [7:0] for Dot L9-CS12	Go
35Dh	pwm_bri349	16-bits PWM higher 8 bits [15:8] for Dot L9-CS12	Go
35Eh	pwm_bri350	16-bits PWM lower 8 bits [7:0] for Dot L9-CS13	Go
35Fh	pwm_bri351	16-bits PWM higher 8 bits [15:8] for Dot L9-CS13	Go
360h	pwm_bri352	16-bits PWM lower 8 bits [7:0] for Dot L9-CS14	Go
361h	pwm_bri353	16-bits PWM higher 8 bits [15:8] for Dot L9-CS14	Go
362h	pwm_bri354	16-bits PWM lower 8 bits [7:0] for Dot L9-CS15	Go
363h	pwm_bri355	16-bits PWM higher 8 bits [15:8] for Dot L9-CS15	Go
364h	pwm_bri356	16-bits PWM lower 8 bits [7:0] for Dot L9-CS16	Go
365h	pwm_bri357	16-bits PWM higher 8 bits [15:8] for Dot L9-CS16	Go

Table 2-381. PWM Registers (continued)

Address	Acronym	Register Name	Section
366h	pwm_bri358	16-bits PWM lower 8 bits [7:0] for Dot L9-CS17	Go
367h	pwm_bri359	16-bits PWM higher 8 bits [15:8] for Dot L9-CS17	Go
368h	pwm_bri360	16-bits PWM lower 8 bits [7:0] for Dot L10-CS0	Go
369h	pwm_bri361	16-bits PWM higher 8 bits [15:8] for Dot L10-CS0	Go
36Ah	pwm_bri362	16-bits PWM lower 8 bits [7:0] for Dot L10-CS1	Go
36Bh	pwm_bri363	16-bits PWM higher 8 bits [15:8] for Dot L10-CS1	Go
36Ch	pwm_bri364	16-bits PWM lower 8 bits [7:0] for Dot L10-CS2	Go
36Dh	pwm_bri365	16-bits PWM higher 8 bits [15:8] for Dot L10-CS2	Go
36Eh	pwm_bri366	16-bits PWM lower 8 bits [7:0] for Dot L10-CS3	Go
36Fh	pwm_bri367	16-bits PWM higher 8 bits [15:8] for Dot L10-CS3	Go
370h	pwm_bri368	16-bits PWM lower 8 bits [7:0] for Dot L10-CS4	Go
371h	pwm_bri369	16-bits PWM higher 8 bits [15:8] for Dot L10-CS4	Go
372h	pwm_bri370	16-bits PWM lower 8 bits [7:0] for Dot L10-CS5	Go
373h	pwm_bri371	16-bits PWM higher 8 bits [15:8] for Dot L10-CS5	Go
374h	pwm_bri372	16-bits PWM lower 8 bits [7:0] for Dot L10-CS6	Go
375h	pwm_bri373	16-bits PWM higher 8 bits [15:8] for Dot L10-CS6	Go
376h	pwm_bri374	16-bits PWM lower 8 bits [7:0] for Dot L10-CS7	Go
377h	pwm_bri375	16-bits PWM higher 8 bits [15:8] for Dot L10-CS7	Go
378h	pwm_bri376	16-bits PWM lower 8 bits [7:0] for Dot L10-CS8	Go
379h	pwm_bri377	16-bits PWM higher 8 bits [15:8] for Dot L10-CS8	Go
37Ah	pwm_bri378	16-bits PWM lower 8 bits [7:0] for Dot L10-CS9	Go
37Bh	pwm_bri379	16-bits PWM higher 8 bits [15:8] for Dot L10-CS9	Go
37Ch	pwm_bri380	16-bits PWM lower 8 bits [7:0] for Dot L10-CS10	Go
37Dh	pwm_bri381	16-bits PWM higher 8 bits [15:8] for Dot L10-CS10	Go
37Eh	pwm_bri382	16-bits PWM lower 8 bits [7:0] for Dot L10-CS11	Go
37Fh	pwm_bri383	16-bits PWM higher 8 bits [15:8] for Dot L10-CS11	Go
380h	pwm_bri384	16-bits PWM lower 8 bits [7:0] for Dot L10-CS12	Go
381h	pwm_bri385	16-bits PWM higher 8 bits [15:8] for Dot L10-CS12	Go
382h	pwm_bri386	16-bits PWM lower 8 bits [7:0] for Dot L10-CS13	Go
383h	pwm_bri387	16-bits PWM higher 8 bits [15:8] for Dot L10-CS13	Go
384h	pwm_bri388	16-bits PWM lower 8 bits [7:0] for Dot L10-CS14	Go
385h	pwm_bri389	16-bits PWM higher 8 bits [15:8] for Dot L10-CS14	Go
386h	pwm_bri390	16-bits PWM lower 8 bits [7:0] for Dot L10-CS15	Go
387h	pwm_bri391	16-bits PWM higher 8 bits [15:8] for Dot L10-CS15	Go
388h	pwm_bri392	16-bits PWM lower 8 bits [7:0] for Dot L10-CS16	Go
389h	pwm_bri393	16-bits PWM higher 8 bits [15:8] for Dot L10-CS16	Go
38Ah	pwm_bri394	16-bits PWM lower 8 bits [7:0] for Dot L10-CS17	Go
38Bh	pwm_bri395	16-bits PWM higher 8 bits [15:8] for Dot L10-CS17	Go

2.13.1 pwm_bri0 Register (Address = 200h) [Default = 0h]

pwm_bri0 is shown in [Figure 2-369](#) and described in [Table 2-382](#).

Return to the [Summary Table](#).

Figure 2-369. pwm_bri0 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L0-CS0_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS0							
R/W-0h							

Table 2-382. pwm_bri0 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L0-CS0_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS0	R/W	0h	8-bits PWM for Dot L0-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS0

2.13.2 pwm_bri1 Register (Address = 201h) [Default = 0h]

pwm_bri1 is shown in [Figure 2-370](#) and described in [Table 2-383](#).

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Figure 2-370. pwm_bri1 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L0-CS1_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS0							
R/W-0h							

Table 2-383. pwm_bri1 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L0-CS1_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS0	R/W	0h	8-bits PWM for Dot L0-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS0

2.13.3 pwm_bri2 Register (Address = 202h) [Default = 0h]

pwm_bri2 is shown in [Figure 2-371](#) and described in [Table 2-384](#).

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Figure 2-371. pwm_bri2 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L0-CS2_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS1							
R/W-0h							

Table 2-384. pwm_bri2 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L0-CS2_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS1	R/W	0h	8-bits PWM for Dot L0-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS1

2.13.4 pwm_bri3 Register (Address = 203h) [Default = 0h]

pwm_bri3 is shown in [Figure 2-372](#) and described in [Table 2-385](#).

Return to the [Summary Table](#).

Figure 2-372. pwm_bri3 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L0-CS3_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS1							
R/W-0h							

Table 2-385. pwm_bri3 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L0-CS3_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS1	R/W	0h	8-bits PWM for Dot L0-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS1

2.13.5 pwm_bri4 Register (Address = 204h) [Default = 0h]

pwm_bri4 is shown in [Figure 2-373](#) and described in [Table 2-386](#).

Return to the [Summary Table](#).

Figure 2-373. pwm_bri4 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L0-CS4_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS2							
R/W-0h							

Table 2-386. pwm_bri4 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L0-CS4_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS2	R/W	0h	8-bits PWM for Dot L0-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS2

2.13.6 pwm_bri5 Register (Address = 205h) [Default = 0h]

pwm_bri5 is shown in [Figure 2-374](#) and described in [Table 2-387](#).

Return to the [Summary Table](#).

Figure 2-374. pwm_bri5 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L0-CS5_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS2							
R/W-0h							

Table 2-387. pwm_bri5 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L0-CS5_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS2	R/W	0h	8-bits PWM for Dot L0-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS2

2.13.7 pwm_bri6 Register (Address = 206h) [Default = 0h]

pwm_bri6 is shown in [Figure 2-375](#) and described in [Table 2-388](#).

Return to the [Summary Table](#).

Figure 2-375. pwm_bri6 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L0-CS6_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS3							
R/W-0h							

Table 2-388. pwm_bri6 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L0-CS6_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS3	R/W	0h	8-bits PWM for Dot L0-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS3

2.13.8 pwm_bri7 Register (Address = 207h) [Default = 0h]

pwm_bri7 is shown in [Figure 2-376](#) and described in [Table 2-389](#).

Return to the [Summary Table](#).

Figure 2-376. pwm_bri7 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L0-CS7_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS3							
R/W-0h							

Table 2-389. pwm_bri7 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L0-CS7_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS3	R/W	0h	8-bits PWM for Dot L0-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS3

2.13.9 pwm_bri8 Register (Address = 208h) [Default = 0h]

pwm_bri8 is shown in [Figure 2-377](#) and described in [Table 2-390](#).

Return to the [Summary Table](#).

Figure 2-377. pwm_bri8 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L0-CS8_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS4							
R/W-0h							

Table 2-390. pwm_bri8 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L0-CS8_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS4	R/W	0h	8-bits PWM for Dot L0-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS4

2.13.10 pwm_bri9 Register (Address = 209h) [Default = 0h]

pwm_bri9 is shown in [Figure 2-378](#) and described in [Table 2-391](#).

Return to the [Summary Table](#).

Figure 2-378. pwm_bri9 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L0-CS9_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS4							
R/W-0h							

Table 2-391. pwm_bri9 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L0-CS9_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS4	R/W	0h	8-bits PWM for Dot L0-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS4

2.13.11 pwm_bri10 Register (Address = 20Ah) [Default = 0h]

pwm_bri10 is shown in [Figure 2-379](#) and described in [Table 2-392](#).

Return to the [Summary Table](#).

Figure 2-379. pwm_bri10 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L0-CS10_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS5							
R/W-0h							

Table 2-392. pwm_bri10 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L0-CS10_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS5	R/W	0h	8-bits PWM for Dot L0-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS5

2.13.12 pwm_bri11 Register (Address = 20Bh) [Default = 0h]

pwm_bri11 is shown in [Figure 2-380](#) and described in [Table 2-393](#).

Return to the [Summary Table](#).

Figure 2-380. pwm_bri11 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L0-CS11_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS5							
R/W-0h							

Table 2-393. pwm_bri11 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L0-CS11_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS5	R/W	0h	8-bits PWM for Dot L0-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS5

2.13.13 pwm_bri12 Register (Address = 20Ch) [Default = 0h]

pwm_bri12 is shown in [Figure 2-381](#) and described in [Table 2-394](#).

Return to the [Summary Table](#).

Figure 2-381. pwm_bri12 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L0-CS12_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS6							
R/W-0h							

Table 2-394. pwm_bri12 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L0-CS12_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS6	R/W	0h	8-bits PWM for Dot L0-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS6

2.13.14 pwm_bri13 Register (Address = 20Dh) [Default = 0h]

pwm_bri13 is shown in [Figure 2-382](#) and described in [Table 2-395](#).

Return to the [Summary Table](#).

Figure 2-382. pwm_bri13 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L0-CS13_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS6							
R/W-0h							

Table 2-395. pwm_bri13 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L0-CS13_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS6	R/W	0h	8-bits PWM for Dot L0-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS6

2.13.15 pwm_bri14 Register (Address = 20Eh) [Default = 0h]

pwm_bri14 is shown in [Figure 2-383](#) and described in [Table 2-396](#).

Return to the [Summary Table](#).

Figure 2-383. pwm_bri14 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L0-CS14_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS7							
R/W-0h							

Table 2-396. pwm_bri14 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L0-CS14_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS7	R/W	0h	8-bits PWM for Dot L0-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS7

2.13.16 pwm_bri15 Register (Address = 20Fh) [Default = 0h]

pwm_bri15 is shown in [Figure 2-384](#) and described in [Table 2-397](#).

Return to the [Summary Table](#).

Figure 2-384. pwm_bri15 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L0-CS15_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS7							
R/W-0h							

Table 2-397. pwm_bri15 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L0-CS15_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS7	R/W	0h	8-bits PWM for Dot L0-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS7

2.13.17 pwm_bri16 Register (Address = 210h) [Default = 0h]

pwm_bri16 is shown in [Figure 2-385](#) and described in [Table 2-398](#).

Return to the [Summary Table](#).

Figure 2-385. pwm_bri16 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L0-CS16_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS8							
R/W-0h							

Table 2-398. pwm_bri16 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L0-CS16_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS8	R/W	0h	8-bits PWM for Dot L0-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS8

2.13.18 pwm_bri17 Register (Address = 211h) [Default = 0h]

pwm_bri17 is shown in [Figure 2-386](#) and described in [Table 2-399](#).

Return to the [Summary Table](#).

Figure 2-386. pwm_bri17 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L0-CS17_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS8							
R/W-0h							

Table 2-399. pwm_bri17 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L0-CS17_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS8	R/W	0h	8-bits PWM for Dot L0-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS8

2.13.19 pwm_bri18 Register (Address = 212h) [Default = 0h]

pwm_bri18 is shown in [Figure 2-387](#) and described in [Table 2-400](#).

Return to the [Summary Table](#).

Figure 2-387. pwm_bri18 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L1-CS0_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS9							
R/W-0h							

Table 2-400. pwm_bri18 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L1-CS0_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS9	R/W	0h	8-bits PWM for Dot L1-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS9

2.13.20 pwm_bri19 Register (Address = 213h) [Default = 0h]

pwm_bri19 is shown in [Figure 2-388](#) and described in [Table 2-401](#).

Return to the [Summary Table](#).

Figure 2-388. pwm_bri19 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L1-CS1_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS9							
R/W-0h							

Table 2-401. pwm_bri19 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L1-CS1_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS9	R/W	0h	8-bits PWM for Dot L1-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS9

2.13.21 pwm_bri20 Register (Address = 214h) [Default = 0h]

pwm_bri20 is shown in [Figure 2-389](#) and described in [Table 2-402](#).

Return to the [Summary Table](#).

Figure 2-389. pwm_bri20 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L1-CS2_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS10							
R/W-0h							

Table 2-402. pwm_bri20 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L1-CS2_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS10	R/W	0h	8-bits PWM for Dot L1-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS10

2.13.22 pwm_bri21 Register (Address = 215h) [Default = 0h]

pwm_bri21 is shown in [Figure 2-390](#) and described in [Table 2-403](#).

Return to the [Summary Table](#).

Figure 2-390. pwm_bri21 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L1-CS3_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS10							
R/W-0h							

Table 2-403. pwm_bri21 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L1-CS3_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS10	R/W	0h	8-bits PWM for Dot L1-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS10

2.13.23 pwm_bri22 Register (Address = 216h) [Default = 0h]

pwm_bri22 is shown in [Figure 2-391](#) and described in [Table 2-404](#).

Return to the [Summary Table](#).

Figure 2-391. pwm_bri22 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L1-CS4_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS11							
R/W-0h							

Table 2-404. pwm_bri22 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L1-CS4_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS11	R/W	0h	8-bits PWM for Dot L1-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS11

2.13.24 pwm_bri23 Register (Address = 217h) [Default = 0h]

pwm_bri23 is shown in [Figure 2-392](#) and described in [Table 2-405](#).

Return to the [Summary Table](#).

Figure 2-392. pwm_bri23 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L1-CS5_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS11							
R/W-0h							

Table 2-405. pwm_bri23 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L1-CS5_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS11	R/W	0h	8-bits PWM for Dot L1-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS11

2.13.25 pwm_bri24 Register (Address = 218h) [Default = 0h]

pwm_bri24 is shown in [Figure 2-393](#) and described in [Table 2-406](#).

Return to the [Summary Table](#).

Figure 2-393. pwm_bri24 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L1-CS6_OR_16-bits_PWM_lower_8_bits_7:0_for_Dot_L0-CS12							
R/W-0h							

Table 2-406. pwm_bri24 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L1-CS6_OR_16-bits_PWM_lower_8_bits_7:0_for_Dot_L0-CS12	R/W	0h	8-bits PWM for Dot L1-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS12

2.13.26 pwm_bri25 Register (Address = 219h) [Default = 0h]

pwm_bri25 is shown in [Figure 2-394](#) and described in [Table 2-407](#).

Return to the [Summary Table](#).

Figure 2-394. pwm_bri25 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L1-CS7_OR_16-bits_PWM_higher_8_bits_15:8_for_Dot_L0-CS12							
R/W-0h							

Table 2-407. pwm_bri25 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L1-CS7_OR_16-bits_PWM_higher_8_bits_15:8_for_Dot_L0-CS12	R/W	0h	8-bits PWM for Dot L1-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS12

2.13.27 pwm_bri26 Register (Address = 21Ah) [Default = 0h]

pwm_bri26 is shown in [Figure 2-395](#) and described in [Table 2-408](#).

Return to the [Summary Table](#).

Figure 2-395. pwm_bri26 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L1-CS8_OR_16-bits_PWM_lower_8_bits_7:0_for_Dot_L0-CS13							
R/W-0h							

Table 2-408. pwm_bri26 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L1-CS8_OR_16-bits_PWM_lower_8_bits_7:0_for_Dot_L0-CS13	R/W	0h	8-bits PWM for Dot L1-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS13

2.13.28 pwm_bri27 Register (Address = 21Bh) [Default = 0h]

pwm_bri27 is shown in [Figure 2-396](#) and described in [Table 2-409](#).

Return to the [Summary Table](#).

Figure 2-396. pwm_bri27 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L1-CS9_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS13							
R/W-0h							

Table 2-409. pwm_bri27 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L1-CS9_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS13	R/W	0h	8-bits PWM for Dot L1-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS13

2.13.29 pwm_bri28 Register (Address = 21Ch) [Default = 0h]

pwm_bri28 is shown in [Figure 2-397](#) and described in [Table 2-410](#).

Return to the [Summary Table](#).

Figure 2-397. pwm_bri28 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L1-CS10_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS14							
R/W-0h							

Table 2-410. pwm_bri28 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L1-CS10_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS14	R/W	0h	8-bits PWM for Dot L1-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS14

2.13.30 pwm_bri29 Register (Address = 21Dh) [Default = 0h]

pwm_bri29 is shown in [Figure 2-398](#) and described in [Table 2-411](#).

Return to the [Summary Table](#).

Figure 2-398. pwm_bri29 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L1-CS11_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS14							
R/W-0h							

Table 2-411. pwm_bri29 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L1-CS11_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS14	R/W	0h	8-bits PWM for Dot L1-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS14

2.13.31 pwm_bri30 Register (Address = 21Eh) [Default = 0h]

pwm_bri30 is shown in [Figure 2-399](#) and described in [Table 2-412](#).

Return to the [Summary Table](#).

Figure 2-399. pwm_bri30 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L1-CS12_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS15							
R/W-0h							

Table 2-412. pwm_bri30 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L1-CS12_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS15	R/W	0h	8-bits PWM for Dot L1-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS15

2.13.32 pwm_bri31 Register (Address = 21Fh) [Default = 0h]

pwm_bri31 is shown in [Figure 2-400](#) and described in [Table 2-413](#).

Return to the [Summary Table](#).

Figure 2-400. pwm_bri31 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L1-CS13_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS15							
R/W-0h							

Table 2-413. pwm_bri31 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L1-CS13_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS15	R/W	0h	8-bits PWM for Dot L1-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS15

2.13.33 pwm_bri32 Register (Address = 220h) [Default = 0h]

pwm_bri32 is shown in [Figure 2-401](#) and described in [Table 2-414](#).

Return to the [Summary Table](#).

Figure 2-401. pwm_bri32 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L1-CS14_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS16							
R/W-0h							

Table 2-414. pwm_bri32 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L1-CS14_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS16	R/W	0h	8-bits PWM for Dot L1-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS16

2.13.34 pwm_bri33 Register (Address = 221h) [Default = 0h]

pwm_bri33 is shown in [Figure 2-402](#) and described in [Table 2-415](#).

Return to the [Summary Table](#).

Figure 2-402. pwm_bri33 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L1-CS15_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS16							
R/W-0h							

Table 2-415. pwm_bri33 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L1-CS15_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS16	R/W	0h	8-bits PWM for Dot L1-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS16

2.13.35 pwm_bri34 Register (Address = 222h) [Default = 0h]

pwm_bri34 is shown in [Figure 2-403](#) and described in [Table 2-416](#).

Return to the [Summary Table](#).

Figure 2-403. pwm_bri34 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L1-CS16_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS17							
R/W-0h							

Table 2-416. pwm_bri34 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L1-CS16_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L0-CS17	R/W	0h	8-bits PWM for Dot L1-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L0-CS17

2.13.36 pwm_bri35 Register (Address = 223h) [Default = 0h]

pwm_bri35 is shown in [Figure 2-404](#) and described in [Table 2-417](#).

Return to the [Summary Table](#).

Figure 2-404. pwm_bri35 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L1-CS17_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS17							
R/W-0h							

Table 2-417. pwm_bri35 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L1-CS17_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L0-CS17	R/W	0h	8-bits PWM for Dot L1-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L0-CS17

2.13.37 pwm_bri36 Register (Address = 224h) [Default = 0h]

pwm_bri36 is shown in [Figure 2-405](#) and described in [Table 2-418](#).

Return to the [Summary Table](#).

Figure 2-405. pwm_bri36 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L2-CS0_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS0							
R/W-0h							

Table 2-418. pwm_bri36 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L2-CS0_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS0	R/W	0h	8-bits PWM for Dot L2-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS0

2.13.38 pwm_bri37 Register (Address = 225h) [Default = 0h]

pwm_bri37 is shown in [Figure 2-406](#) and described in [Table 2-419](#).

Return to the [Summary Table](#).

Figure 2-406. pwm_bri37 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L2-CS1_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS0							
R/W-0h							

Table 2-419. pwm_bri37 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L2-CS1_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS0	R/W	0h	8-bits PWM for Dot L2-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS0

2.13.39 pwm_bri38 Register (Address = 226h) [Default = 0h]

pwm_bri38 is shown in [Figure 2-407](#) and described in [Table 2-420](#).

Return to the [Summary Table](#).

Figure 2-407. pwm_bri38 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L2-CS2_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS1							
R/W-0h							

Table 2-420. pwm_bri38 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L2-CS2_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS1	R/W	0h	8-bits PWM for Dot L2-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS1

2.13.40 pwm_bri39 Register (Address = 227h) [Default = 0h]

pwm_bri39 is shown in [Figure 2-408](#) and described in [Table 2-421](#).

Return to the [Summary Table](#).

Figure 2-408. pwm_bri39 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L2-CS3_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS1							
R/W-0h							

Table 2-421. pwm_bri39 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L2-CS3_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS1	R/W	0h	8-bits PWM for Dot L2-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS1

2.13.41 pwm_bri40 Register (Address = 228h) [Default = 0h]

pwm_bri40 is shown in [Figure 2-409](#) and described in [Table 2-422](#).

Return to the [Summary Table](#).

Figure 2-409. pwm_bri40 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L2-CS4_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS2							
R/W-0h							

Table 2-422. pwm_bri40 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L2-CS4_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS2	R/W	0h	8-bits PWM for Dot L2-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS2

2.13.42 pwm_bri41 Register (Address = 229h) [Default = 0h]

pwm_bri41 is shown in [Figure 2-410](#) and described in [Table 2-423](#).

Return to the [Summary Table](#).

Figure 2-410. pwm_bri41 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L2-CS5_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS2							
R/W-0h							

Table 2-423. pwm_bri41 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L2-CS5_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS2	R/W	0h	8-bits PWM for Dot L2-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS2

2.13.43 pwm_bri42 Register (Address = 22Ah) [Default = 0h]

pwm_bri42 is shown in [Figure 2-411](#) and described in [Table 2-424](#).

Return to the [Summary Table](#).

Figure 2-411. pwm_bri42 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L2-CS6_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS3							
R/W-0h							

Table 2-424. pwm_bri42 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L2-CS6_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS3	R/W	0h	8-bits PWM for Dot L2-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS3

2.13.44 pwm_bri43 Register (Address = 22Bh) [Default = 0h]

pwm_bri43 is shown in [Figure 2-412](#) and described in [Table 2-425](#).

Return to the [Summary Table](#).

Figure 2-412. pwm_bri43 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L2-CS7_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS3							
R/W-0h							

Table 2-425. pwm_bri43 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L2-CS7_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS3	R/W	0h	8-bits PWM for Dot L2-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS3

2.13.45 pwm_bri44 Register (Address = 22Ch) [Default = 0h]

pwm_bri44 is shown in [Figure 2-413](#) and described in [Table 2-426](#).

Return to the [Summary Table](#).

Figure 2-413. pwm_bri44 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L2-CS8_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS4							
R/W-0h							

Table 2-426. pwm_bri44 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L2-CS8_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS4	R/W	0h	8-bits PWM for Dot L2-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS4

2.13.46 pwm_bri45 Register (Address = 22Dh) [Default = 0h]

pwm_bri45 is shown in [Figure 2-414](#) and described in [Table 2-427](#).

Return to the [Summary Table](#).

Figure 2-414. pwm_bri45 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L2-CS9_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS4							
R/W-0h							

Table 2-427. pwm_bri45 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L2-CS9_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS4	R/W	0h	8-bits PWM for Dot L2-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS4

2.13.47 pwm_bri46 Register (Address = 22Eh) [Default = 0h]

pwm_bri46 is shown in [Figure 2-415](#) and described in [Table 2-428](#).

Return to the [Summary Table](#).

Figure 2-415. pwm_bri46 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L2-CS10_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS5							
R/W-0h							

Table 2-428. pwm_bri46 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L2-CS10_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS5	R/W	0h	8-bits PWM for Dot L2-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS5

2.13.48 pwm_bri47 Register (Address = 22Fh) [Default = 0h]

pwm_bri47 is shown in [Figure 2-416](#) and described in [Table 2-429](#).

Return to the [Summary Table](#).

Figure 2-416. pwm_bri47 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L2-CS11_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS5							
R/W-0h							

Table 2-429. pwm_bri47 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L2-CS11_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS5	R/W	0h	8-bits PWM for Dot L2-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS5

2.13.49 pwm_bri48 Register (Address = 230h) [Default = 0h]

pwm_bri48 is shown in [Figure 2-417](#) and described in [Table 2-430](#).

Return to the [Summary Table](#).

Figure 2-417. pwm_bri48 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L2-CS12_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS6							
R/W-0h							

Table 2-430. pwm_bri48 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L2-CS12_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS6	R/W	0h	8-bits PWM for Dot L2-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS6

2.13.50 pwm_bri49 Register (Address = 231h) [Default = 0h]

pwm_bri49 is shown in [Figure 2-418](#) and described in [Table 2-431](#).

Return to the [Summary Table](#).

Figure 2-418. pwm_bri49 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L2-CS13_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS6							
R/W-0h							

Table 2-431. pwm_bri49 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L2-CS13_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS6	R/W	0h	8-bits PWM for Dot L2-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS6

2.13.51 pwm_bri50 Register (Address = 232h) [Default = 0h]

pwm_bri50 is shown in [Figure 2-419](#) and described in [Table 2-432](#).

Return to the [Summary Table](#).

Figure 2-419. pwm_bri50 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L2-CS14_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS7							
R/W-0h							

Table 2-432. pwm_bri50 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L2-CS14_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS7	R/W	0h	8-bits PWM for Dot L2-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS7

2.13.52 pwm_bri51 Register (Address = 233h) [Default = 0h]

pwm_bri51 is shown in [Figure 2-420](#) and described in [Table 2-433](#).

Return to the [Summary Table](#).

Figure 2-420. pwm_bri51 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L2-CS15_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS7							
R/W-0h							

Table 2-433. pwm_bri51 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L2-CS15_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS7	R/W	0h	8-bits PWM for Dot L2-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS7

2.13.53 pwm_bri52 Register (Address = 234h) [Default = 0h]

pwm_bri52 is shown in [Figure 2-421](#) and described in [Table 2-434](#).

Return to the [Summary Table](#).

Figure 2-421. pwm_bri52 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L2-CS16_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS8							
R/W-0h							

Table 2-434. pwm_bri52 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L2-CS16_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS8	R/W	0h	8-bits PWM for Dot L2-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS8

2.13.54 pwm_bri53 Register (Address = 235h) [Default = 0h]

pwm_bri53 is shown in [Figure 2-422](#) and described in [Table 2-435](#).

Return to the [Summary Table](#).

Figure 2-422. pwm_bri53 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L2-CS17_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS8							
R/W-0h							

Table 2-435. pwm_bri53 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L2-CS17_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS8	R/W	0h	8-bits PWM for Dot L2-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS8

2.13.55 pwm_bri54 Register (Address = 236h) [Default = 0h]

pwm_bri54 is shown in [Figure 2-423](#) and described in [Table 2-436](#).

Return to the [Summary Table](#).

Figure 2-423. pwm_bri54 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L3-CS0_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS9							
R/W-0h							

Table 2-436. pwm_bri54 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L3-CS0_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS9	R/W	0h	8-bits PWM for Dot L3-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS9

2.13.56 pwm_bri55 Register (Address = 237h) [Default = 0h]

pwm_bri55 is shown in [Figure 2-424](#) and described in [Table 2-437](#).

Return to the [Summary Table](#).

Figure 2-424. pwm_bri55 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L3-CS1_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS9							
R/W-0h							

Table 2-437. pwm_bri55 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L3-CS1_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS9	R/W	0h	8-bits PWM for Dot L3-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS9

2.13.57 pwm_bri56 Register (Address = 238h) [Default = 0h]

pwm_bri56 is shown in [Figure 2-425](#) and described in [Table 2-438](#).

Return to the [Summary Table](#).

Figure 2-425. pwm_bri56 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L3-CS2_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS10							
R/W-0h							

Table 2-438. pwm_bri56 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L3-CS2_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS10	R/W	0h	8-bits PWM for Dot L3-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS10

2.13.58 pwm_bri57 Register (Address = 239h) [Default = 0h]

pwm_bri57 is shown in [Figure 2-426](#) and described in [Table 2-439](#).

Return to the [Summary Table](#).

Figure 2-426. pwm_bri57 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L3-CS3_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS10							
R/W-0h							

Table 2-439. pwm_bri57 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L3-CS3_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS10	R/W	0h	8-bits PWM for Dot L3-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS10

2.13.59 pwm_bri58 Register (Address = 23Ah) [Default = 0h]

pwm_bri58 is shown in [Figure 2-427](#) and described in [Table 2-440](#).

Return to the [Summary Table](#).

Figure 2-427. pwm_bri58 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L3-CS4_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS11							
R/W-0h							

Table 2-440. pwm_bri58 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L3-CS4_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS11	R/W	0h	8-bits PWM for Dot L3-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS11

2.13.60 pwm_bri59 Register (Address = 23Bh) [Default = 0h]

pwm_bri59 is shown in [Figure 2-428](#) and described in [Table 2-441](#).

Return to the [Summary Table](#).

Figure 2-428. pwm_bri59 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L3-CS5_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS11							
R/W-0h							

Table 2-441. pwm_bri59 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L3-CS5_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS11	R/W	0h	8-bits PWM for Dot L3-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS11

2.13.61 pwm_bri60 Register (Address = 23Ch) [Default = 0h]

pwm_bri60 is shown in [Figure 2-429](#) and described in [Table 2-442](#).

Return to the [Summary Table](#).

Figure 2-429. pwm_bri60 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L3-CS6_OR_16-bits_PWM_lower_8_bits_7:0_for_Dot_L1-CS12							
R/W-0h							

Table 2-442. pwm_bri60 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L3-CS6_OR_16-bits_PWM_lower_8_bits_7:0_for_Dot_L1-CS12	R/W	0h	8-bits PWM for Dot L3-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS12

2.13.62 pwm_bri61 Register (Address = 23Dh) [Default = 0h]

pwm_bri61 is shown in [Figure 2-430](#) and described in [Table 2-443](#).

Return to the [Summary Table](#).

Figure 2-430. pwm_bri61 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L3-CS7_OR_16-bits_PWM_higher_8_bits_15:8_for_Dot_L1-CS12							
R/W-0h							

Table 2-443. pwm_bri61 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L3-CS7_OR_16-bits_PWM_higher_8_bits_15:8_for_Dot_L1-CS12	R/W	0h	8-bits PWM for Dot L3-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS12

2.13.63 pwm_bri62 Register (Address = 23Eh) [Default = 0h]

pwm_bri62 is shown in [Figure 2-431](#) and described in [Table 2-444](#).

Return to the [Summary Table](#).

Figure 2-431. pwm_bri62 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L3-CS8_OR_16-bits_PWM_lower_8_bits_7:0_for_Dot_L1-CS13							
R/W-0h							

Table 2-444. pwm_bri62 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L3-CS8_OR_16-bits_PWM_lower_8_bits_7:0_for_Dot_L1-CS13	R/W	0h	8-bits PWM for Dot L3-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS13

2.13.64 pwm_bri63 Register (Address = 23Fh) [Default = 0h]

pwm_bri63 is shown in [Figure 2-432](#) and described in [Table 2-445](#).

Return to the [Summary Table](#).

Figure 2-432. pwm_bri63 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L3-CS9_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS13							
R/W-0h							

Table 2-445. pwm_bri63 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L3-CS9_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS13	R/W	0h	8-bits PWM for Dot L3-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS13

2.13.65 pwm_bri64 Register (Address = 240h) [Default = 0h]

pwm_bri64 is shown in [Figure 2-433](#) and described in [Table 2-446](#).

Return to the [Summary Table](#).

Figure 2-433. pwm_bri64 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L3-CS10_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS14							
R/W-0h							

Table 2-446. pwm_bri64 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L3-CS10_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS14	R/W	0h	8-bits PWM for Dot L3-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS14

2.13.66 pwm_bri65 Register (Address = 241h) [Default = 0h]

pwm_bri65 is shown in [Figure 2-434](#) and described in [Table 2-447](#).

Return to the [Summary Table](#).

Figure 2-434. pwm_bri65 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L3-CS11_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS14							
R/W-0h							

Table 2-447. pwm_bri65 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L3-CS11_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS14	R/W	0h	8-bits PWM for Dot L3-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS14

2.13.67 pwm_bri66 Register (Address = 242h) [Default = 0h]

pwm_bri66 is shown in [Figure 2-435](#) and described in [Table 2-448](#).

Return to the [Summary Table](#).

Figure 2-435. pwm_bri66 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L3-CS12_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS15							
R/W-0h							

Table 2-448. pwm_bri66 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L3-CS12_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS15	R/W	0h	8-bits PWM for Dot L3-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS15

2.13.68 pwm_bri67 Register (Address = 243h) [Default = 0h]

pwm_bri67 is shown in [Figure 2-436](#) and described in [Table 2-449](#).

Return to the [Summary Table](#).

Figure 2-436. pwm_bri67 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L3-CS13_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS15							
R/W-0h							

Table 2-449. pwm_bri67 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L3-CS13_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS15	R/W	0h	8-bits PWM for Dot L3-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS15

2.13.69 pwm_bri68 Register (Address = 244h) [Default = 0h]

pwm_bri68 is shown in [Figure 2-437](#) and described in [Table 2-450](#).

Return to the [Summary Table](#).

Figure 2-437. pwm_bri68 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L3-CS14_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS16							
R/W-0h							

Table 2-450. pwm_bri68 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L3-CS14_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS16	R/W	0h	8-bits PWM for Dot L3-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS16

2.13.70 pwm_bri69 Register (Address = 245h) [Default = 0h]

pwm_bri69 is shown in [Figure 2-438](#) and described in [Table 2-451](#).

Return to the [Summary Table](#).

Figure 2-438. pwm_bri69 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L3-CS15_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS16							
R/W-0h							

Table 2-451. pwm_bri69 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L3-CS15_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS16	R/W	0h	8-bits PWM for Dot L3-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS16

2.13.71 pwm_bri70 Register (Address = 246h) [Default = 0h]

pwm_bri70 is shown in [Figure 2-439](#) and described in [Table 2-452](#).

Return to the [Summary Table](#).

Figure 2-439. pwm_bri70 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L3-CS16_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS17							
R/W-0h							

Table 2-452. pwm_bri70 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L3-CS16_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L1-CS17	R/W	0h	8-bits PWM for Dot L3-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L1-CS17

2.13.72 pwm_bri71 Register (Address = 247h) [Default = 0h]

pwm_bri71 is shown in [Figure 2-440](#) and described in [Table 2-453](#).

Return to the [Summary Table](#).

Figure 2-440. pwm_bri71 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L3-CS17_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS17							
R/W-0h							

Table 2-453. pwm_bri71 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L3-CS17_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L1-CS17	R/W	0h	8-bits PWM for Dot L3-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L1-CS17

2.13.73 pwm_bri72 Register (Address = 248h) [Default = 0h]

pwm_bri72 is shown in [Figure 2-441](#) and described in [Table 2-454](#).

Return to the [Summary Table](#).

Figure 2-441. pwm_bri72 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L4-CS0_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS0							
R/W-0h							

Table 2-454. pwm_bri72 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L4-CS0_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS0	R/W	0h	8-bits PWM for Dot L4-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS0

2.13.74 pwm_bri73 Register (Address = 249h) [Default = 0h]

pwm_bri73 is shown in [Figure 2-442](#) and described in [Table 2-455](#).

Return to the [Summary Table](#).

Figure 2-442. pwm_bri73 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L4-CS1_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS0							
R/W-0h							

Table 2-455. pwm_bri73 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L4-CS1_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS0	R/W	0h	8-bits PWM for Dot L4-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS0

2.13.75 pwm_bri74 Register (Address = 24Ah) [Default = 0h]

pwm_bri74 is shown in [Figure 2-443](#) and described in [Table 2-456](#).

Return to the [Summary Table](#).

Figure 2-443. pwm_bri74 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L4-CS2_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS1							
R/W-0h							

Table 2-456. pwm_bri74 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L4-CS2_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS1	R/W	0h	8-bits PWM for Dot L4-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS1

2.13.76 pwm_bri75 Register (Address = 24Bh) [Default = 0h]

pwm_bri75 is shown in [Figure 2-444](#) and described in [Table 2-457](#).

Return to the [Summary Table](#).

Figure 2-444. pwm_bri75 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L4-CS3_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS1							
R/W-0h							

Table 2-457. pwm_bri75 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L4-CS3_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS1	R/W	0h	8-bits PWM for Dot L4-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS1

2.13.77 pwm_bri76 Register (Address = 24Ch) [Default = 0h]

pwm_bri76 is shown in [Figure 2-445](#) and described in [Table 2-458](#).

Return to the [Summary Table](#).

Figure 2-445. pwm_bri76 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L4-CS4_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS2							
R/W-0h							

Table 2-458. pwm_bri76 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L4-CS4_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS2	R/W	0h	8-bits PWM for Dot L4-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS2

2.13.78 pwm_bri77 Register (Address = 24Dh) [Default = 0h]

pwm_bri77 is shown in [Figure 2-446](#) and described in [Table 2-459](#).

Return to the [Summary Table](#).

Figure 2-446. pwm_bri77 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L4-CS5_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS2							
R/W-0h							

Table 2-459. pwm_bri77 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L4-CS5_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS2	R/W	0h	8-bits PWM for Dot L4-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS2

2.13.79 pwm_bri78 Register (Address = 24Eh) [Default = 0h]

pwm_bri78 is shown in [Figure 2-447](#) and described in [Table 2-460](#).

Return to the [Summary Table](#).

Figure 2-447. pwm_bri78 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L4-CS6_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS3							
R/W-0h							

Table 2-460. pwm_bri78 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L4-CS6_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS3	R/W	0h	8-bits PWM for Dot L4-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS3

2.13.80 pwm_bri79 Register (Address = 24Fh) [Default = 0h]

pwm_bri79 is shown in [Figure 2-448](#) and described in [Table 2-461](#).

Return to the [Summary Table](#).

Figure 2-448. pwm_bri79 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L4-CS7_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS3							
R/W-0h							

Table 2-461. pwm_bri79 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L4-CS7_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS3	R/W	0h	8-bits PWM for Dot L4-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS3

2.13.81 pwm_bri80 Register (Address = 250h) [Default = 0h]

pwm_bri80 is shown in [Figure 2-449](#) and described in [Table 2-462](#).

Return to the [Summary Table](#).

Figure 2-449. pwm_bri80 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L4-CS8_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS4							
R/W-0h							

Table 2-462. pwm_bri80 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L4-CS8_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS4	R/W	0h	8-bits PWM for Dot L4-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS4

2.13.82 pwm_bri81 Register (Address = 251h) [Default = 0h]

pwm_bri81 is shown in [Figure 2-450](#) and described in [Table 2-463](#).

Return to the [Summary Table](#).

Figure 2-450. pwm_bri81 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L4-CS9_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS4							
R/W-0h							

Table 2-463. pwm_bri81 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L4-CS9_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS4	R/W	0h	8-bits PWM for Dot L4-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS4

2.13.83 pwm_bri82 Register (Address = 252h) [Default = 0h]

pwm_bri82 is shown in [Figure 2-451](#) and described in [Table 2-464](#).

Return to the [Summary Table](#).

Figure 2-451. pwm_bri82 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L4-CS10_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS5							
R/W-0h							

Table 2-464. pwm_bri82 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L4-CS10_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS5	R/W	0h	8-bits PWM for Dot L4-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS5

2.13.84 pwm_bri83 Register (Address = 253h) [Default = 0h]

pwm_bri83 is shown in [Figure 2-452](#) and described in [Table 2-465](#).

Return to the [Summary Table](#).

Figure 2-452. pwm_bri83 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L4-CS11_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS5							
R/W-0h							

Table 2-465. pwm_bri83 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L4-CS11_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS5	R/W	0h	8-bits PWM for Dot L4-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS5

2.13.85 pwm_bri84 Register (Address = 254h) [Default = 0h]

pwm_bri84 is shown in [Figure 2-453](#) and described in [Table 2-466](#).

Return to the [Summary Table](#).

Figure 2-453. pwm_bri84 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L4-CS12_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS6							
R/W-0h							

Table 2-466. pwm_bri84 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L4-CS12_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS6	R/W	0h	8-bits PWM for Dot L4-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS6

2.13.86 pwm_bri85 Register (Address = 255h) [Default = 0h]

pwm_bri85 is shown in [Figure 2-454](#) and described in [Table 2-467](#).

Return to the [Summary Table](#).

Figure 2-454. pwm_bri85 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L4-CS13_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS6							
R/W-0h							

Table 2-467. pwm_bri85 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L4-CS13_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS6	R/W	0h	8-bits PWM for Dot L4-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS6

2.13.87 pwm_bri86 Register (Address = 256h) [Default = 0h]

pwm_bri86 is shown in [Figure 2-455](#) and described in [Table 2-468](#).

Return to the [Summary Table](#).

Figure 2-455. pwm_bri86 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L4-CS14_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS7							
R/W-0h							

Table 2-468. pwm_bri86 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L4-CS14_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS7	R/W	0h	8-bits PWM for Dot L4-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS7

2.13.88 pwm_bri87 Register (Address = 257h) [Default = 0h]

pwm_bri87 is shown in [Figure 2-456](#) and described in [Table 2-469](#).

Return to the [Summary Table](#).

Figure 2-456. pwm_bri87 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L4-CS15_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS7							
R/W-0h							

Table 2-469. pwm_bri87 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L4-CS15_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS7	R/W	0h	8-bits PWM for Dot L4-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS7

2.13.89 pwm_bri88 Register (Address = 258h) [Default = 0h]

pwm_bri88 is shown in [Figure 2-457](#) and described in [Table 2-470](#).

Return to the [Summary Table](#).

Figure 2-457. pwm_bri88 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L4-CS16_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS8							
R/W-0h							

Table 2-470. pwm_bri88 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L4-CS16_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS8	R/W	0h	8-bits PWM for Dot L4-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS8

2.13.90 pwm_bri89 Register (Address = 259h) [Default = 0h]

pwm_bri89 is shown in [Figure 2-458](#) and described in [Table 2-471](#).

Return to the [Summary Table](#).

Figure 2-458. pwm_bri89 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L4-CS17_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS8							
R/W-0h							

Table 2-471. pwm_bri89 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L4-CS17_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS8	R/W	0h	8-bits PWM for Dot L4-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS8

2.13.91 pwm_bri90 Register (Address = 25Ah) [Default = 0h]

pwm_bri90 is shown in [Figure 2-459](#) and described in [Table 2-472](#).

Return to the [Summary Table](#).

Figure 2-459. pwm_bri90 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L5-CS0_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS9							
R/W-0h							

Table 2-472. pwm_bri90 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L5-CS0_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS9	R/W	0h	8-bits PWM for Dot L5-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS9

2.13.92 pwm_bri91 Register (Address = 25Bh) [Default = 0h]

pwm_bri91 is shown in [Figure 2-460](#) and described in [Table 2-473](#).

Return to the [Summary Table](#).

Figure 2-460. pwm_bri91 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L5-CS1_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS9							
R/W-0h							

Table 2-473. pwm_bri91 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L5-CS1_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS9	R/W	0h	8-bits PWM for Dot L5-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS9

2.13.93 pwm_bri92 Register (Address = 25Ch) [Default = 0h]

pwm_bri92 is shown in [Figure 2-461](#) and described in [Table 2-474](#).

Return to the [Summary Table](#).

Figure 2-461. pwm_bri92 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L5-CS2_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS10							
R/W-0h							

Table 2-474. pwm_bri92 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L5-CS2_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS10	R/W	0h	8-bits PWM for Dot L5-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS10

2.13.94 pwm_bri93 Register (Address = 25Dh) [Default = 0h]

pwm_bri93 is shown in [Figure 2-462](#) and described in [Table 2-475](#).

Return to the [Summary Table](#).

Figure 2-462. pwm_bri93 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L5-CS3_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS10							
R/W-0h							

Table 2-475. pwm_bri93 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L5-CS3_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS10	R/W	0h	8-bits PWM for Dot L5-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS10

2.13.95 pwm_bri94 Register (Address = 25Eh) [Default = 0h]

pwm_bri94 is shown in [Figure 2-463](#) and described in [Table 2-476](#).

Return to the [Summary Table](#).

Figure 2-463. pwm_bri94 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L5-CS4_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS11							
R/W-0h							

Table 2-476. pwm_bri94 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L5-CS4_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS11	R/W	0h	8-bits PWM for Dot L5-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS11

2.13.96 pwm_bri95 Register (Address = 25Fh) [Default = 0h]

pwm_bri95 is shown in [Figure 2-464](#) and described in [Table 2-477](#).

Return to the [Summary Table](#).

Figure 2-464. pwm_bri95 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L5-CS5_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS11							
R/W-0h							

Table 2-477. pwm_bri95 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L5-CS5_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS11	R/W	0h	8-bits PWM for Dot L5-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS11

2.13.97 pwm_bri96 Register (Address = 260h) [Default = 0h]

pwm_bri96 is shown in [Figure 2-465](#) and described in [Table 2-478](#).

Return to the [Summary Table](#).

Figure 2-465. pwm_bri96 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L5-CS6_OR_16-bits_PWM_lower_8_bits_7:0_for_Dot_L2-CS12							
R/W-0h							

Table 2-478. pwm_bri96 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L5-CS6_OR_16-bits_PWM_lower_8_bits_7:0_for_Dot_L2-CS12	R/W	0h	8-bits PWM for Dot L5-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS12

2.13.98 pwm_bri97 Register (Address = 261h) [Default = 0h]

pwm_bri97 is shown in [Figure 2-466](#) and described in [Table 2-479](#).

Return to the [Summary Table](#).

Figure 2-466. pwm_bri97 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L5-CS7_OR_16-bits_PWM_higher_8_bits_15:8_for_Dot_L2-CS12							
R/W-0h							

Table 2-479. pwm_bri97 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L5-CS7_OR_16-bits_PWM_higher_8_bits_15:8_for_Dot_L2-CS12	R/W	0h	8-bits PWM for Dot L5-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS12

2.13.99 pwm_bri98 Register (Address = 262h) [Default = 0h]

pwm_bri98 is shown in [Figure 2-467](#) and described in [Table 2-480](#).

Return to the [Summary Table](#).

Figure 2-467. pwm_bri98 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L5-CS8_OR_16-bits_PWM_lower_8_bits_7:0_for_Dot_L2-CS13							
R/W-0h							

Table 2-480. pwm_bri98 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L5-CS8_OR_16-bits_PWM_lower_8_bits_7:0_for_Dot_L2-CS13	R/W	0h	8-bits PWM for Dot L5-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS13

2.13.100 pwm_bri99 Register (Address = 263h) [Default = 0h]

pwm_bri99 is shown in [Figure 2-468](#) and described in [Table 2-481](#).

Return to the [Summary Table](#).

Figure 2-468. pwm_bri99 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L5-CS9_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS13							
R/W-0h							

Table 2-481. pwm_bri99 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L5-CS9_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS13	R/W	0h	8-bits PWM for Dot L5-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS13

2.13.101 pwm_bri100 Register (Address = 264h) [Default = 0h]

pwm_bri100 is shown in [Figure 2-469](#) and described in [Table 2-482](#).

Return to the [Summary Table](#).

Figure 2-469. pwm_bri100 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L5-CS10_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS14							
R/W-0h							

Table 2-482. pwm_bri100 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L5-CS10_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS14	R/W	0h	8-bits PWM for Dot L5-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS14

2.13.102 pwm_bri101 Register (Address = 265h) [Default = 0h]

pwm_bri101 is shown in [Figure 2-470](#) and described in [Table 2-483](#).

Return to the [Summary Table](#).

Figure 2-470. pwm_bri101 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L5-CS11_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS14							
R/W-0h							

Table 2-483. pwm_bri101 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L5-CS11_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS14	R/W	0h	8-bits PWM for Dot L5-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS14

2.13.103 pwm_bri102 Register (Address = 266h) [Default = 0h]

pwm_bri102 is shown in [Figure 2-471](#) and described in [Table 2-484](#).

Return to the [Summary Table](#).

Figure 2-471. pwm_bri102 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L5-CS12_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS15							
R/W-0h							

Table 2-484. pwm_bri102 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L5-CS12_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS15	R/W	0h	8-bits PWM for Dot L5-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS15

2.13.104 pwm_bri103 Register (Address = 267h) [Default = 0h]

pwm_bri103 is shown in [Figure 2-472](#) and described in [Table 2-485](#).

Return to the [Summary Table](#).

Figure 2-472. pwm_bri103 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L5-CS13_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS15							
R/W-0h							

Table 2-485. pwm_bri103 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L5-CS13_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS15	R/W	0h	8-bits PWM for Dot L5-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS15

2.13.105 pwm_bri104 Register (Address = 268h) [Default = 0h]

pwm_bri104 is shown in [Figure 2-473](#) and described in [Table 2-486](#).

Return to the [Summary Table](#).

Figure 2-473. pwm_bri104 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L5-CS14_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS16							
R/W-0h							

Table 2-486. pwm_bri104 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L5-CS14_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS16	R/W	0h	8-bits PWM for Dot L5-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS16

2.13.106 pwm_bri105 Register (Address = 269h) [Default = 0h]

pwm_bri105 is shown in [Figure 2-474](#) and described in [Table 2-487](#).

Return to the [Summary Table](#).

Figure 2-474. pwm_bri105 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L5-CS15_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS16							
R/W-0h							

Table 2-487. pwm_bri105 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L5-CS15_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS16	R/W	0h	8-bits PWM for Dot L5-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS16

2.13.107 pwm_bri106 Register (Address = 26Ah) [Default = 0h]

pwm_bri106 is shown in [Figure 2-475](#) and described in [Table 2-488](#).

Return to the [Summary Table](#).

Figure 2-475. pwm_bri106 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L5-CS16_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS17							
R/W-0h							

Table 2-488. pwm_bri106 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L5-CS16_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L2-CS17	R/W	0h	8-bits PWM for Dot L5-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L2-CS17

2.13.108 pwm_bri107 Register (Address = 26Bh) [Default = 0h]

pwm_bri107 is shown in [Figure 2-476](#) and described in [Table 2-489](#).

Return to the [Summary Table](#).

Figure 2-476. pwm_bri107 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L5-CS17_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS17							
R/W-0h							

Table 2-489. pwm_bri107 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L5-CS17_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L2-CS17	R/W	0h	8-bits PWM for Dot L5-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L2-CS17

2.13.109 pwm_bri108 Register (Address = 26Ch) [Default = 0h]

pwm_bri108 is shown in [Figure 2-477](#) and described in [Table 2-490](#).

Return to the [Summary Table](#).

Figure 2-477. pwm_bri108 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L6-CS0_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS0							
R/W-0h							

Table 2-490. pwm_bri108 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L6-CS0_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS0	R/W	0h	8-bits PWM for Dot L6-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS0

2.13.110 pwm_bri109 Register (Address = 26Dh) [Default = 0h]

pwm_bri109 is shown in [Figure 2-478](#) and described in [Table 2-491](#).

Return to the [Summary Table](#).

Figure 2-478. pwm_bri109 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L6-CS1_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS0							
R/W-0h							

Table 2-491. pwm_bri109 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L6-CS1_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS0	R/W	0h	8-bits PWM for Dot L6-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS0

2.13.111 pwm_bri110 Register (Address = 26Eh) [Default = 0h]

pwm_bri110 is shown in [Figure 2-479](#) and described in [Table 2-492](#).

Return to the [Summary Table](#).

Figure 2-479. pwm_bri110 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L6-CS2_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS1							
R/W-0h							

Table 2-492. pwm_bri110 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L6-CS2_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS1	R/W	0h	8-bits PWM for Dot L6-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS1

2.13.112 pwm_bri111 Register (Address = 26Fh) [Default = 0h]

pwm_bri111 is shown in [Figure 2-480](#) and described in [Table 2-493](#).

Return to the [Summary Table](#).

Figure 2-480. pwm_bri111 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L6-CS3_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS1							
R/W-0h							

Table 2-493. pwm_bri111 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L6-CS3_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS1	R/W	0h	8-bits PWM for Dot L6-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS1

2.13.113 pwm_bri112 Register (Address = 270h) [Default = 0h]

pwm_bri112 is shown in [Figure 2-481](#) and described in [Table 2-494](#).

Return to the [Summary Table](#).

Figure 2-481. pwm_bri112 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L6-CS4_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS2							
R/W-0h							

Table 2-494. pwm_bri112 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L6-CS4_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS2	R/W	0h	8-bits PWM for Dot L6-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS2

2.13.114 pwm_bri113 Register (Address = 271h) [Default = 0h]

pwm_bri113 is shown in [Figure 2-482](#) and described in [Table 2-495](#).

Return to the [Summary Table](#).

Figure 2-482. pwm_bri113 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L6-CS5_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS2							
R/W-0h							

Table 2-495. pwm_bri113 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L6-CS5_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS2	R/W	0h	8-bits PWM for Dot L6-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS2

2.13.115 pwm_bri114 Register (Address = 272h) [Default = 0h]

pwm_bri114 is shown in [Figure 2-483](#) and described in [Table 2-496](#).

Return to the [Summary Table](#).

Figure 2-483. pwm_bri114 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L6-CS6_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS3							
R/W-0h							

Table 2-496. pwm_bri114 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L6-CS6_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS3	R/W	0h	8-bits PWM for Dot L6-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS3

2.13.116 pwm_bri115 Register (Address = 273h) [Default = 0h]

pwm_bri115 is shown in [Figure 2-484](#) and described in [Table 2-497](#).

Return to the [Summary Table](#).

Figure 2-484. pwm_bri115 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L6-CS7_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS3							
R/W-0h							

Table 2-497. pwm_bri115 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L6-CS7_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS3	R/W	0h	8-bits PWM for Dot L6-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS3

2.13.117 pwm_bri116 Register (Address = 274h) [Default = 0h]

pwm_bri116 is shown in [Figure 2-485](#) and described in [Table 2-498](#).

Return to the [Summary Table](#).

Figure 2-485. pwm_bri116 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L6-CS8_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS4							
R/W-0h							

Table 2-498. pwm_bri116 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L6-CS8_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS4	R/W	0h	8-bits PWM for Dot L6-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS4

2.13.118 pwm_bri117 Register (Address = 275h) [Default = 0h]

pwm_bri117 is shown in [Figure 2-486](#) and described in [Table 2-499](#).

Return to the [Summary Table](#).

Figure 2-486. pwm_bri117 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L6-CS9_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS4							
R/W-0h							

Table 2-499. pwm_bri117 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L6-CS9_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS4	R/W	0h	8-bits PWM for Dot L6-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS4

2.13.119 pwm_bri118 Register (Address = 276h) [Default = 0h]

pwm_bri118 is shown in [Figure 2-487](#) and described in [Table 2-500](#).

Return to the [Summary Table](#).

Figure 2-487. pwm_bri118 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L6-CS10_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS5							
R/W-0h							

Table 2-500. pwm_bri118 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L6-CS10_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS5	R/W	0h	8-bits PWM for Dot L6-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS5

2.13.120 pwm_bri119 Register (Address = 277h) [Default = 0h]

pwm_bri119 is shown in [Figure 2-488](#) and described in [Table 2-501](#).

Return to the [Summary Table](#).

Figure 2-488. pwm_bri119 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L6-CS11_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS5							
R/W-0h							

Table 2-501. pwm_bri119 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L6-CS11_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS5	R/W	0h	8-bits PWM for Dot L6-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS5

2.13.121 pwm_bri120 Register (Address = 278h) [Default = 0h]

pwm_bri120 is shown in [Figure 2-489](#) and described in [Table 2-502](#).

Return to the [Summary Table](#).

Figure 2-489. pwm_bri120 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L6-CS12_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS6							
R/W-0h							

Table 2-502. pwm_bri120 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L6-CS12_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS6	R/W	0h	8-bits PWM for Dot L6-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS6

2.13.122 pwm_bri121 Register (Address = 279h) [Default = 0h]

pwm_bri121 is shown in [Figure 2-490](#) and described in [Table 2-503](#).

Return to the [Summary Table](#).

Figure 2-490. pwm_bri121 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L6-CS13_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS6							
R/W-0h							

Table 2-503. pwm_bri121 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L6-CS13_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS6	R/W	0h	8-bits PWM for Dot L6-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS6

2.13.123 pwm_bri122 Register (Address = 27Ah) [Default = 0h]

pwm_bri122 is shown in [Figure 2-491](#) and described in [Table 2-504](#).

Return to the [Summary Table](#).

Figure 2-491. pwm_bri122 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L6-CS14_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS7							
R/W-0h							

Table 2-504. pwm_bri122 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L6-CS14_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS7	R/W	0h	8-bits PWM for Dot L6-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS7

2.13.124 pwm_bri123 Register (Address = 27Bh) [Default = 0h]

pwm_bri123 is shown in [Figure 2-492](#) and described in [Table 2-505](#).

Return to the [Summary Table](#).

Figure 2-492. pwm_bri123 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L6-CS15_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS7							
R/W-0h							

Table 2-505. pwm_bri123 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L6-CS15_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS7	R/W	0h	8-bits PWM for Dot L6-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS7

2.13.125 pwm_bri124 Register (Address = 27Ch) [Default = 0h]

pwm_bri124 is shown in [Figure 2-493](#) and described in [Table 2-506](#).

Return to the [Summary Table](#).

Figure 2-493. pwm_bri124 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L6-CS16_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS8							
R/W-0h							

Table 2-506. pwm_bri124 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L6-CS16_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS8	R/W	0h	8-bits PWM for Dot L6-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS8

2.13.126 pwm_bri125 Register (Address = 27Dh) [Default = 0h]

pwm_bri125 is shown in [Figure 2-494](#) and described in [Table 2-507](#).

Return to the [Summary Table](#).

Figure 2-494. pwm_bri125 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L6-CS17_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS8							
R/W-0h							

Table 2-507. pwm_bri125 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L6-CS17_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS8	R/W	0h	8-bits PWM for Dot L6-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS8

2.13.127 pwm_bri126 Register (Address = 27Eh) [Default = 0h]

pwm_bri126 is shown in [Figure 2-495](#) and described in [Table 2-508](#).

Return to the [Summary Table](#).

Figure 2-495. pwm_bri126 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L7-CS0_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS9							
R/W-0h							

Table 2-508. pwm_bri126 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L7-CS0_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS9	R/W	0h	8-bits PWM for Dot L7-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS9

2.13.128 pwm_bri127 Register (Address = 27Fh) [Default = 0h]

pwm_bri127 is shown in [Figure 2-496](#) and described in [Table 2-509](#).

Return to the [Summary Table](#).

Figure 2-496. pwm_bri127 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L7-CS1_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS9							
R/W-0h							

Table 2-509. pwm_bri127 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L7-CS1_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS9	R/W	0h	8-bits PWM for Dot L7-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS9

2.13.129 pwm_bri128 Register (Address = 280h) [Default = 0h]

pwm_bri128 is shown in [Figure 2-497](#) and described in [Table 2-510](#).

Return to the [Summary Table](#).

Figure 2-497. pwm_bri128 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L7-CS2_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS10							
R/W-0h							

Table 2-510. pwm_bri128 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L7-CS2_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS10	R/W	0h	8-bits PWM for Dot L7-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS10

2.13.130 pwm_bri129 Register (Address = 281h) [Default = 0h]

pwm_bri129 is shown in [Figure 2-498](#) and described in [Table 2-511](#).

Return to the [Summary Table](#).

Figure 2-498. pwm_bri129 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L7-CS3_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS10							
R/W-0h							

Table 2-511. pwm_bri129 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L7-CS3_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS10	R/W	0h	8-bits PWM for Dot L7-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS10

2.13.131 pwm_bri130 Register (Address = 282h) [Default = 0h]

pwm_bri130 is shown in [Figure 2-499](#) and described in [Table 2-512](#).

Return to the [Summary Table](#).

Figure 2-499. pwm_bri130 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L7-CS4_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS11							
R/W-0h							

Table 2-512. pwm_bri130 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L7-CS4_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS11	R/W	0h	8-bits PWM for Dot L7-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS11

2.13.132 pwm_bri131 Register (Address = 283h) [Default = 0h]

pwm_bri131 is shown in [Figure 2-500](#) and described in [Table 2-513](#).

Return to the [Summary Table](#).

Figure 2-500. pwm_bri131 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L7-CS5_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS11							
R/W-0h							

Table 2-513. pwm_bri131 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L7-CS5_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS11	R/W	0h	8-bits PWM for Dot L7-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS11

2.13.133 pwm_bri132 Register (Address = 284h) [Default = 0h]

pwm_bri132 is shown in [Figure 2-501](#) and described in [Table 2-514](#).

Return to the [Summary Table](#).

Figure 2-501. pwm_bri132 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L7-CS6_OR_16-bits_PWM_lower_8_bits_7:0_for_Dot_L3-CS12							
R/W-0h							

Table 2-514. pwm_bri132 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L7-CS6_OR_16-bits_PWM_lower_8_bits_7:0_for_Dot_L3-CS12	R/W	0h	8-bits PWM for Dot L7-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS12

2.13.134 pwm_bri133 Register (Address = 285h) [Default = 0h]

pwm_bri133 is shown in [Figure 2-502](#) and described in [Table 2-515](#).

Return to the [Summary Table](#).

Figure 2-502. pwm_bri133 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L7-CS7_OR_16-bits_PWM_higher_8_bits_15:8_for_Dot_L3-CS12							
R/W-0h							

Table 2-515. pwm_bri133 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L7-CS7_OR_16-bits_PWM_higher_8_bits_15:8_for_Dot_L3-CS12	R/W	0h	8-bits PWM for Dot L7-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS12

2.13.135 pwm_bri134 Register (Address = 286h) [Default = 0h]

pwm_bri134 is shown in [Figure 2-503](#) and described in [Table 2-516](#).

Return to the [Summary Table](#).

Figure 2-503. pwm_bri134 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L7-CS8_OR_16-bits_PWM_lower_8_bits_7:0_for_Dot_L3-CS13							
R/W-0h							

Table 2-516. pwm_bri134 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L7-CS8_OR_16-bits_PWM_lower_8_bits_7:0_for_Dot_L3-CS13	R/W	0h	8-bits PWM for Dot L7-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS13

2.13.136 pwm_bri135 Register (Address = 287h) [Default = 0h]

pwm_bri135 is shown in [Figure 2-504](#) and described in [Table 2-517](#).

Return to the [Summary Table](#).

Figure 2-504. pwm_bri135 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L7-CS9_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS13							
R/W-0h							

Table 2-517. pwm_bri135 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L7-CS9_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS13	R/W	0h	8-bits PWM for Dot L7-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS13

2.13.137 pwm_bri136 Register (Address = 288h) [Default = 0h]

pwm_bri136 is shown in [Figure 2-505](#) and described in [Table 2-518](#).

Return to the [Summary Table](#).

Figure 2-505. pwm_bri136 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L7-CS10_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS14							
R/W-0h							

Table 2-518. pwm_bri136 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L7-CS10_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS14	R/W	0h	8-bits PWM for Dot L7-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS14

2.13.138 pwm_bri137 Register (Address = 289h) [Default = 0h]

pwm_bri137 is shown in [Figure 2-506](#) and described in [Table 2-519](#).

Return to the [Summary Table](#).

Figure 2-506. pwm_bri137 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L7-CS11_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS14							
R/W-0h							

Table 2-519. pwm_bri137 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L7-CS11_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS14	R/W	0h	8-bits PWM for Dot L7-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS14

2.13.139 pwm_bri138 Register (Address = 28Ah) [Default = 0h]

pwm_bri138 is shown in [Figure 2-507](#) and described in [Table 2-520](#).

Return to the [Summary Table](#).

Figure 2-507. pwm_bri138 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L7-CS12_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS15							
R/W-0h							

Table 2-520. pwm_bri138 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L7-CS12_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS15	R/W	0h	8-bits PWM for Dot L7-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS15

2.13.140 pwm_bri139 Register (Address = 28Bh) [Default = 0h]

pwm_bri139 is shown in [Figure 2-508](#) and described in [Table 2-521](#).

Return to the [Summary Table](#).

Figure 2-508. pwm_bri139 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L7-CS13_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS15							
R/W-0h							

Table 2-521. pwm_bri139 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L7-CS13_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS15	R/W	0h	8-bits PWM for Dot L7-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS15

2.13.141 pwm_bri140 Register (Address = 28Ch) [Default = 0h]

pwm_bri140 is shown in [Figure 2-509](#) and described in [Table 2-522](#).

Return to the [Summary Table](#).

Figure 2-509. pwm_bri140 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L7-CS14_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS16							
R/W-0h							

Table 2-522. pwm_bri140 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L7-CS14_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS16	R/W	0h	8-bits PWM for Dot L7-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS16

2.13.142 pwm_bri141 Register (Address = 28Dh) [Default = 0h]

pwm_bri141 is shown in [Figure 2-510](#) and described in [Table 2-523](#).

Return to the [Summary Table](#).

Figure 2-510. pwm_bri141 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L7-CS15_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS16							
R/W-0h							

Table 2-523. pwm_bri141 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L7-CS15_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS16	R/W	0h	8-bits PWM for Dot L7-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS16

2.13.143 pwm_bri142 Register (Address = 28Eh) [Default = 0h]

pwm_bri142 is shown in [Figure 2-511](#) and described in [Table 2-524](#).

Return to the [Summary Table](#).

Figure 2-511. pwm_bri142 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L7-CS16_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS17							
R/W-0h							

Table 2-524. pwm_bri142 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L7-CS16_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L3-CS17	R/W	0h	8-bits PWM for Dot L7-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L3-CS17

2.13.144 pwm_bri143 Register (Address = 28Fh) [Default = 0h]

pwm_bri143 is shown in [Figure 2-512](#) and described in [Table 2-525](#).

Return to the [Summary Table](#).

Figure 2-512. pwm_bri143 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L7-CS17_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS17							
R/W-0h							

Table 2-525. pwm_bri143 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L7-CS17_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L3-CS17	R/W	0h	8-bits PWM for Dot L7-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L3-CS17

2.13.145 pwm_bri144 Register (Address = 290h) [Default = 0h]

pwm_bri144 is shown in [Figure 2-513](#) and described in [Table 2-526](#).

Return to the [Summary Table](#).

Figure 2-513. pwm_bri144 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L8-CS0_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS0							
R/W-0h							

Table 2-526. pwm_bri144 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L8-CS0_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS0	R/W	0h	8-bits PWM for Dot L8-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS0

2.13.146 pwm_bri145 Register (Address = 291h) [Default = 0h]

pwm_bri145 is shown in [Figure 2-514](#) and described in [Table 2-527](#).

Return to the [Summary Table](#).

Figure 2-514. pwm_bri145 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L8-CS1_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS0							
R/W-0h							

Table 2-527. pwm_bri145 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L8-CS1_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS0	R/W	0h	8-bits PWM for Dot L8-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS0

2.13.147 pwm_bri146 Register (Address = 292h) [Default = 0h]

pwm_bri146 is shown in [Figure 2-515](#) and described in [Table 2-528](#).

Return to the [Summary Table](#).

Figure 2-515. pwm_bri146 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L8-CS2_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS1							
R/W-0h							

Table 2-528. pwm_bri146 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L8-CS2_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS1	R/W	0h	8-bits PWM for Dot L8-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS1

2.13.148 pwm_bri147 Register (Address = 293h) [Default = 0h]

pwm_bri147 is shown in [Figure 2-516](#) and described in [Table 2-529](#).

Return to the [Summary Table](#).

Figure 2-516. pwm_bri147 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L8-CS3_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS1							
R/W-0h							

Table 2-529. pwm_bri147 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L8-CS3_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS1	R/W	0h	8-bits PWM for Dot L8-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS1

2.13.149 pwm_bri148 Register (Address = 294h) [Default = 0h]

pwm_bri148 is shown in [Figure 2-517](#) and described in [Table 2-530](#).

Return to the [Summary Table](#).

Figure 2-517. pwm_bri148 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L8-CS4_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS2							
R/W-0h							

Table 2-530. pwm_bri148 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L8-CS4_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS2	R/W	0h	8-bits PWM for Dot L8-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS2

2.13.150 pwm_bri149 Register (Address = 295h) [Default = 0h]

pwm_bri149 is shown in [Figure 2-518](#) and described in [Table 2-531](#).

Return to the [Summary Table](#).

Figure 2-518. pwm_bri149 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L8-CS5_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS2							
R/W-0h							

Table 2-531. pwm_bri149 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L8-CS5_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS2	R/W	0h	8-bits PWM for Dot L8-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS2

2.13.151 pwm_bri150 Register (Address = 296h) [Default = 0h]

pwm_bri150 is shown in [Figure 2-519](#) and described in [Table 2-532](#).

Return to the [Summary Table](#).

Figure 2-519. pwm_bri150 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L8-CS6_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS3							
R/W-0h							

Table 2-532. pwm_bri150 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L8-CS6_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS3	R/W	0h	8-bits PWM for Dot L8-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS3

2.13.152 pwm_bri151 Register (Address = 297h) [Default = 0h]

pwm_bri151 is shown in [Figure 2-520](#) and described in [Table 2-533](#).

Return to the [Summary Table](#).

Figure 2-520. pwm_bri151 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L8-CS7_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS3							
R/W-0h							

Table 2-533. pwm_bri151 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L8-CS7_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS3	R/W	0h	8-bits PWM for Dot L8-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS3

2.13.153 pwm_bri152 Register (Address = 298h) [Default = 0h]

pwm_bri152 is shown in [Figure 2-521](#) and described in [Table 2-534](#).

Return to the [Summary Table](#).

Figure 2-521. pwm_bri152 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L8-CS8_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS4							
R/W-0h							

Table 2-534. pwm_bri152 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L8-CS8_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS4	R/W	0h	8-bits PWM for Dot L8-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS4

2.13.154 pwm_bri153 Register (Address = 299h) [Default = 0h]

pwm_bri153 is shown in [Figure 2-522](#) and described in [Table 2-535](#).

Return to the [Summary Table](#).

Figure 2-522. pwm_bri153 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L8-CS9_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS4							
R/W-0h							

Table 2-535. pwm_bri153 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L8-CS9_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS4	R/W	0h	8-bits PWM for Dot L8-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS4

2.13.155 pwm_bri154 Register (Address = 29Ah) [Default = 0h]

pwm_bri154 is shown in [Figure 2-523](#) and described in [Table 2-536](#).

Return to the [Summary Table](#).

Figure 2-523. pwm_bri154 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L8-CS10_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS5							
R/W-0h							

Table 2-536. pwm_bri154 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L8-CS10_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS5	R/W	0h	8-bits PWM for Dot L8-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS5

2.13.156 pwm_bri155 Register (Address = 29Bh) [Default = 0h]

pwm_bri155 is shown in [Figure 2-524](#) and described in [Table 2-537](#).

Return to the [Summary Table](#).

Figure 2-524. pwm_bri155 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L8-CS11_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS5							
R/W-0h							

Table 2-537. pwm_bri155 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L8-CS11_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS5	R/W	0h	8-bits PWM for Dot L8-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS5

2.13.157 pwm_bri156 Register (Address = 29Ch) [Default = 0h]

pwm_bri156 is shown in [Figure 2-525](#) and described in [Table 2-538](#).

Return to the [Summary Table](#).

Figure 2-525. pwm_bri156 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L8-CS12_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS6							
R/W-0h							

Table 2-538. pwm_bri156 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L8-CS12_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS6	R/W	0h	8-bits PWM for Dot L8-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS6

2.13.158 pwm_bri157 Register (Address = 29Dh) [Default = 0h]

pwm_bri157 is shown in [Figure 2-526](#) and described in [Table 2-539](#).

Return to the [Summary Table](#).

Figure 2-526. pwm_bri157 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L8-CS13_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS6							
R/W-0h							

Table 2-539. pwm_bri157 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L8-CS13_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS6	R/W	0h	8-bits PWM for Dot L8-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS6

2.13.159 pwm_bri158 Register (Address = 29Eh) [Default = 0h]

pwm_bri158 is shown in [Figure 2-527](#) and described in [Table 2-540](#).

Return to the [Summary Table](#).

Figure 2-527. pwm_bri158 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L8-CS14_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS7							
R/W-0h							

Table 2-540. pwm_bri158 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L8-CS14_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS7	R/W	0h	8-bits PWM for Dot L8-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS7

2.13.160 pwm_bri159 Register (Address = 29Fh) [Default = 0h]

pwm_bri159 is shown in [Figure 2-528](#) and described in [Table 2-541](#).

Return to the [Summary Table](#).

Figure 2-528. pwm_bri159 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L8-CS15_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS7							
R/W-0h							

Table 2-541. pwm_bri159 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L8-CS15_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS7	R/W	0h	8-bits PWM for Dot L8-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS7

2.13.161 pwm_bri160 Register (Address = 2A0h) [Default = 0h]

pwm_bri160 is shown in [Figure 2-529](#) and described in [Table 2-542](#).

Return to the [Summary Table](#).

Figure 2-529. pwm_bri160 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L8-CS16_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS8							
R/W-0h							

Table 2-542. pwm_bri160 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L8-CS16_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS8	R/W	0h	8-bits PWM for Dot L8-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS8

2.13.162 pwm_bri161 Register (Address = 2A1h) [Default = 0h]

pwm_bri161 is shown in [Figure 2-530](#) and described in [Table 2-543](#).

Return to the [Summary Table](#).

Figure 2-530. pwm_bri161 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L8-CS17_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS8							
R/W-0h							

Table 2-543. pwm_bri161 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L8-CS17_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS8	R/W	0h	8-bits PWM for Dot L8-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS8

2.13.163 pwm_bri162 Register (Address = 2A2h) [Default = 0h]

pwm_bri162 is shown in [Figure 2-531](#) and described in [Table 2-544](#).

Return to the [Summary Table](#).

Figure 2-531. pwm_bri162 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L9-CS0_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS9							
R/W-0h							

Table 2-544. pwm_bri162 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L9-CS0_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS9	R/W	0h	8-bits PWM for Dot L9-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS9

2.13.164 pwm_bri163 Register (Address = 2A3h) [Default = 0h]

pwm_bri163 is shown in [Figure 2-532](#) and described in [Table 2-545](#).

Return to the [Summary Table](#).

Figure 2-532. pwm_bri163 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L9-CS1_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS9							
R/W-0h							

Table 2-545. pwm_bri163 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L9-CS1_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS9	R/W	0h	8-bits PWM for Dot L9-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS9

2.13.165 pwm_bri164 Register (Address = 2A4h) [Default = 0h]

pwm_bri164 is shown in [Figure 2-533](#) and described in [Table 2-546](#).

Return to the [Summary Table](#).

Figure 2-533. pwm_bri164 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L9-CS2_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS10							
R/W-0h							

Table 2-546. pwm_bri164 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L9-CS2_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS10	R/W	0h	8-bits PWM for Dot L9-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS10

2.13.166 pwm_bri165 Register (Address = 2A5h) [Default = 0h]

pwm_bri165 is shown in [Figure 2-534](#) and described in [Table 2-547](#).

Return to the [Summary Table](#).

Figure 2-534. pwm_bri165 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L9-CS3_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS10							
R/W-0h							

Table 2-547. pwm_bri165 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L9-CS3_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS10	R/W	0h	8-bits PWM for Dot L9-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS10

2.13.167 pwm_bri166 Register (Address = 2A6h) [Default = 0h]

pwm_bri166 is shown in [Figure 2-535](#) and described in [Table 2-548](#).

Return to the [Summary Table](#).

Figure 2-535. pwm_bri166 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L9-CS4_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS11							
R/W-0h							

Table 2-548. pwm_bri166 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L9-CS4_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS11	R/W	0h	8-bits PWM for Dot L9-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS11

2.13.168 pwm_bri167 Register (Address = 2A7h) [Default = 0h]

pwm_bri167 is shown in [Figure 2-536](#) and described in [Table 2-549](#).

Return to the [Summary Table](#).

Figure 2-536. pwm_bri167 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L9-CS5_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS11							
R/W-0h							

Table 2-549. pwm_bri167 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L9-CS5_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS11	R/W	0h	8-bits PWM for Dot L9-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS11

2.13.169 pwm_bri168 Register (Address = 2A8h) [Default = 0h]

pwm_bri168 is shown in [Figure 2-537](#) and described in [Table 2-550](#).

Return to the [Summary Table](#).

Figure 2-537. pwm_bri168 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L9-CS6_OR_16-bits_PWM_lower_8_bits_7:0_for_Dot_L4-CS12							
R/W-0h							

Table 2-550. pwm_bri168 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L9-CS6_OR_16-bits_PWM_lower_8_bits_7:0_for_Dot_L4-CS12	R/W	0h	8-bits PWM for Dot L9-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS12

2.13.170 pwm_bri169 Register (Address = 2A9h) [Default = 0h]

pwm_bri169 is shown in [Figure 2-538](#) and described in [Table 2-551](#).

Return to the [Summary Table](#).

Figure 2-538. pwm_bri169 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L9-CS7_OR_16-bits_PWM_higher_8_bits_15:8_for_Dot_L4-CS12							
R/W-0h							

Table 2-551. pwm_bri169 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L9-CS7_OR_16-bits_PWM_higher_8_bits_15:8_for_Dot_L4-CS12	R/W	0h	8-bits PWM for Dot L9-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS12

2.13.171 pwm_bri170 Register (Address = 2AAh) [Default = 0h]

pwm_bri170 is shown in [Figure 2-539](#) and described in [Table 2-552](#).

Return to the [Summary Table](#).

Figure 2-539. pwm_bri170 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L9-CS8_OR_16-bits_PWM_lower_8_bits_7:0_for_Dot_L4-CS13							
R/W-0h							

Table 2-552. pwm_bri170 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L9-CS8_OR_16-bits_PWM_lower_8_bits_7:0_for_Dot_L4-CS13	R/W	0h	8-bits PWM for Dot L9-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS13

2.13.172 pwm_bri171 Register (Address = 2ABh) [Default = 0h]

pwm_bri171 is shown in [Figure 2-540](#) and described in [Table 2-553](#).

Return to the [Summary Table](#).

Figure 2-540. pwm_bri171 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L9-CS9_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS13							
R/W-0h							

Table 2-553. pwm_bri171 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L9-CS9_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS13	R/W	0h	8-bits PWM for Dot L9-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS13

2.13.173 pwm_bri172 Register (Address = 2ACh) [Default = 0h]

pwm_bri172 is shown in [Figure 2-541](#) and described in [Table 2-554](#).

Return to the [Summary Table](#).

Figure 2-541. pwm_bri172 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L9-CS10_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS14							
R/W-0h							

Table 2-554. pwm_bri172 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L9-CS10_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS14	R/W	0h	8-bits PWM for Dot L9-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS14

2.13.174 pwm_bri173 Register (Address = 2ADh) [Default = 0h]

pwm_bri173 is shown in [Figure 2-542](#) and described in [Table 2-555](#).

Return to the [Summary Table](#).

Figure 2-542. pwm_bri173 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L9-CS11_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS14							
R/W-0h							

Table 2-555. pwm_bri173 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L9-CS11_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS14	R/W	0h	8-bits PWM for Dot L9-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS14

2.13.175 pwm_bri174 Register (Address = 2AEh) [Default = 0h]

pwm_bri174 is shown in [Figure 2-543](#) and described in [Table 2-556](#).

Return to the [Summary Table](#).

Figure 2-543. pwm_bri174 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L9-CS12_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS15							
R/W-0h							

Table 2-556. pwm_bri174 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L9-CS12_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS15	R/W	0h	8-bits PWM for Dot L9-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS15

2.13.176 pwm_bri175 Register (Address = 2AFh) [Default = 0h]

pwm_bri175 is shown in [Figure 2-544](#) and described in [Table 2-557](#).

Return to the [Summary Table](#).

Figure 2-544. pwm_bri175 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L9-CS13_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS15							
R/W-0h							

Table 2-557. pwm_bri175 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L9-CS13_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS15	R/W	0h	8-bits PWM for Dot L9-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS15

2.13.177 pwm_bri176 Register (Address = 2B0h) [Default = 0h]

pwm_bri176 is shown in [Figure 2-545](#) and described in [Table 2-558](#).

Return to the [Summary Table](#).

Figure 2-545. pwm_bri176 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L9-CS14_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS16							
R/W-0h							

Table 2-558. pwm_bri176 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L9-CS14_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS16	R/W	0h	8-bits PWM for Dot L9-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS16

2.13.178 pwm_bri177 Register (Address = 2B1h) [Default = 0h]

pwm_bri177 is shown in [Figure 2-546](#) and described in [Table 2-559](#).

Return to the [Summary Table](#).

Figure 2-546. pwm_bri177 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L9-CS15_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS16							
R/W-0h							

Table 2-559. pwm_bri177 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L9-CS15_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS16	R/W	0h	8-bits PWM for Dot L9-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS16

2.13.179 pwm_bri178 Register (Address = 2B2h) [Default = 0h]

pwm_bri178 is shown in [Figure 2-547](#) and described in [Table 2-560](#).

Return to the [Summary Table](#).

Figure 2-547. pwm_bri178 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L9-CS16_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS17							
R/W-0h							

Table 2-560. pwm_bri178 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L9-CS16_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L4-CS17	R/W	0h	8-bits PWM for Dot L9-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L4-CS17

2.13.180 pwm_bri179 Register (Address = 2B3h) [Default = 0h]

pwm_bri179 is shown in [Figure 2-548](#) and described in [Table 2-561](#).

Return to the [Summary Table](#).

Figure 2-548. pwm_bri179 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L9-CS17_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS17							
R/W-0h							

Table 2-561. pwm_bri179 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L9-CS17_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L4-CS17	R/W	0h	8-bits PWM for Dot L9-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L4-CS17

2.13.181 pwm_bri180 Register (Address = 2B4h) [Default = 0h]

pwm_bri180 is shown in [Figure 2-549](#) and described in [Table 2-562](#).

Return to the [Summary Table](#).

Figure 2-549. pwm_bri180 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L10-CS0_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS0							
R/W-0h							

Table 2-562. pwm_bri180 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L10-CS0_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS0	R/W	0h	8-bits PWM for Dot L10-CS0 OR 16-bits PWM lower 8 bits [7:0] for Dot L5-CS0

2.13.182 pwm_bri181 Register (Address = 2B5h) [Default = 0h]

pwm_bri181 is shown in [Figure 2-550](#) and described in [Table 2-563](#).

Return to the [Summary Table](#).

Figure 2-550. pwm_bri181 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L10-CS1_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L5-CS0							
R/W-0h							

Table 2-563. pwm_bri181 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L10-CS1_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L5-CS0	R/W	0h	8-bits PWM for Dot L10-CS1 OR 16-bits PWM higher 8 bits [15:8] for Dot L5-CS0

2.13.183 pwm_bri182 Register (Address = 2B6h) [Default = 0h]

pwm_bri182 is shown in [Figure 2-551](#) and described in [Table 2-564](#).

Return to the [Summary Table](#).

Figure 2-551. pwm_bri182 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L10-CS2_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS1							
R/W-0h							

Table 2-564. pwm_bri182 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L10-CS2_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS1	R/W	0h	8-bits PWM for Dot L10-CS2 OR 16-bits PWM lower 8 bits [7:0] for Dot L5-CS1

2.13.184 pwm_bri183 Register (Address = 2B7h) [Default = 0h]

pwm_bri183 is shown in [Figure 2-552](#) and described in [Table 2-565](#).

Return to the [Summary Table](#).

Figure 2-552. pwm_bri183 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L10-CS3_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L5-CS1							
R/W-0h							

Table 2-565. pwm_bri183 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L10-CS3_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L5-CS1	R/W	0h	8-bits PWM for Dot L10-CS3 OR 16-bits PWM higher 8 bits [15:8] for Dot L5-CS1

2.13.185 pwm_bri184 Register (Address = 2B8h) [Default = 0h]

pwm_bri184 is shown in [Figure 2-553](#) and described in [Table 2-566](#).

Return to the [Summary Table](#).

Figure 2-553. pwm_bri184 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L10-CS4_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS2							
R/W-0h							

Table 2-566. pwm_bri184 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L10-CS4_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS2	R/W	0h	8-bits PWM for Dot L10-CS4 OR 16-bits PWM lower 8 bits [7:0] for Dot L5-CS2

2.13.186 pwm_bri185 Register (Address = 2B9h) [Default = 0h]

pwm_bri185 is shown in [Figure 2-554](#) and described in [Table 2-567](#).

Return to the [Summary Table](#).

Figure 2-554. pwm_bri185 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L10-CS5_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L5-CS2							
R/W-0h							

Table 2-567. pwm_bri185 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L10-CS5_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L5-CS2	R/W	0h	8-bits PWM for Dot L10-CS5 OR 16-bits PWM higher 8 bits [15:8] for Dot L5-CS2

2.13.187 pwm_bri186 Register (Address = 2BAh) [Default = 0h]

pwm_bri186 is shown in [Figure 2-555](#) and described in [Table 2-568](#).

Return to the [Summary Table](#).

Figure 2-555. pwm_bri186 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L10-CS6_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS3							
R/W-0h							

Table 2-568. pwm_bri186 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L10-CS6_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS3	R/W	0h	8-bits PWM for Dot L10-CS6 OR 16-bits PWM lower 8 bits [7:0] for Dot L5-CS3

2.13.188 pwm_bri187 Register (Address = 2BBh) [Default = 0h]

pwm_bri187 is shown in [Figure 2-556](#) and described in [Table 2-569](#).

Return to the [Summary Table](#).

Figure 2-556. pwm_bri187 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L10-CS7_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L5-CS3							
R/W-0h							

Table 2-569. pwm_bri187 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L10-CS7_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L5-CS3	R/W	0h	8-bits PWM for Dot L10-CS7 OR 16-bits PWM higher 8 bits [15:8] for Dot L5-CS3

2.13.189 pwm_bri188 Register (Address = 2BCh) [Default = 0h]

pwm_bri188 is shown in [Figure 2-557](#) and described in [Table 2-570](#).

Return to the [Summary Table](#).

Figure 2-557. pwm_bri188 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L10-CS8_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS4							
R/W-0h							

Table 2-570. pwm_bri188 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L10-CS8_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS4	R/W	0h	8-bits PWM for Dot L10-CS8 OR 16-bits PWM lower 8 bits [7:0] for Dot L5-CS4

2.13.190 pwm_bri189 Register (Address = 2BDh) [Default = 0h]

pwm_bri189 is shown in [Figure 2-558](#) and described in [Table 2-571](#).

Return to the [Summary Table](#).

Figure 2-558. pwm_bri189 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L10-CS9_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L5-CS4							
R/W-0h							

Table 2-571. pwm_bri189 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L10-CS9_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L5-CS4	R/W	0h	8-bits PWM for Dot L10-CS9 OR 16-bits PWM higher 8 bits [15:8] for Dot L5-CS4

2.13.191 pwm_bri190 Register (Address = 2BEh) [Default = 0h]

pwm_bri190 is shown in [Figure 2-559](#) and described in [Table 2-572](#).

Return to the [Summary Table](#).

Figure 2-559. pwm_bri190 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L10-CS10_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS5							
R/W-0h							

Table 2-572. pwm_bri190 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L10-CS10_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS5	R/W	0h	8-bits PWM for Dot L10-CS10 OR 16-bits PWM lower 8 bits [7:0] for Dot L5-CS5

2.13.192 pwm_bri191 Register (Address = 2BFh) [Default = 0h]

pwm_bri191 is shown in [Figure 2-560](#) and described in [Table 2-573](#).

Return to the [Summary Table](#).

Figure 2-560. pwm_bri191 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L10-CS11_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L5-CS5							
R/W-0h							

Table 2-573. pwm_bri191 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L10-CS11_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L5-CS5	R/W	0h	8-bits PWM for Dot L10-CS11 OR 16-bits PWM higher 8 bits [15:8] for Dot L5-CS5

2.13.193 pwm_bri192 Register (Address = 2C0h) [Default = 0h]

pwm_bri192 is shown in [Figure 2-561](#) and described in [Table 2-574](#).

Return to the [Summary Table](#).

Figure 2-561. pwm_bri192 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L10-CS12_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS6							
R/W-0h							

Table 2-574. pwm_bri192 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L10-CS12_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS6	R/W	0h	8-bits PWM for Dot L10-CS12 OR 16-bits PWM lower 8 bits [7:0] for Dot L5-CS6

2.13.194 pwm_bri193 Register (Address = 2C1h) [Default = 0h]

pwm_bri193 is shown in [Figure 2-562](#) and described in [Table 2-575](#).

Return to the [Summary Table](#).

Figure 2-562. pwm_bri193 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L10-CS13_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L5-CS6							
R/W-0h							

Table 2-575. pwm_bri193 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L10-CS13_OR_16-bits_PWM_higher_8_bits__15:8__for_Dot_L5-CS6	R/W	0h	8-bits PWM for Dot L10-CS13 OR 16-bits PWM higher 8 bits [15:8] for Dot L5-CS6

2.13.195 pwm_bri194 Register (Address = 2C2h) [Default = 0h]

pwm_bri194 is shown in [Figure 2-563](#) and described in [Table 2-576](#).

Return to the [Summary Table](#).

Figure 2-563. pwm_bri194 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L10-CS14_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS7							
R/W-0h							

Table 2-576. pwm_bri194 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L10-CS14_OR_16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS7	R/W	0h	8-bits PWM for Dot L10-CS14 OR 16-bits PWM lower 8 bits [7:0] for Dot L5-CS7

2.13.196 pwm_bri195 Register (Address = 2C3h) [Default = 0h]

pwm_bri195 is shown in [Figure 2-564](#) and described in [Table 2-577](#).

Return to the [Summary Table](#).

Figure 2-564. pwm_bri195 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L10-CS15_OR_16-bits_PWM_higher_8_bits_15:8_for_Dot_L5-CS7							
R/W-0h							

Table 2-577. pwm_bri195 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L10-CS15_OR_16-bits_PWM_higher_8_bits_15:8_for_Dot_L5-CS7	R/W	0h	8-bits PWM for Dot L10-CS15 OR 16-bits PWM higher 8 bits [15:8] for Dot L5-CS7

2.13.197 pwm_bri196 Register (Address = 2C4h) [Default = 0h]

pwm_bri196 is shown in [Figure 2-565](#) and described in [Table 2-578](#).

Return to the [Summary Table](#).

Figure 2-565. pwm_bri196 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L10-CS16_OR_16-bits_PWM_lower_8_bits_7:0_for_Dot_L5-CS8							
R/W-0h							

Table 2-578. pwm_bri196 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L10-CS16_OR_16-bits_PWM_lower_8_bits_7:0_for_Dot_L5-CS8	R/W	0h	8-bits PWM for Dot L10-CS16 OR 16-bits PWM lower 8 bits [7:0] for Dot L5-CS8

2.13.198 pwm_bri197 Register (Address = 2C5h) [Default = 0h]

pwm_bri197 is shown in [Figure 2-566](#) and described in [Table 2-579](#).

Return to the [Summary Table](#).

Figure 2-566. pwm_bri197 Register

7	6	5	4	3	2	1	0
8-bits_PWM_for_Dot_L10-CS17_OR_16-bits_PWM_higher_8_bits_15:8_for_Dot_L5-CS8							
R/W-0h							

Table 2-579. pwm_bri197 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	8-bits_PWM_for_Dot_L10-CS17_OR_16-bits_PWM_higher_8_bits_15:8_for_Dot_L5-CS8	R/W	0h	8-bits PWM for Dot L10-CS17 OR 16-bits PWM higher 8 bits [15:8] for Dot L5-CS8

2.13.199 pwm_bri198 Register (Address = 2C6h) [Default = 0h]

pwm_bri198 is shown in [Figure 2-567](#) and described in [Table 2-580](#).

Return to the [Summary Table](#).

Figure 2-567. pwm_bri198 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS9							
R/W-0h							

Table 2-580. pwm_bri198 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS9	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L5-CS9

2.13.200 pwm_bri199 Register (Address = 2C7h) [Default = 0h]

pwm_bri199 is shown in [Figure 2-568](#) and described in [Table 2-581](#).

Return to the [Summary Table](#).

Figure 2-568. pwm_bri199 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8__for_Dot_L5-CS9							
R/W-0h							

Table 2-581. pwm_bri199 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16-bits_PWM_higher_8_bits__15:8__for_Dot_L5-CS9	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L5-CS9

2.13.201 pwm_bri200 Register (Address = 2C8h) [Default = 0h]

pwm_bri200 is shown in [Figure 2-569](#) and described in [Table 2-582](#).

Return to the [Summary Table](#).

Figure 2-569. pwm_bri200 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS10							
R/W-0h							

Table 2-582. pwm_bri200 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS10	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L5-CS10

2.13.202 pwm_bri201 Register (Address = 2C9h) [Default = 0h]

pwm_bri201 is shown in [Figure 2-570](#) and described in [Table 2-583](#).

Return to the [Summary Table](#).

Figure 2-570. pwm_bri201 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8__for_Dot_L5-CS10							
R/W-0h							

Table 2-583. pwm_bri201 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16-bits_PWM_higher_8_bits__15:8__for_Dot_L5-CS10	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L5-CS10

2.13.203 pwm_bri202 Register (Address = 2CAh) [Default = 0h]

pwm_bri202 is shown in [Figure 2-571](#) and described in [Table 2-584](#).

Return to the [Summary Table](#).

Figure 2-571. pwm_bri202 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS11							
R/W-0h							

Table 2-584. pwm_bri202 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS11	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L5-CS11

2.13.204 pwm_bri203 Register (Address = 2CBh) [Default = 0h]

pwm_bri203 is shown in [Figure 2-572](#) and described in [Table 2-585](#).

Return to the [Summary Table](#).

Figure 2-572. pwm_bri203 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8__for_Dot_L5-CS11							
R/W-0h							

Table 2-585. pwm_bri203 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16-bits_PWM_higher_8_bits__15:8__for_Dot_L5-CS11	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L5-CS11

2.13.205 pwm_bri204 Register (Address = 2CCh) [Default = 0h]

pwm_bri204 is shown in [Figure 2-573](#) and described in [Table 2-586](#).

Return to the [Summary Table](#).

Figure 2-573. pwm_bri204 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS12							

Figure 2-573. pwm_bri204 Register (continued)

R/W-0h

Table 2-586. pwm_bri204 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS12	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L5-CS12

2.13.206 pwm_bri205 Register (Address = 2CDh) [Default = 0h]

pwm_bri205 is shown in [Figure 2-574](#) and described in [Table 2-587](#).

Return to the [Summary Table](#).

Figure 2-574. pwm_bri205 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8__for_Dot_L5-CS12							
R/W-0h							

Table 2-587. pwm_bri205 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16-bits_PWM_higher_8_bits__15:8__for_Dot_L5-CS12	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L5-CS12

2.13.207 pwm_bri206 Register (Address = 2CEh) [Default = 0h]

pwm_bri206 is shown in [Figure 2-575](#) and described in [Table 2-588](#).

Return to the [Summary Table](#).

Figure 2-575. pwm_bri206 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS13							
R/W-0h							

Table 2-588. pwm_bri206 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS13	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L5-CS13

2.13.208 pwm_bri207 Register (Address = 2CFh) [Default = 0h]

pwm_bri207 is shown in [Figure 2-576](#) and described in [Table 2-589](#).

Return to the [Summary Table](#).

Figure 2-576. pwm_bri207 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8__for_Dot_L5-CS13							
R/W-0h							

Table 2-589. pwm_bri207 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L5-CS13	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L5-CS13

2.13.209 pwm_bri208 Register (Address = 2D0h) [Default = 0h]

pwm_bri208 is shown in [Figure 2-577](#) and described in [Table 2-590](#).

Return to the [Summary Table](#).

Figure 2-577. pwm_bri208 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS14							
R/W-0h							

Table 2-590. pwm_bri208 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0__for_Dot_L5-CS14	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L5-CS14

2.13.210 pwm_bri209 Register (Address = 2D1h) [Default = 0h]

pwm_bri209 is shown in [Figure 2-578](#) and described in [Table 2-591](#).

Return to the [Summary Table](#).

Figure 2-578. pwm_bri209 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8__for_Dot_L5-CS14							
R/W-0h							

Table 2-591. pwm_bri209 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L5-CS14	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L5-CS14

2.13.211 pwm_bri210 Register (Address = 2D2h) [Default = 0h]

pwm_bri210 is shown in [Figure 2-579](#) and described in [Table 2-592](#).

Return to the [Summary Table](#).

Figure 2-579. pwm_bri210 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS15							
R/W-0h							

Table 2-592. pwm_bri210 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L5-CS15	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L5-CS15

2.13.212 pwm_bri211 Register (Address = 2D3h) [Default = 0h]

pwm_bri211 is shown in [Figure 2-580](#) and described in [Table 2-593](#).

Return to the [Summary Table](#).

Figure 2-580. pwm_bri211 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L5-CS15							
R/W-0h							

Table 2-593. pwm_bri211 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L5-CS15	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L5-CS15

2.13.213 pwm_bri212 Register (Address = 2D4h) [Default = 0h]

pwm_bri212 is shown in [Figure 2-581](#) and described in [Table 2-594](#).

Return to the [Summary Table](#).

Figure 2-581. pwm_bri212 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L5-CS16							
R/W-0h							

Table 2-594. pwm_bri212 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L5-CS16	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L5-CS16

2.13.214 pwm_bri213 Register (Address = 2D5h) [Default = 0h]

pwm_bri213 is shown in [Figure 2-582](#) and described in [Table 2-595](#).

Return to the [Summary Table](#).

Figure 2-582. pwm_bri213 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L5-CS16							
R/W-0h							

Table 2-595. pwm_bri213 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16-bits_PWM_higher_8_bits__15:8__for_Dot_L5-CS16	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L5-CS16

2.13.215 pwm_bri214 Register (Address = 2D6h) [Default = 0h]

pwm_bri214 is shown in [Figure 2-583](#) and described in [Table 2-596](#).

Return to the [Summary Table](#).

Figure 2-583. pwm_bri214 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS17							
R/W-0h							

Table 2-596. pwm_bri214 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16-bits_PWM_lower_8_bits__7:0__for_Dot_L5-CS17	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L5-CS17

2.13.216 pwm_bri215 Register (Address = 2D7h) [Default = 0h]

pwm_bri215 is shown in [Figure 2-584](#) and described in [Table 2-597](#).

Return to the [Summary Table](#).

Figure 2-584. pwm_bri215 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8__for_Dot_L5-CS17							
R/W-0h							

Table 2-597. pwm_bri215 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16-bits_PWM_higher_8_bits__15:8__for_Dot_L5-CS17	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L5-CS17

2.13.217 pwm_bri216 Register (Address = 2D8h) [Default = 0h]

pwm_bri216 is shown in [Figure 2-585](#) and described in [Table 2-598](#).

Return to the [Summary Table](#).

Figure 2-585. pwm_bri216 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L6-CS0							
R/W-0h							

Table 2-598. pwm_bri216 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L6-CS0	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L6-CS0

2.13.218 pwm_bri217 Register (Address = 2D9h) [Default = 0h]

pwm_bri217 is shown in [Figure 2-586](#) and described in [Table 2-599](#).

Return to the [Summary Table](#).

Figure 2-586. pwm_bri217 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_ 15:8_for_Dot_L6-CS0							
R/W-0h							

Table 2-599. pwm_bri217 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ 15:8_for_Dot_L6-CS0	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L6-CS0

2.13.219 pwm_bri218 Register (Address = 2DAh) [Default = 0h]

pwm_bri218 is shown in [Figure 2-587](#) and described in [Table 2-600](#).

Return to the [Summary Table](#).

Figure 2-587. pwm_bri218 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits_ 7:0_for_Dot_L6-CS1							
R/W-0h							

Table 2-600. pwm_bri218 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L6-CS1	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L6-CS1

2.13.220 pwm_bri219 Register (Address = 2DBh) [Default = 0h]

pwm_bri219 is shown in [Figure 2-588](#) and described in [Table 2-601](#).

Return to the [Summary Table](#).

Figure 2-588. pwm_bri219 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_ 15:8_for_Dot_L6-CS1							
R/W-0h							

Table 2-601. pwm_bri219 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L6-CS1	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L6-CS1

2.13.221 pwm_bri220 Register (Address = 2DCh) [Default = 0h]

pwm_bri220 is shown in [Figure 2-589](#) and described in [Table 2-602](#).

Return to the [Summary Table](#).

Figure 2-589. pwm_bri220 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L6-CS2							
R/W-0h							

Table 2-602. pwm_bri220 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits__ 7:0_for_Dot_L6-CS2	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L6-CS2

2.13.222 pwm_bri221 Register (Address = 2DDh) [Default = 0h]

pwm_bri221 is shown in [Figure 2-590](#) and described in [Table 2-603](#).

Return to the [Summary Table](#).

Figure 2-590. pwm_bri221 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L6-CS2							
R/W-0h							

Table 2-603. pwm_bri221 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L6-CS2	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L6-CS2

2.13.223 pwm_bri222 Register (Address = 2DEh) [Default = 0h]

pwm_bri222 is shown in [Figure 2-591](#) and described in [Table 2-604](#).

Return to the [Summary Table](#).

Figure 2-591. pwm_bri222 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L6-CS3							
R/W-0h							

Table 2-604. pwm_bri222 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L6-CS3	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L6-CS3

2.13.224 pwm_bri223 Register (Address = 2DFh) [Default = 0h]

pwm_bri223 is shown in [Figure 2-592](#) and described in [Table 2-605](#).

Return to the [Summary Table](#).

Figure 2-592. pwm_bri223 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_ 15:8_for_Dot_L6-CS3							
R/W-0h							

Table 2-605. pwm_bri223 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ 15:8_for_Dot_L6-CS3	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L6-CS3

2.13.225 pwm_bri224 Register (Address = 2E0h) [Default = 0h]

pwm_bri224 is shown in [Figure 2-593](#) and described in [Table 2-606](#).

Return to the [Summary Table](#).

Figure 2-593. pwm_bri224 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits_ 7:0_for_Dot_L6-CS4							
R/W-0h							

Table 2-606. pwm_bri224 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L6-CS4	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L6-CS4

2.13.226 pwm_bri225 Register (Address = 2E1h) [Default = 0h]

pwm_bri225 is shown in [Figure 2-594](#) and described in [Table 2-607](#).

Return to the [Summary Table](#).

Figure 2-594. pwm_bri225 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_ 15:8_for_Dot_L6-CS4							
R/W-0h							

Table 2-607. pwm_bri225 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L6-CS4	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L6-CS4

2.13.227 pwm_bri226 Register (Address = 2E2h) [Default = 0h]

pwm_bri226 is shown in [Figure 2-595](#) and described in [Table 2-608](#).

Return to the [Summary Table](#).

Figure 2-595. pwm_bri226 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L6-CS5							
R/W-0h							

Table 2-608. pwm_bri226 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits__ 7:0_for_Dot_L6-CS5	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L6-CS5

2.13.228 pwm_bri227 Register (Address = 2E3h) [Default = 0h]

pwm_bri227 is shown in [Figure 2-596](#) and described in [Table 2-609](#).

Return to the [Summary Table](#).

Figure 2-596. pwm_bri227 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L6-CS5							
R/W-0h							

Table 2-609. pwm_bri227 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L6-CS5	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L6-CS5

2.13.229 pwm_bri228 Register (Address = 2E4h) [Default = 0h]

pwm_bri228 is shown in [Figure 2-597](#) and described in [Table 2-610](#).

Return to the [Summary Table](#).

Figure 2-597. pwm_bri228 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L6-CS6							
R/W-0h							

Table 2-610. pwm_bri228 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L6-CS6	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L6-CS6

2.13.230 pwm_bri229 Register (Address = 2E5h) [Default = 0h]

pwm_bri229 is shown in [Figure 2-598](#) and described in [Table 2-611](#).

Return to the [Summary Table](#).

Figure 2-598. pwm_bri229 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_ 15:8_for_Dot_L6-CS6							
R/W-0h							

Table 2-611. pwm_bri229 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ 15:8_for_Dot_L6-CS6	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L6-CS6

2.13.231 pwm_bri230 Register (Address = 2E6h) [Default = 0h]

pwm_bri230 is shown in [Figure 2-599](#) and described in [Table 2-612](#).

Return to the [Summary Table](#).

Figure 2-599. pwm_bri230 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits_ 7:0_for_Dot_L6-CS7							
R/W-0h							

Table 2-612. pwm_bri230 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L6-CS7	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L6-CS7

2.13.232 pwm_bri231 Register (Address = 2E7h) [Default = 0h]

pwm_bri231 is shown in [Figure 2-600](#) and described in [Table 2-613](#).

Return to the [Summary Table](#).

Figure 2-600. pwm_bri231 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_ 15:8_for_Dot_L6-CS7							
R/W-0h							

Table 2-613. pwm_bri231 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L6-CS7	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L6-CS7

2.13.233 pwm_bri232 Register (Address = 2E8h) [Default = 0h]

pwm_bri232 is shown in [Figure 2-601](#) and described in [Table 2-614](#).

Return to the [Summary Table](#).

Figure 2-601. pwm_bri232 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L6-CS8							
R/W-0h							

Table 2-614. pwm_bri232 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits__ 7:0_for_Dot_L6-CS8	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L6-CS8

2.13.234 pwm_bri233 Register (Address = 2E9h) [Default = 0h]

pwm_bri233 is shown in [Figure 2-602](#) and described in [Table 2-615](#).

Return to the [Summary Table](#).

Figure 2-602. pwm_bri233 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L6-CS8							
R/W-0h							

Table 2-615. pwm_bri233 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L6-CS8	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L6-CS8

2.13.235 pwm_bri234 Register (Address = 2EAh) [Default = 0h]

pwm_bri234 is shown in [Figure 2-603](#) and described in [Table 2-616](#).

Return to the [Summary Table](#).

Figure 2-603. pwm_bri234 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L6-CS9							
R/W-0h							

Table 2-616. pwm_bri234 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L6-CS9	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L6-CS9

2.13.236 pwm_bri235 Register (Address = 2EBh) [Default = 0h]

pwm_bri235 is shown in [Figure 2-604](#) and described in [Table 2-617](#).

Return to the [Summary Table](#).

Figure 2-604. pwm_bri235 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_ 15:8_for_Dot_L6-CS9							
R/W-0h							

Table 2-617. pwm_bri235 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ 15:8_for_Dot_L6-CS9	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L6-CS9

2.13.237 pwm_bri236 Register (Address = 2ECh) [Default = 0h]

pwm_bri236 is shown in [Figure 2-605](#) and described in [Table 2-618](#).

Return to the [Summary Table](#).

Figure 2-605. pwm_bri236 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits_ 7:0_for_Dot_L6-CS10							
R/W-0h							

Table 2-618. pwm_bri236 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L6-CS10	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L6-CS10

2.13.238 pwm_bri237 Register (Address = 2EDh) [Default = 0h]

pwm_bri237 is shown in [Figure 2-606](#) and described in [Table 2-619](#).

Return to the [Summary Table](#).

Figure 2-606. pwm_bri237 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_ 15:8_for_Dot_L6-CS10							
R/W-0h							

Table 2-619. pwm_bri237 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L6-CS10	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L6-CS10

2.13.239 pwm_bri238 Register (Address = 2EEh) [Default = 0h]

pwm_bri238 is shown in [Figure 2-607](#) and described in [Table 2-620](#).

Return to the [Summary Table](#).

Figure 2-607. pwm_bri238 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L6-CS11							
R/W-0h							

Table 2-620. pwm_bri238 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0__for_Dot_L6-CS11	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L6-CS11

2.13.240 pwm_bri239 Register (Address = 2EFh) [Default = 0h]

pwm_bri239 is shown in [Figure 2-608](#) and described in [Table 2-621](#).

Return to the [Summary Table](#).

Figure 2-608. pwm_bri239 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8__for_Dot_L6-CS11							
R/W-0h							

Table 2-621. pwm_bri239 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L6-CS11	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L6-CS11

2.13.241 pwm_bri240 Register (Address = 2F0h) [Default = 0h]

pwm_bri240 is shown in [Figure 2-609](#) and described in [Table 2-622](#).

Return to the [Summary Table](#).

Figure 2-609. pwm_bri240 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L6-CS12							
R/W-0h							

Table 2-622. pwm_bri240 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L6-CS12	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L6-CS12

2.13.242 pwm_bri241 Register (Address = 2F1h) [Default = 0h]

pwm_bri241 is shown in [Figure 2-610](#) and described in [Table 2-623](#).

Return to the [Summary Table](#).

Figure 2-610. pwm_bri241 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L6-CS12							
R/W-0h							

Table 2-623. pwm_bri241 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L6-CS12	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L6-CS12

2.13.243 pwm_bri242 Register (Address = 2F2h) [Default = 0h]

pwm_bri242 is shown in [Figure 2-611](#) and described in [Table 2-624](#).

Return to the [Summary Table](#).

Figure 2-611. pwm_bri242 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L6-CS13							
R/W-0h							

Table 2-624. pwm_bri242 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L6-CS13	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L6-CS13

2.13.244 pwm_bri243 Register (Address = 2F3h) [Default = 0h]

pwm_bri243 is shown in [Figure 2-612](#) and described in [Table 2-625](#).

Return to the [Summary Table](#).

Figure 2-612. pwm_bri243 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L6-CS13							
R/W-0h							

Table 2-625. pwm_bri243 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16-bits_PWM_higher_8_bits__15:8__for_Dot_L6-CS13	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L6-CS13

2.13.245 pwm_bri244 Register (Address = 2F4h) [Default = 0h]

pwm_bri244 is shown in [Figure 2-613](#) and described in [Table 2-626](#).

Return to the [Summary Table](#).

Figure 2-613. pwm_bri244 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L6-CS14							
R/W-0h							

Table 2-626. pwm_bri244 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16-bits_PWM_lower_8_bits__7:0__for_Dot_L6-CS14	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L6-CS14

2.13.246 pwm_bri245 Register (Address = 2F5h) [Default = 0h]

pwm_bri245 is shown in [Figure 2-614](#) and described in [Table 2-627](#).

Return to the [Summary Table](#).

Figure 2-614. pwm_bri245 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8__for_Dot_L6-CS14							
R/W-0h							

Table 2-627. pwm_bri245 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16-bits_PWM_higher_8_bits__15:8__for_Dot_L6-CS14	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L6-CS14

2.13.247 pwm_bri246 Register (Address = 2F6h) [Default = 0h]

pwm_bri246 is shown in [Figure 2-615](#) and described in [Table 2-628](#).

Return to the [Summary Table](#).

Figure 2-615. pwm_bri246 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L6-CS15							
R/W-0h							

Table 2-628. pwm_bri246 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L6-CS15	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L6-CS15

2.13.248 pwm_bri247 Register (Address = 2F7h) [Default = 0h]

pwm_bri247 is shown in [Figure 2-616](#) and described in [Table 2-629](#).

Return to the [Summary Table](#).

Figure 2-616. pwm_bri247 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L6-CS15							
R/W-0h							

Table 2-629. pwm_bri247 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L6-CS15	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L6-CS15

2.13.249 pwm_bri248 Register (Address = 2F8h) [Default = 0h]

pwm_bri248 is shown in [Figure 2-617](#) and described in [Table 2-630](#).

Return to the [Summary Table](#).

Figure 2-617. pwm_bri248 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L6-CS16							
R/W-0h							

Table 2-630. pwm_bri248 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L6-CS16	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L6-CS16

2.13.250 pwm_bri249 Register (Address = 2F9h) [Default = 0h]

pwm_bri249 is shown in [Figure 2-618](#) and described in [Table 2-631](#).

Return to the [Summary Table](#).

Figure 2-618. pwm_bri249 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L6-CS16							
R/W-0h							

Table 2-631. pwm_bri249 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L6-CS16	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L6-CS16

2.13.251 pwm_bri250 Register (Address = 2FAh) [Default = 0h]

pwm_bri250 is shown in [Figure 2-619](#) and described in [Table 2-632](#).

Return to the [Summary Table](#).

Figure 2-619. pwm_bri250 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L6-CS17							
R/W-0h							

Table 2-632. pwm_bri250 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits__ 7:0_for_Dot_L6-CS17	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L6-CS17

2.13.252 pwm_bri251 Register (Address = 2FBh) [Default = 0h]

pwm_bri251 is shown in [Figure 2-620](#) and described in [Table 2-633](#).

Return to the [Summary Table](#).

Figure 2-620. pwm_bri251 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L6-CS17							
R/W-0h							

Table 2-633. pwm_bri251 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L6-CS17	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L6-CS17

2.13.253 pwm_bri252 Register (Address = 2FCh) [Default = 0h]

pwm_bri252 is shown in [Figure 2-621](#) and described in [Table 2-634](#).

Return to the [Summary Table](#).

Figure 2-621. pwm_bri252 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L7-CS0							
R/W-0h							

Table 2-634. pwm_bri252 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L7-CS0	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L7-CS0

2.13.254 pwm_bri253 Register (Address = 2FDh) [Default = 0h]

pwm_bri253 is shown in [Figure 2-622](#) and described in [Table 2-635](#).

Return to the [Summary Table](#).

Figure 2-622. pwm_bri253 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_ 15:8_for_Dot_L7-CS0							
R/W-0h							

Table 2-635. pwm_bri253 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ 15:8_for_Dot_L7-CS0	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L7-CS0

2.13.255 pwm_bri254 Register (Address = 2FEh) [Default = 0h]

pwm_bri254 is shown in [Figure 2-623](#) and described in [Table 2-636](#).

Return to the [Summary Table](#).

Figure 2-623. pwm_bri254 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits_ 7:0_for_Dot_L7-CS1							
R/W-0h							

Table 2-636. pwm_bri254 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L7-CS1	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L7-CS1

2.13.256 pwm_bri255 Register (Address = 2FFh) [Default = 0h]

pwm_bri255 is shown in [Figure 2-624](#) and described in [Table 2-637](#).

Return to the [Summary Table](#).

Figure 2-624. pwm_bri255 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_ 15:8_for_Dot_L7-CS1							
R/W-0h							

Table 2-637. pwm_bri255 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16-bits_PWM_higher_8_bits__15:8__for_Dot_L7-CS1	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L7-CS1

2.13.257 pwm_bri256 Register (Address = 300h) [Default = 0h]

pwm_bri256 is shown in [Figure 2-625](#) and described in [Table 2-638](#).

Return to the [Summary Table](#).

Figure 2-625. pwm_bri256 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L7-CS2							
R/W-0h							

Table 2-638. pwm_bri256 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16-bits_PWM_lower_8_bits__7:0__for_Dot_L7-CS2	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L7-CS2

2.13.258 pwm_bri257 Register (Address = 301h) [Default = 0h]

pwm_bri257 is shown in [Figure 2-626](#) and described in [Table 2-639](#).

Return to the [Summary Table](#).

Figure 2-626. pwm_bri257 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8__for_Dot_L7-CS2							
R/W-0h							

Table 2-639. pwm_bri257 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16-bits_PWM_higher_8_bits__15:8__for_Dot_L7-CS2	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L7-CS2

2.13.259 pwm_bri258 Register (Address = 302h) [Default = 0h]

pwm_bri258 is shown in [Figure 2-627](#) and described in [Table 2-640](#).

Return to the [Summary Table](#).

Figure 2-627. pwm_bri258 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L7-CS3							
R/W-0h							

Table 2-640. pwm_bri258 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L7-CS3	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L7-CS3

2.13.260 pwm_bri259 Register (Address = 303h) [Default = 0h]

pwm_bri259 is shown in [Figure 2-628](#) and described in [Table 2-641](#).

Return to the [Summary Table](#).

Figure 2-628. pwm_bri259 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_ 15:8_for_Dot_L7-CS3							
R/W-0h							

Table 2-641. pwm_bri259 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ 15:8_for_Dot_L7-CS3	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L7-CS3

2.13.261 pwm_bri260 Register (Address = 304h) [Default = 0h]

pwm_bri260 is shown in [Figure 2-629](#) and described in [Table 2-642](#).

Return to the [Summary Table](#).

Figure 2-629. pwm_bri260 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits_ 7:0_for_Dot_L7-CS4							
R/W-0h							

Table 2-642. pwm_bri260 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L7-CS4	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L7-CS4

2.13.262 pwm_bri261 Register (Address = 305h) [Default = 0h]

pwm_bri261 is shown in [Figure 2-630](#) and described in [Table 2-643](#).

Return to the [Summary Table](#).

Figure 2-630. pwm_bri261 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_ 15:8_for_Dot_L7-CS4							
R/W-0h							

Table 2-643. pwm_bri261 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L7-CS4	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L7-CS4

2.13.263 pwm_bri262 Register (Address = 306h) [Default = 0h]

pwm_bri262 is shown in [Figure 2-631](#) and described in [Table 2-644](#).

Return to the [Summary Table](#).

Figure 2-631. pwm_bri262 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L7-CS5							
R/W-0h							

Table 2-644. pwm_bri262 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits__ 7:0_for_Dot_L7-CS5	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L7-CS5

2.13.264 pwm_bri263 Register (Address = 307h) [Default = 0h]

pwm_bri263 is shown in [Figure 2-632](#) and described in [Table 2-645](#).

Return to the [Summary Table](#).

Figure 2-632. pwm_bri263 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L7-CS5							
R/W-0h							

Table 2-645. pwm_bri263 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L7-CS5	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L7-CS5

2.13.265 pwm_bri264 Register (Address = 308h) [Default = 0h]

pwm_bri264 is shown in [Figure 2-633](#) and described in [Table 2-646](#).

Return to the [Summary Table](#).

Figure 2-633. pwm_bri264 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L7-CS6							
R/W-0h							

Table 2-646. pwm_bri264 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L7-CS6	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L7-CS6

2.13.266 pwm_bri265 Register (Address = 309h) [Default = 0h]

pwm_bri265 is shown in [Figure 2-634](#) and described in [Table 2-647](#).

Return to the [Summary Table](#).

Figure 2-634. pwm_bri265 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_ 15:8_for_Dot_L7-CS6							
R/W-0h							

Table 2-647. pwm_bri265 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ 15:8_for_Dot_L7-CS6	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L7-CS6

2.13.267 pwm_bri266 Register (Address = 30Ah) [Default = 0h]

pwm_bri266 is shown in [Figure 2-635](#) and described in [Table 2-648](#).

Return to the [Summary Table](#).

Figure 2-635. pwm_bri266 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits_ 7:0_for_Dot_L7-CS7							
R/W-0h							

Table 2-648. pwm_bri266 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L7-CS7	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L7-CS7

2.13.268 pwm_bri267 Register (Address = 30Bh) [Default = 0h]

pwm_bri267 is shown in [Figure 2-636](#) and described in [Table 2-649](#).

Return to the [Summary Table](#).

Figure 2-636. pwm_bri267 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_ 15:8_for_Dot_L7-CS7							
R/W-0h							

Table 2-649. pwm_bri267 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L7-CS7	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L7-CS7

2.13.269 pwm_bri268 Register (Address = 30Ch) [Default = 0h]

pwm_bri268 is shown in [Figure 2-637](#) and described in [Table 2-650](#).

Return to the [Summary Table](#).

Figure 2-637. pwm_bri268 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L7-CS8							
R/W-0h							

Table 2-650. pwm_bri268 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits__ 7:0_for_Dot_L7-CS8	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L7-CS8

2.13.270 pwm_bri269 Register (Address = 30Dh) [Default = 0h]

pwm_bri269 is shown in [Figure 2-638](#) and described in [Table 2-651](#).

Return to the [Summary Table](#).

Figure 2-638. pwm_bri269 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L7-CS8							
R/W-0h							

Table 2-651. pwm_bri269 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L7-CS8	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L7-CS8

2.13.271 pwm_bri270 Register (Address = 30Eh) [Default = 0h]

pwm_bri270 is shown in [Figure 2-639](#) and described in [Table 2-652](#).

Return to the [Summary Table](#).

Figure 2-639. pwm_bri270 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L7-CS9							
R/W-0h							

Table 2-652. pwm_bri270 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L7-CS9	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L7-CS9

2.13.272 pwm_bri271 Register (Address = 30Fh) [Default = 0h]

pwm_bri271 is shown in [Figure 2-640](#) and described in [Table 2-653](#).

Return to the [Summary Table](#).

Figure 2-640. pwm_bri271 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_15:8_for_Dot_L7-CS9							
R/W-0h							

Table 2-653. pwm_bri271 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ 15:8_for_Dot_L7-CS9	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L7-CS9

2.13.273 pwm_bri272 Register (Address = 310h) [Default = 0h]

pwm_bri272 is shown in [Figure 2-641](#) and described in [Table 2-654](#).

Return to the [Summary Table](#).

Figure 2-641. pwm_bri272 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits_7:0_for_Dot_L7-CS10							
R/W-0h							

Table 2-654. pwm_bri272 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L7-CS10	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L7-CS10

2.13.274 pwm_bri273 Register (Address = 311h) [Default = 0h]

pwm_bri273 is shown in [Figure 2-642](#) and described in [Table 2-655](#).

Return to the [Summary Table](#).

Figure 2-642. pwm_bri273 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_15:8_for_Dot_L7-CS10							
R/W-0h							

Table 2-655. pwm_bri273 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L7-CS10	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L7-CS10

2.13.275 pwm_bri274 Register (Address = 312h) [Default = 0h]

pwm_bri274 is shown in [Figure 2-643](#) and described in [Table 2-656](#).

Return to the [Summary Table](#).

Figure 2-643. pwm_bri274 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L7-CS11							
R/W-0h							

Table 2-656. pwm_bri274 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0__for_Dot_L7-CS11	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L7-CS11

2.13.276 pwm_bri275 Register (Address = 313h) [Default = 0h]

pwm_bri275 is shown in [Figure 2-644](#) and described in [Table 2-657](#).

Return to the [Summary Table](#).

Figure 2-644. pwm_bri275 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8__for_Dot_L7-CS11							
R/W-0h							

Table 2-657. pwm_bri275 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L7-CS11	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L7-CS11

2.13.277 pwm_bri276 Register (Address = 314h) [Default = 0h]

pwm_bri276 is shown in [Figure 2-645](#) and described in [Table 2-658](#).

Return to the [Summary Table](#).

Figure 2-645. pwm_bri276 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L7-CS12							
R/W-0h							

Table 2-658. pwm_bri276 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L7-CS12	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L7-CS12

2.13.278 pwm_bri277 Register (Address = 315h) [Default = 0h]

pwm_bri277 is shown in [Figure 2-646](#) and described in [Table 2-659](#).

Return to the [Summary Table](#).

Figure 2-646. pwm_bri277 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L7-CS12							
R/W-0h							

Table 2-659. pwm_bri277 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L7-CS12	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L7-CS12

2.13.279 pwm_bri278 Register (Address = 316h) [Default = 0h]

pwm_bri278 is shown in [Figure 2-647](#) and described in [Table 2-660](#).

Return to the [Summary Table](#).

Figure 2-647. pwm_bri278 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L7-CS13							
R/W-0h							

Table 2-660. pwm_bri278 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L7-CS13	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L7-CS13

2.13.280 pwm_bri279 Register (Address = 317h) [Default = 0h]

pwm_bri279 is shown in [Figure 2-648](#) and described in [Table 2-661](#).

Return to the [Summary Table](#).

Figure 2-648. pwm_bri279 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L7-CS13							
R/W-0h							

Table 2-661. pwm_bri279 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L7-CS13	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L7-CS13

2.13.281 pwm_bri280 Register (Address = 318h) [Default = 0h]

pwm_bri280 is shown in [Figure 2-649](#) and described in [Table 2-662](#).

Return to the [Summary Table](#).

Figure 2-649. pwm_bri280 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L7-CS14							
R/W-0h							

Table 2-662. pwm_bri280 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0__for_Dot_L7-CS14	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L7-CS14

2.13.282 pwm_bri281 Register (Address = 319h) [Default = 0h]

pwm_bri281 is shown in [Figure 2-650](#) and described in [Table 2-663](#).

Return to the [Summary Table](#).

Figure 2-650. pwm_bri281 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8__for_Dot_L7-CS14							
R/W-0h							

Table 2-663. pwm_bri281 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L7-CS14	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L7-CS14

2.13.283 pwm_bri282 Register (Address = 31Ah) [Default = 0h]

pwm_bri282 is shown in [Figure 2-651](#) and described in [Table 2-664](#).

Return to the [Summary Table](#).

Figure 2-651. pwm_bri282 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L7-CS15							
R/W-0h							

Table 2-664. pwm_bri282 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L7-CS15	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L7-CS15

2.13.284 pwm_bri283 Register (Address = 31Bh) [Default = 0h]

pwm_bri283 is shown in [Figure 2-652](#) and described in [Table 2-665](#).

Return to the [Summary Table](#).

Figure 2-652. pwm_bri283 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L7-CS15							
R/W-0h							

Table 2-665. pwm_bri283 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L7-CS15	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L7-CS15

2.13.285 pwm_bri284 Register (Address = 31Ch) [Default = 0h]

pwm_bri284 is shown in [Figure 2-653](#) and described in [Table 2-666](#).

Return to the [Summary Table](#).

Figure 2-653. pwm_bri284 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L7-CS16							
R/W-0h							

Table 2-666. pwm_bri284 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L7-CS16	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L7-CS16

2.13.286 pwm_bri285 Register (Address = 31Dh) [Default = 0h]

pwm_bri285 is shown in [Figure 2-654](#) and described in [Table 2-667](#).

Return to the [Summary Table](#).

Figure 2-654. pwm_bri285 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L7-CS16							
R/W-0h							

Table 2-667. pwm_bri285 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L7-CS16	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L7-CS16

2.13.287 pwm_bri286 Register (Address = 31Eh) [Default = 0h]

pwm_bri286 is shown in [Figure 2-655](#) and described in [Table 2-668](#).

Return to the [Summary Table](#).

Figure 2-655. pwm_bri286 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L7-CS17							
R/W-0h							

Table 2-668. pwm_bri286 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits__ 7:0_for_Dot_L7-CS17	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L7-CS17

2.13.288 pwm_bri287 Register (Address = 31Fh) [Default = 0h]

pwm_bri287 is shown in [Figure 2-656](#) and described in [Table 2-669](#).

Return to the [Summary Table](#).

Figure 2-656. pwm_bri287 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L7-CS17							
R/W-0h							

Table 2-669. pwm_bri287 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L7-CS17	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L7-CS17

2.13.289 pwm_bri288 Register (Address = 320h) [Default = 0h]

pwm_bri288 is shown in [Figure 2-657](#) and described in [Table 2-670](#).

Return to the [Summary Table](#).

Figure 2-657. pwm_bri288 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L8-CS0							
R/W-0h							

Table 2-670. pwm_bri288 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L8-CS0	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L8-CS0

2.13.290 pwm_bri289 Register (Address = 321h) [Default = 0h]

pwm_bri289 is shown in [Figure 2-658](#) and described in [Table 2-671](#).

Return to the [Summary Table](#).

Figure 2-658. pwm_bri289 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_ 15:8_for_Dot_L8-CS0							
R/W-0h							

Table 2-671. pwm_bri289 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ 15:8_for_Dot_L8-CS0	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L8-CS0

2.13.291 pwm_bri290 Register (Address = 322h) [Default = 0h]

pwm_bri290 is shown in [Figure 2-659](#) and described in [Table 2-672](#).

Return to the [Summary Table](#).

Figure 2-659. pwm_bri290 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits_ 7:0_for_Dot_L8-CS1							
R/W-0h							

Table 2-672. pwm_bri290 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L8-CS1	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L8-CS1

2.13.292 pwm_bri291 Register (Address = 323h) [Default = 0h]

pwm_bri291 is shown in [Figure 2-660](#) and described in [Table 2-673](#).

Return to the [Summary Table](#).

Figure 2-660. pwm_bri291 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_ 15:8_for_Dot_L8-CS1							
R/W-0h							

Table 2-673. pwm_bri291 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L8-CS1	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L8-CS1

2.13.293 pwm_bri292 Register (Address = 324h) [Default = 0h]

pwm_bri292 is shown in [Figure 2-661](#) and described in [Table 2-674](#).

Return to the [Summary Table](#).

Figure 2-661. pwm_bri292 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L8-CS2							
R/W-0h							

Table 2-674. pwm_bri292 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits__ 7:0_for_Dot_L8-CS2	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L8-CS2

2.13.294 pwm_bri293 Register (Address = 325h) [Default = 0h]

pwm_bri293 is shown in [Figure 2-662](#) and described in [Table 2-675](#).

Return to the [Summary Table](#).

Figure 2-662. pwm_bri293 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L8-CS2							
R/W-0h							

Table 2-675. pwm_bri293 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L8-CS2	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L8-CS2

2.13.295 pwm_bri294 Register (Address = 326h) [Default = 0h]

pwm_bri294 is shown in [Figure 2-663](#) and described in [Table 2-676](#).

Return to the [Summary Table](#).

Figure 2-663. pwm_bri294 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L8-CS3							
R/W-0h							

Table 2-676. pwm_bri294 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L8-CS3	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L8-CS3

2.13.296 pwm_bri295 Register (Address = 327h) [Default = 0h]

pwm_bri295 is shown in [Figure 2-664](#) and described in [Table 2-677](#).

Return to the [Summary Table](#).

Figure 2-664. pwm_bri295 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_ 15:8_for_Dot_L8-CS3							
R/W-0h							

Table 2-677. pwm_bri295 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ 15:8_for_Dot_L8-CS3	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L8-CS3

2.13.297 pwm_bri296 Register (Address = 328h) [Default = 0h]

pwm_bri296 is shown in [Figure 2-665](#) and described in [Table 2-678](#).

Return to the [Summary Table](#).

Figure 2-665. pwm_bri296 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits_ 7:0_for_Dot_L8-CS4							
R/W-0h							

Table 2-678. pwm_bri296 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L8-CS4	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L8-CS4

2.13.298 pwm_bri297 Register (Address = 329h) [Default = 0h]

pwm_bri297 is shown in [Figure 2-666](#) and described in [Table 2-679](#).

Return to the [Summary Table](#).

Figure 2-666. pwm_bri297 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_ 15:8_for_Dot_L8-CS4							
R/W-0h							

Table 2-679. pwm_bri297 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L8-CS4	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L8-CS4

2.13.299 pwm_bri298 Register (Address = 32Ah) [Default = 0h]

pwm_bri298 is shown in [Figure 2-667](#) and described in [Table 2-680](#).

Return to the [Summary Table](#).

Figure 2-667. pwm_bri298 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L8-CS5							
R/W-0h							

Table 2-680. pwm_bri298 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits__ 7:0_for_Dot_L8-CS5	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L8-CS5

2.13.300 pwm_bri299 Register (Address = 32Bh) [Default = 0h]

pwm_bri299 is shown in [Figure 2-668](#) and described in [Table 2-681](#).

Return to the [Summary Table](#).

Figure 2-668. pwm_bri299 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L8-CS5							
R/W-0h							

Table 2-681. pwm_bri299 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L8-CS5	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L8-CS5

2.13.301 pwm_bri300 Register (Address = 32Ch) [Default = 0h]

pwm_bri300 is shown in [Figure 2-669](#) and described in [Table 2-682](#).

Return to the [Summary Table](#).

Figure 2-669. pwm_bri300 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L8-CS6							
R/W-0h							

Table 2-682. pwm_bri300 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L8-CS6	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L8-CS6

2.13.302 pwm_bri301 Register (Address = 32Dh) [Default = 0h]

pwm_bri301 is shown in [Figure 2-670](#) and described in [Table 2-683](#).

Return to the [Summary Table](#).

Figure 2-670. pwm_bri301 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_ 15:8_for_Dot_L8-CS6							
R/W-0h							

Table 2-683. pwm_bri301 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ 15:8_for_Dot_L8-CS6	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L8-CS6

2.13.303 pwm_bri302 Register (Address = 32Eh) [Default = 0h]

pwm_bri302 is shown in [Figure 2-671](#) and described in [Table 2-684](#).

Return to the [Summary Table](#).

Figure 2-671. pwm_bri302 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits_ 7:0_for_Dot_L8-CS7							
R/W-0h							

Table 2-684. pwm_bri302 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L8-CS7	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L8-CS7

2.13.304 pwm_bri303 Register (Address = 32Fh) [Default = 0h]

pwm_bri303 is shown in [Figure 2-672](#) and described in [Table 2-685](#).

Return to the [Summary Table](#).

Figure 2-672. pwm_bri303 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_ 15:8_for_Dot_L8-CS7							
R/W-0h							

Table 2-685. pwm_bri303 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L8-CS7	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L8-CS7

2.13.305 pwm_bri304 Register (Address = 330h) [Default = 0h]

pwm_bri304 is shown in [Figure 2-673](#) and described in [Table 2-686](#).

Return to the [Summary Table](#).

Figure 2-673. pwm_bri304 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L8-CS8							
R/W-0h							

Table 2-686. pwm_bri304 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits__ 7:0_for_Dot_L8-CS8	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L8-CS8

2.13.306 pwm_bri305 Register (Address = 331h) [Default = 0h]

pwm_bri305 is shown in [Figure 2-674](#) and described in [Table 2-687](#).

Return to the [Summary Table](#).

Figure 2-674. pwm_bri305 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L8-CS8							
R/W-0h							

Table 2-687. pwm_bri305 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L8-CS8	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L8-CS8

2.13.307 pwm_bri306 Register (Address = 332h) [Default = 0h]

pwm_bri306 is shown in [Figure 2-675](#) and described in [Table 2-688](#).

Return to the [Summary Table](#).

Figure 2-675. pwm_bri306 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L8-CS9							
R/W-0h							

Table 2-688. pwm_bri306 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L8-CS9	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L8-CS9

2.13.308 pwm_bri307 Register (Address = 333h) [Default = 0h]

pwm_bri307 is shown in [Figure 2-676](#) and described in [Table 2-689](#).

Return to the [Summary Table](#).

Figure 2-676. pwm_bri307 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_15:8_for_Dot_L8-CS9							
R/W-0h							

Table 2-689. pwm_bri307 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ 15:8_for_Dot_L8-CS9	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L8-CS9

2.13.309 pwm_bri308 Register (Address = 334h) [Default = 0h]

pwm_bri308 is shown in [Figure 2-677](#) and described in [Table 2-690](#).

Return to the [Summary Table](#).

Figure 2-677. pwm_bri308 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits_7:0_for_Dot_L8-CS10							
R/W-0h							

Table 2-690. pwm_bri308 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L8-CS10	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L8-CS10

2.13.310 pwm_bri309 Register (Address = 335h) [Default = 0h]

pwm_bri309 is shown in [Figure 2-678](#) and described in [Table 2-691](#).

Return to the [Summary Table](#).

Figure 2-678. pwm_bri309 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_15:8_for_Dot_L8-CS10							
R/W-0h							

Table 2-691. pwm_bri309 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L8-CS10	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L8-CS10

2.13.311 pwm_bri310 Register (Address = 336h) [Default = 0h]

pwm_bri310 is shown in [Figure 2-679](#) and described in [Table 2-692](#).

Return to the [Summary Table](#).

Figure 2-679. pwm_bri310 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L8-CS11							
R/W-0h							

Table 2-692. pwm_bri310 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits__ 7:0__for_Dot_L8-CS11	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L8-CS11

2.13.312 pwm_bri311 Register (Address = 337h) [Default = 0h]

pwm_bri311 is shown in [Figure 2-680](#) and described in [Table 2-693](#).

Return to the [Summary Table](#).

Figure 2-680. pwm_bri311 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8__for_Dot_L8-CS11							
R/W-0h							

Table 2-693. pwm_bri311 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L8-CS11	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L8-CS11

2.13.313 pwm_bri312 Register (Address = 338h) [Default = 0h]

pwm_bri312 is shown in [Figure 2-681](#) and described in [Table 2-694](#).

Return to the [Summary Table](#).

Figure 2-681. pwm_bri312 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L8-CS12							
R/W-0h							

Table 2-694. pwm_bri312 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L8-CS12	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L8-CS12

2.13.314 pwm_bri313 Register (Address = 339h) [Default = 0h]

pwm_bri313 is shown in [Figure 2-682](#) and described in [Table 2-695](#).

Return to the [Summary Table](#).

Figure 2-682. pwm_bri313 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L8-CS12							
R/W-0h							

Table 2-695. pwm_bri313 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L8-CS12	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L8-CS12

2.13.315 pwm_bri314 Register (Address = 33Ah) [Default = 0h]

pwm_bri314 is shown in [Figure 2-683](#) and described in [Table 2-696](#).

Return to the [Summary Table](#).

Figure 2-683. pwm_bri314 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L8-CS13							
R/W-0h							

Table 2-696. pwm_bri314 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L8-CS13	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L8-CS13

2.13.316 pwm_bri315 Register (Address = 33Bh) [Default = 0h]

pwm_bri315 is shown in [Figure 2-684](#) and described in [Table 2-697](#).

Return to the [Summary Table](#).

Figure 2-684. pwm_bri315 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L8-CS13							
R/W-0h							

Table 2-697. pwm_bri315 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L8-CS13	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L8-CS13

2.13.317 pwm_bri316 Register (Address = 33Ch) [Default = 0h]

pwm_bri316 is shown in [Figure 2-685](#) and described in [Table 2-698](#).

Return to the [Summary Table](#).

Figure 2-685. pwm_bri316 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L8-CS14							
R/W-0h							

Table 2-698. pwm_bri316 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0__for_Dot_L8-CS14	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L8-CS14

2.13.318 pwm_bri317 Register (Address = 33Dh) [Default = 0h]

pwm_bri317 is shown in [Figure 2-686](#) and described in [Table 2-699](#).

Return to the [Summary Table](#).

Figure 2-686. pwm_bri317 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8__for_Dot_L8-CS14							
R/W-0h							

Table 2-699. pwm_bri317 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L8-CS14	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L8-CS14

2.13.319 pwm_bri318 Register (Address = 33Eh) [Default = 0h]

pwm_bri318 is shown in [Figure 2-687](#) and described in [Table 2-700](#).

Return to the [Summary Table](#).

Figure 2-687. pwm_bri318 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L8-CS15							
R/W-0h							

Table 2-700. pwm_bri318 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L8-CS15	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L8-CS15

2.13.320 pwm_bri319 Register (Address = 33Fh) [Default = 0h]

pwm_bri319 is shown in [Figure 2-688](#) and described in [Table 2-701](#).

Return to the [Summary Table](#).

Figure 2-688. pwm_bri319 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L8-CS15							
R/W-0h							

Table 2-701. pwm_bri319 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L8-CS15	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L8-CS15

2.13.321 pwm_bri320 Register (Address = 340h) [Default = 0h]

pwm_bri320 is shown in [Figure 2-689](#) and described in [Table 2-702](#).

Return to the [Summary Table](#).

Figure 2-689. pwm_bri320 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L8-CS16							
R/W-0h							

Table 2-702. pwm_bri320 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L8-CS16	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L8-CS16

2.13.322 pwm_bri321 Register (Address = 341h) [Default = 0h]

pwm_bri321 is shown in [Figure 2-690](#) and described in [Table 2-703](#).

Return to the [Summary Table](#).

Figure 2-690. pwm_bri321 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L8-CS16							
R/W-0h							

Table 2-703. pwm_bri321 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16-bits_PWM_higher_8_bits__15:8__for_Dot_L8-CS16	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L8-CS16

2.13.323 pwm_bri322 Register (Address = 342h) [Default = 0h]

pwm_bri322 is shown in [Figure 2-691](#) and described in [Table 2-704](#).

Return to the [Summary Table](#).

Figure 2-691. pwm_bri322 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L8-CS17							
R/W-0h							

Table 2-704. pwm_bri322 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16-bits_PWM_lower_8_bits__7:0__for_Dot_L8-CS17	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L8-CS17

2.13.324 pwm_bri323 Register (Address = 343h) [Default = 0h]

pwm_bri323 is shown in [Figure 2-692](#) and described in [Table 2-705](#).

Return to the [Summary Table](#).

Figure 2-692. pwm_bri323 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8__for_Dot_L8-CS17							
R/W-0h							

Table 2-705. pwm_bri323 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16-bits_PWM_higher_8_bits__15:8__for_Dot_L8-CS17	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L8-CS17

2.13.325 pwm_bri324 Register (Address = 344h) [Default = 0h]

pwm_bri324 is shown in [Figure 2-693](#) and described in [Table 2-706](#).

Return to the [Summary Table](#).

Figure 2-693. pwm_bri324 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L9-CS0							
R/W-0h							

Table 2-706. pwm_bri324 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L9-CS0	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L9-CS0

2.13.326 pwm_bri325 Register (Address = 345h) [Default = 0h]

pwm_bri325 is shown in [Figure 2-694](#) and described in [Table 2-707](#).

Return to the [Summary Table](#).

Figure 2-694. pwm_bri325 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_ 15:8_for_Dot_L9-CS0							
R/W-0h							

Table 2-707. pwm_bri325 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ 15:8_for_Dot_L9-CS0	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L9-CS0

2.13.327 pwm_bri326 Register (Address = 346h) [Default = 0h]

pwm_bri326 is shown in [Figure 2-695](#) and described in [Table 2-708](#).

Return to the [Summary Table](#).

Figure 2-695. pwm_bri326 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits_ 7:0_for_Dot_L9-CS1							
R/W-0h							

Table 2-708. pwm_bri326 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L9-CS1	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L9-CS1

2.13.328 pwm_bri327 Register (Address = 347h) [Default = 0h]

pwm_bri327 is shown in [Figure 2-696](#) and described in [Table 2-709](#).

Return to the [Summary Table](#).

Figure 2-696. pwm_bri327 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_ 15:8_for_Dot_L9-CS1							
R/W-0h							

Table 2-709. pwm_bri327 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L9-CS1	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L9-CS1

2.13.329 pwm_bri328 Register (Address = 348h) [Default = 0h]

pwm_bri328 is shown in [Figure 2-697](#) and described in [Table 2-710](#).

Return to the [Summary Table](#).

Figure 2-697. pwm_bri328 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L9-CS2							
R/W-0h							

Table 2-710. pwm_bri328 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits__ 7:0__for_Dot_L9-CS2	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L9-CS2

2.13.330 pwm_bri329 Register (Address = 349h) [Default = 0h]

pwm_bri329 is shown in [Figure 2-698](#) and described in [Table 2-711](#).

Return to the [Summary Table](#).

Figure 2-698. pwm_bri329 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8__for_Dot_L9-CS2							
R/W-0h							

Table 2-711. pwm_bri329 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L9-CS2	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L9-CS2

2.13.331 pwm_bri330 Register (Address = 34Ah) [Default = 0h]

pwm_bri330 is shown in [Figure 2-699](#) and described in [Table 2-712](#).

Return to the [Summary Table](#).

Figure 2-699. pwm_bri330 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L9-CS3							
R/W-0h							

Table 2-712. pwm_bri330 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L9-CS3	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L9-CS3

2.13.332 pwm_bri331 Register (Address = 34Bh) [Default = 0h]

pwm_bri331 is shown in [Figure 2-700](#) and described in [Table 2-713](#).

Return to the [Summary Table](#).

Figure 2-700. pwm_bri331 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_ 15:8_for_Dot_L9-CS3							
R/W-0h							

Table 2-713. pwm_bri331 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ 15:8_for_Dot_L9-CS3	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L9-CS3

2.13.333 pwm_bri332 Register (Address = 34Ch) [Default = 0h]

pwm_bri332 is shown in [Figure 2-701](#) and described in [Table 2-714](#).

Return to the [Summary Table](#).

Figure 2-701. pwm_bri332 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits_ 7:0_for_Dot_L9-CS4							
R/W-0h							

Table 2-714. pwm_bri332 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L9-CS4	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L9-CS4

2.13.334 pwm_bri333 Register (Address = 34Dh) [Default = 0h]

pwm_bri333 is shown in [Figure 2-702](#) and described in [Table 2-715](#).

Return to the [Summary Table](#).

Figure 2-702. pwm_bri333 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_ 15:8_for_Dot_L9-CS4							
R/W-0h							

Table 2-715. pwm_bri333 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L9-CS4	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L9-CS4

2.13.335 pwm_bri334 Register (Address = 34Eh) [Default = 0h]

pwm_bri334 is shown in [Figure 2-703](#) and described in [Table 2-716](#).

Return to the [Summary Table](#).

Figure 2-703. pwm_bri334 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L9-CS5							
R/W-0h							

Table 2-716. pwm_bri334 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits__ 7:0__for_Dot_L9-CS5	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L9-CS5

2.13.336 pwm_bri335 Register (Address = 34Fh) [Default = 0h]

pwm_bri335 is shown in [Figure 2-704](#) and described in [Table 2-717](#).

Return to the [Summary Table](#).

Figure 2-704. pwm_bri335 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8__for_Dot_L9-CS5							
R/W-0h							

Table 2-717. pwm_bri335 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L9-CS5	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L9-CS5

2.13.337 pwm_bri336 Register (Address = 350h) [Default = 0h]

pwm_bri336 is shown in [Figure 2-705](#) and described in [Table 2-718](#).

Return to the [Summary Table](#).

Figure 2-705. pwm_bri336 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L9-CS6							
R/W-0h							

Table 2-718. pwm_bri336 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L9-CS6	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L9-CS6

2.13.338 pwm_bri337 Register (Address = 351h) [Default = 0h]

pwm_bri337 is shown in [Figure 2-706](#) and described in [Table 2-719](#).

Return to the [Summary Table](#).

Figure 2-706. pwm_bri337 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_ 15:8_for_Dot_L9-CS6							
R/W-0h							

Table 2-719. pwm_bri337 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ 15:8_for_Dot_L9-CS6	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L9-CS6

2.13.339 pwm_bri338 Register (Address = 352h) [Default = 0h]

pwm_bri338 is shown in [Figure 2-707](#) and described in [Table 2-720](#).

Return to the [Summary Table](#).

Figure 2-707. pwm_bri338 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits_ 7:0_for_Dot_L9-CS7							
R/W-0h							

Table 2-720. pwm_bri338 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L9-CS7	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L9-CS7

2.13.340 pwm_bri339 Register (Address = 353h) [Default = 0h]

pwm_bri339 is shown in [Figure 2-708](#) and described in [Table 2-721](#).

Return to the [Summary Table](#).

Figure 2-708. pwm_bri339 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_ 15:8_for_Dot_L9-CS7							
R/W-0h							

Table 2-721. pwm_bri339 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L9-CS7	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L9-CS7

2.13.341 pwm_bri340 Register (Address = 354h) [Default = 0h]

pwm_bri340 is shown in [Figure 2-709](#) and described in [Table 2-722](#).

Return to the [Summary Table](#).

Figure 2-709. pwm_bri340 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L9-CS8							
R/W-0h							

Table 2-722. pwm_bri340 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits__ 7:0_for_Dot_L9-CS8	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L9-CS8

2.13.342 pwm_bri341 Register (Address = 355h) [Default = 0h]

pwm_bri341 is shown in [Figure 2-710](#) and described in [Table 2-723](#).

Return to the [Summary Table](#).

Figure 2-710. pwm_bri341 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L9-CS8							
R/W-0h							

Table 2-723. pwm_bri341 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L9-CS8	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L9-CS8

2.13.343 pwm_bri342 Register (Address = 356h) [Default = 0h]

pwm_bri342 is shown in [Figure 2-711](#) and described in [Table 2-724](#).

Return to the [Summary Table](#).

Figure 2-711. pwm_bri342 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L9-CS9							
R/W-0h							

Table 2-724. pwm_bri342 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L9-CS9	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L9-CS9

2.13.344 pwm_bri343 Register (Address = 357h) [Default = 0h]

pwm_bri343 is shown in [Figure 2-712](#) and described in [Table 2-725](#).

Return to the [Summary Table](#).

Figure 2-712. pwm_bri343 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_ 15:8_for_Dot_L9-CS9							
R/W-0h							

Table 2-725. pwm_bri343 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ 15:8_for_Dot_L9-CS9	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L9-CS9

2.13.345 pwm_bri344 Register (Address = 358h) [Default = 0h]

pwm_bri344 is shown in [Figure 2-713](#) and described in [Table 2-726](#).

Return to the [Summary Table](#).

Figure 2-713. pwm_bri344 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits_ 7:0_for_Dot_L9-CS10							
R/W-0h							

Table 2-726. pwm_bri344 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L9-CS10	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L9-CS10

2.13.346 pwm_bri345 Register (Address = 359h) [Default = 0h]

pwm_bri345 is shown in [Figure 2-714](#) and described in [Table 2-727](#).

Return to the [Summary Table](#).

Figure 2-714. pwm_bri345 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_ 15:8_for_Dot_L9-CS10							
R/W-0h							

Table 2-727. pwm_bri345 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L9-CS10	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L9-CS10

2.13.347 pwm_bri346 Register (Address = 35Ah) [Default = 0h]

pwm_bri346 is shown in [Figure 2-715](#) and described in [Table 2-728](#).

Return to the [Summary Table](#).

Figure 2-715. pwm_bri346 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L9-CS11							
R/W-0h							

Table 2-728. pwm_bri346 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0__for_Dot_L9-CS11	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L9-CS11

2.13.348 pwm_bri347 Register (Address = 35Bh) [Default = 0h]

pwm_bri347 is shown in [Figure 2-716](#) and described in [Table 2-729](#).

Return to the [Summary Table](#).

Figure 2-716. pwm_bri347 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8__for_Dot_L9-CS11							
R/W-0h							

Table 2-729. pwm_bri347 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L9-CS11	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L9-CS11

2.13.349 pwm_bri348 Register (Address = 35Ch) [Default = 0h]

pwm_bri348 is shown in [Figure 2-717](#) and described in [Table 2-730](#).

Return to the [Summary Table](#).

Figure 2-717. pwm_bri348 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L9-CS12							
R/W-0h							

Table 2-730. pwm_bri348 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L9-CS12	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L9-CS12

2.13.350 pwm_bri349 Register (Address = 35Dh) [Default = 0h]

pwm_bri349 is shown in [Figure 2-718](#) and described in [Table 2-731](#).

Return to the [Summary Table](#).

Figure 2-718. pwm_bri349 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L9-CS12							
R/W-0h							

Table 2-731. pwm_bri349 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L9-CS12	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L9-CS12

2.13.351 pwm_bri350 Register (Address = 35Eh) [Default = 0h]

pwm_bri350 is shown in [Figure 2-719](#) and described in [Table 2-732](#).

Return to the [Summary Table](#).

Figure 2-719. pwm_bri350 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L9-CS13							
R/W-0h							

Table 2-732. pwm_bri350 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L9-CS13	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L9-CS13

2.13.352 pwm_bri351 Register (Address = 35Fh) [Default = 0h]

pwm_bri351 is shown in [Figure 2-720](#) and described in [Table 2-733](#).

Return to the [Summary Table](#).

Figure 2-720. pwm_bri351 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L9-CS13							
R/W-0h							

Table 2-733. pwm_bri351 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L9-CS13	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L9-CS13

2.13.353 pwm_bri352 Register (Address = 360h) [Default = 0h]

pwm_bri352 is shown in [Figure 2-721](#) and described in [Table 2-734](#).

Return to the [Summary Table](#).

Figure 2-721. pwm_bri352 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L9-CS14							
R/W-0h							

Table 2-734. pwm_bri352 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits__ 7:0__for_Dot_L9-CS14	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L9-CS14

2.13.354 pwm_bri353 Register (Address = 361h) [Default = 0h]

pwm_bri353 is shown in [Figure 2-722](#) and described in [Table 2-735](#).

Return to the [Summary Table](#).

Figure 2-722. pwm_bri353 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8__for_Dot_L9-CS14							
R/W-0h							

Table 2-735. pwm_bri353 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L9-CS14	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L9-CS14

2.13.355 pwm_bri354 Register (Address = 362h) [Default = 0h]

pwm_bri354 is shown in [Figure 2-723](#) and described in [Table 2-736](#).

Return to the [Summary Table](#).

Figure 2-723. pwm_bri354 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L9-CS15							
R/W-0h							

Table 2-736. pwm_bri354 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L9-CS15	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L9-CS15

2.13.356 pwm_bri355 Register (Address = 363h) [Default = 0h]

pwm_bri355 is shown in [Figure 2-724](#) and described in [Table 2-737](#).

Return to the [Summary Table](#).

Figure 2-724. pwm_bri355 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L9-CS15							
R/W-0h							

Table 2-737. pwm_bri355 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L9-CS15	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L9-CS15

2.13.357 pwm_bri356 Register (Address = 364h) [Default = 0h]

pwm_bri356 is shown in [Figure 2-725](#) and described in [Table 2-738](#).

Return to the [Summary Table](#).

Figure 2-725. pwm_bri356 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L9-CS16							
R/W-0h							

Table 2-738. pwm_bri356 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L9-CS16	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L9-CS16

2.13.358 pwm_bri357 Register (Address = 365h) [Default = 0h]

pwm_bri357 is shown in [Figure 2-726](#) and described in [Table 2-739](#).

Return to the [Summary Table](#).

Figure 2-726. pwm_bri357 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L9-CS16							
R/W-0h							

Table 2-739. pwm_bri357 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L9-CS16	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L9-CS16

2.13.359 pwm_bri358 Register (Address = 366h) [Default = 0h]

pwm_bri358 is shown in [Figure 2-727](#) and described in [Table 2-740](#).

Return to the [Summary Table](#).

Figure 2-727. pwm_bri358 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L9-CS17							
R/W-0h							

Table 2-740. pwm_bri358 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0__for_Dot_L9-CS17	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L9-CS17

2.13.360 pwm_bri359 Register (Address = 367h) [Default = 0h]

pwm_bri359 is shown in [Figure 2-728](#) and described in [Table 2-741](#).

Return to the [Summary Table](#).

Figure 2-728. pwm_bri359 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8__for_Dot_L9-CS17							
R/W-0h							

Table 2-741. pwm_bri359 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L9-CS17	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L9-CS17

2.13.361 pwm_bri360 Register (Address = 368h) [Default = 0h]

pwm_bri360 is shown in [Figure 2-729](#) and described in [Table 2-742](#).

Return to the [Summary Table](#).

Figure 2-729. pwm_bri360 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L10-CS0							
R/W-0h							

Table 2-742. pwm_bri360 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L10-CS0	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L10-CS0

2.13.362 pwm_bri361 Register (Address = 369h) [Default = 0h]

pwm_bri361 is shown in [Figure 2-730](#) and described in [Table 2-743](#).

Return to the [Summary Table](#).

Figure 2-730. pwm_bri361 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L10-CS0							
R/W-0h							

Table 2-743. pwm_bri361 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L10-CS0	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L10-CS0

2.13.363 pwm_bri362 Register (Address = 36Ah) [Default = 0h]

pwm_bri362 is shown in [Figure 2-731](#) and described in [Table 2-744](#).

Return to the [Summary Table](#).

Figure 2-731. pwm_bri362 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L10-CS1							
R/W-0h							

Table 2-744. pwm_bri362 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L10-CS1	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L10-CS1

2.13.364 pwm_bri363 Register (Address = 36Bh) [Default = 0h]

pwm_bri363 is shown in [Figure 2-732](#) and described in [Table 2-745](#).

Return to the [Summary Table](#).

Figure 2-732. pwm_bri363 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L10-CS1							
R/W-0h							

Table 2-745. pwm_bri363 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L10-CS1	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L10-CS1

2.13.365 pwm_bri364 Register (Address = 36Ch) [Default = 0h]

pwm_bri364 is shown in [Figure 2-733](#) and described in [Table 2-746](#).

Return to the [Summary Table](#).

Figure 2-733. pwm_bri364 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L10-CS2							
R/W-0h							

Table 2-746. pwm_bri364 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits__ 7:0__for_Dot_L10-CS2	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L10-CS2

2.13.366 pwm_bri365 Register (Address = 36Dh) [Default = 0h]

pwm_bri365 is shown in [Figure 2-734](#) and described in [Table 2-747](#).

Return to the [Summary Table](#).

Figure 2-734. pwm_bri365 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8__for_Dot_L10-CS2							
R/W-0h							

Table 2-747. pwm_bri365 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L10-CS2	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L10-CS2

2.13.367 pwm_bri366 Register (Address = 36Eh) [Default = 0h]

pwm_bri366 is shown in [Figure 2-735](#) and described in [Table 2-748](#).

Return to the [Summary Table](#).

Figure 2-735. pwm_bri366 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L10-CS3							
R/W-0h							

Table 2-748. pwm_bri366 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L10-CS3	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L10-CS3

2.13.368 pwm_bri367 Register (Address = 36Fh) [Default = 0h]

pwm_bri367 is shown in [Figure 2-736](#) and described in [Table 2-749](#).

Return to the [Summary Table](#).

Figure 2-736. pwm_bri367 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L10-CS3							
R/W-0h							

Table 2-749. pwm_bri367 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L10-CS3	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L10-CS3

2.13.369 pwm_bri368 Register (Address = 370h) [Default = 0h]

pwm_bri368 is shown in [Figure 2-737](#) and described in [Table 2-750](#).

Return to the [Summary Table](#).

Figure 2-737. pwm_bri368 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L10-CS4							
R/W-0h							

Table 2-750. pwm_bri368 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L10-CS4	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L10-CS4

2.13.370 pwm_bri369 Register (Address = 371h) [Default = 0h]

pwm_bri369 is shown in [Figure 2-738](#) and described in [Table 2-751](#).

Return to the [Summary Table](#).

Figure 2-738. pwm_bri369 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L10-CS4							
R/W-0h							

Table 2-751. pwm_bri369 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L10-CS4	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L10-CS4

2.13.371 pwm_bri370 Register (Address = 372h) [Default = 0h]

pwm_bri370 is shown in [Figure 2-739](#) and described in [Table 2-752](#).

Return to the [Summary Table](#).

Figure 2-739. pwm_bri370 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L10-CS5							
R/W-0h							

Table 2-752. pwm_bri370 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0__for_Dot_L10-CS5	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L10-CS5

2.13.372 pwm_bri371 Register (Address = 373h) [Default = 0h]

pwm_bri371 is shown in [Figure 2-740](#) and described in [Table 2-753](#).

Return to the [Summary Table](#).

Figure 2-740. pwm_bri371 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8__for_Dot_L10-CS5							
R/W-0h							

Table 2-753. pwm_bri371 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L10-CS5	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L10-CS5

2.13.373 pwm_bri372 Register (Address = 374h) [Default = 0h]

pwm_bri372 is shown in [Figure 2-741](#) and described in [Table 2-754](#).

Return to the [Summary Table](#).

Figure 2-741. pwm_bri372 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L10-CS6							
R/W-0h							

Table 2-754. pwm_bri372 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits__ 7:0_for_Dot_L10-CS6	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L10-CS6

2.13.374 pwm_bri373 Register (Address = 375h) [Default = 0h]

pwm_bri373 is shown in [Figure 2-742](#) and described in [Table 2-755](#).

Return to the [Summary Table](#).

Figure 2-742. pwm_bri373 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L10-CS6							
R/W-0h							

Table 2-755. pwm_bri373 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits__ _15:8_for_Dot_L10-CS6	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L10-CS6

2.13.375 pwm_bri374 Register (Address = 376h) [Default = 0h]

pwm_bri374 is shown in [Figure 2-743](#) and described in [Table 2-756](#).

Return to the [Summary Table](#).

Figure 2-743. pwm_bri374 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L10-CS7							
R/W-0h							

Table 2-756. pwm_bri374 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits__ 7:0_for_Dot_L10-CS7	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L10-CS7

2.13.376 pwm_bri375 Register (Address = 377h) [Default = 0h]

pwm_bri375 is shown in [Figure 2-744](#) and described in [Table 2-757](#).

Return to the [Summary Table](#).

Figure 2-744. pwm_bri375 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L10-CS7							
R/W-0h							

Table 2-757. pwm_bri375 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L10-CS7	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L10-CS7

2.13.377 pwm_bri376 Register (Address = 378h) [Default = 0h]

pwm_bri376 is shown in [Figure 2-745](#) and described in [Table 2-758](#).

Return to the [Summary Table](#).

Figure 2-745. pwm_bri376 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L10-CS8							
R/W-0h							

Table 2-758. pwm_bri376 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits__ 7:0__for_Dot_L10-CS8	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L10-CS8

2.13.378 pwm_bri377 Register (Address = 379h) [Default = 0h]

pwm_bri377 is shown in [Figure 2-746](#) and described in [Table 2-759](#).

Return to the [Summary Table](#).

Figure 2-746. pwm_bri377 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8__for_Dot_L10-CS8							
R/W-0h							

Table 2-759. pwm_bri377 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L10-CS8	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L10-CS8

2.13.379 pwm_bri378 Register (Address = 37Ah) [Default = 0h]

pwm_bri378 is shown in [Figure 2-747](#) and described in [Table 2-760](#).

Return to the [Summary Table](#).

Figure 2-747. pwm_bri378 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L10-CS9							
R/W-0h							

Table 2-760. pwm_bri378 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L10-CS9	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L10-CS9

2.13.380 pwm_bri379 Register (Address = 37Bh) [Default = 0h]

pwm_bri379 is shown in [Figure 2-748](#) and described in [Table 2-761](#).

Return to the [Summary Table](#).

Figure 2-748. pwm_bri379 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L10-CS9							
R/W-0h							

Table 2-761. pwm_bri379 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8_for_Dot_L10-CS9	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L10-CS9

2.13.381 pwm_bri380 Register (Address = 37Ch) [Default = 0h]

pwm_bri380 is shown in [Figure 2-749](#) and described in [Table 2-762](#).

Return to the [Summary Table](#).

Figure 2-749. pwm_bri380 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0_for_Dot_L10-CS10							
R/W-0h							

Table 2-762. pwm_bri380 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0_for_Dot_L10-CS10	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L10-CS10

2.13.382 pwm_bri381 Register (Address = 37Dh) [Default = 0h]

pwm_bri381 is shown in [Figure 2-750](#) and described in [Table 2-763](#).

Return to the [Summary Table](#).

Figure 2-750. pwm_bri381 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8_for_Dot_L10-CS10							
R/W-0h							

Table 2-763. pwm_bri381 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L10- CS10	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L10-CS10

2.13.383 pwm_bri382 Register (Address = 37Eh) [Default = 0h]

pwm_bri382 is shown in [Figure 2-751](#) and described in [Table 2-764](#).

Return to the [Summary Table](#).

Figure 2-751. pwm_bri382 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L10-CS11							
R/W-0h							

Table 2-764. pwm_bri382 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0__for_Dot_L10-CS11	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L10-CS11

2.13.384 pwm_bri383 Register (Address = 37Fh) [Default = 0h]

pwm_bri383 is shown in [Figure 2-752](#) and described in [Table 2-765](#).

Return to the [Summary Table](#).

Figure 2-752. pwm_bri383 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8__for_Dot_L10-CS11							
R/W-0h							

Table 2-765. pwm_bri383 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L10-CS11	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L10-CS11

2.13.385 pwm_bri384 Register (Address = 380h) [Default = 0h]

pwm_bri384 is shown in [Figure 2-753](#) and described in [Table 2-766](#).

Return to the [Summary Table](#).

Figure 2-753. pwm_bri384 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L10-CS12							
R/W-0h							

Table 2-766. pwm_bri384 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits__ 7:0__for_Dot_L10-CS12	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L10-CS12

2.13.386 pwm_bri385 Register (Address = 381h) [Default = 0h]

pwm_bri385 is shown in [Figure 2-754](#) and described in [Table 2-767](#).

Return to the [Summary Table](#).

Figure 2-754. pwm_bri385 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8__for_Dot_L10-CS12							
R/W-0h							

Table 2-767. pwm_bri385 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits__ 15:8__for_Dot_L10- CS12	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L10-CS12

2.13.387 pwm_bri386 Register (Address = 382h) [Default = 0h]

pwm_bri386 is shown in [Figure 2-755](#) and described in [Table 2-768](#).

Return to the [Summary Table](#).

Figure 2-755. pwm_bri386 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L10-CS13							
R/W-0h							

Table 2-768. pwm_bri386 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits__ 7:0__for_Dot_L10-CS13	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L10-CS13

2.13.388 pwm_bri387 Register (Address = 383h) [Default = 0h]

pwm_bri387 is shown in [Figure 2-756](#) and described in [Table 2-769](#).

Return to the [Summary Table](#).

Figure 2-756. pwm_bri387 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8__for_Dot_L10-CS13							
R/W-0h							

Table 2-769. pwm_bri387 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16-bits_PWM_higher_8_bits_15:8_for_Dot_L10-CS13	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L10-CS13

2.13.389 pwm_bri388 Register (Address = 384h) [Default = 0h]

pwm_bri388 is shown in [Figure 2-757](#) and described in [Table 2-770](#).

Return to the [Summary Table](#).

Figure 2-757. pwm_bri388 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits_7:0_for_Dot_L10-CS14							
R/W-0h							

Table 2-770. pwm_bri388 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16-bits_PWM_lower_8_bits_7:0_for_Dot_L10-CS14	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L10-CS14

2.13.390 pwm_bri389 Register (Address = 385h) [Default = 0h]

pwm_bri389 is shown in [Figure 2-758](#) and described in [Table 2-771](#).

Return to the [Summary Table](#).

Figure 2-758. pwm_bri389 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits_15:8_for_Dot_L10-CS14							
R/W-0h							

Table 2-771. pwm_bri389 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16-bits_PWM_higher_8_bits_15:8_for_Dot_L10-CS14	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L10-CS14

2.13.391 pwm_bri390 Register (Address = 386h) [Default = 0h]

pwm_bri390 is shown in [Figure 2-759](#) and described in [Table 2-772](#).

Return to the [Summary Table](#).

Figure 2-759. pwm_bri390 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits_7:0_for_Dot_L10-CS15							
R/W-0h							

Table 2-772. pwm_bri390 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits__ 7:0__for_Dot_L10-CS15	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L10-CS15

2.13.392 pwm_bri391 Register (Address = 387h) [Default = 0h]

pwm_bri391 is shown in [Figure 2-760](#) and described in [Table 2-773](#).

Return to the [Summary Table](#).

Figure 2-760. pwm_bri391 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8__for_Dot_L10-CS15							
R/W-0h							

Table 2-773. pwm_bri391 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits__ 15:8__for_Dot_L10- CS15	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L10-CS15

2.13.393 pwm_bri392 Register (Address = 388h) [Default = 0h]

pwm_bri392 is shown in [Figure 2-761](#) and described in [Table 2-774](#).

Return to the [Summary Table](#).

Figure 2-761. pwm_bri392 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L10-CS16							
R/W-0h							

Table 2-774. pwm_bri392 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits__ 7:0__for_Dot_L10-CS16	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L10-CS16

2.13.394 pwm_bri393 Register (Address = 389h) [Default = 0h]

pwm_bri393 is shown in [Figure 2-762](#) and described in [Table 2-775](#).

Return to the [Summary Table](#).

Figure 2-762. pwm_bri393 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8__for_Dot_L10-CS16							
R/W-0h							

Table 2-775. pwm_bri393 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L10- CS16	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L10-CS16

2.13.395 pwm_bri394 Register (Address = 38Ah) [Default = 0h]

pwm_bri394 is shown in [Figure 2-763](#) and described in [Table 2-776](#).

Return to the [Summary Table](#).

Figure 2-763. pwm_bri394 Register

7	6	5	4	3	2	1	0
16-bits_PWM_lower_8_bits__7:0__for_Dot_L10-CS17							
R/W-0h							

Table 2-776. pwm_bri394 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_lower_8_bits_ 7:0__for_Dot_L10-CS17	R/W	0h	16-bits PWM lower 8 bits [7:0] for Dot L10-CS17

2.13.396 pwm_bri395 Register (Address = 38Bh) [Default = 0h]

pwm_bri395 is shown in [Figure 2-764](#) and described in [Table 2-777](#).

Return to the [Summary Table](#).

Figure 2-764. pwm_bri395 Register

7	6	5	4	3	2	1	0
16-bits_PWM_higher_8_bits__15:8__for_Dot_L10-CS17							
R/W-0h							

Table 2-777. pwm_bri395 Register Field Descriptions

Bit	Field	Type	Default	Description
7-0	16- bits_PWM_higher_8_bits_ _15:8__for_Dot_L10- CS17	R/W	0h	16-bits PWM higher 8 bits [15:8] for Dot L10-CS17

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

DATE	REVISION	NOTES
May 2021	*	Initial Release

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