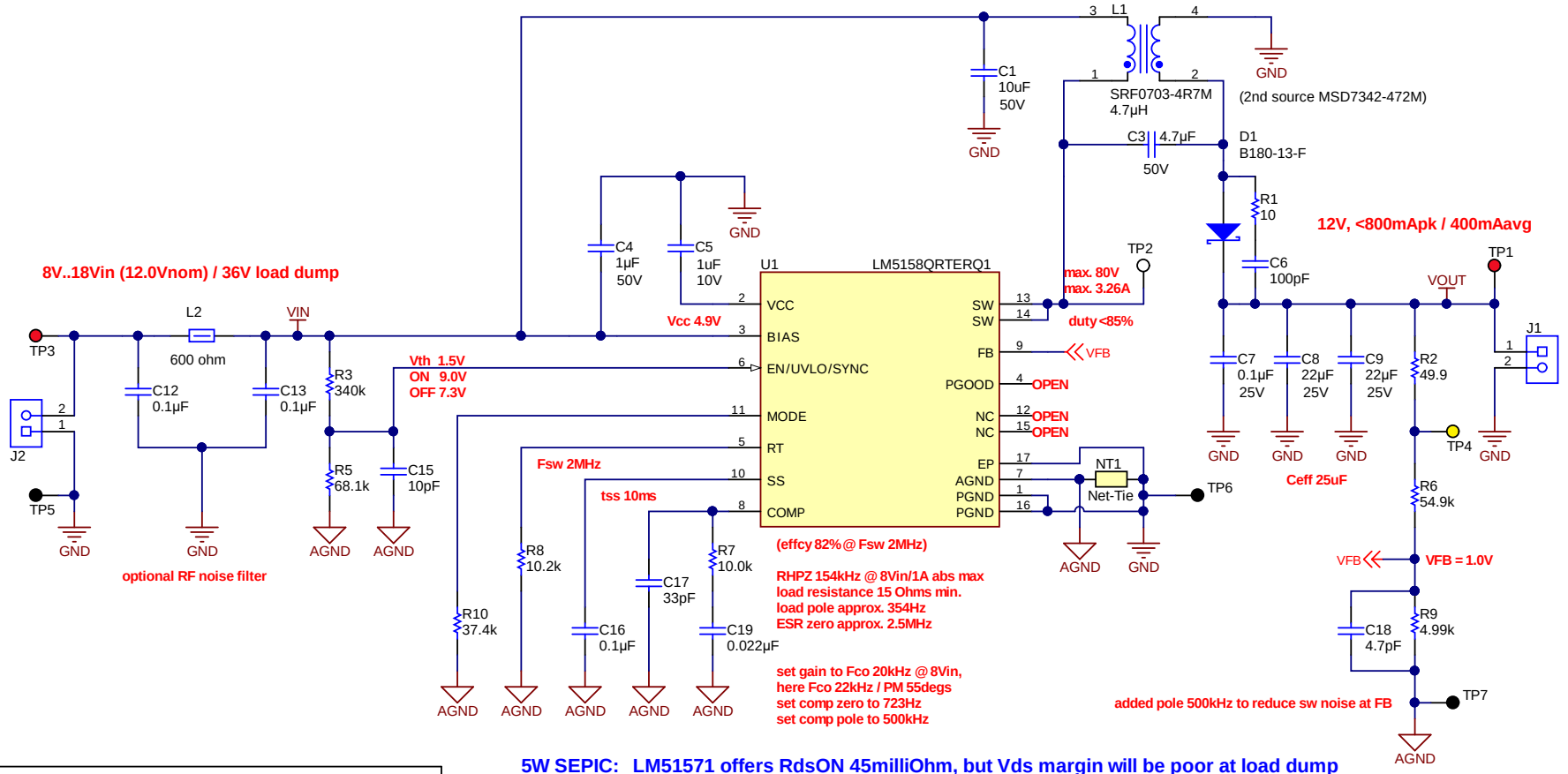

5W SEPIC: LM51571 offers RdsON 45milliOhm, but Vds margin will be poor at load dump

dynamic load specifications, loop designed for maximum bandwidth

Design Notes:

- R2 for test purpose only

Revision History

Revision	Notes
A	PRELIMINARY, designed for small size
A1	LAYOUT, removed R14 and C14 EP to GND, R5 and C15 to AGND L1: Pin 1 to switchnode
A2	added R10 37k4 in between MODE and AGND tuned loop (reduced gain by -6dB)

Number: PMP31139	Rev: A2	Designed for: Public Release	Mod. Date: 2/11/2022
SVN Rev: Version control disabled		Project Title: Tiny Automotive SEPIC using 80V Integrated FET	
Drawn By:		Sheet Title: PMP31139_RevA1	
Engineer: B.Geck		Assembly Variant: [No Variations]	Sheet: 1 of 1
		File: PMP31139RevA2_Schematic.SchDoc	Size: A4
		Contact: http://www.ti.com/support	

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