



T1, custom transformer, t.b.d.  
 windings ratio PRI : Aux : SEC = 2 : 3 : 6  
 sandwich Aux/SEC layers in between PRI  
 primary inductance Lpri < 2uH, 1.8uH +/- 10%  
 leakage inductance < 20nH, target xxnH (?)  
 isolation voltage 3000Vac

Input 8Vdc..16Vdc, cranking 5V / load dump 42V

optional bulk capacitor  
 >125c use ZE series

2.46Ams

max. duty cycle 60%

7.42Apk  
 3.30Ams

keep this BIAS current around 1mA

HV HS FETs 1, max. 75mA

HV HS FETs 2, max. 75mA

HV LS FETs, max. 150mA

LV FETs, max. 200mA

Revision History	
Revision	Notes
A	preliminary
B	replaced RCD snubber by TVS, tuned loop

- NOTES:
- 1) R18 for test purposes only
  - 2) stress analysis for min. input 5V

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Orderable:	Designed for Public Release	Mod. Date: 2/4/2021
TID #:	Project Title: Isolated 10W PSR DCM Flyback	
Number: PMP30951	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant [No Variations]	Sheet 1 of 1
Drawn By:	File: PMP30951RevB_schematic.SchDoc	Size: A3
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