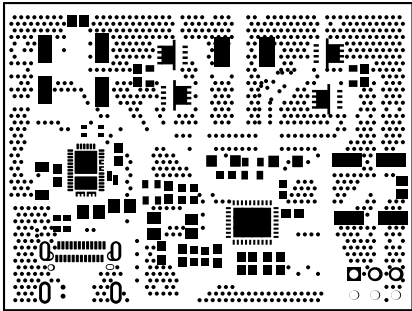
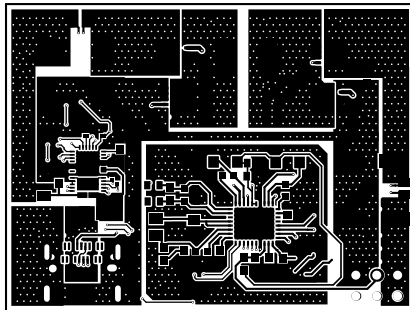


100001

ALL DIMENSIONS UNLESS NOTED OTHERWISE	DATE: 10/10/00	REV: 10	BY: 10/10/00	CHK: 10/10/00
UNLESS NOTED OTHERWISE				
ALL DIMENSIONS UNLESS NOTED OTHERWISE	DESIGNED BY: 10/10/00	DATE: 10/10/00	TSMC 0.18UM 1P6	



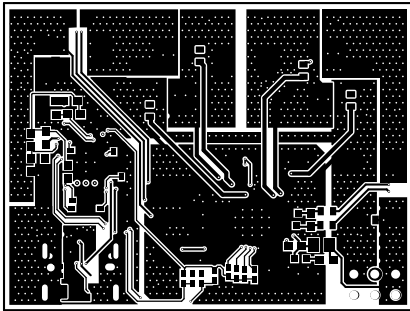
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DRAWN BY: [Name]			
CHECKED BY: [Name]	APPROVED BY: [Name]	DATE: 10/10/2020	



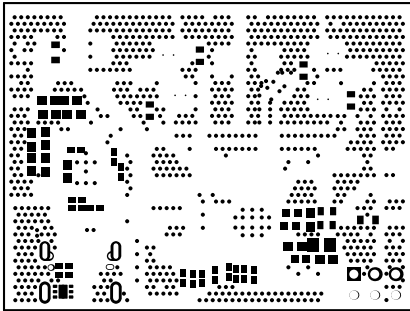
Layer	Name	Material	Thickness	Constant	Board Layer Stack
	Top Overlay				
	Top Solder	Solder Resist	0.010mm	3.5	
1	Top Layer		0.036mm		
	Dielectric1	FR-4 High Tg	0.945mm	4.8	
2	Bottom Layer		0.036mm		
	Bottom Solder	Solder Resist	0.010mm	3.5	
	Bottom Overlay				

Total board thickness: 1.036mm

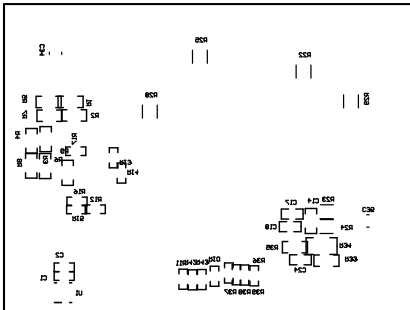
ALL DIMENSIONS UNLESS OTHERWISE SPECIFIED	DATE: 10/20/2020	REV: 1.0	DR: [Name]	NOT TO SCALE
LAYER NAME: Top Layer				
LAYER NAME: Top Layer	DESIGNED BY: [Name]	DATE: [Date]	TOLERANCES	



ALL DIMENSIONS UNLESS OTHERWISE SPECIFIED	BOARD No. PP-0200	REV. 12	SUN TECH. INC. IN WASHINGTON, D.C.
LAYER ONE - Bottom Layer			
LAYER TWO - Bottom Layer	SHEET NO. 1-0-001	SHEET OF	TOTAL SHEETS



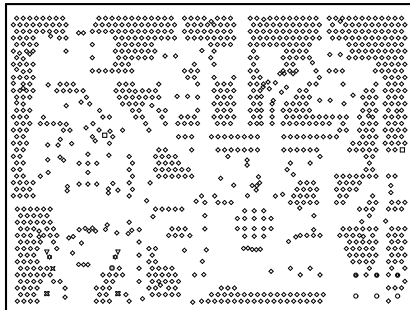
ALL INFORMATION CONTAINED HEREIN IS UNCLASSIFIED	DATE 10/10/2013	BY 60322	FOR 10/10/2013	REASON FOR DECLASSIFICATION
CLASSIFICATION				
AUTHORITY	50 USC 1352	1352 (a)	1352 (a)	1352 (a)



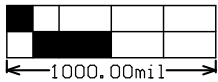
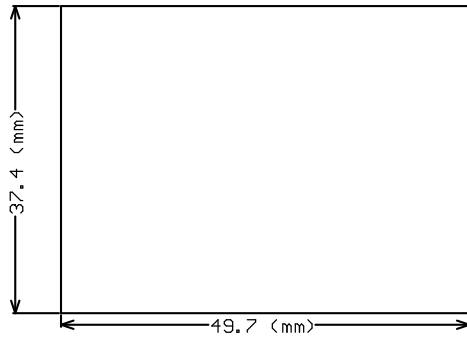
ALL INFORMATION CONTAINED HEREIN IS UNCLASSIFIED	DATE 10-10-2001	BY 60321	FOR 60321	REASON FOR DECLASSIFICATION
CLASSIFICATION				
AUTHORITY	50 USC 1352	50 CFR 17.102	E.O. 13526	OTHER APPLICABLE LAWS

Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
◇	1130	0.200mm (7.87mil)	PTH	Round	Top Layer - Bottom Layer	
□	2	0.203mm (8.00mil)	PTH	Round	Top Layer - Bottom Layer	
⊕	1	0.500mm (19.69mil)	NPTH	Slot	Top Layer - Bottom Layer	+/-0.030mm
⊗	1	0.600mm (23.62mil)	NPTH	Round	Top Layer - Bottom Layer	+/-0.030mm
▽	2	0.600mm (23.62mil)	PTH	Slot	Top Layer - Bottom Layer	+/-0.050mm
☆	2	0.700mm (27.56mil)	PTH	Slot	Top Layer - Bottom Layer	+/-0.050mm
⊗	2	0.700mm (27.56mil)	PTH	Slot	Top Layer - Bottom Layer	+/-0.050mm
○	3	1.100mm (43.31mil)	NPTH	Round	Top Layer - Bottom Layer	
⊙	3	1.100mm (43.31mil)	PTH	Round	Top Layer - Bottom Layer	
	1146 Total					

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout



ALL HATCH COLORED FROM TOP SIDE	DRILL SIZE	PP-0200	DRILL	02	DRILL SIZE	Not to machine control
LAYER NAME *	DRILL Drilling					
LAYER NAME *	DRILL Drilling	DRILLING *	←-DRILL	DRILL IN	TOOL DRILLING	



ALL METERS USED FOR THIS SIZE	SHEET NO. 000000	REV. 02	SHEET NO. Not to be used for control
LAYER NAME - 00 Board Dimension			
SHEET NAME - Board Dimension	SHEET NO. 000000	SHEET NO. 000000	SHEET NO. 000000

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