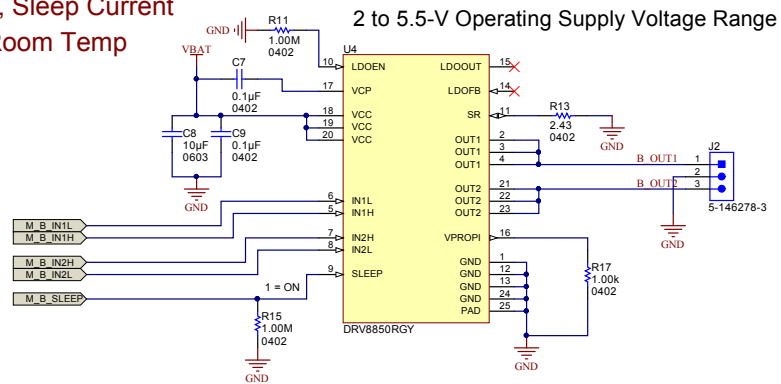
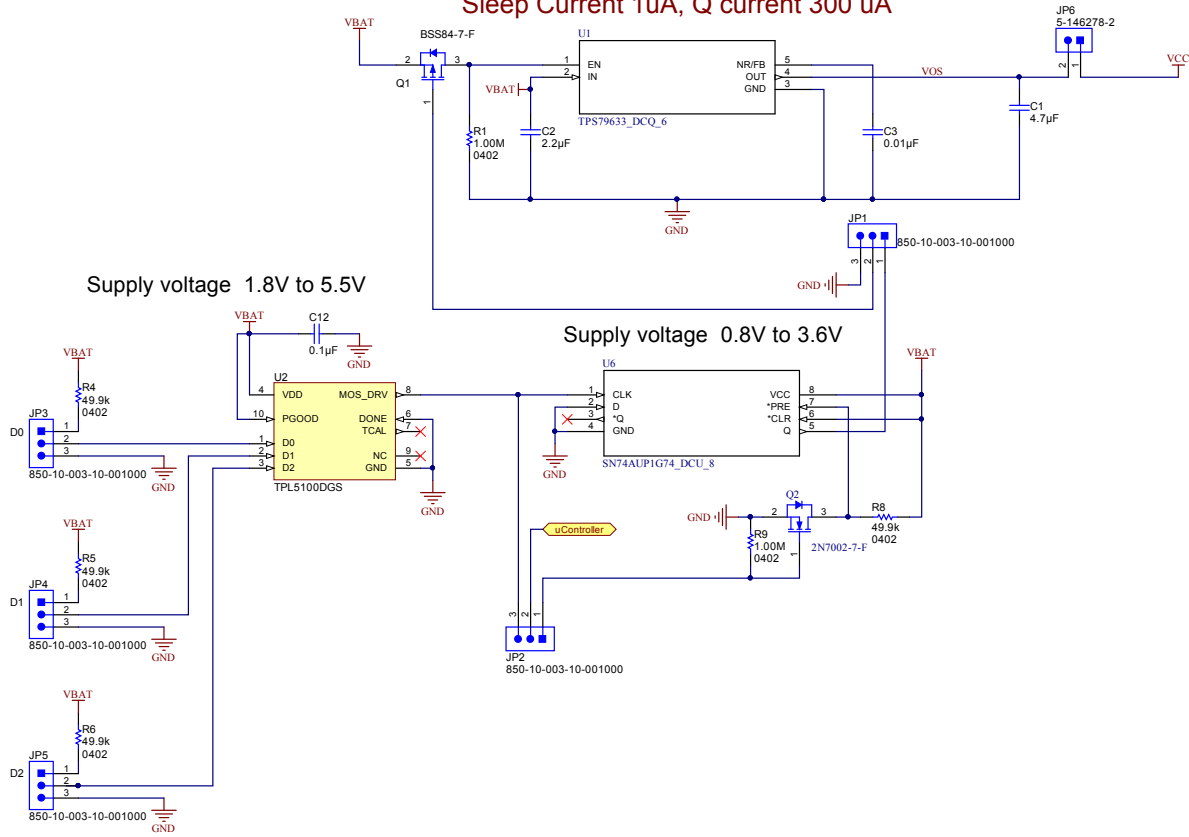


Quiescent Current With Motor Driver off  
and LDO off, Sleep Current  
< 1.0uA at Room Temp



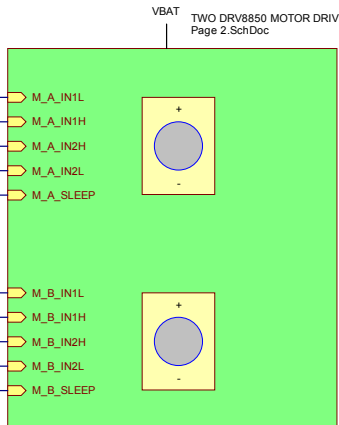
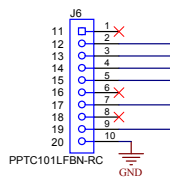
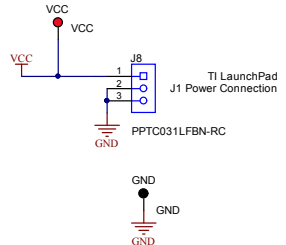
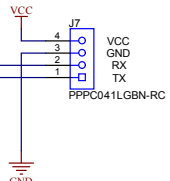
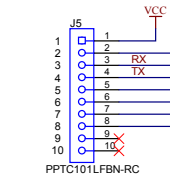
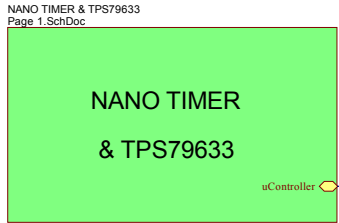
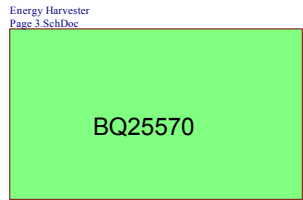
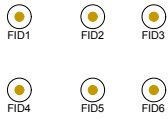
Input Voltage Range 2.7 to 5.5V  
 Sleep Current 1uA, Q current 300 uA



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Orderable: N/A	Designed for: Public Release	Mod. Date: 7/23/2015
TID #: TIDA-00616	Project Title: IoT Energy Harvesting BoosterPack	
Number: UTDBoosterPack	Rev: Rev2	Sheet Title:
SNV: Rev. Not in version control	Assembly Variant: [No Variations]	Sheet: 2 of 3
Drawn By:	File: Page 1 SchDoc	Size: B
Engineer: Ahmad Rashed	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	





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Orderable: N/A	Designed for: Public Release	Mod. Date: 7/23/2015	
TID #: TIDA-00616	Project Title: IoT Energy Harvesting BoosterPack		
Number: UTDBoosterPackRev: Rev2	Sheet Title:		
Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 2 of 3	
Drawn By:	File: Block Diagram_SchDoc	Size: B	http://www.ti.com
Engineer: Ahmad Rashed	Contact: http://www.ti.com/support		© Texas Instruments 2014

Layer	Name	Material	Thickness	Constant	Board Layer Stack
1	Top Overlay				
2	Top Solder	Solder Resist	0.40mil	3,5	
3	Top Layer	Copper	1.40mil		
4	Dielectric1	FR-4	59.20mil	4,8	
5	Bottom Layer	Copper	1.40mil		
6	Bottom Solder	Solder Resist	0.40mil	3,5	
7	Bottom Overlay				

**DESIGN INFORMATION**

MIN. TRACK WIDTH: 8 MIL  
 MIN. CLEARANCE: 0.2 mm  
 MIN. VIA PAD SIZE: 24 MIL  
 MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL  
 PER IPC-D-275 CLASS 2 LEVEL C  
 REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL

**MATERIAL:**  
 FR-408  FR-4 High Tg  OTHER \_\_\_\_\_  
 THICKNESS:  62 MIL (1.6mm) +/-10%  OTHER \_\_\_\_\_  
 TOLERANCE:  ANSI IPC-6012 TYPE 3 CLASS 2  
 OTHER +/- \_\_\_\_\_  
 BOW & TWIST:  ANSI IPC-6012 TYPE 3 CLASS 2  
 OTHER +/- \_\_\_\_\_

**DRILLING:**  
 REFERENCE:  AS SHOWN  NC\_DRILL FILES  
 PTH MIN COPPER THICKNESS:  1MIL  OTHER \_\_\_\_\_

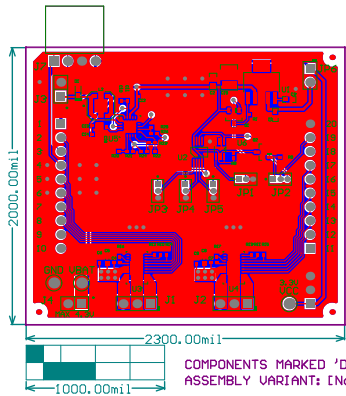
**BOARD FINISH:**  
 SILKSCREEN:  TOP  BOTTOM  
 SILKSCREEN COLOR:  WHITE  OTHER \_\_\_\_\_  
 SOLDER RESIST COLOR:  
 GREEN  BLUE  OTHER \_\_\_\_\_

**SURFACE FINISH:**  IMMERSION GOLD (ENG)  ENEPIG  
 IMM. TIN/SILVER OR EQUIV  OTHER \_\_\_\_\_

**ARRAY/PANEL:**  CUT AND TRIM PER MECH LAYER 1  
 N.C. ROUTE  V. SCORE

**CERTIFICATION:** MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF:  
 ANSI IPC-A-600F CLASS ->  1  2  3  
 UL 94V-0  RoHS  OTHER PER ORDER

**ADDITIONAL REQUIREMENTS:**  
 MICROSECTION:  YES  
 BARE BOARD ELEC. TEST:  NONE  REQUIRED  PER ORDER  
 MANUFACTURER'S UL:  RAIL  METAL  SILK



COMPONENTS MARKED 'DNP' SHOULD NOT BE ASSUMED TO BE PRESENT IN THE FINAL ASSEMBLY. COMPONENTS MARKED 'DNP' SHOULD NOT BE ASSUMED TO BE PRESENT IN THE FINAL ASSEMBLY.  
 ASSEMBLY VARIANT: [No Variations]

ADDITIONAL COMMENTS: TOP/BOTTOM	REVISED: 8/22/2016	REV: 2B	DATE: 8/22/2016	DESIGNED BY: AHMAD RASHED	DESIGNED FOR: IoT Energy Harvesting BoosterPack	FILE NAME: TID_PcbDoc
LAYER NAME = TOP	COMPONENTS: 38	TRACES: 10	DRILLS: 0	TECH: 1.0	SCALE: 1.00	LAYOUT BY: Ahmad Rashed
PLATTING: 10/20/16	DATE: 10/20/16	TIME: 10:20:35	USER: ahmad	COMPANY: TEXAS INSTRUMENTS	SCALE: 1.00	ALTIUM DESIGNER VERSION: 14.3.9.33548

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ENGINEER: Ahmad Rashed	LAYOUT BY: Ahmad Rashed
SCALE: 1.00	ALTIUM DESIGNER VERSION: 14.3.9.33548

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