

1 2 3 4 5 6

A

A

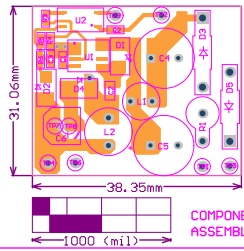
B

B

C

C

The reference design PMP30123 Rev_B has been built on PMP8509 Rev_A Board



COMPONENTS MARKED 'DNP' SHOULD NOT BE POPULATED, AND MAY NOT BE IN BILL OF MATERIALS
ASSEMBLY VARIANT: [No Variations]

TABLE 1

Variant	Label text
001	ChangeMe!
002	ChangeMe!



PROJECT TITLE: .PRJ_Title	
DESIGNED FOR: .PRJ_Customer	
FILE NAME: PMP8509_Rev_A.PcbDoc	
ENGINEER: .PRJ_Engineer	LAYOUT BY: .PCB_Layout
SCALE: 0.72	ALTIUM DESIGNER VERSION: 16.0.9.368

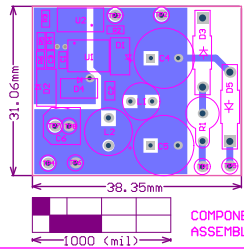
PCB VIEWED FROM TOP SIDE	BOARD #: .PRJ_Number	REV: .PCB_Rev	SUN REV:	Texas Instruments (TI) and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. TI and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. TI and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.
PLOT NAME = Top Layer	GENERATED : 10/7/2016	2:08:00 PM	TEXAS INSTRUMENTS	

1 2 3 4 5 6

D

D

The reference design PMP30123 Rev_B has been built on PMP8509 Rev_A Board



COMPONENTS MARKED 'DNP' SHOULD NOT BE POPULATED, AND MAY NOT BE IN BILL OF MATERIALS
ASSEMBLY VARIANT: [No Variations]

TABLE 1	
Variant	Label text
001	ChangeMe!
002	ChangeMe!

DESIGN INFORMATION

BOARD SIZE (REFER ALSO ARRAY/PANEL PROFILING INFORMATION)
3450MIL X 4950MIL

Number of Layers : 2
 MIN. TRACK WIDTH: 8 MIL
 MIN. CLEARANCE: 8 MIL
 MIN. VIA PAD SIZE: 24 MIL

MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL
 PER IPC-D-275 CLASS 2 LEVEL C
 REGISTRATION TOLERANCES: METAL +/- .5 MIL, HOLES +/- .3 MIL

MATERIAL:
 FR-4 FR-4 High Tg OTHER _____
 THICKNESS: 62 MIL (1.6mm) +/-10% OTHER _____
 TOLERANCE: ANSI IPC-6012 TYPE 3 CLASS 2
 OTHER +/- _____
 BOW & TWIST: ANSI IPC-6012 TYPE 3 CLASS 2
 OTHER +/- _____

COPPER THICKNESS (FINISHED):
 OUTER: 1.4MIL (1oz) 2MIL (1.4oz) 2.8MIL (2oz)
 INNER SIGNAL: 1.4MIL (1oz) 2.8MIL (2oz)

DRILLING:
 REFERENCE: AS SHOWN NC_DRILL FILES
 PTH MIN COPPER THICKNESS: 1MIL OTHER _____

BOARD FINISH:
 SILKSCREEN: TOP BOTTOM
 SILKSCREEN COLOR: WHITE OTHER _____
 SOLDER RESIST COLOR:
 GREEN BLUE OTHER _____

SURFACE FINISH: IMMERSION GOLD (ENG) Pb-FREE HASL
 OTHER _____

ARRAY/PANEL: CUT AND TRM PER MECH LAYER 1
 N.C. ROUTE V. SCORE

CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF:
 ANSI IPC-A-600F CLASS -> 1 2 3
 UL 94V-0 RoHS OTHER PER ORDER

ADDITIONAL REQUIREMENTS:
 MICROSECTION: YES
 BARE BOARD ELEC. TEST: NONE REQUIRED PER ORDER
 MANUFACTURER'S ID/LOGO: RAIL METAL SILK



PROJECT TITLE:
 .PRJ_Title

DESIGNED FOR:
 .PRJ_Customer

FILE NAME:
 PMP8509_Rev_A.PcbDoc

PCB VIEWED FROM TOP SIDE	BOARD #: .PRJ_Number	REV: .PCB_Rev	SUN REV:	Texas Instruments (TI) and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. TI and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. TI and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.	ENGINEER: .PRJ_Engineer	LAYOUT BY: .PCB_Layout
PLOT NAME = Bottom Layer	GENERATED : 10/7/2016	2:08:01 PM	TEXAS INSTRUMENTS		SCALE: 0.72	ALTIUM DESIGNER VERSION: 16.0.9.368

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