

User options for analog input:  
 1. S/E input, AC-coupled:  
 a. Default populate option  
 b. Balun is (400MHz, 3GHz)  
 2. Differential DC-coupled:  
 a. Remove C3, C5.  
 b. Populate C1 = C6 = 0 ohm.  
 3. Differential AC-coupled:  
 a. Remove C3, C5.  
 b. Populate C1 = C6 = 100 pF.

Let C1 and C3 share a pad on the common net. Route from VIN\_DIFF+ to VIN\_P net as 50 ohm S/E.

Let C5 and C6 share a pad on the common net. Route from VIN\_DIFF- to VIN\_N net as 50 ohm S/E.

Let C32, C30 and C262 share a pad on the common net. Let C33, C36 and C263 share a pad on the common net.

Let R18 and R19 share a pad on the common net. Let R20 and R21 share a pad on the common net.

Locate VIN\_SE, TMST+, TMST- at edge opposite FMC connector

Priorities for placement:  
 1. Decoupling caps close to IC.  
 2. J\_VA12, J\_VA19, J\_VD12 close to IC.

The IC pad is the only ground connection for this IC. Ensure good connection through multiple vias to the PCB ground planes.

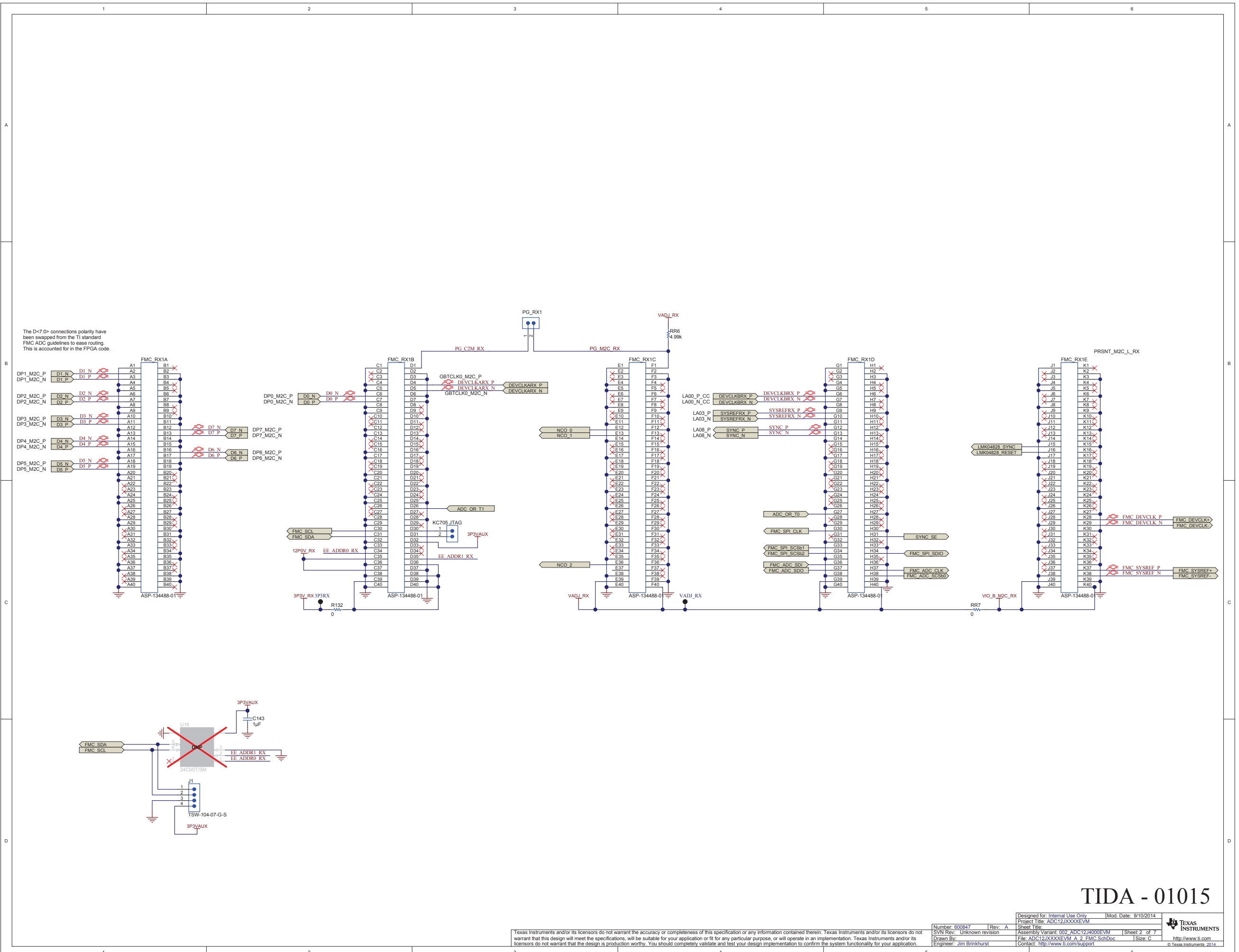
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# TIDA - 01015

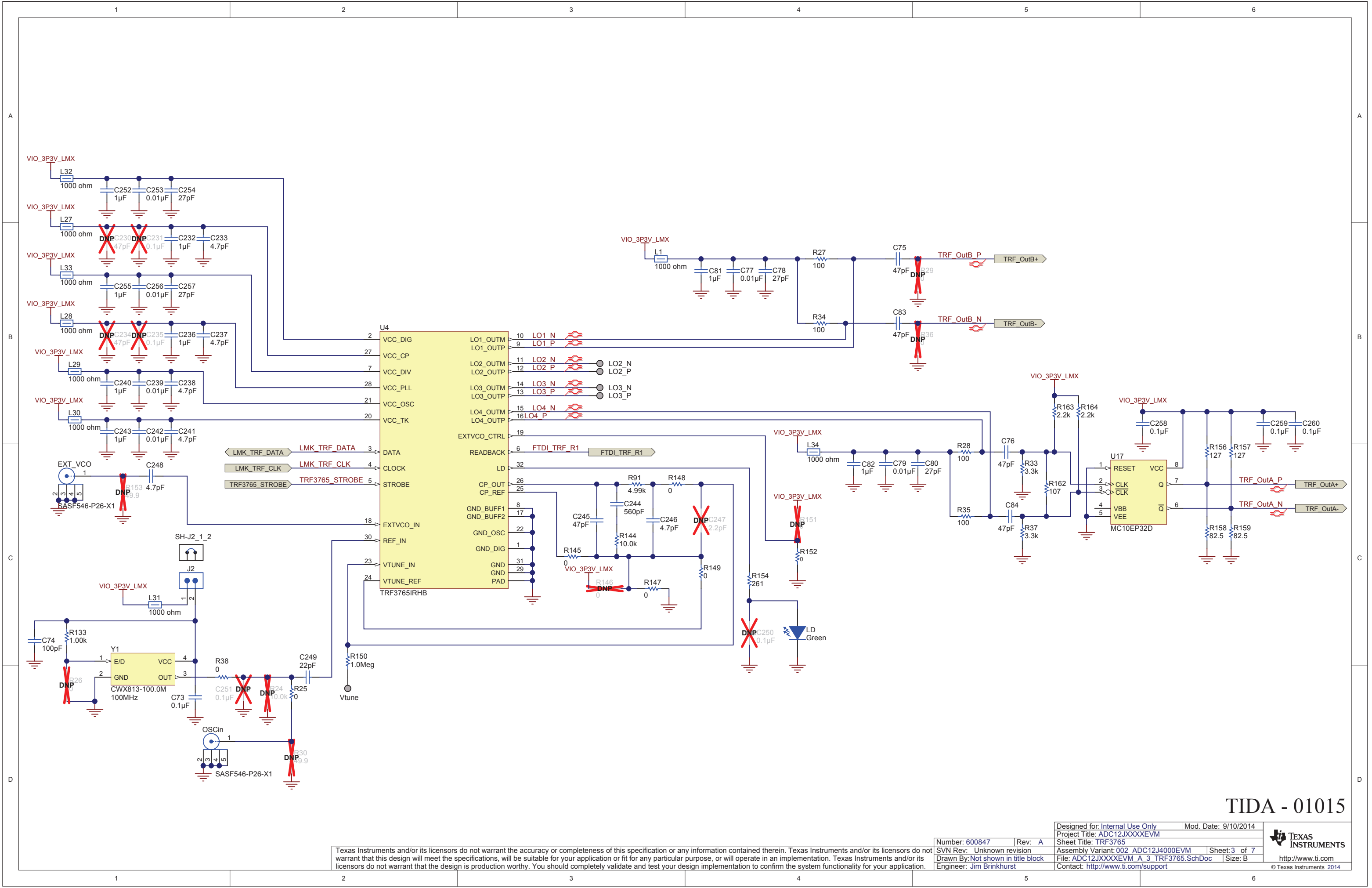
Number: 600847	Rev: A	Mod. Date: 9/10/2014
SVN Rev: Unknown revision	Project Title: ADC12JXXXXEVM	Sheet Title:
Engineer: Jim Brinkhurst	Assembly Variant: 002_ADC12J4000EVM	Sheet: 1 of 7
	File: ADC12JXXXXEVM A_1_ADC_IO.SchDoc	Size: B
	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	



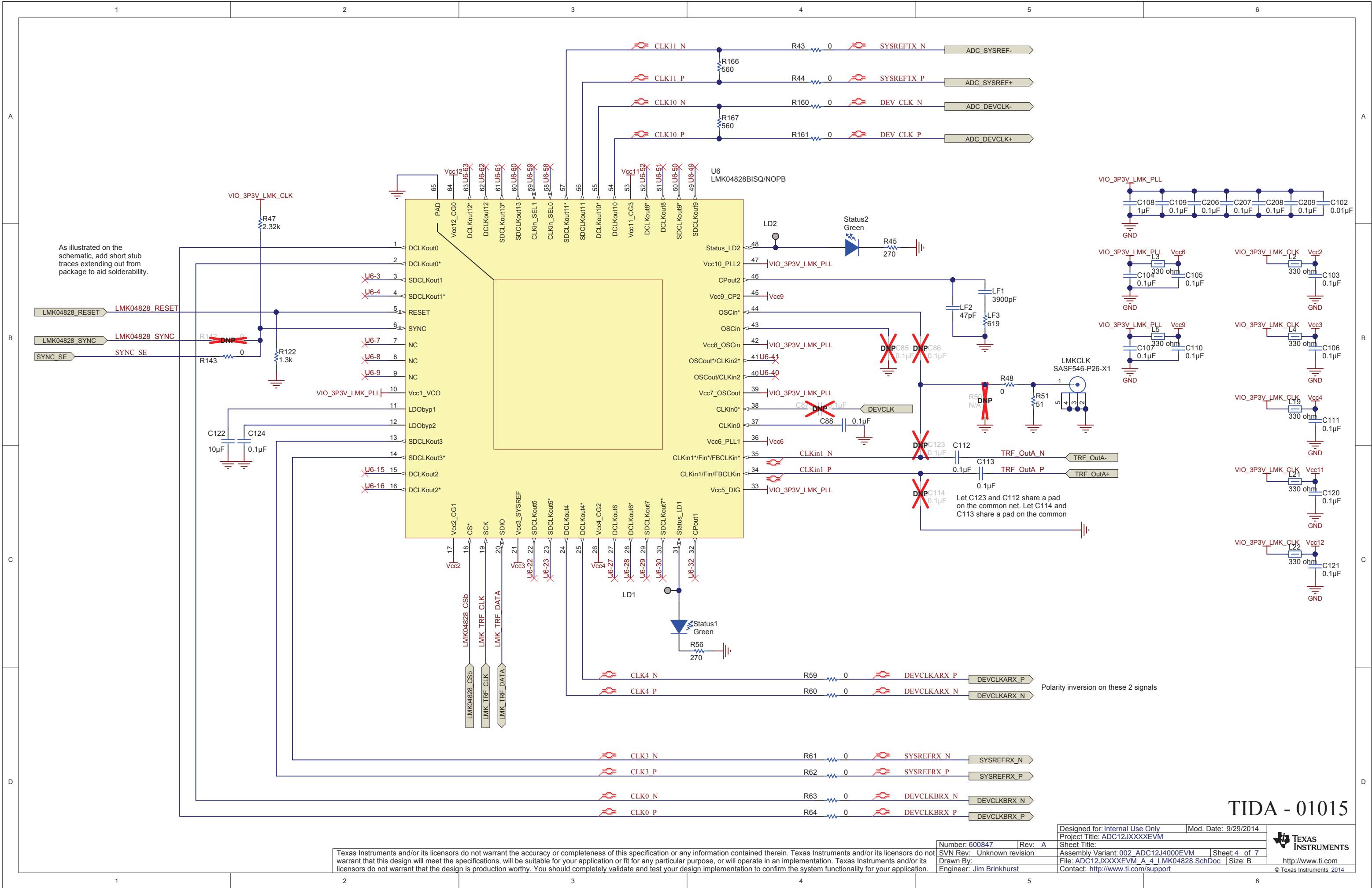
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As illustrated on the schematic, add short stub traces extending out from package to aid solderability.

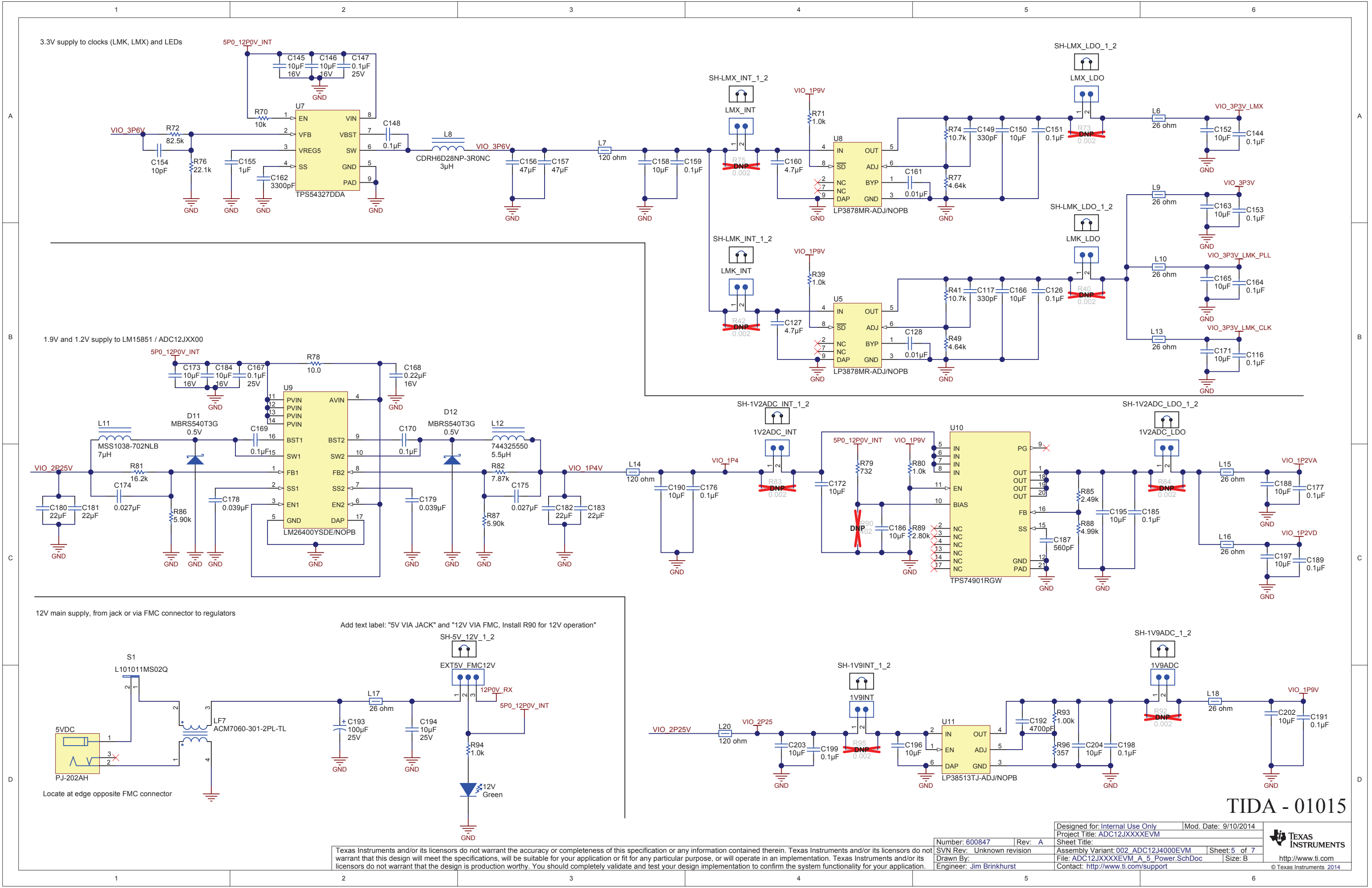
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Number: 600847	Rev: A	Designed for: Internal Use Only	Mod. Date: 9/29/2014
SVN Rev: Unknown revision	Assembly Variant: 002_ADC12J4000EVM	Project Title: ADC12JXXXXEVM	Sheet: 4 of 7
Drawn By:	Engineer: Jim Brinkhurst	File: ADC12JXXXXEVM_A_4_LMK04828_SchDoc	Size: B
		Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	<a href="http://www.ti.com">http://www.ti.com</a>

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3.3V supply to clocks (LMK, LMX) and LEDs

1.9V and 1.2V supply to LM15851 / ADC12JXX00

12V main supply, from jack or via FMC connector to regulators

Add text label: "5V VIA JACK" and "12V VIA FMC, Install R90 for 12V operation"

Locate at edge opposite FMC connector

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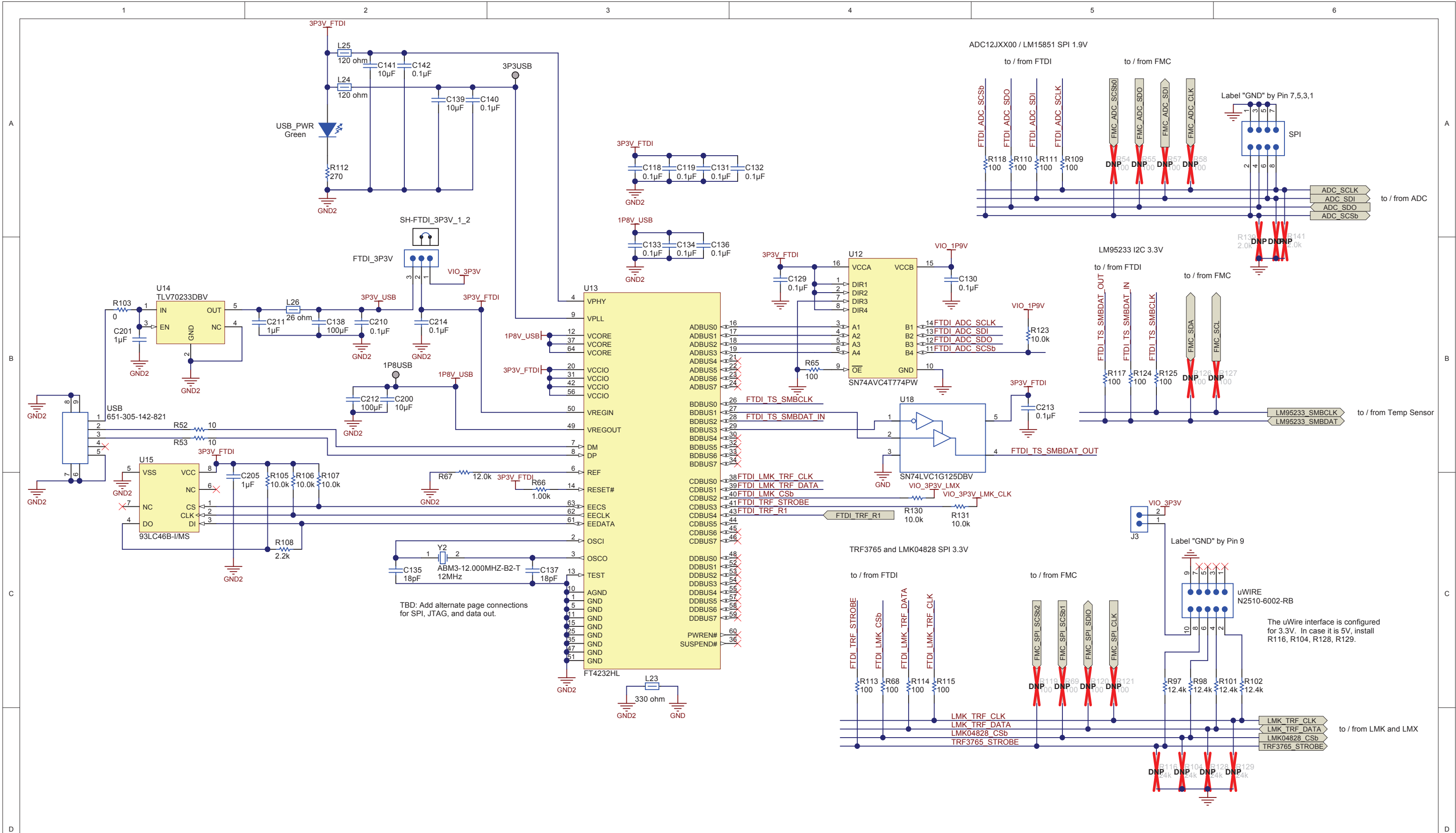
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SVN Rev: Unknown revision	Project Title: ADC12JXXXEVM	Sheet Title:	
Drawn By:	Assembly Variant: 002_ADC12J4000EVM	Sheet: 5 of 7	
Engineer: Jim Brinkhurst	File: ADC12JXXXEVM_A_5_Power.SchDoc	Size: B	
	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>		



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TBD: Add alternate page connections for SPI, JTAG, and data out.

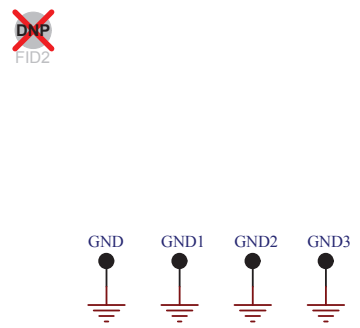
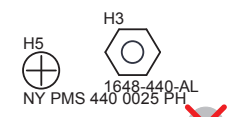
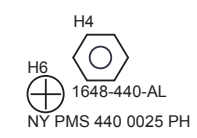
The uWire interface is configured for 3.3V. In case it is 5V, install R116, R104, R128, R129.

# TIDA - 01015

PCB Number: 600847  
PCB Rev: A

PCB  
ESD LOGO  
ESD Susceptible

PCB  
LOGO  
Texas Instruments



Place at least two of the GND test points in the power section.

H9  
MECH  
FMC - FMC Screw  
PMSSS 256 0075 PH

H10  
MECH  
FMC - FMC Nut

H11  
MECH  
FMC - FMC Screw  
PMSSS 256 0075 PH

H12  
MECH  
FMC - FMC Nut

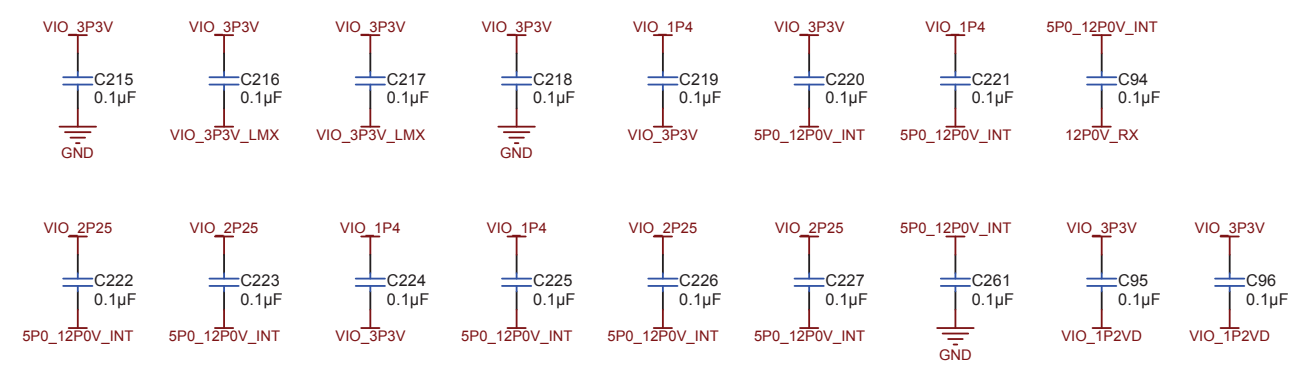
LBL1  
PCB Label  
Size: 0.65" x 0.20"

ZZ1  
Label Assembly Note  
This Assembly Note is for PCB labels only

ZZ2  
Assembly Note  
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3  
Assembly Note  
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4  
Assembly Note  
These assemblies must comply with workmanship standards IPC-A-610 Class 2., unless otherwise specified.



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