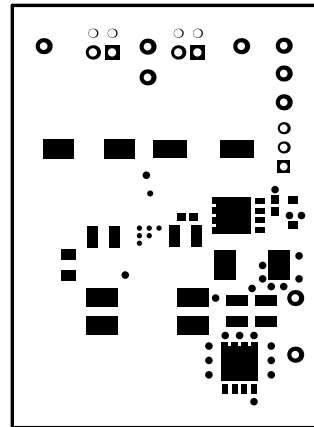


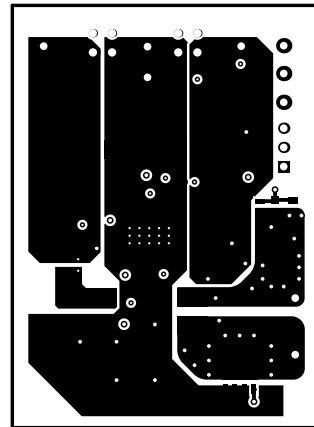
PMP30491RevA built on PCB PMP30232RevA

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP30232	REV: A	SUN REV: Not In VersionControl
LAYER NAME = Top Overlay	TID #: N/A		
PLOT NAME = Top Overlay	GENERATED : 12/4/2018 10:18:36 AM	TEXAS INSTRUMENTS	



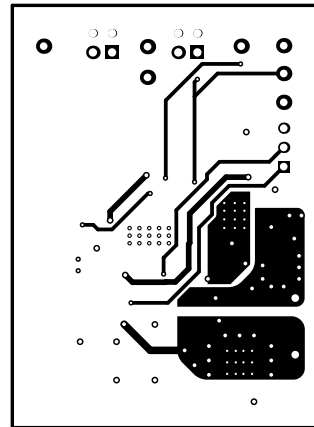
PMP30491RevA built on PCB PMP30232RevA

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP30232	REV: A	SUN REV: Not In VersionControl
LAYER NAME = Top Solder	TID #: N/A		
PLOT NAME = Top Solder Mask	GENERATED : 12/4/2018 10:18:37 AM	TEXAS INSTRUMENTS	



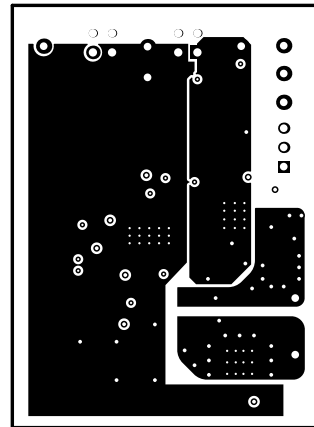
PMP30491RevA built on PCB PMP30232RevA

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP30232	REV: A	SUN REV: Not In VersionControl
LAYER NAME = Top Layer	TID #: N/A		
PLOT NAME = Top Layer	GENERATED : 12/4/2018 10:18:38 AM	TEXAS INSTRUMENTS	



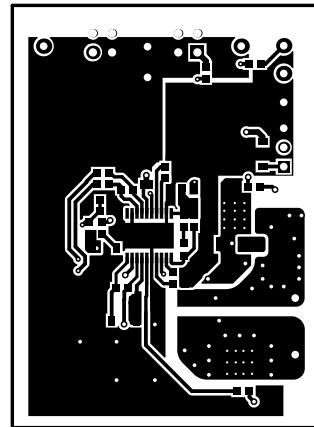
PMP30491RevA built on PCB PMP30232RevA

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP30232	REV: A	SUN REV: Not In VersionControl
LAYER NAME =	TID #: N/A		
PLOT NAME = Signal Layer 1	GENERATED : 12/4/2018 10:18:39 AM	TEXAS INSTRUMENTS	



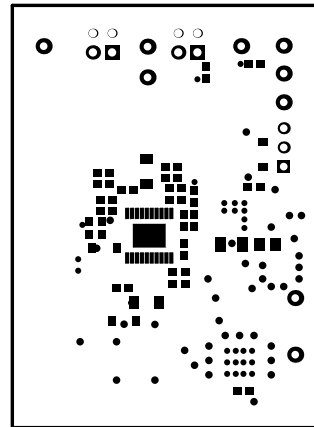
PMP30491RevA built on PCB PMP30232RevA

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP30232	REV: A	SUN REV: Not In VersionControl
LAYER NAME =	TID #: N/A		
PLOT NAME = Signal Layer 2	GENERATED : 12/4/2018 10:18:40 AM	TEXAS INSTRUMENTS	



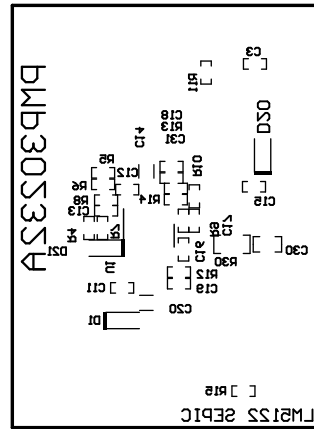
PMP30491RevA built on PCB PMP30232RevA

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP30232	REV: A	SUN REV: Not In VersionControl
LAYER NAME = Bottom Layer	TID #: N/A		
PLOT NAME = Bottom Layer	GENERATED : 12/4/2018 10:18:41 AM	TEXAS INSTRUMENTS	



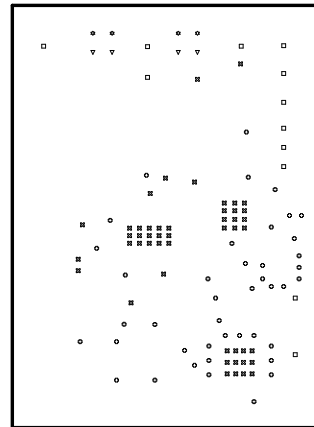
PMP30491RevA built on PCB PMP30232RevA

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP30232	REV: A	SUN REV: Not In VersionControl
LAYER NAME = Bottom Solder	TID #: N/A		
PLOT NAME = Bottom Solder Mask	GENERATED : 12/4/2018 10:18:41 AM	TEXAS INSTRUMENTS	



PMP30491RevA built on PCB PMP30232RevA

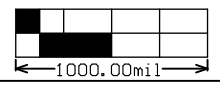
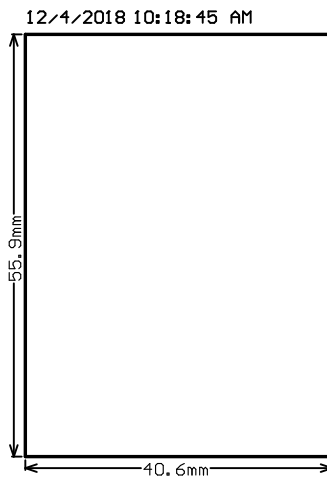
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP30232	REV: A	SUN REV: Not In VersionControl
LAYER NAME = Bottom Overlay	TID #: N/A		
PLOT NAME = Bottom Overlay	GENERATED : 12/4/2018 10:18:42 AM	TEXAS INSTRUMENTS	



Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Drill Layer Pair	Tolerance
⊗	4	43.31mil (1.100mm)	NPTH	Round	Top Layer - Bottom Layer	
⊗	49	11.81mil (0.300mm)	PTH	Round	Top Layer - Bottom Layer	
⊗	42	19.69mil (0.500mm)	PTH	Round	Top Layer - Bottom Layer	
□	12	40.00mil (1.016mm)	PTH	Round	Top Layer - Bottom Layer	
▽	4	43.31mil (1.100mm)	PTH	Round	Top Layer - Bottom Layer	
	111 Total					

PMP30491RevA built on PCB PMP30232RevA

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP30232	REV: A	SUN REV: Not In VersionControl
LAYER NAME = Drill Drawing	TID #: N/A		
PLOT NAME = Drill Drawing	GENERATED : 12/4/2018	10:18:43 AM	TEXAS INSTRUMENTS



PMP30491RevA built on PCB PMP30232RevA

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP30232	REV: A	SUN REV: Not In VersionControl
LAYER NAME = M2 Board Dimensions	TID #: N/A		
PLOT NAME = Board Dimensions	GENERATED : 12/4/2018 10:18:45 AM	TEXAS INSTRUMENTS	

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