

**Test Data
For PMP10572
2/4/2015**

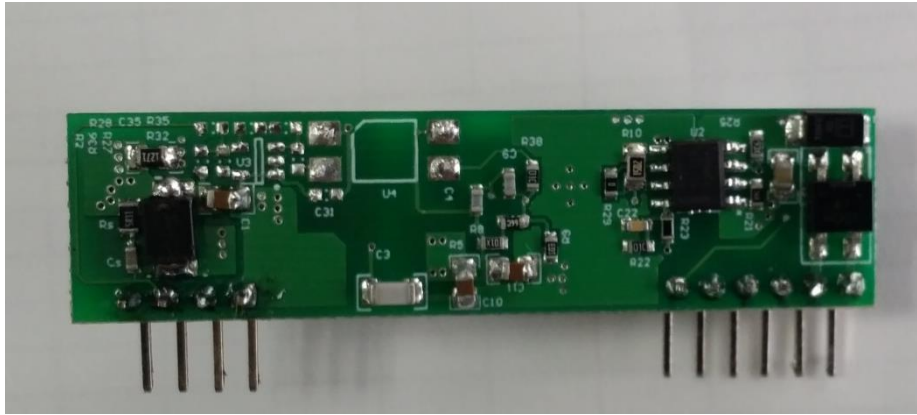


Context

1. Board Picture	2
2. Design Specifications	3
3. TYPICAL PERFORMANCE	4
3.1 EFFICIENCY	4
3.2 Load regulation	4
3.3 Load Transient Response:	5
3.4 Power up and Power down.....	6
3.5 Ripple	8
3.6 SW and Diode Stress	10
3.7 Short.....	11
3.8 Thermal-test in the room temp:	12

1. Board Picture





Board Size: 56.8mm*14.4mm

2. Design Specifications

Vin Min.	40Vdc
Vin Max.	57Vdc
Vout	12Vdc
Iout	1A

3. TYPICAL PERFORMANCE

3.1 EFFICIENCY

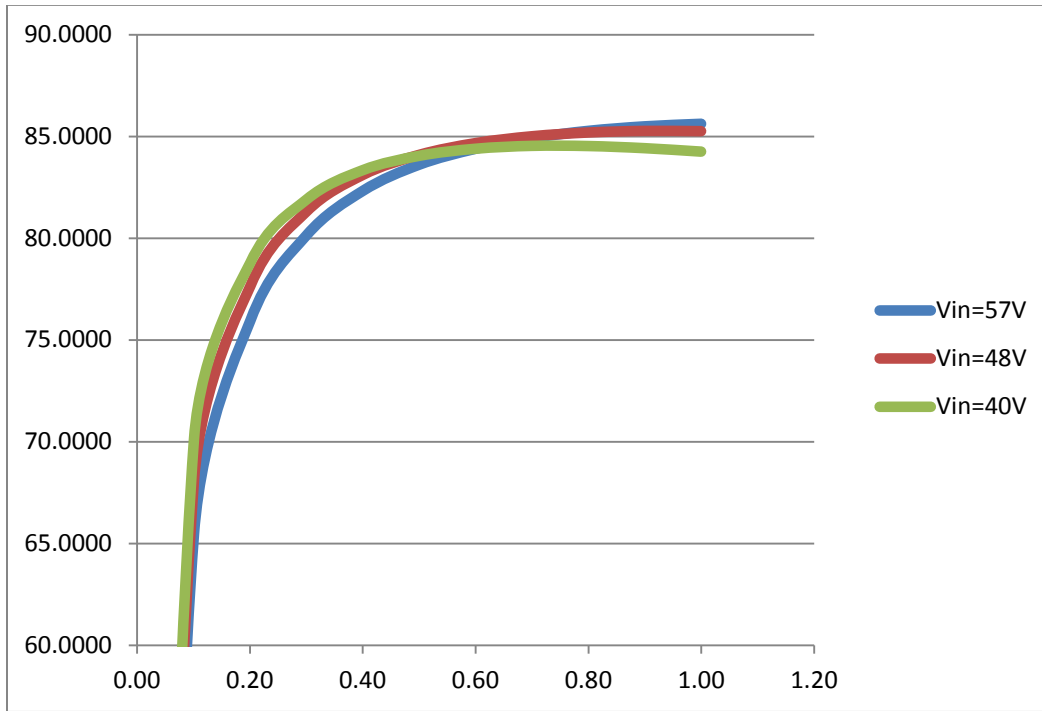


Fig1 Efficiency Curve

3.2 Load regulation

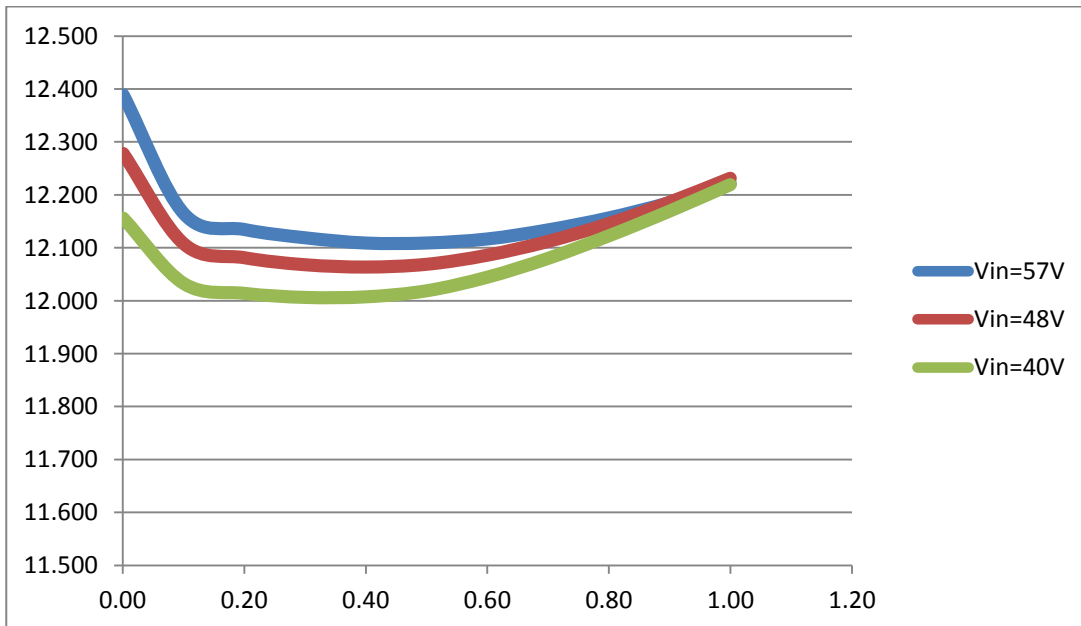


Fig2 Load regulation Curve

3.3 Load Transient Response:

Ch3=Vo_ac(200mV/DIV), CH4=Io(0.5A/DIV)

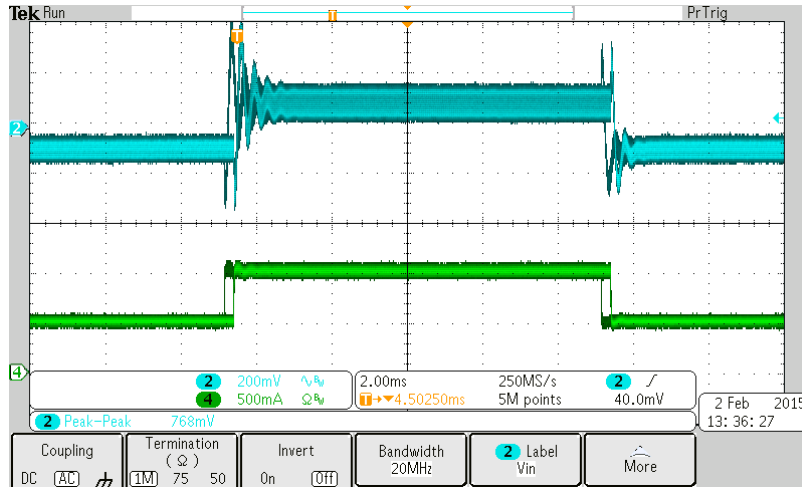


Fig3 Transient Response, Vin=40V DC Io=50%~100%~50%

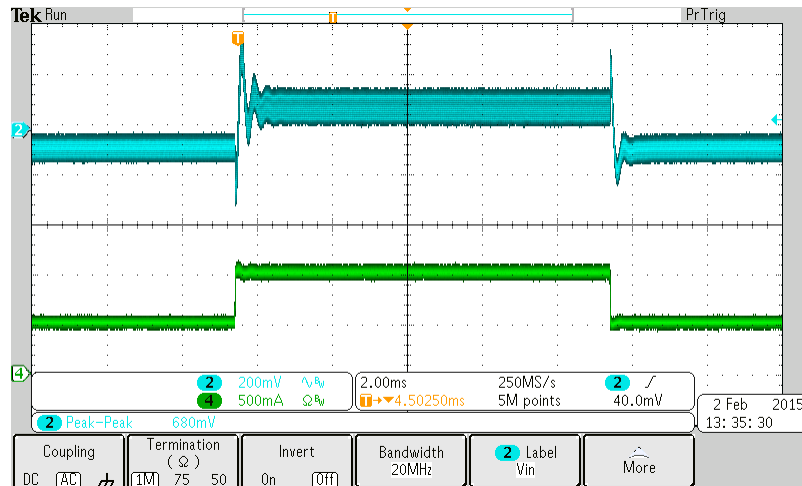


Fig 4 Transient Response, Vin=48V DC Io=50%~100%~50%

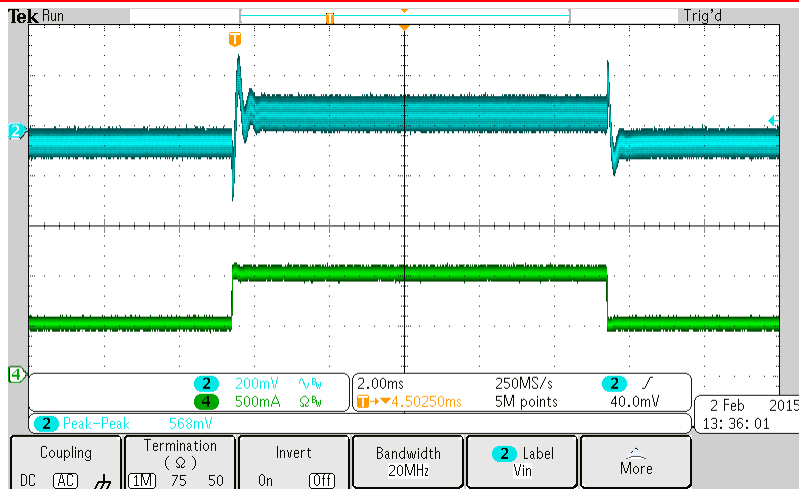


Fig 5 Transient Response, Vin=57V DC Io=50%~100%~50%

3.4 Power up and Power down

Ch1=Vo(5V/DIV), Ch3=Vin(10V/DIV),CH4=Io(2A/div)

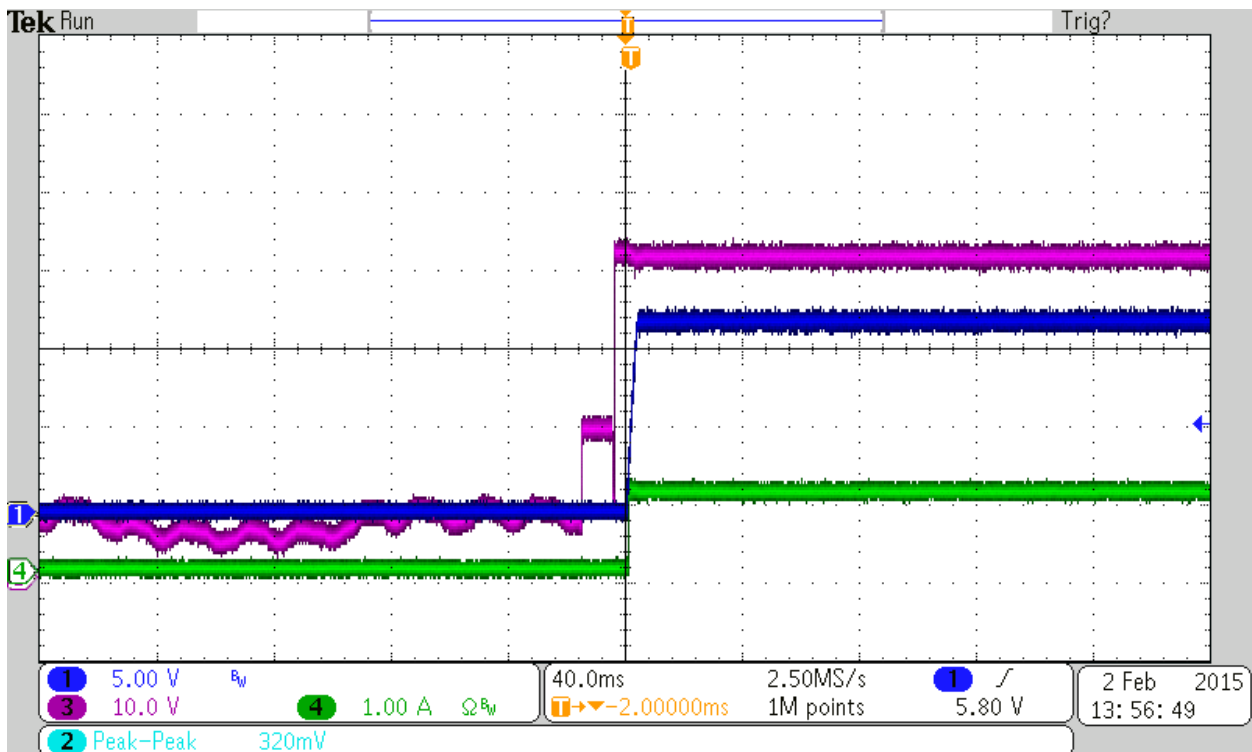


Fig 6 Vin=40V Io=1A Power up

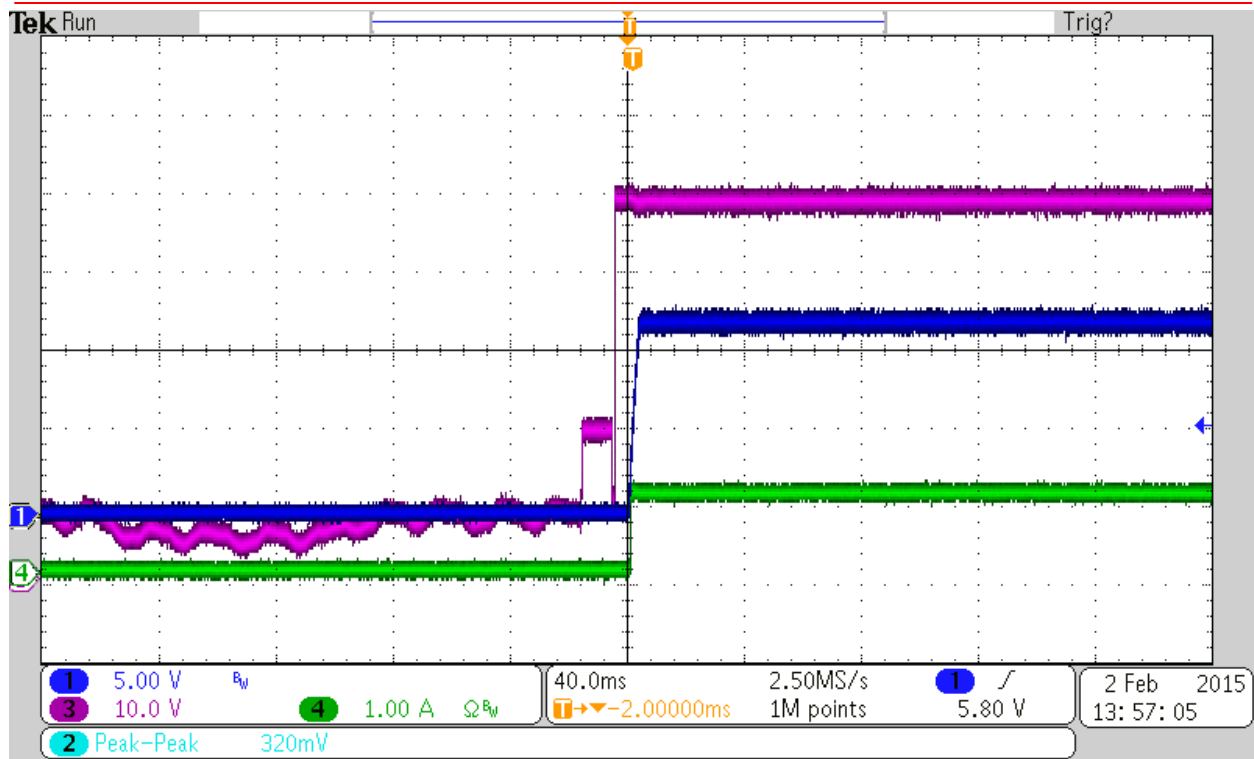


Fig 7 Vin=48V Io=1A Power up

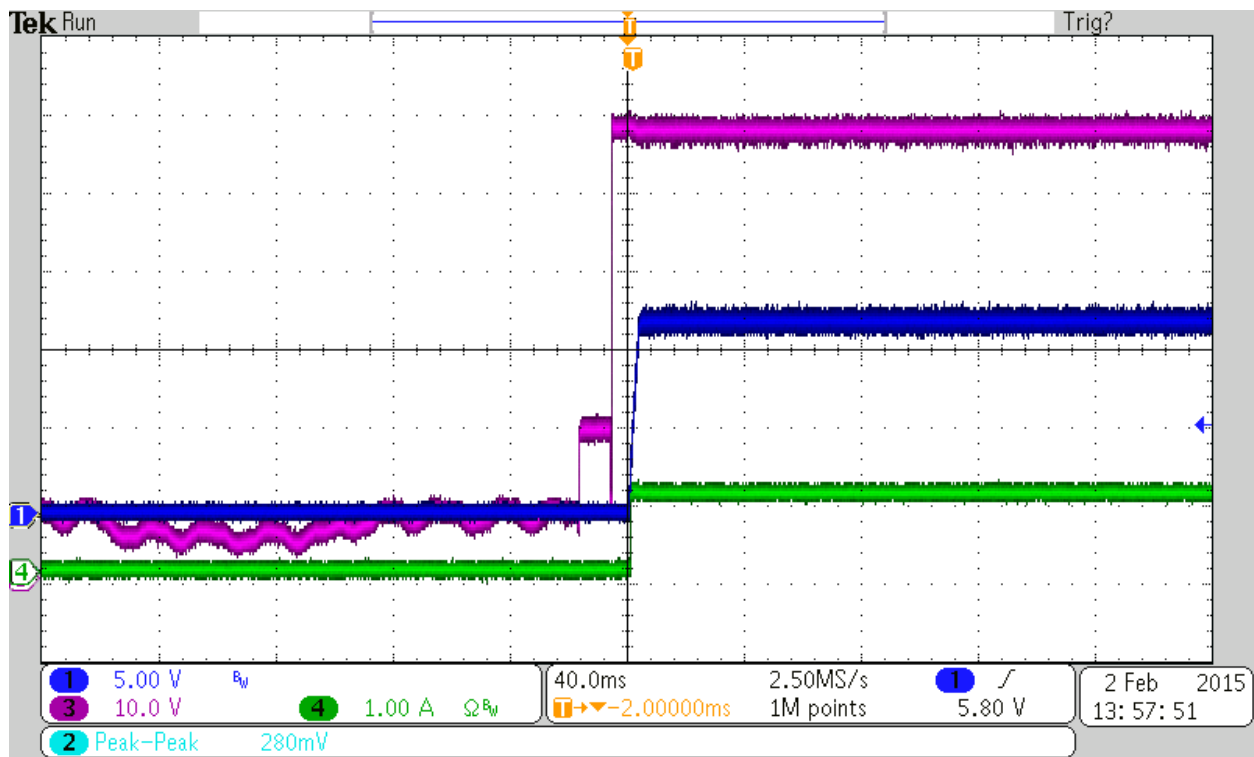


Fig 8 Vin=57V Io=1A Power Up

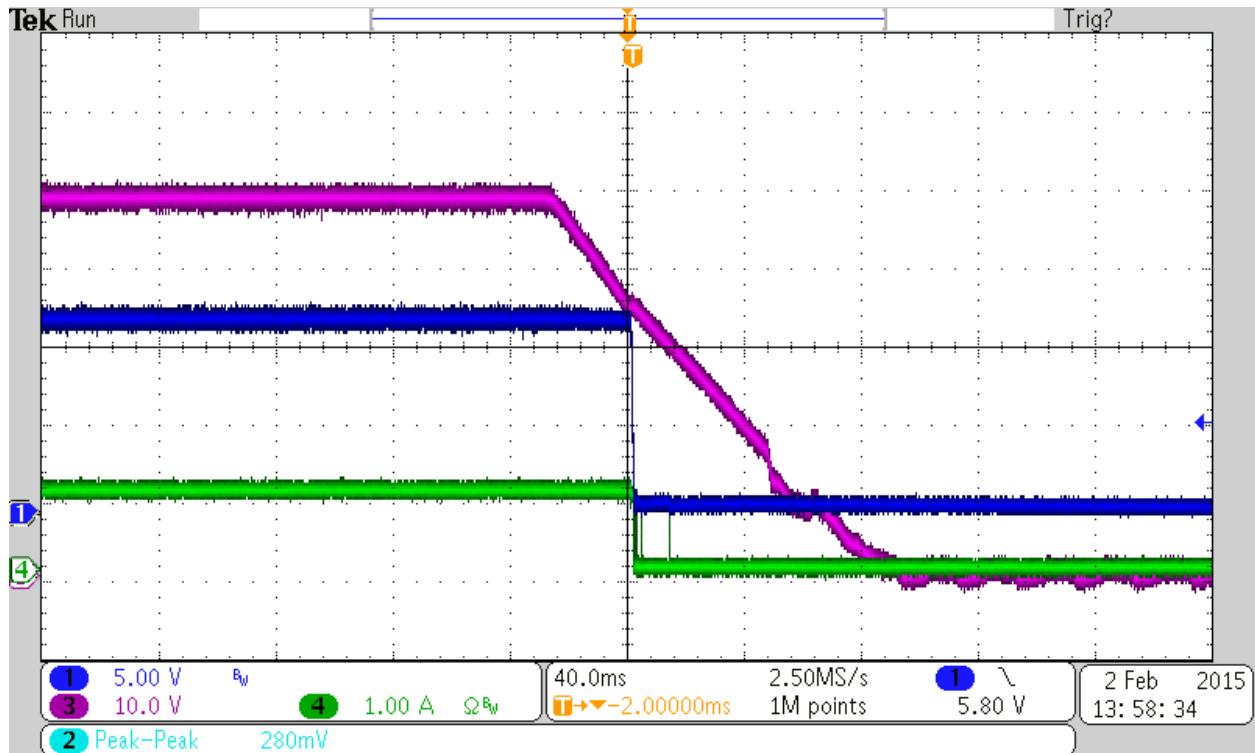


Fig 9 Vin=48V Io=1A Power down

3.5 Ripple

Ch1= V_{o_ac} (200mV/DIV) with 20 MHz Bandwidth (have a 0.1uF ceramic cap at output port)

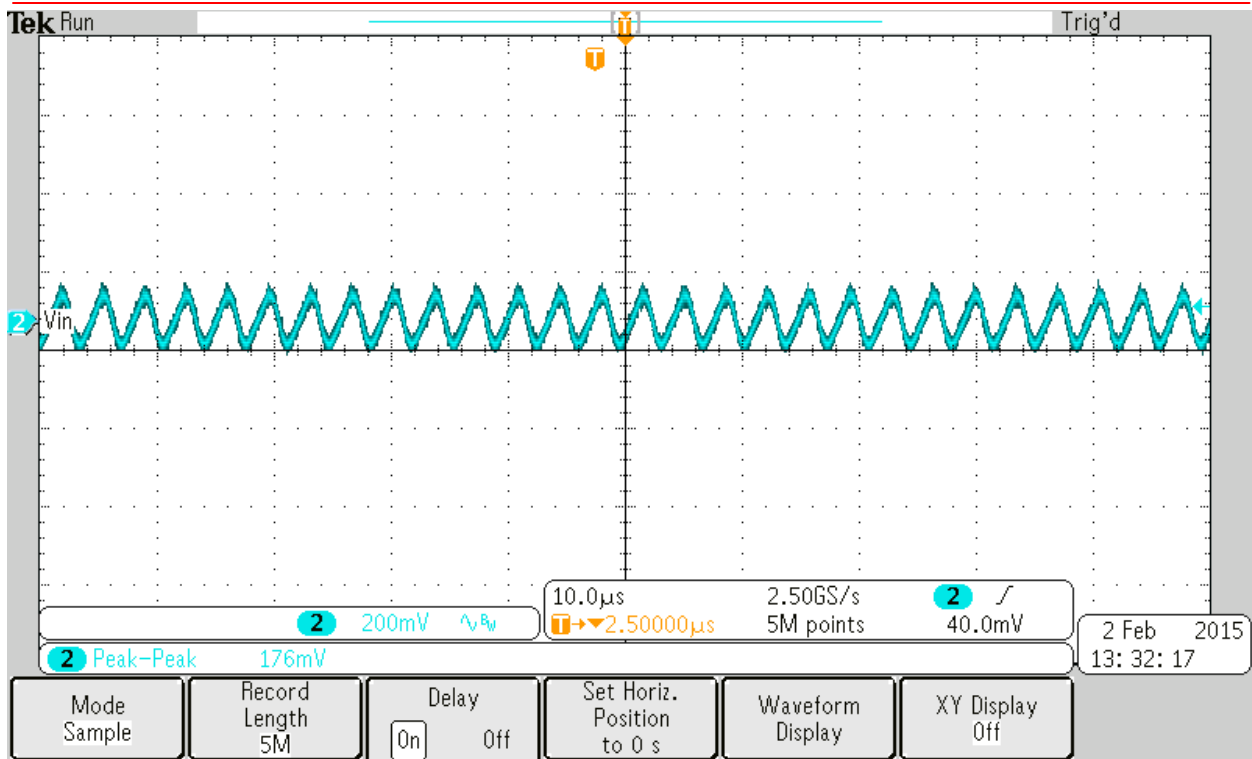


Fig 10 VIN=40V, Io=1A,

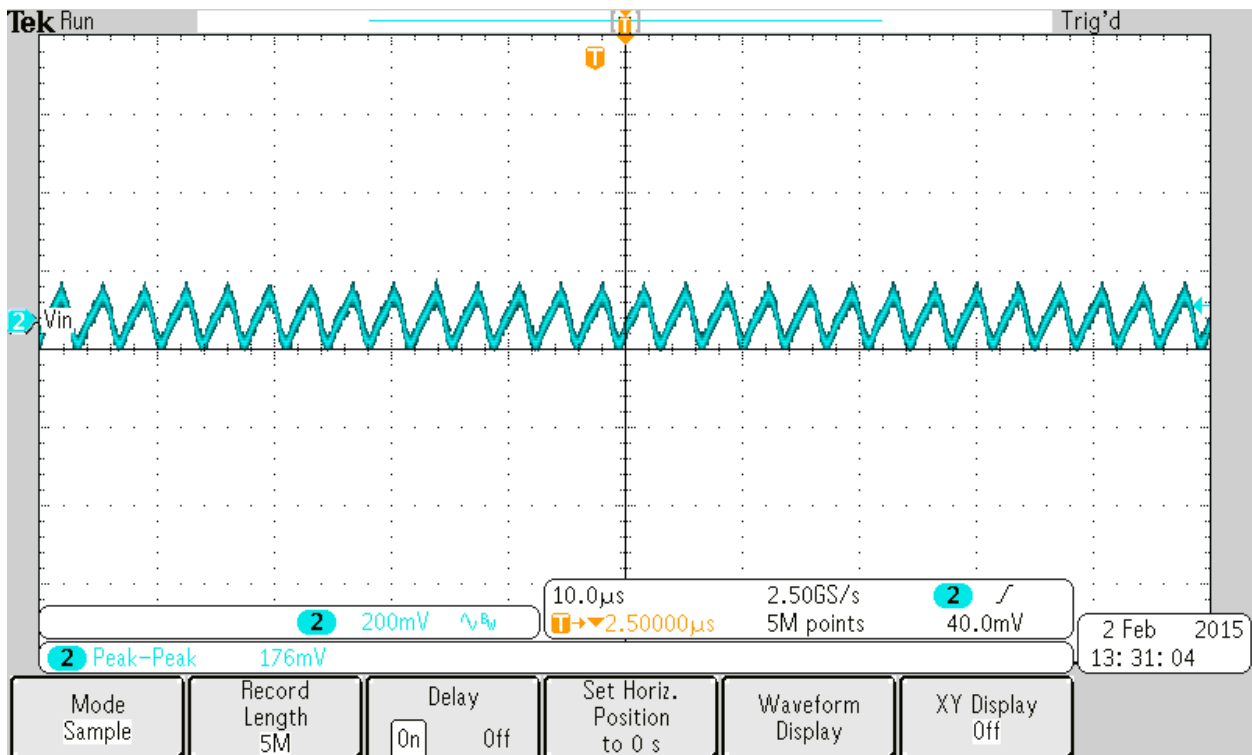


Fig 11 VIN=48V, Io=1A,

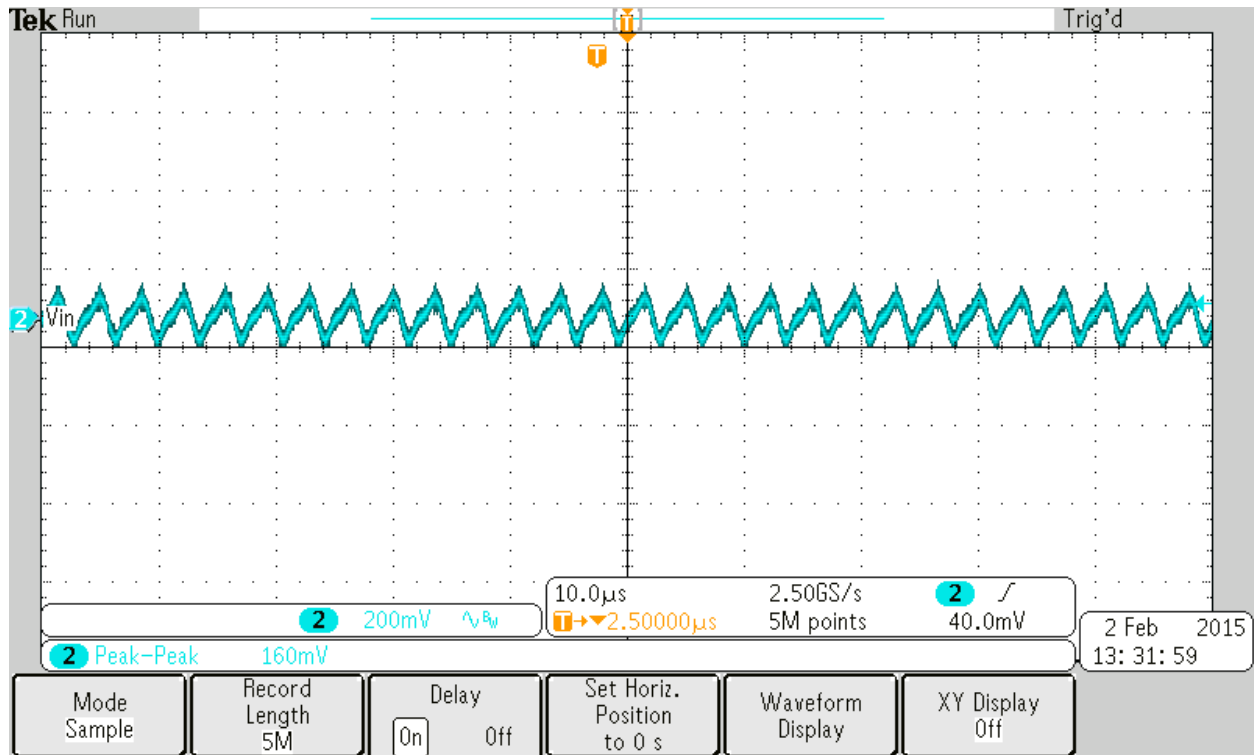


Fig 12 VIN=57VDC, Io=1A,

3.6 SW and Diode Stress

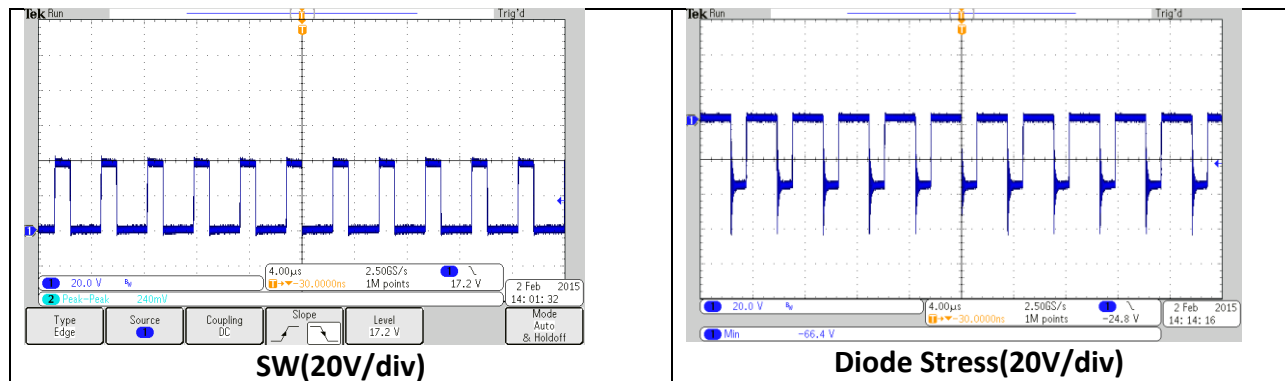


Fig 13 VIN=40VDC, Io=1A

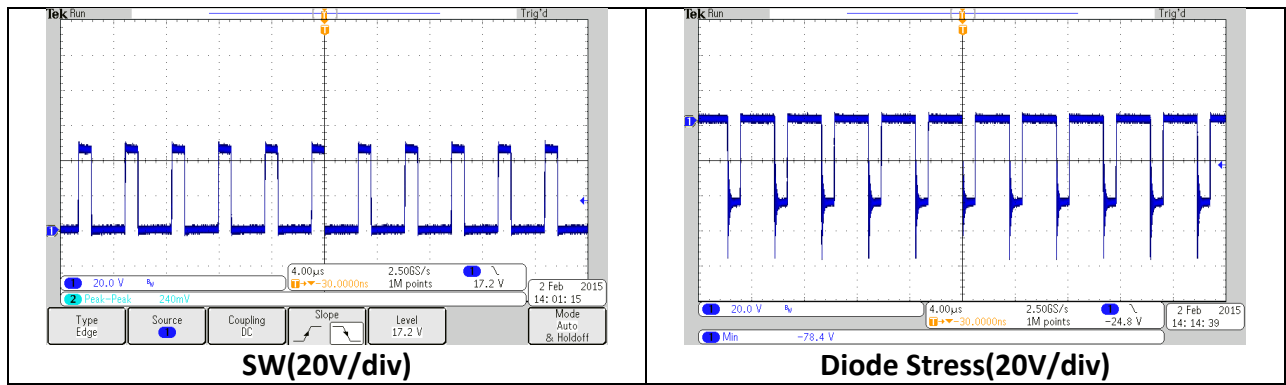


Fig 14 VIN=48VDC, Io=1A

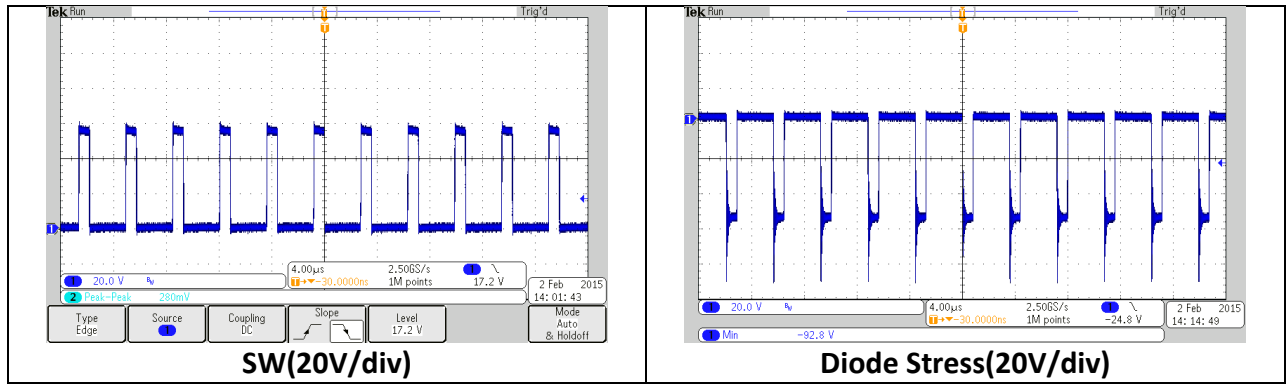


Fig 15 VIN=57VDC, Io=1A

3.7 Short

Ch2=Vo(5V/DIV), Ch4=Io(2A/DIV)

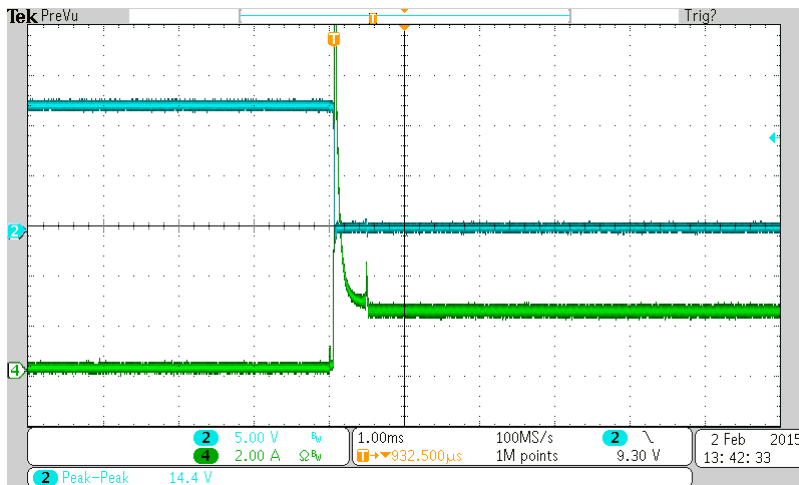


Fig 16 Vin=48V Short from No Load

3.8 Thermal-test in the room temp:

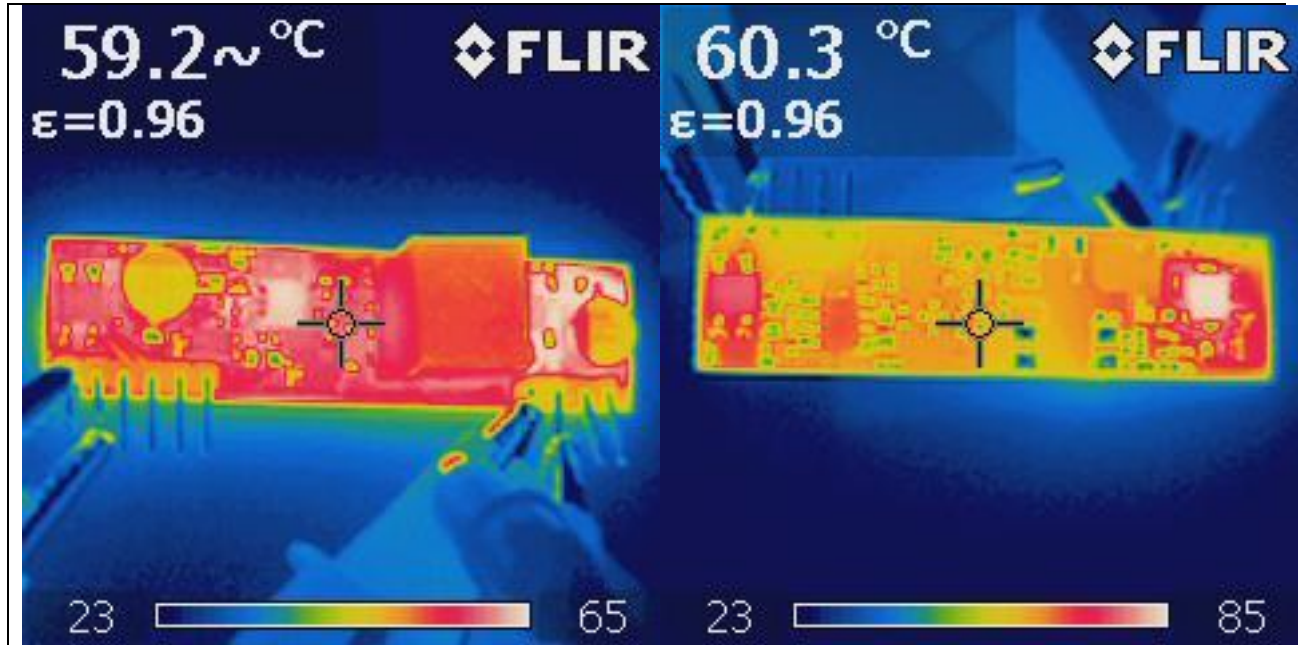


Figure 17 Vin=48V Io=1A

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