

NOTES, UNLESS OTHERWISE SPECIFIED:

1. The netname "V\_PRE\_REG" represents connection to the +10V power plane.
2. All components with designators "U", "D", "Y" and "Q" are electrostatic discharge sensitive.
3. The letters DNI near a part mean "do not install".

COMPUTER GENERATED DRAWING. DO NOT REVISE MANUALLY			
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	Initial Release	TBD	

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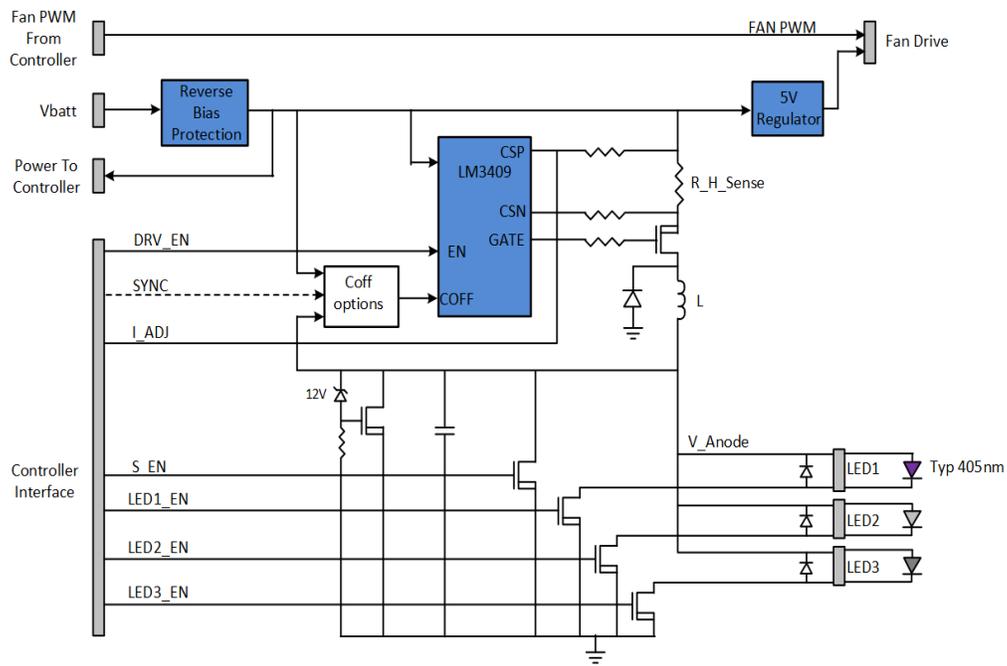
TI has specifically designated certain components as meeting ISO/TS16949 requirements, mainly for automotive use. In any case of use of non-designated products, TI will not be responsible for any failure to meet ISO/TS16949.

Z PCB1

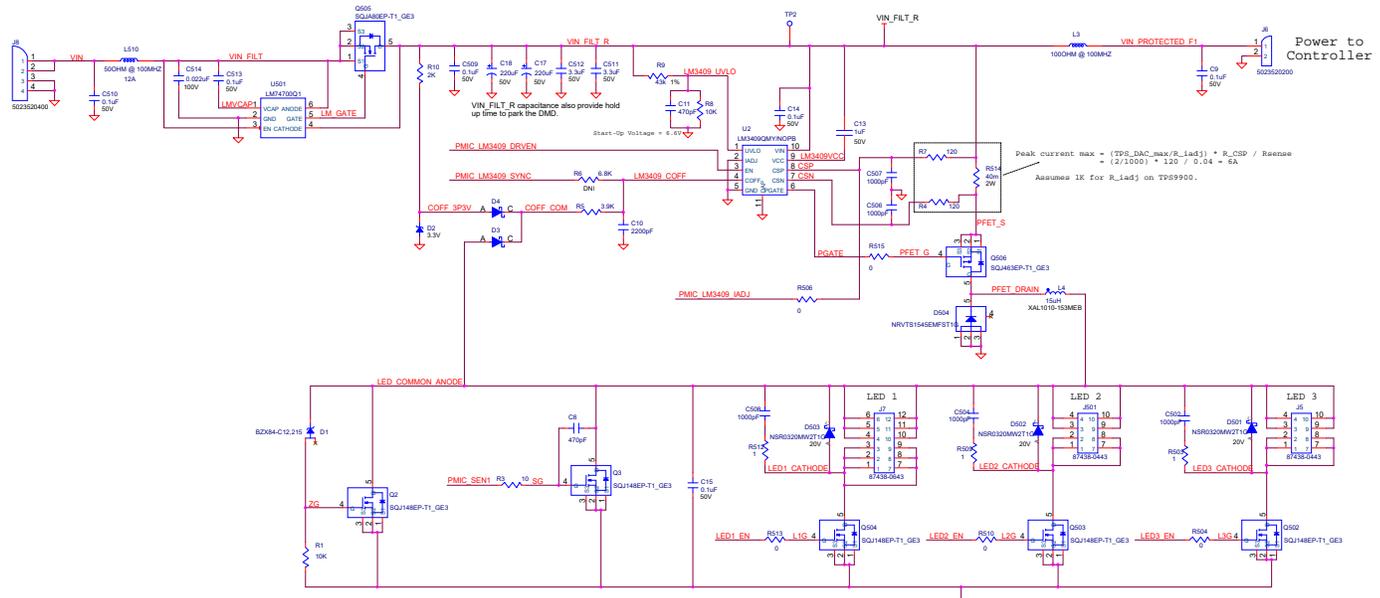
PCB: DLP5534Q1EVM Driver  
DLP034



OWN	George Pawlowski	DATE	5/01/2019	<b>TEXAS INSTRUMENTS</b> <small>© COPYRIGHT 2015 TEXAS INSTRUMENTS                  ALL RIGHTS RESERVED</small>	
ENGR					
DESIGN				TITLE DLP5534Q1EVM LED Driver	
CHKD					
APPV				DRAWING NO DLP034	
DATE					
NEXT ASSY	USED ON			REV	A
APPLICATION	REV	Cadence Capture 16.6		SCALE	SHEET 1 of 4



8V to 18V  
(must be  
higher than  
LED forward  
voltage)



Peak current max =  $(TPS\_DAC\_max/R\_Iadj) * R\_CSIP / Resense$   
 $= (2.1/1000) * 120 / 0.04 = 6A$   
 Assumes 1K for R\_Iadj on TPS9900.

