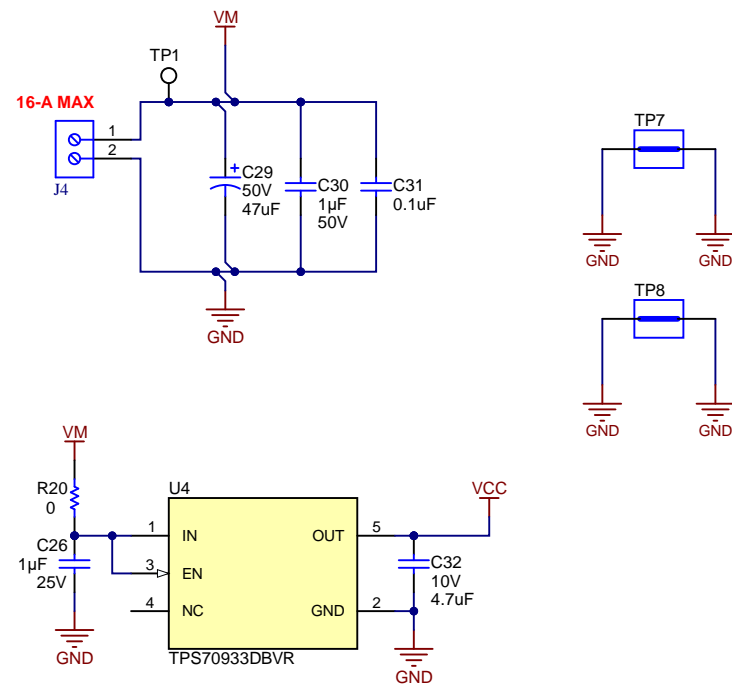
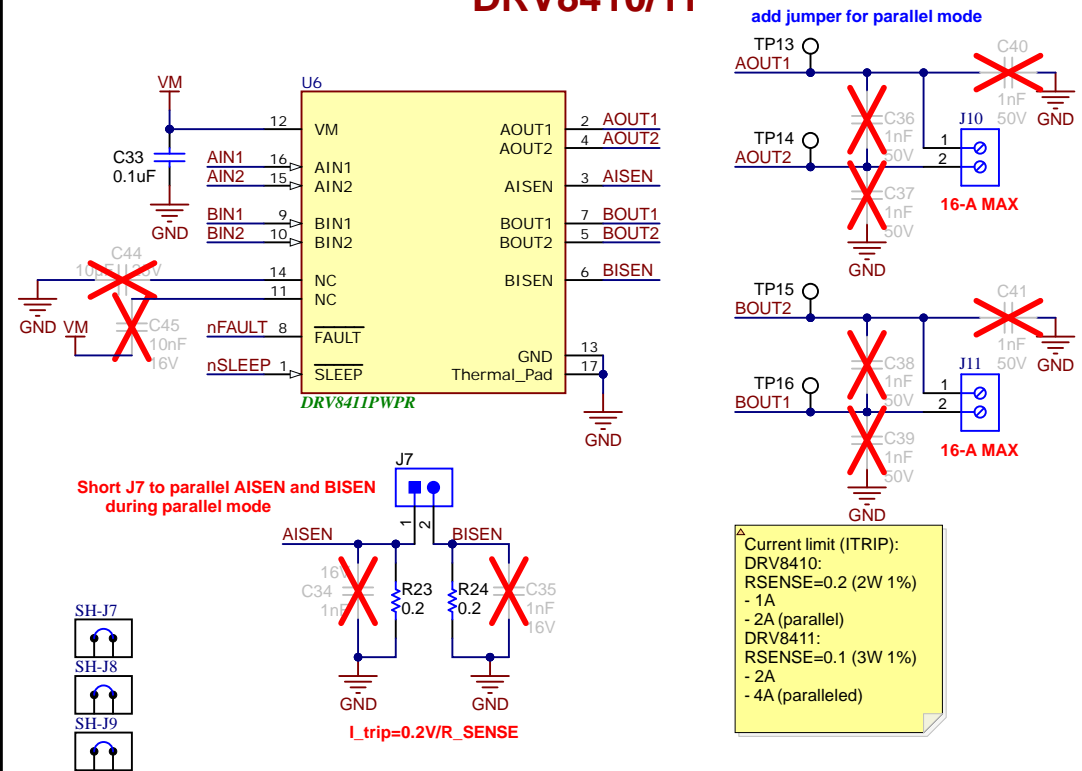


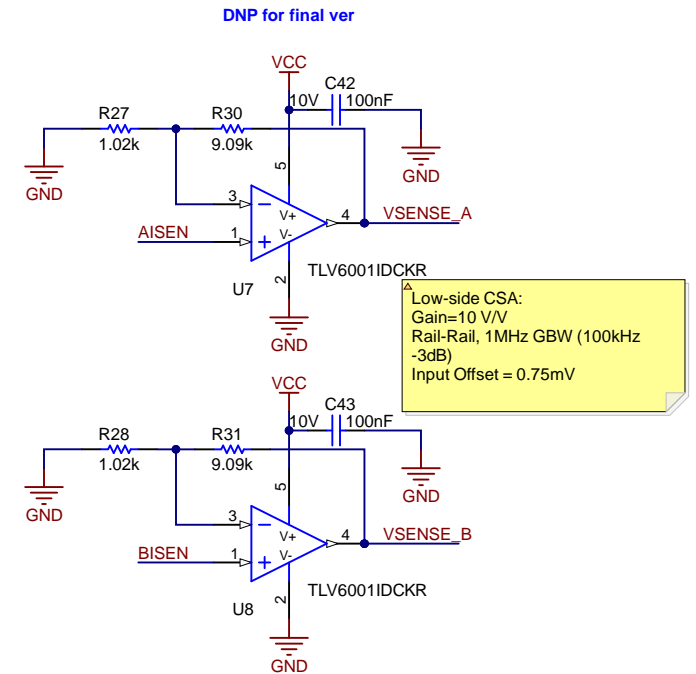
Power Connector / LDO



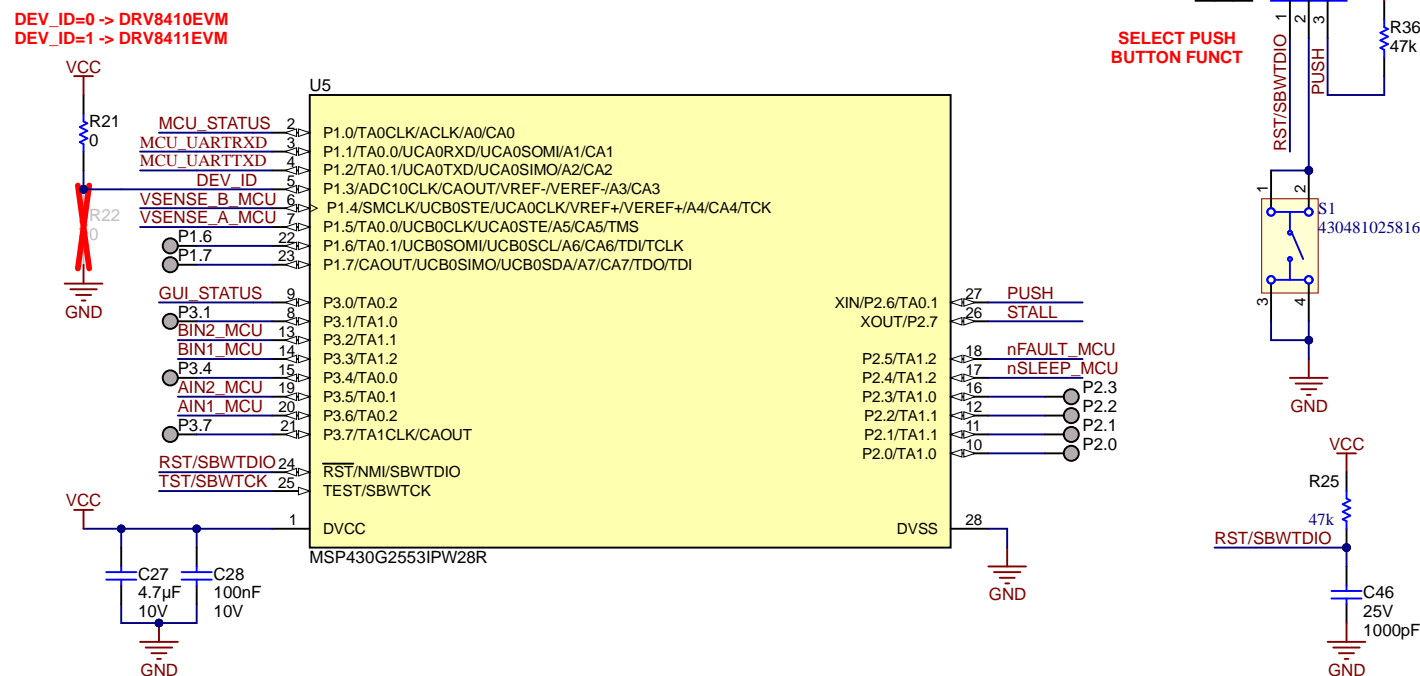
DRV8410/11



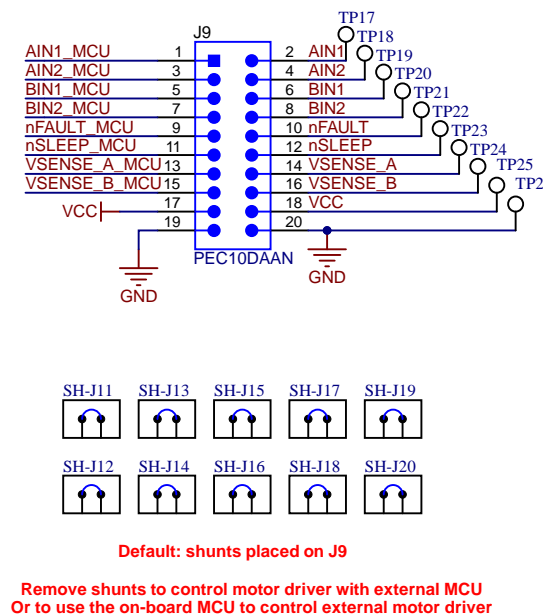
Low-Side CSA



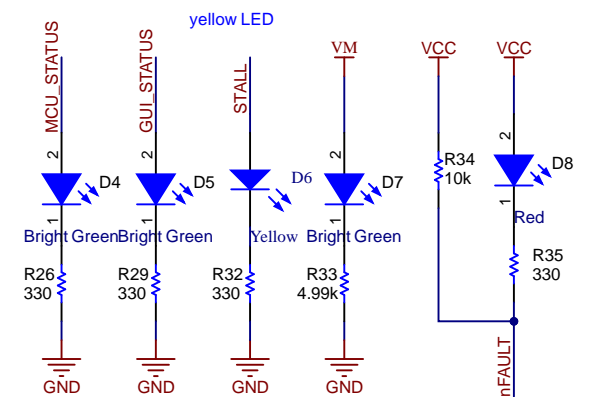
MSP430G2553



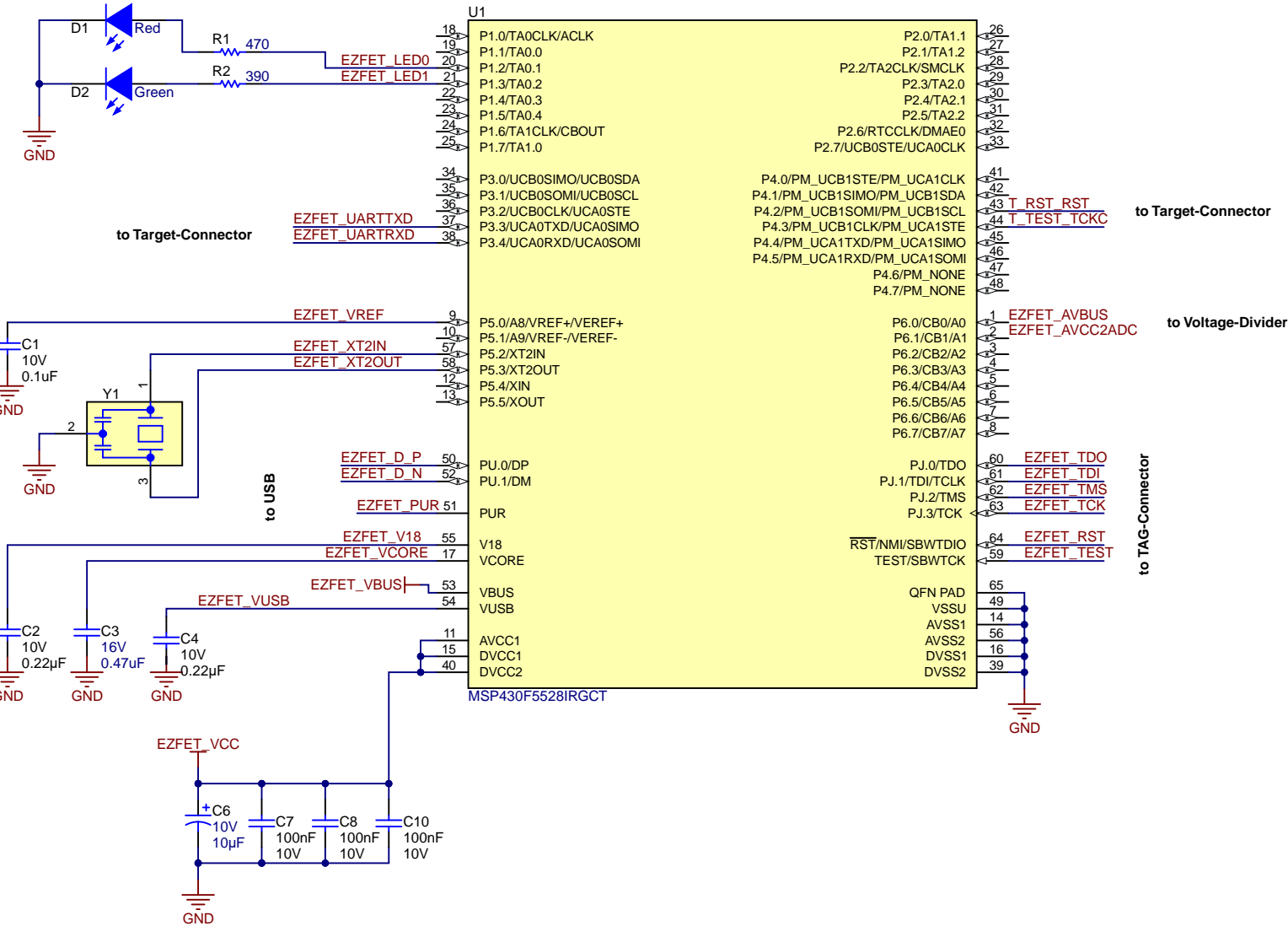
Signal Header



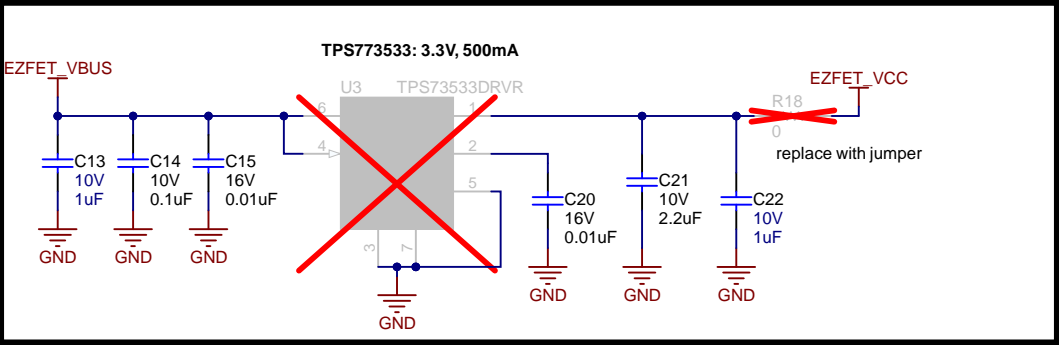
LED Indicators/Miscellaneous



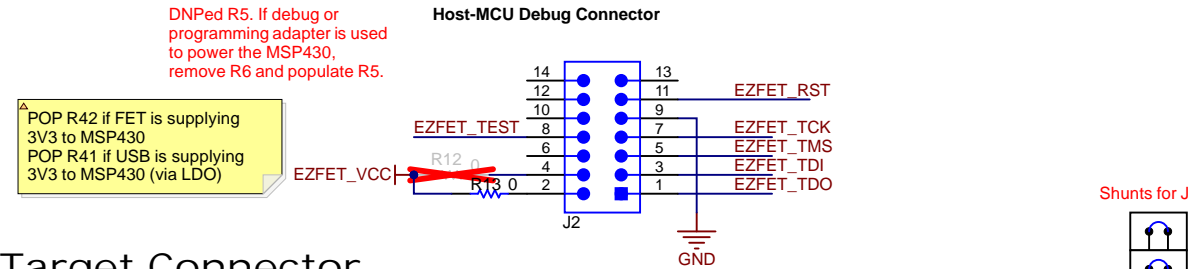
Host MCU for Emulation



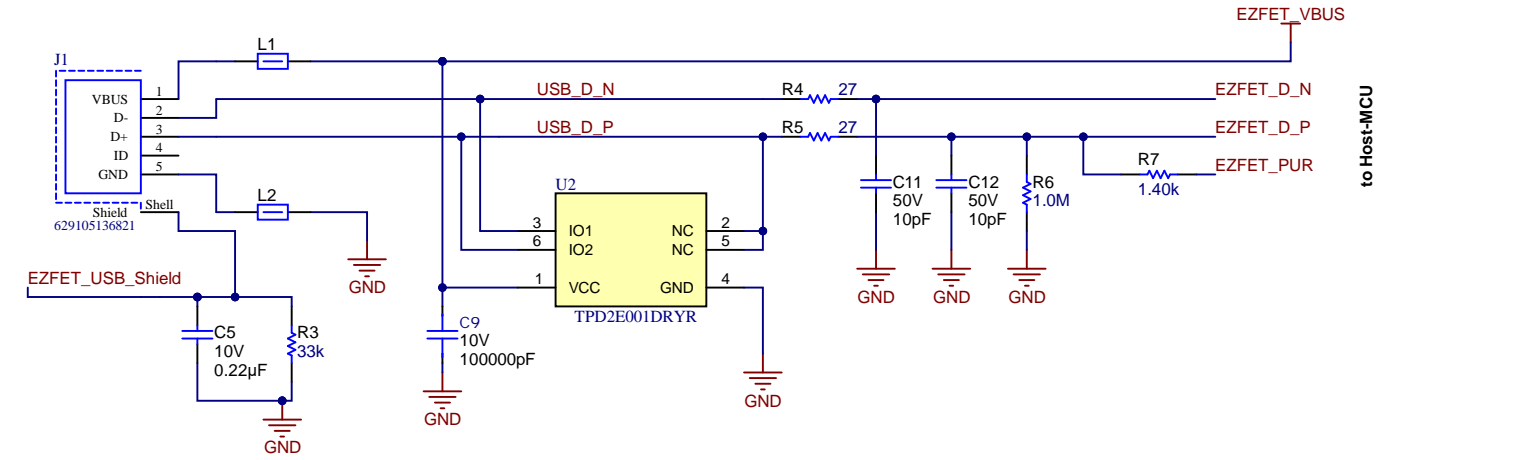
3.3V Power (EZFET_VCC)



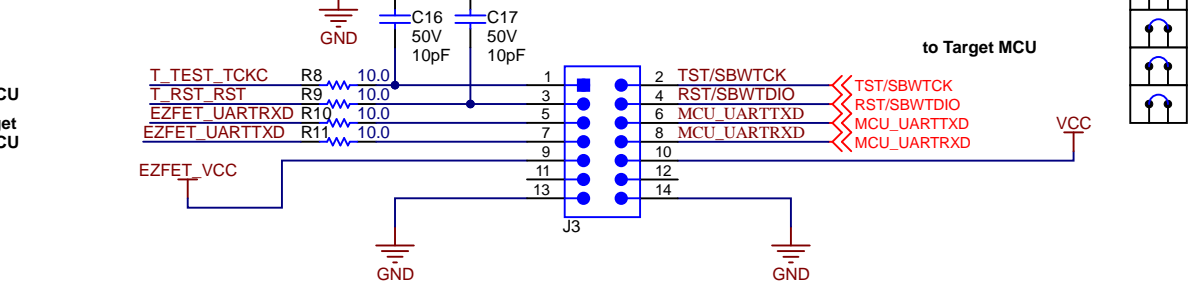
TAG-Connector (Host Debug)



USB-I-Interface



Target Connector



H1
SJ-5303 (CLEAR)

H2
SJ-5303 (CLEAR)

H3
SJ-5303 (CLEAR)

H4
SJ-5303 (CLEAR)

FID1

FID2

FID3

PCB Number: MD062
PCB Rev: E2

Logo1
PCB
LOGO
Texas Instruments


CE Mark

PCB
LOGO
FCC disclaimer

PCB
LOGO
WEEE logo

LBL1

PCB Label

THT-14-423-10
Size: 0.65" x 0.20 "

ZZ1

Label Assembly Note

This Assembly Note is for PCB labels only

Variant/Label Table	
Variant	Label Text
001	DRV8410EVM
002	DRV8411EVM

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.