Driver simulation setup example

**SN65DSI8X
LVDS driver**
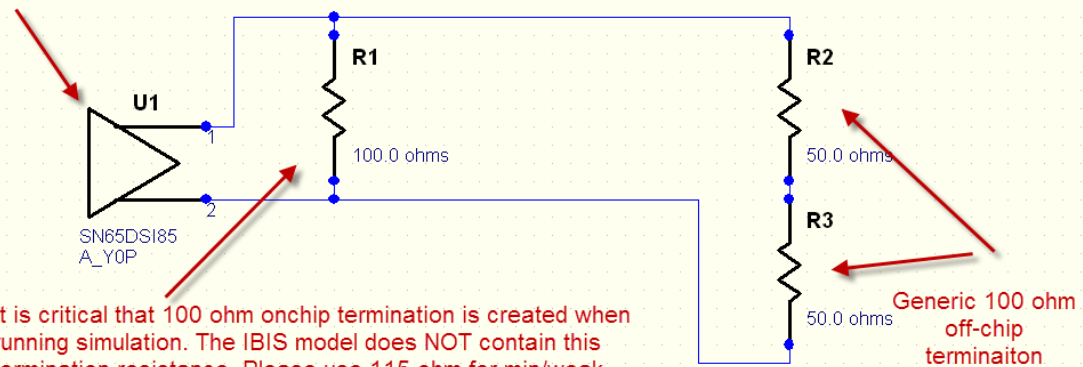
U1

1

2

SN65DSI85
A_Y0P

R1

100.0 ohms

R2

50.0 ohms

R3

50.0 ohms

It is critical that 100 ohm onchip termination is created when running simulation. The IBIS model does NOT contain this termination resistance. Please use 115 ohm for min/weak corner and 85 ohm for max/strong corner.
For LVDS driver with 200 ohm onchip setting, please change the resistor valuse to 200ohm(typ corner), 230ohm(min corner) and 170 ohm(max corner)
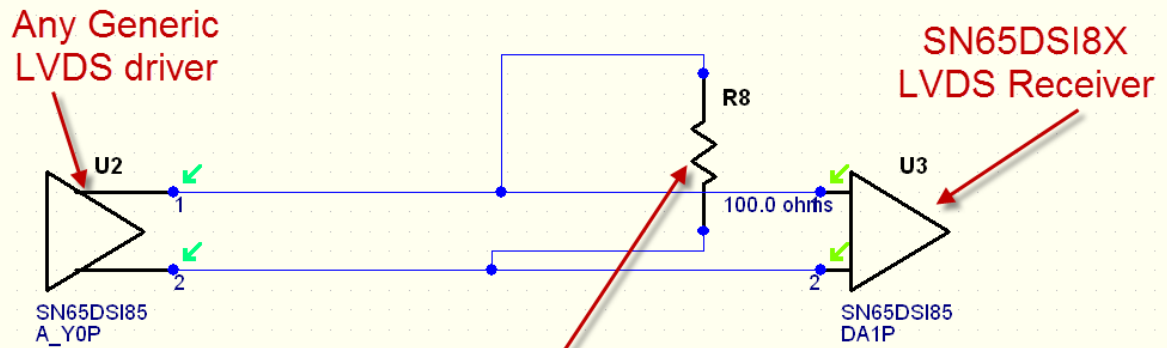
Generic 100 ohm off-chip terminaiton

Receiver simulation setup example

Any Generic LVDS driver

SN65DSI8X LVDS Receiver

U2

R8

100.0 ohms

U3

SN65DSI85
A_Y0P

SN65DSI85
DA1P

It is critical that 100 ohm onchip termination is created when running simulation. The IBIS receiver model does NOT contain this termination resistance. Please use 115 ohm for min/weak corner and 85 ohm for max/strong corner.