TPS7H1101-SP Pspice model is located on TI website under product tools & software tab this is standard for all TI designs. See link below.

<http://www.ti.com/product/TPS7H1101-SP/toolssoftware>

Once you open the TINA-TI Reference Design it will contain two schematics that showcase different behaviors.

TPS7H1101-SP simulation contains two Schematics.

**Schematic 1 – transient as shown below**.



Running this simulation will show the transient behavior Vin-turn on, Vout, powergood (PG)

**Schematic 2 - TPS7H1101-SP Output Impedance addresses - stability.**

**Here one measures output impedance as shown below.**



Group delay is converted to phase margin as shown by the equation highlighted.





